



Centre of Excellence in VLSI

# CERTIFICATE OF COMPLETION

*Celebrating*  
**10 YEARS**  
*Of Excellence*

*This is to certify that*

**Siddhant Chaurasia**

*has successfully completed*

**VLSI SoC Design using Verilog HDL**

*Online Course on 12th day of July 2021*



**SIVAKUMAR P R**  
CEO, MAVEN SILICON