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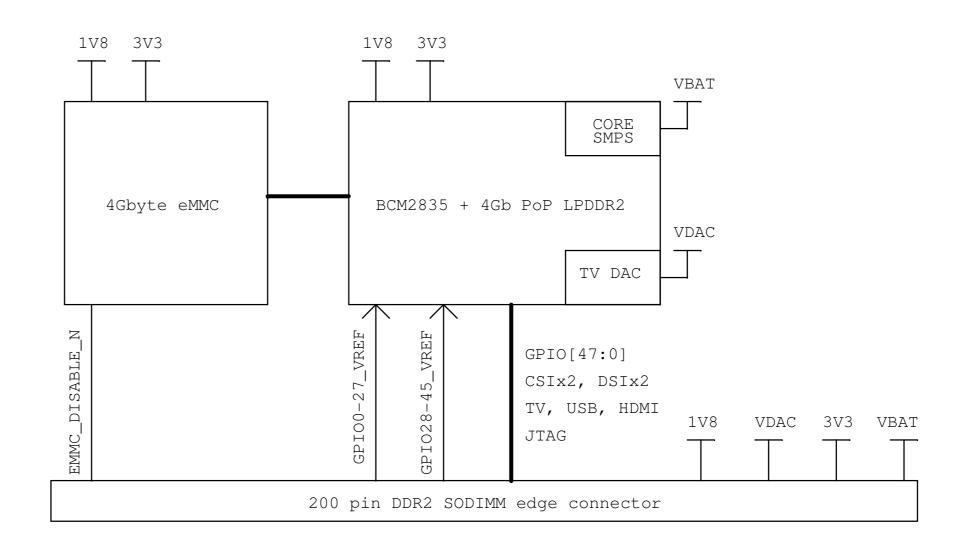
PAGE4 - SODIMM CONNECTOR

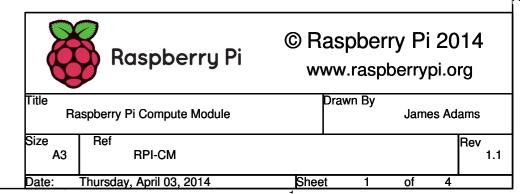
## REVISION HISTORY:

23/01/2014 - V1.0

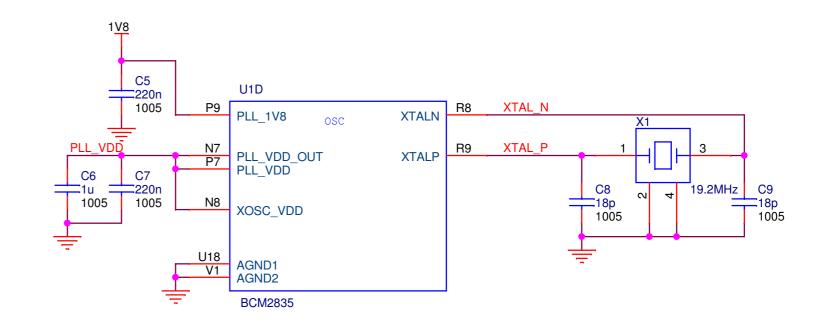
03/04/2014 - V1.1 - Production Version

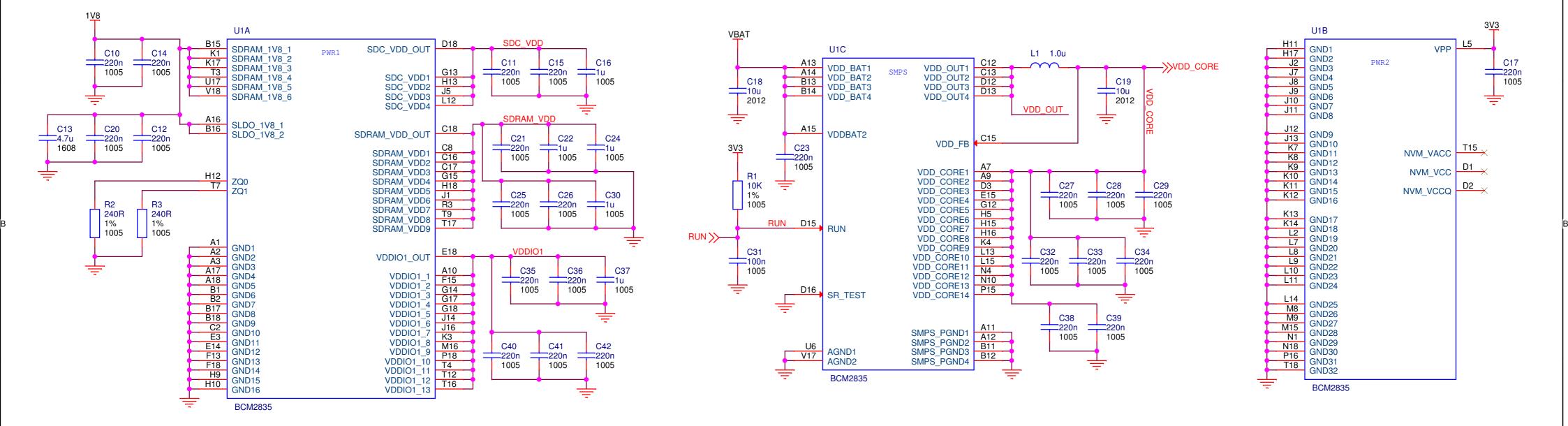
## BLOCK DIAGRAM:

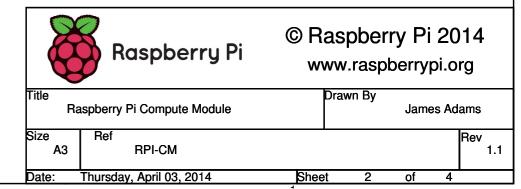


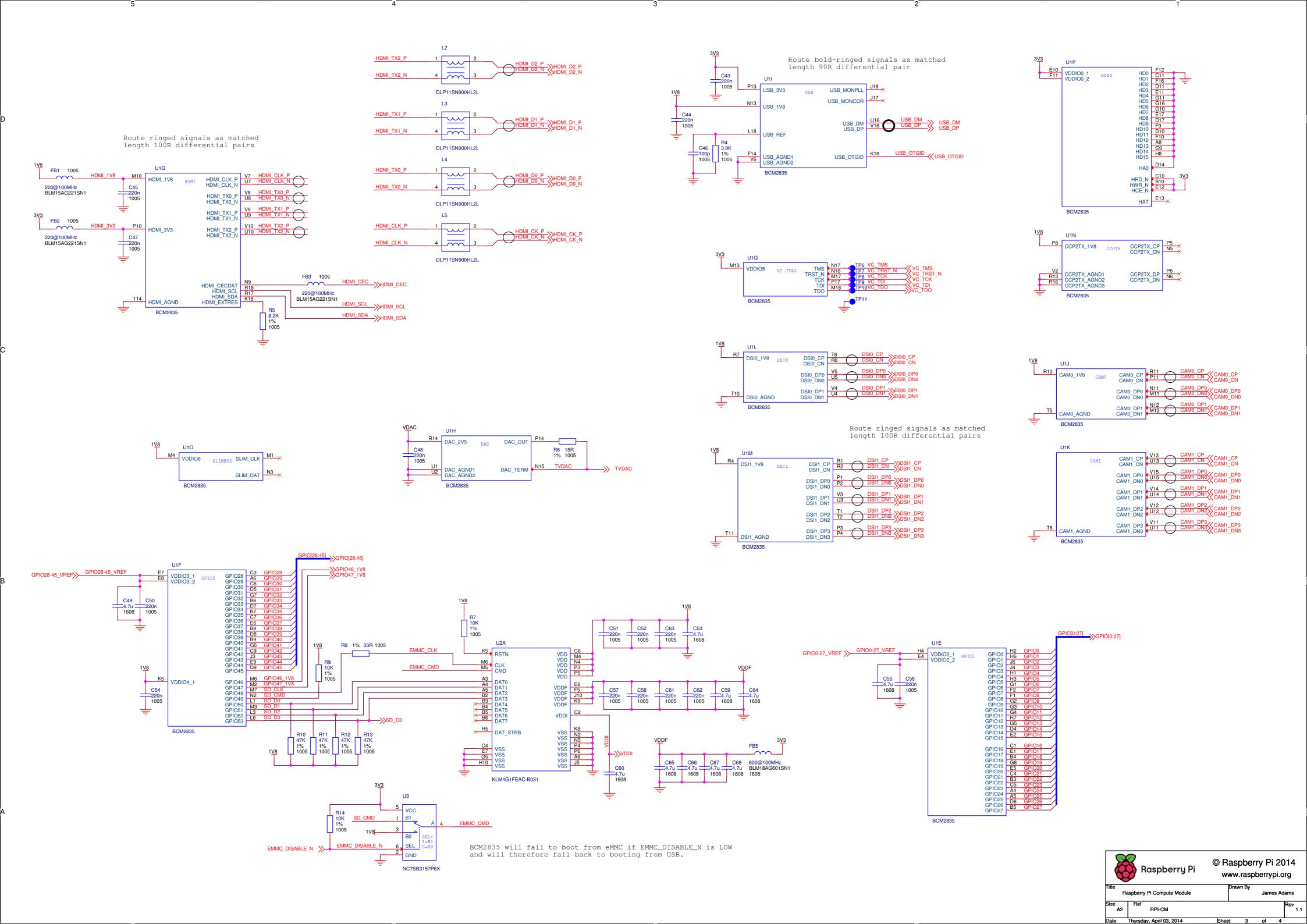












EMMC\_DISABLE\_N >>> EMMC\_DISABLE\_N GPIO0-27\_VREF 
GPIO0-27\_VREF GPIO0-27\_VREF GPIO28-45\_VREF < GPIO28-45\_VRE GPIO[0:27] >> GPIO[0:27] GPIO[28:45] >> GPIO[28:45] Route ringed signals as matched length 100R differential pairs Route bold-ringed signals as matched length 90R differential pair VDD\_CORE used for module test only (do not use in normal operation)

DDR2 SODIMM

Raspberry Pi

Raspberry Pi

Raspberry Pi 2014

www.raspberrypi.org

Prawn By

James Adams

Size

A2

Ref

RPI-CM

Rev

1.1