Micropower Undervoltage Sensing Circuits with Programmable Output Delay

The MC33465 series are micropower undervoltage sensing circuits that are specifically designed for use with battery powered microprocessor based systems, where extended battery life is required. A choice of several threshold voltages from 0.9 V to 4.5 V are available. This device features a very low quiescent bias current of 1.0 μ A typical.

The MC33465 series features a highly accurate voltage reference, a comparator with precise thresholds and built—in hysteresis to prevent erratic reset operation, a choice of output configurations between open drain or complementary, a time delayed output, which can be programmed by the system designer, and guaranteed operation below 1.0 V with extremely low standby current. This device is available in a SOT–23 5–pin surface mount package.

Applications include direct monitoring of the MPU/logic power supply used in appliance, automotive, industrial and portable equipment.

Features:

- Extremely Low Standby Current of 1.0 μ A at $V_{in} = 3.5 \text{ V}$
- Wide Input Voltage Range (0.7 V to 10 V)
- Monitors Power Supply Voltages from 1.1 V to 5.0 V
- High Accuracy Detector Threshold (±2.5%)
- Two Reset Output Types (Open Drain or Complementary Drive)
- Programmable Output Delay by External Capacitor (100 ms typ. with 0.15 μF)
- Surface Mount Package (SOT-23 5-Pin)
- Convenient Tape and Reel (3000 per Reel)



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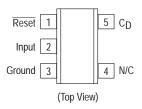
Formerly a Division of Motorola

http://onsemi.com



SOT-23-5 N SUFFIX CASE 1212

PIN CONNECTIONS

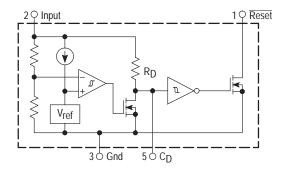


ORDERING INFORMATION

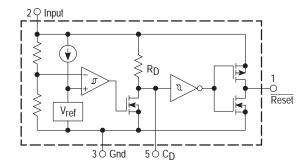
See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

Representative Block Diagrams

MC33465N-YYATR Open Drain Output Configuration



MC33465N-YYCTR Complementary Output Configuration



YY Denotes Threshold Voltage

This device contains 28 active transistors.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Input Supply Voltage	V _{in}	0 to 12	V
Reset Output Voltage	Vo	-0.3 to 12	V
Reset Output Current (Source or Sink)	IO	70	mA
Power Dissipation and Thermal Characteristics Maximum Power Dissipation at T _A = 25°C Case 1212 (SOT–23) N Suffix	PD	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	667	°C/W
Operating Junction Temperature	TJ	+125	°C
Operating Ambient Temperature	T _A	-40 to +85	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C
Lead Temperature (Soldering)	T _{solder}	260°C, 10 s	

ELECTRICAL CHARACTERISTICS (For all values $T_A = 25$ °C (Note 1), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
SENSE COMPARATOR					
Threshold Voltage					V
High to Low State Output (Vin Decreasing)	VIL				
09 Suffix		0.878	0.9	0.922	
20 Suffix		1.950	2.0	2.050	
22 Suffix		2.145	2.2	2.255	
27 Suffix		2.633	2.7	2.767	
28 Suffix		2.730	2.8	2.870	
30 Suffix		2.925	3.0	3.075	
32 Suffix		3.120	3.2	3.280	
34 Suffix		3.315	3.4	3.485	
38 Suffix		3.705	3.8	3.895	
43 Suffix 44 Suffix		4.193 4.290	4.3 4.4	4.407 4.510	
45 Suffix		4.290	4.4	4.612	
46 Suffix		4.485	4.6	4.715	
47 Suffix		4.583	4.7	4.817	
	\/				V
Threshold Hysteresis (V _{in} Increasing) 09 Suffix	VН	0.027	0.045	0.063	\ \ \
20 Suffix		0.027	0.100	0.003	
22 Suffix		0.066	0.100	0.154	
27 Suffix		0.081	0.110	0.189	
28 Suffix		0.084	0.140	0.196	
30 Suffix		0.090	0.150	0.210	
32 Suffix		0.096	0.160	0.224	
34 Suffix		0.102	0.170	0.238	
38 Suffix		0.114	0.190	0.266	
43 Suffix		0.129	0.215	0.301	
44 Suffix		0.132	0.220	0.308	
45 Suffix		0.135	0.225	0.315	
46 Suffix		0.138	0.230	0.322	
47 Suffix		0.141	0.235	0.329	
Threshold Voltage Temperature Coefficient	TC	_	±100	_	PPM/°C
RESET OUTPUT					
Output Voltage					V
High State (Complementary Output: I _{SOURCE} = 1.0 mA)	VOH	V _{in} – 2.1	V _{in} – 1.0	Vin	
Low State (Complementary or Open Drain Output: I _{Sink} = 1.0 mA)	VOL	_	0.25	0.5	
Output Sink Current (V _{in} = 1.5 V, V _{OL} = 0.5 V)	lOL	1.0	2.0	_	mA
Output Source Current ($V_{in} = 4.5 \text{ V}$, $V_{OH} = 2.4 \text{ V}$)	IOH	1.0	2.0	-	mA
DELAY OUTPUT					
Output Sink Current (V _{in} = 1.5 V, V _{OL} = 0.5 V)	lOL	0.2	0.8	_	mA
Delay Resistance	R _D	0.5	1.0	2.0	ΜΩ
TOTAL DEVICE					
Operating Input Voltage Range	V _{in}	0.8 to 10	-	-	V
Quiescent Input Current	l _{in}				μА
V _{in} = 4.34 V	"'	_	5.5	11	,
V _{in} = 6.50 V		_	1.3	3.9	

NOTE: 1. Low duty pulse techniques are used during test to maintain junction temperature as close to ambient as possible.

Output Voltage MC33465N-45ATR $T_A = 20^{\circ}C$ V_{in} = 2.5 V

Figure 1. Reset Low State Sink Current versus

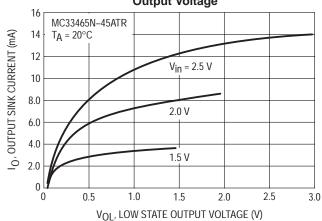
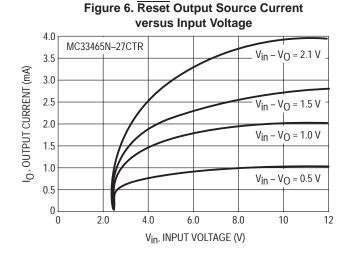


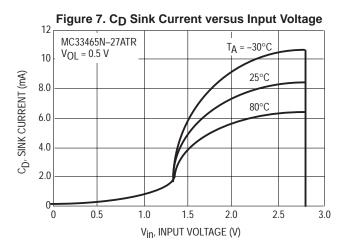
Figure 2. Output Voltage versus Input Voltage 7.0 MC33465N-45ATR 470 k Ω Pull–up 6.0 V_O, OUTPUT VOLTAGE (V) 5.0 30°C 4.0 3.0 $T_A = -30^{\circ}C$ T_A = 25°C T_A = 80°C 80°C, 20°C 1.0 2.0 3.0 0 1.0 4.0 5.0 6.0 Vin, INPUT VOLTAGE (V)

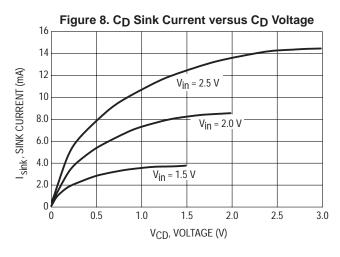
Figure 3. Input Current versus Input Voltage 8.0 MC33465N-45ATR 7.0 I_{in}, SUPPLY CURRENT (μA) $T_A = 80^{\circ}C$ 6.0 5.0 $T_A = 25^{\circ}C$ 4.0 3.0 80°C 25°C T_A = −30°C 1.0 0 2.0 4.0 8.0 10 V_{in}, INPUT VOLTAGE (V)

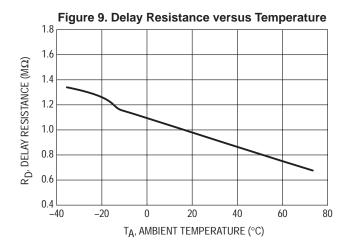
Figure 4. Comparator Input Threshold Voltage versus Temperature 3.0 V_{in} , INPUT THRESHOLD VOLTAGE (V) 2.9 V_{IL} + V_H 2.85 2.8 ۷н 2.75 V_{IL} 2.7 MC33465N-27ATR 2.6 -40 -20 80 TA, AMBIENT TEMPERATURE (°C)

Figure 5. Reset Output Sink Current versus Input Voltage MC33465N-27ATR T_A = −30°C $V_{OL} = 0.5 V$ IO, OUTPUT SINK CURRENT (mA) 10 25°C 8.0 80°C 6.0 2.0 0 0.5 V_{in}, INPUT VOLTAGE (V)









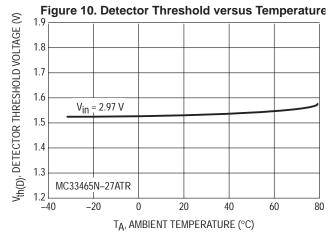


Figure 11. Output Delay Time versus Delay Capacitance

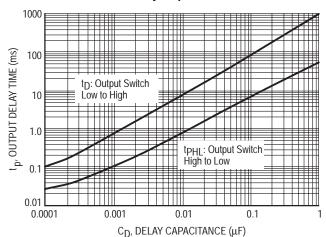


Figure 12. Typical Open Drain Application

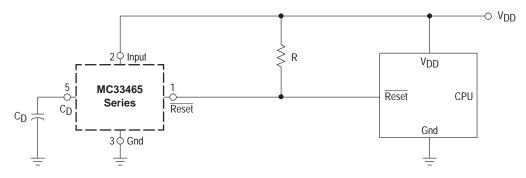
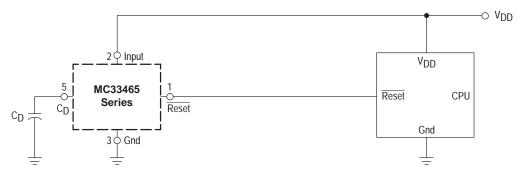


Figure 13. Typical Complementary Output Application

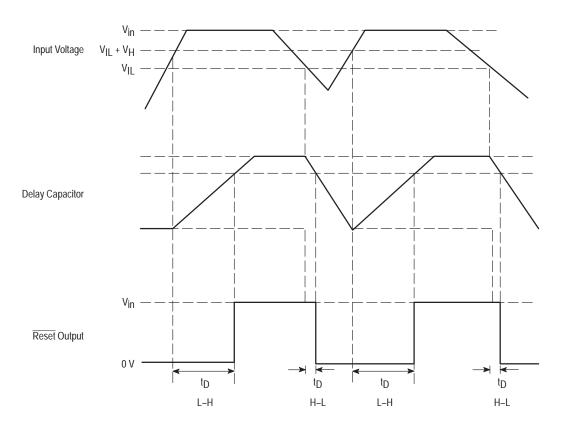


APPLICATION CIRCUIT INFORMATION

The MC33465 series are micropower undervoltage sensing circuits which offer a programmable time delayed output with the choice of either complementary output drive or open drain output configurations. Figure 14 shows the timing relationships between the input voltage and the resulting circuit waveforms. When the input voltage (V_{in}) exceeds the sense comparator threshold, the timing capacitor is allowed to charge through the internal delay resistor. When the output inverting driver threshold is exceeded, the Reset output switches from a logic "0" to a

logic "1". The top curve of Figure 11 provides the nominal delay time for a given value of delay capacitance. When V_{in} drops below the comparator threshold minus hysteresis voltage, the delay capacitor discharges. When the capacitance voltage drops below the inverting drive threshold, the output switches from a logic "1" to a logic "0". The bottom curve in Figure 11 provides typical delay time for given delay capacitance values. The inverting driver threshold voltage is typically about $V_{in}/2$, as shown in Figure 10.

Figure 14. Timing Waveforms



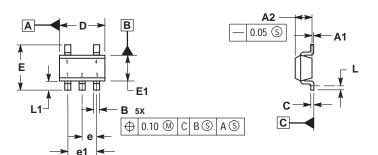
ORDERING INFORMATION

Device	Threshold Voltage	Туре	Marking	Package	Package
MC33465N-09ATR MC33465N-20ATR MC33465N-22ATR MC33465N-27ATR MC33465N-28ATR MC33465N-30ATR MC33465N-32ATR MC33465N-34ATR MC33465N-34ATR MC33465N-45ATR MC33465N-45ATR MC33465N-45ATR MC33465N-45ATR MC33465N-46ATR MC33465N-47ATR	0.9 2.0 2.2 2.7 2.8 3.0 3.2 3.4 3.8 4.5 4.6	Open Drain Reset	A9 C0 C2 C7 D8 D0 D2 D4 D8 E5 E6 E7	SOT-23 5-Lead	3000 Tape & Reel
MC33465N-09CTR MC33465N-20CTR MC33465N-27CTR MC33465N-30CTR MC33465N-43CTR MC33465N-44CTR MC33465N-44CTR	0.9 2.0 2.7 3.0 4.3 4.4 4.5	Compl. MOS Reset	9Ā 0Ĉ 7Ĉ 0D 3Ē 4Ē 5Ē		

Other voltages from 0.9 to 6.0 V, in 0.1 V increments, are available. Consult factory for information.

PACKAGE DIMENSIONS

(SOT-23) N SUFFIX PLASTIC PACKAGE CASE 1212-01 ISSUE O

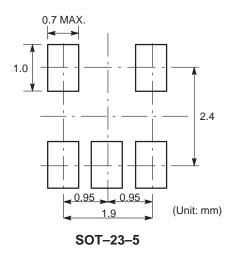


NOTES:

- . DIMENSIONS ARE IN MILLIMETERS.
- INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
- 3. DATUM C IS A SEATING PLANE.

	MILLIMETERS		
DIM	MIN	MAX	
A1	0.00	0.10	
A2	1.00	1.30	
В	0.30	0.50	
С	0.10	0.25	
D	2.80	3.00	
Е	2.50	3.10	
E1	1.50	1.80	
е	0.95 BSC		
e1	1.90 BSC		
L	0.20		
L1	0.45	0.75	

Recommended Footprint for Surface Mount Applications



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