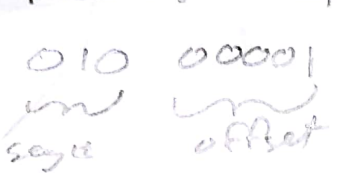
11/13/16

Segmentation ~~like paging with diff size frames~~

can have diff sized segments (~~like frames in paging~~)

like paging logical address has 2 parts



diff is the segment table

has base & length

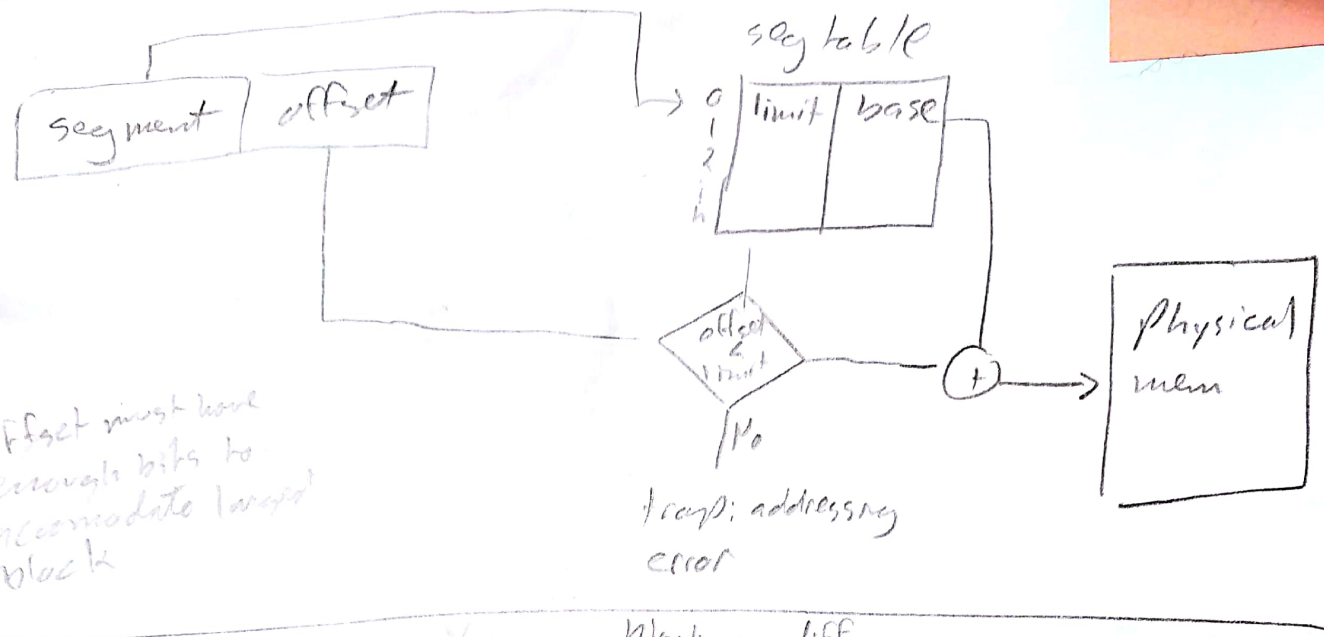
↑ like paging

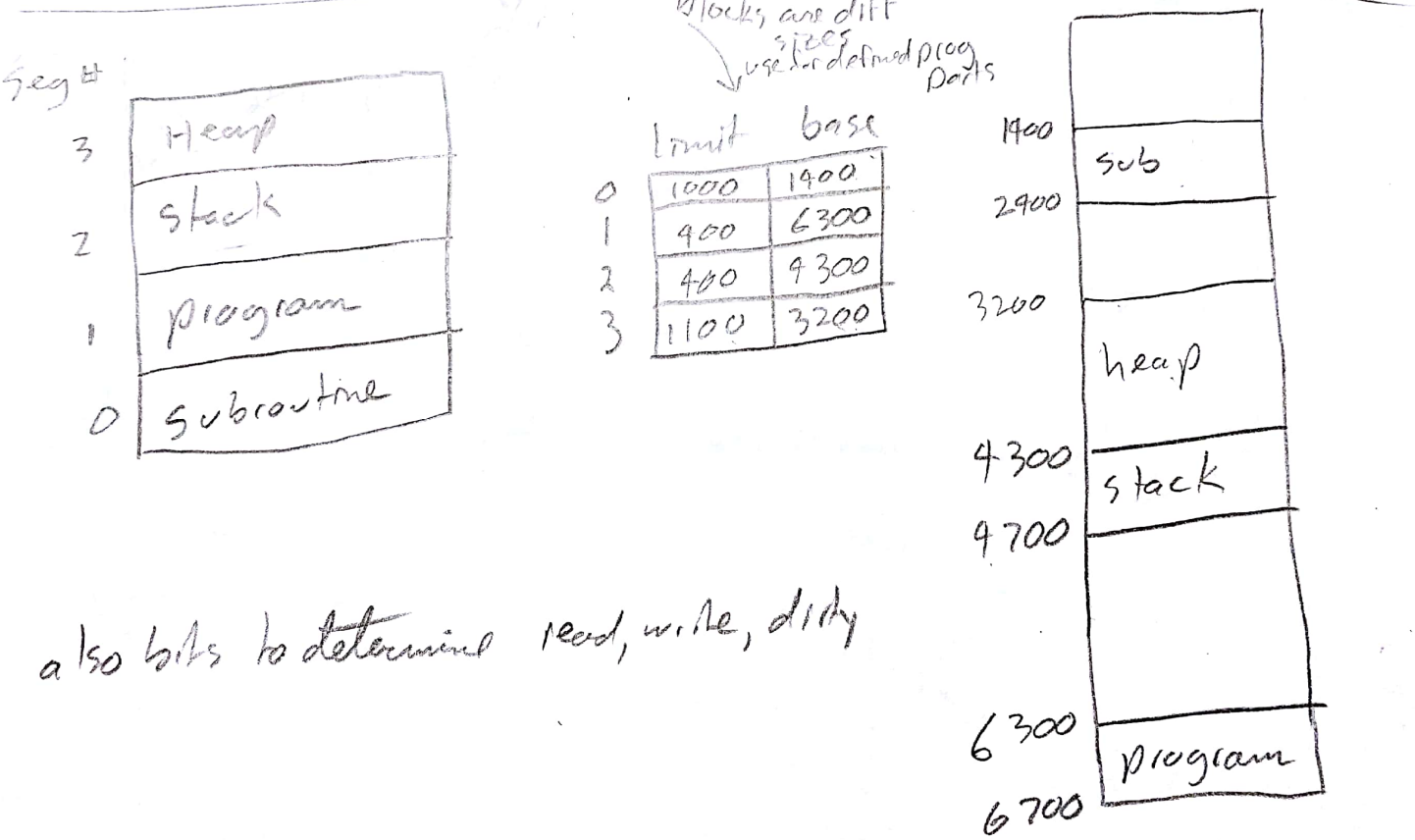
↑ max size

- these segments map to blocks of memory
- no internal frag. (external though) best fit? first fit?
- compilers can control how process is segmented (data, programs)

bit more complex than simple paging.

still all process in mem at once





also bits to determine read, write, dirty

11/13/16 (1)

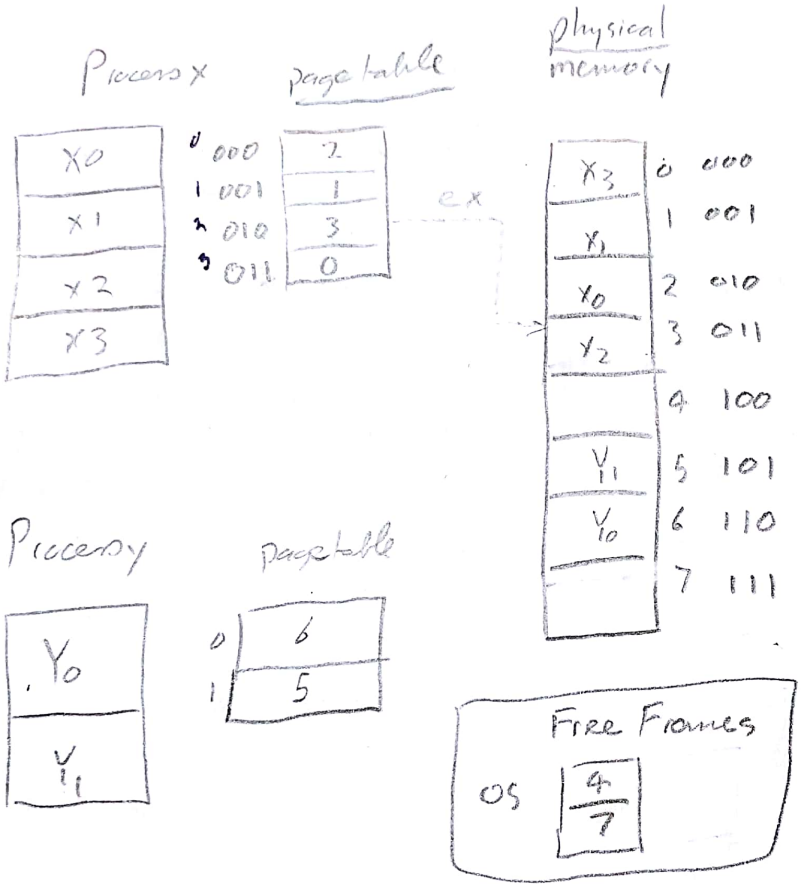
Paging2

16 bits 6 for frame # Then have 2^6 frames (64) each with 2^{10} words (1024)

Process page = frame size

slide 37–41

each page table kept with process control block



say have 8 bit address space

have 8 frames (3 bits)

frames have $2^5 = 32$ words

but still need all of process in mem at once!

also internal frag on last block of process.

ex 8 bit logical address for process X

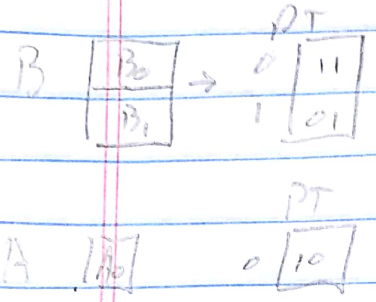
logical

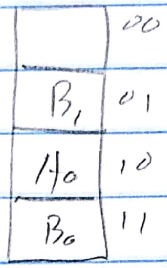
010 00001

page # offset

Ex. 5 bits 2 add 3 data

2^2 = 4 blocks 2^3 8 words block





Free blocks

0, X, Z, B

what if A had 3 blocks? Next chapter, virtual

have a system virtual address is 00|1111

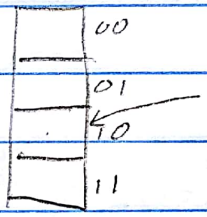
physical address is 10|1111

what can you tell me?

- really need block size but at least 2 frame # bits

OK block size = 2

Know this much



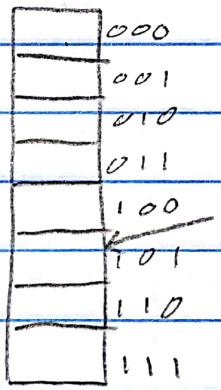
physical address is here

program has at least 1 block

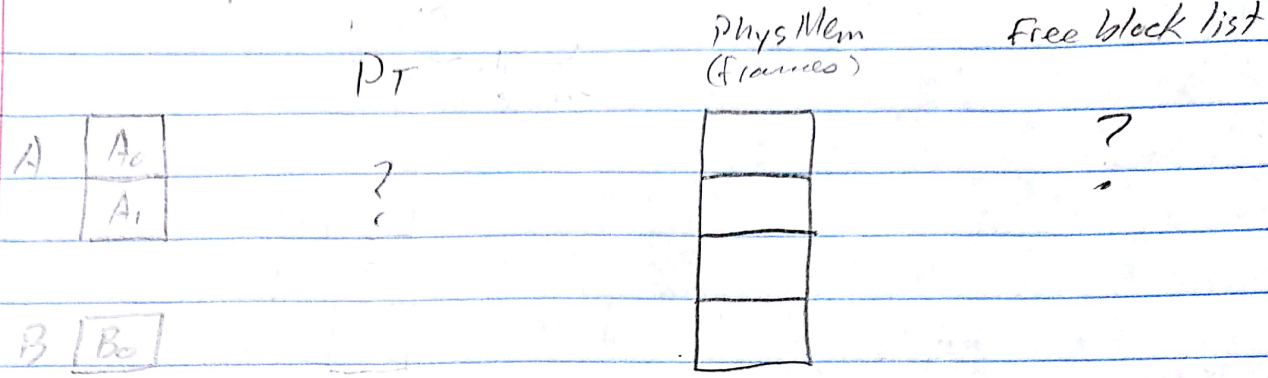
(because virtual is 00)

what if 10|1111? Then prog has at least 3 blocks so 2 other blocks occupied

block size = 3



what if you have this



	virtual address	Physical address
A	01 1111	00 1111
A	00 1111	11 1111
B	00 1111	10 1111

show where process pages are in physical mem.
show PT for each process
show free block list
determine block size (# page bits) & # address bits