Flexible Vectors update

Petr Penzin

Intel Corporation

September 27, 2021

Agenda

- ► Current state of the proposal
- ▶ Poll for Phase 2

Proposal summary

- ▶ Length-agnostic operation analagous, at a higher level, to *simd*128
- ▶ Vector length resolved statically at runtime
- ▶ No new non-determinsm if possible

https://github.com/WebAssembly/flexible-vectors

Proposal updates

- ► Written (English) spec
- ▶ Instruction encoding scheme and opcodes for most of the instructions
- A few open discussions about additional fetures, in line with the above

Phase 2 criteria

Entry requirements:

- ► Full proposed English spec text available in a forked repo around which a reasonably high level of consensus exists.
- ▶ Updates to the formal notation, test suite, and reference interpreter are NOT yet required.

Poll

Flexible Vectors proposal to Phase 2?

Thank you