SPEC CPU®2017 Integer Speed Result Copyright 2017-2020 Standard Performance Evaluation Corporation not applicable SPECspeed®2017_int_base **QJ**196 SPECspeed®2017 int **Mot Run** Test Date: CPU2017 License: nnn (Your SPEC license number) Oct-2020 **Test Sponsor:** not applicable Hardware Availability: Software Availability: **Tested by:** not applicable Threads 0 0.0200 0.0300 0.0700 0.0900 0.110 0.120 0.120 600.perlbench_s 1 602.gcc_s 605.mcf s 620.omnetpp_s 623.xalancbmk_s 625.x264 s 631.deepsjeng_s 641.leela s 648.exchange2_s 657.xz_s SRECspeed®2017_int_base (0.1196) Hardware Software OS: Ubuntu 18.04.3 LTS CPU Name: Intel Xeon Bronze 3106 Max MHz: 4.15.0-70-generic C/C++/Fortran: Version 10.1.0 of GCC, the Nominal: Compiler: Enabled: cores, 1 chip, threads/core **GNU** Compiler Collection Orderable: Parallel: Yes Cache L1: Firmware: L2: File System: nfs L3: System State: Run level 5 (add definition here) Base Pointers: 64-bit Other: 78.334 GB fixme: If using DDR4, the format is: Peak Pointers: Not Applicable Memory: 'N GB (Nx N GB nRxn PC4-nnnnX-X)' Other: 9 TB add more disk info here Power Management: --Storage: **Ø**ther: **Errors** 'reportable' flag not set during run 657.xz_s (base) did not have enough runs! 623.xalanchmk_s (base) did not have enough runs!

600.perlbench (s (base) did not have enough runs! 631.deepsjeng_s (base) did not have enough runs! 648.exchange2_s (base) did not have enough runs! 605.mcf_s (base) did not have enough runs! 625.x264_s (base) did not have enough runs! 602.gcc_s (base) did not have enough runs! 620.omnetpp_s (base) did not have enough runs! 641.leela s (base) did not have enough runs!

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SPECspeed®2017_int_base

SPECspeed®2017 int peak 7 **Mot Run**

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Software Availability:

Errors (Continued)

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl for information about how to get rid of this error.

Results Table

	Base							\searrow	Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
600.perlbench_s	1	<u>14840</u>	<u>0.120</u>	\cup				$ \rangle \rangle$							
602.gcc_s							$\overline{}$								
605.mcf_s					$\langle \rangle$										
620.omnetpp_s							_								
623.xalancbmk_s							1								
625.x264_s)/									
631.deepsjeng_s						Ý									
641.leela_s															
648.exchange2_s					N	1									
657.xz_s		Λ	$\bigcup \mathcal{I}$												

SPECspeed[®]2017_int_base = 0.1196

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Environment Variables Notes

Environment variables set by runcpu before the start of the run: D NBRARY PATH

/u/home/s/hmidtf/riscv-gnu-toolchain/build/lib64/:/u/home/schmidtf/risc v-qnu-todichain/build/lib/:/lib64"

Platform Notes

Sysinfo program /u/home/schmidtf/spec/bin/sysinfo Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011 running on sksmall Sat Oct 24 11:21:36 2020

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see

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SPECspeed[®]2017 int base = 0.1190

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Hardware Availability: Software Availability:

Oct-2020

Platform Notes (Continued)

```
https://www.spec.org/cpu2017/Docs/config.html#sysinfo
```

```
From /proc/cpuinfo
```

model name : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz

1 "physical id"s (chips)

8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not/be reliable. Use with caution.)

cpu cores : 8
siblings : 8

physical 0: cores 0 1 2 3 4 5 6 7

From lscpu:

Architecture: X86_

CPU op-mode(s): 32-bit, 64-bit Byte Order: Little Endian

CPU(s):

On-line CPU(s) list; 0-

Thread(s) per core 1 Core(s) per socket: 8

Socket(s):

NUMA node(s):

Vendor ID: Genuine Intel

CPU family:

L3 cache

Model: / \ 85

Model name: Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz

Stepping:

CPU MHz: 1684.286
CPU max MHz: 1700.0000
CPU min MHz: 800.0000
FOGOMIPS: 3400.00

Wirtualization: VT-x
Lld cache: 32K
Lli cache: 32K
L2 cache: 1024K

11264K

NUMA nøde0 CPU(s): 0-7

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13 invpcid_single pti intel_ppin ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local

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Platform Notes (Continued)

dtherm arat pln pts hwp hwp_act_window hwp_pkg_xeq pku ospke md_clear flush_lld

/proc/cpuinfo cache data cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'hode' might or might not correspond to a physical chip.

From /proc/meminfo

MemTotal: 82139464 kB HugePages_Total: 0 Hugepagesize: 2048 kB

/usr/bin/lsb_release -d Ubuntu 18.04.3 LTS

VERSION="18.04.3 LTS (Bionic Beaver)"
ID=ubuntu

ID_LIKE=debian

PRETTY_NAME="Ubuntu 18.04.3 LTS"

VERSION_1D="18.04"

HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"

uname -a/:

Linux sksmall 4.15.0-70-generic #79-Ubuntu SMP Tue Nov 12 10:36:11 UTC 2019 x86_64 x86_64 GNU/Linux

Kernel self-keported vulnerability status:

itlb_multihit: KVM: Mitigation: Split huge pages

CVE 2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: conditional

cache flushes, SMT disabled

Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT disabled

CVE-2017/5754 (Meltdown): Mitigation: PTI

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

via prctl and seccomp

CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user

pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB:

conditional, IBRS_FW, STIBP: disabled, RSB

filling

tsx_async_abort: Mitigation: Clear CPU buffers; SMT disabled

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Used Avail Use% Mounted

Platform Notes (Continued)

run-level 5 Nov 28 09:46

SPEC is set to: /u/home/schmidtf/spec

on

nasil10.informatik.tu-muenchen.de:/srv/j110/home nfs 6.9T 4.4T 2.3T 66% /u/home

Type

From /sys/devices/virtual/dmi/id

BIOS: HPE U32 11/13/2019

Vendor: HPE

Product: ProLiant DL36 Gen10

Product Family: ProLiant

Cannot run dmidecode; consider saying (as root

chmod +s /usr/sbin/dmidecode

(End of data from sysinfo program)

Compiler Version Notes

C | 600.perlbench_s(base)

Using built-in specs

COLLECT_GCC=/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-gcc COLLECT_LTO_WRAPPER=/u/home/schmidtf/riscv-gnu-toolchain/build/libexec/gcc/riscv64-unknown-linux-gnu/10.1.0/lto-wrapper Target: riscv64-unknown-linux-gnu

Configured with: \u/home/schmidtf/riscv-gnu-toolchain/riscv-gcc/configure

- --tayget=riscv64-unknown-linux-gnu
- --prefix=/u/home/sghmidtf/riscv-gnu-toolchain/build
- --with-systoot=/whome/schmidtf/riscv-gnu-toolchain/build/sysroot
- -with-system-zlib --enable-shared --enable-tls
- --enable-languages=c,c++,fortran --disable-libmudflap --disable-libssp
- -disable-lipquadmath --disable-libsanitizer --disable-nls
- --disable-cotstrap --src=.././riscv-gcc --disable-multilib --with-abi=lp64
- --with arch=rv64ima --with-tune=rocket 'CFLAGS_FOR_TARGET=-02
- -mcmodel=medlow' 'CXXFLAGS_FOR_TARGET=-02 -mcmodel=medlow'

Thread model: posix

Supported LTO compression algorithms: zlib

gcc version 10.1.0 (GCC)

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Base Unknown Flags

600.perlbench_s: "/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-" (in CC) "/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-" (in LD)

Base Runtime Environment

C benchmarks:

600.perlbench_s: No flags used

Base Compiler Invocation

C benchmarks:

600.perlbench_s: gcc

Base Portability Flags

600.perlbench_s: -static(*) -DSPEC_LINUX_X64 -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Base Optimization Flags

C benchmarks:

600.perlbench_s: -std=c99 -O3 -DSPEC_SUPPRESS_OPENMP

- -fno-unsafe-math-optimizations -fno-openmp
- -fno-strict-aliasing -fgnu89-inline

Base Other Flags

C benchmarks:

600.perlbench_s: -fcommon -fallow-argument-mismatch

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SPECspeed®2017 int_base = 0.1

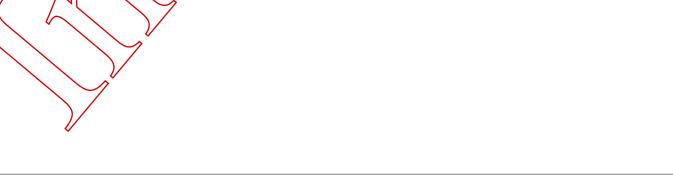
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Tested with SPEC CPU®2017 v1.1.0 on 2020-10-24 11:21:35+0000.

Report generated on 2020-10-24 15:29:44 by CPU2017 PDF formatter v6255.