SPEC CPU®2017 Integer Speed Result Copyright 2017-2020 Standard Performance Evaluation Corporation not applicable SPECspeed®2017_int_base 0.2055 SPECspeed®2017 int Vot Run Test Date: CPU2017 License: nnn (Your SPEC license number) Oct-2020 Hardware Availability: **Test Sponsor:** not applicable Software Availability: **Tested by:** not applicable 0.0150 0.0300 0.0450 0.0600 0.120 0.165 0.195 0.210 600.perlbench_s 602.gcc_s 605.mcf s 620.omnetpp_s 623.xalancbmk_s 625.x264 s 631.deepsjeng_s 641.leela s 648.exchange2_s 0.205 657.xz_s SRECspeed®2017_int_base (0.2055) Hardware Software OS: CPU Name: Intel Xeon Bronze 3106 Ubuntu 18.04.3 LTS Max MHz: 4.15.0-70-generic C/C++/Fortran: Version 10.1.0 of GCC, the Nominal: Compiler: Enabled: cores, 1 chip, threads/core **GNU** Compiler Collection Orderable: Parallel: Yes Cache L1: Firmware: L2: File System: nfs L3: System State: Run level 5 (add definition here) Base Pointers: 64-bit Other: 78.334 GB fixme: If using DDR4, the format is: Peak Pointers: Not Applicable Memory: 'N GB (Nx N GB nRxn PC4-nnnnX-X)' Other: 9 TB add more disk info here Power Management: --Storage: **Ø**ther: **Errors** 'reportable' flag not set during run

600.perbench_s (base) did not have enough runs!
602.gcc_s (base) did not have enough runs!
641.leela_s (base) did not have enough runs!
623.xalancbmk_s (base) did not have enough runs!
648.exchange2_s (base) did not have enough runs!
657.xz_s (base) did not have enough runs!
620.omnetpp_s (base) did not have enough runs!
605.mcf_s (base) did not have enough runs!
625.x264_s (base) did not have enough runs!
631.deepsjeng_s (base) did not have enough runs!

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SPECspeed®2017_int_base 0.2055

SPECspeed®2017 int peak 7 **Mot Run**

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Software Availability:

Errors (Continued)

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl for information about how to get rid of this error.

Results Table

	Base							Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Second	s Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s				\bigcirc)N						
602.gcc_s														
605.mcf_s					$\langle \rangle$		\bigvee							
620.omnetpp_s							_							
623.xalancbmk_s							egthinspace = egt							
625.x264_s)/								
631.deepsjeng_s						~)×								
641.leela_s														
648.exchange2_s					N									
657.xz_s	1	<u>30085</u>	0.205											

SPECspeed®2017_int_base = 0.2055

SPECspeed®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Environment Variables Notes

Environment variables set by runcpu before the start of the run: D NBRARY PATH

/u/home/s/hmidtf/riscv-gnu-toolchain/build/lib64/:/u/home/schmidtf/risc v-qnu-todichain/build/lib/:/lib64"

Platform Notes

Sysinfo program /u/home/schmidtf/spec/bin/sysinfo Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011 running on sksmall Sun Oct 25 11:57:33 2020

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see

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SPECspeed®2017_int_base

SPECspeed®2017_int Mot Run

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Test Sponsor: not applicable **Tested by:** not applicable

Platform Notes (Continued)

```
https://www.spec.org/cpu2017/Docs/config.html#sysinfo
```

```
From /proc/cpuinfo
```

model name : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz

386

Genuine Intel

1 "physical id"s (chips)

8 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not/be reliable. Use with caution.)

cpu cores : 8 siblings : 8

physical 0: cores 0 1 2

From lscpu:

Architecture:

64-bit CPU op-mode(s): 32-bit, Byte Order: Little Endian

CPU(s):

On-line CPU(s) list;

Thread(s) per core Core(s) per socket 8

Socket(s):

NUMA node(s):

Vendor ID:

CPU family;

Model:

Model name:

Stepping:

1636.199 CPU MHz: CPU max MNz: 1700.0000 CPU min MHz 800.0000

Bog dMIPS: 3400.00 Wirtualizati x-TVLld cache: 32K Lli cache: 32K L2 cache: 1024K

11264K L3 cache

NUMA nøde0 CPU(s): 0 - 7

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat/pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13

Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz

invpcid_single pti intel_ppin ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local

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Test Sponsor:

Tested by:

SPECspeed®2017_int_base = 0.203

SPECspeed®2017_int_peak 7 Not Run

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Software Availability:

Platform Notes (Continued)

dtherm arat pln pts hwp hwp_act_window hwp_pkg_req pku ospke md_clear flush_11d

/proc/cpuinfo cache data
 cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'hode' might or might not correspond to a physical chip.

From /proc/meminfo

MemTotal: 82139464 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d Ubuntu 18.04.3 LTS

VERSION="18.04.3 LTS (Bionic Beaver)"

ID=ubuntu
ID_LIKE=debian

PRETTY_NAME="Ubuntu 18.04.3 LTS"

VERSION_ZD="18.04\"

HOME_UPL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"

uname -a/:

Linux sksmall 4.15.0-70-generic #79-Ubuntu SMP Tue Nov 12 10:36:11 UTC 2019 x86_64 x86_64 GNU/Linux

Kernel self-keported vulnerability status:

itlb_multihit: KVM: Mitigation: Split huge pages

CVE 2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: conditional

cache flushes, SMT disabled

Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT disabled

CVE-2017/5754 (Meltdown): Mitigation: PTI

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

via prctl and seccomp

CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user

pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB:

conditional, IBRS_FW, STIBP: disabled, RSB

filling

tsx_async_abort: Mitigation: Clear CPU buffers; SMT disabled

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SPECspeed®2017_int_base = 0.205

SPECspeed®2017_int_peak 7 Not Run

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Used Avail Use% Mounted

Platform Notes (Continued)

run-level 5 Nov 28 09:46

SPEC is set to: /u/home/schmidtf/spec

on

nasil10.informatik.tu-muenchen.de:/srv/j110/home nfs 6.9T 4.4T 2.3T 66% /u/home

Type

From /sys/devices/virtual/dmi/id

BIOS: HPE U32 11/13/2019

Vendor: HPE

Product: ProLiant DL360 Gen10

Product Family: ProLiant

Cannot run dmidecode; consider saying (as root

chmod +s /usr/sbin/dmidecode

(End of data from sysinfo program)

Compiler Version Notes

C | 657.xz_s(base)

Using built-in specs

COLLECT_GCC=/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-gcc COLLECT_LTO_WRAPPER=/u/home/schmidtf/riscv-gnu-toolchain/build/libexec/gcc/riscv64-unknown-linux-gnu/10.1.0/lto-wrapper Target: riscv64-unknown-linux-gnu

Configured with: \u/home/schmidtf/riscv-gnu-toolchain/riscv-gcc/configure

- --tayget=riscv64-unknown-linux-gnu
- --prefix=/u/home/sghmidtf/riscv-gnu-toolchain/build
- --with-systoot=/whome/schmidtf/riscv-gnu-toolchain/build/sysroot
- -with-system-zlib --enable-shared --enable-tls
- --erable-languages=c,c++,fortran --disable-libmudflap --disable-libssp
- -disable-lipquadmath --disable-libsanitizer --disable-nls
- --disable-cotstrap --src=.././riscv-gcc --disable-multilib --with-abi=lp64
- --with arch=rv64ima --with-tune=rocket 'CFLAGS_FOR_TARGET=-02
- -mcmodel=medlow' 'CXXFLAGS_FOR_TARGET=-02 -mcmodel=medlow'

Thread model: posix

Supported LTO compression algorithms: zlib

gcc version 10.1.0 (GCC)

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Software Availability:

Base Unknown Flags

657.xz_s: "/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-" (in CC) "/u/home/schmidtf/riscv-gnu-toolchain/build/bin/riscv64-unknown-linux-gnu-" (in LD)

Base Runtime Environment

C benchmarks:

657.xz_s: No flags used

Base Compiler Invocation

C benchmarks:

657.xz_s: gcc

Base Portability Flags

657.xz_s: -static(*) DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Base Optimization Flags

C benchmarks:

657.xz_s: std=c99 -O3 -DSPEC_SUPPRESS_OPENMP

- -fno-unsake-math-optimizations -fno-openmp -fno-strict-aliasing
- -fgnu89-inline

Base Other Flags

C benchmarks:

657.xz_s:-fcommon -fallow-argument-mismatch

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Hardware Availability: Software Availability:

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