# **AVR 32-bit GNU Toolchain: Release 3.4.1.348**

The AVR 32-bit GNU Toolchain supports all AVR 32-bit devices. The AVR 32-bit Toolchain is based on the free and open-source GCC compiler. The toolchain includes compiler, assembler, linker and binutils (GCC and Binutils), Standard C library (Newlib).



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Release 3.4.1.348





## **Installation Instructions**

# **System Requirements**

AVR 32-bit GNU Toolchain is supported under the following configurations

# **Hardware requirements**

- Minimum processor Pentium 4, 1GHz
- Minimum 512 MB RAM
- Minimum 500 MB free disk space

AVR 32-bit GNU Toolchain has not been tested on computers with less resources, but may run satisfactorily depending on the number and size of projects and the user's patience.

## Software requirements

- Windows 2000, Windows XP, Windows Vista or Windows 7 (x86 or x86-64).
- Fedora 13 or 12 (x86 or x86-64), RedHat Enterprise Linux 4 or 5, Ubuntu Linux 10.04 or 8.04 (x86 or x86-64), or SUSE Linux 11.2 or 11.1 (x86 or x86-64). AVR 32-bit GNU Toolchain may very well work on other distributions. However those would be untested and unsupported.

AVR 32-bit GNU Toolchain is not supported on Windows 98, NT or ME.

# **Downloading and Installing**

The package comes in two forms.

- As part of a standalone installer
- As Atmel Studio 6 Toolchain Extension

It may be downloaded from Atmel's website at <a href="http://www.atmel.com">http://www.atmel.com</a> or from the Atmel Studio 6 Extension Gallery.

## **Installing on Windows**

In order to install using standalone installer, the AVR Toolchain installer can be downloaded from Atmel website. After downloading the installer, double-click the executable file to install. You may use "Custom Installation" in order to install in a specific location.

In order to install the Atmel AVR Toolchain as Atmel Studio 6 Toolchain Extension, please refer to the Atmel Studio 6 documentation.

## **Installing on Linux**

On Linux AVR 32-bit GNU Toolchain is available as a TAR.GZ archive which can be extracted using the 'tar' utility. In order to install, simply extract to the location where you want the toolchain to run from.

Note that if you will develop Linux applications for the AT32AP7000 you must also install the AVR32 Buildroot. For more details on AVR32 Buildroot please refer http://www.atmel.no/buildroot/

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## **Upgrading from previous versions**

If you have used the standalone installer on MS-Windows, you might do a clean upgrade by first un-installing the old version or just upgrade using the latest installer.

Please refer to the Atmel Studio 6 documentation for learning how to upgrade the toolchain extension.

On Linux, if you have it unpacked to a local folder, you just delete the old folder and unpack the latest version in a new folder.

## **Manifest**

- 1. AVR 32-bit GNU Binutils 2.22
  - Binary utilities for AVR 32-bit target (including assembler, linker, etc.).
- 2. AVR 32-bit GNU Compiler Collection (avr32-gcc) 4.4.3
  - C language and C++ language compiler for AVR 32-bit target.
- 3. Newlib (for AVR 32-bit) 1.16.0
  - Standard C Library for AVR 32-bit

## Layout

Listed below are some directories you might want to know about.

`<install\_dir>` = The directory where you installed AVR 32-bit GNU Toolchain.

- <install\_dir>\bin
  - The AVR software development programs. This directory should be in your `PATH` environment variable.
     This includes:
    - GNU Binutils
    - GCC
- <install dir>\avr32\lib
  - avr32-newlib libraries, startup files, linker scripts, and stuff.
- <install\_dir>\avr32\include
  - avr32-newlib header files. This is where, for example, #include <string.h> comes from.
- <install dir>\avr32\include\avr32
  - avr32-newlib header files specific to the AVR32 microprocessor. This is where, for example, #include <avr32/io.h> comes from.
- <install\_dir>\lib
  - · GCC libraries, other libraries, headers and stuff.
- <install dir>\libexec
  - GCC program components
- <install\_dir>\doc
  - · Various documentation.
- <install\_dir>\source
  - Documentation on where to find the source code for the various projects and source code patches that were
    used to build the tools.

# **Toolset Background**

AVR 32-bit GNU Toolchain is a collection of executable software development tools for the Atmel AVR 32-bit processor.





These software development tools include:

- 1. Compiler
- 2. Assembler
- 3. Linker
- 4. Archiver
- 5. File converter
- 6. Other file utilities
- 7. C Library

## Compiler

The compiler is the GNU Compiler Collection, or GCC. This compiler is incredibly flexible and can be hosted on many platforms, it can target many different different processors/operating systems (back-ends), and can be configured for multiple different languages (front-ends).

The GCC included is targeted for the AVR 32-bit processor, and is configured to compile C, or C++.

Because this GCC is targeted for the AVR 32-bit, the main executable that is created is prefixed with the target name: `avr32-gcc`. It is also referred to as AVR 32-bit GCC.

`avr32-gcc` is just a "driver" program only. The compiler itself is called `cc1.exe` for C, or `cc1plus.exe` for C++. Also, the preprocessor `cpp.exe` will usually automatically be prepended with the target name: `avr32-cpp.exe`. The actual set of component programs called is usually derived from the suffix of each source code file being processed.

GCC compiles a high-level computer language into assembly, and that is all. It cannot work alone. GCC is coupled with another project, GNU Binutils, which provides the assembler, linker, librarian and more. Since GCC is just a "driver" program, it can automatically call the assembler and linker directly to build the final program.

## Assembler, Linker, Librarian and More

GNU Binutils is a collection of binary utilities. This also includes the assembler, as. Sometimes you will see it referenced as GNU as or gas. Binutils includes the linker, ld; the librarian or archiver, ar. There are many other programs included that provide various functionality.

Binutils is configured for the AVR 32-bit target and each of the programs is prefixed with the target name. So you have programs such as:

- avr32-as: The Assembler.
- avr32-ld: The Linker.
- avr32-ar: Create, modify, and extract from archives (libraries).
- avr32-ranlib: Generate index to archive (library) contents.
- avr32-objcopy: Copy and translate object files.
- avr32-objdump: Display information from object files including disassembly.
- avr32-size: List section sizes and total size.
- avr32-nm: List symbols from object files.
- avr32-strings: List printable strings from files.
- avr32-strip: Discard symbols.
- avr32-readelf: Display the contents of ELF format files.
- avr32-addr2line: Convert addresses to file and line.
- avr32-c++filt: Filter to demangle encoded C++ symbols.

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See the binutils user manual for more information on what each program can do.

# **C** Library

Newlib is used as the Standard C Library for AVR32 GCC Toolchain. Newlib is a Standard C library intended to be used for embedded systems. This library has been ported to support the AVR32 processor architectures.

# **Debugging**

Atmel Studio 6 provides a debugger and also provides simulators for the parts that can be used for debugging as well. Note that `Atmel Studio 6` is currently free to the public, but it is not Open Source.

#### **Source Code**

Atmel AVR32 GNU Toolchain uses modified source code of GCC, Binutils and Newlib. The source code used for building the packaged binaries are available at:

http://distribute.atmel.no/tools/opensource/Atmel-AVR-Toolchain-3.4.1/avr32/

The modifications are also available as patches in the above link. Please refer to the README for the instructions on how to use the supplied script to build the toolchain.





# **New and Noteworthy**

This chapter lists new and noteworthy items for the AVR 32-bit GNU Toolchain release.

# **AVR 32-bit GNU Toolchain**

# **Supported devices**



AVR 32-bit GNU Toolchain supports the following devices:

Note:- Devices which are newly supported in this release are marked with \*

uc3a0128	uc3a0256	uc3a0512	uc3a0512es	uc3a1128
uc3a1512	uc3a1512es	uc3a3revd	uc3a364	uc3a364s
uc3a3128s	uc3a3256	uc3a3256s	uc3a464	uc3a464s
uc3a4128s	uc3a4256	uc3a4256s	uc3b064	uc3b0128
uc3b0256es	uc3b0512	uc3b0512revc	uc3b164	uc3b1128
uc3b1256es	uc3b1512	uc3b1512revc	uc64d3	uc128d3
uc128d4	uc3c0512crevc	uc3c1512crevc	uc3c2512crevc	uc310256
uc31064	uc31032	uc31016	uc31064revb	uc3c064c
uc3c0256c	uc3c0512c	uc3c164c	uc3c1128c	uc3c1256c
uc3c264c	uc3c2128c	uc3c2256c	uc3c2512c	

# **Issues Fixed**

• PR 176: Alignment argument for .lcomm directive is made optional. Assembler uses the implicit alignment if alignment argument is not present in .lcomm directive.

# **Known issues**

#### • Issue #AVRTC-2:

'.debug\_line' section in the ELF file isn't updated when relaxing. This results in that correct relocation info is not added to the '.debug-line' section that are generated and that there is a mismatch between the code and the debug-line after the linker has done relaxation. Debugging the ELF file will give wrong source line as a result.



## **Contact Information**

For support on AVR 32-bit GNU Toolchain please contact avr32@atmel.com.

Users of AVR 32-bit GNU Toolchain are also welcome to discuss on the AVRFreaks website forum for AVR32 Software Tools.

## **Disclaimer and Credits**

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