

- 1) Get K-layout
- 2) Get Siepic EDK → and install that menu
- 3) Create new project Ebeam 1
- 4) "Layers" → silicon, layer 1/0
- 5) Bottom left: libraries = ebeam_dc-te 1310 ↓ we don't want 1550
- 6) Top hierarchy → show as new top
- 7) changing the number changes level
- 8) Drag + Drop interconnects } look for "ebeam_splitter_swg_assist_te 1310_ANT"
- 9) Draw constraint box → layers → Misc → floorplan
↳ Double click to change size
- 10) Waveguide layer
- 11) "Path object" place it roughly.
↳ "path to waveguide"
- 13) Strip TE 1310, 350
- 14) Can use spiral paperclip to get densely wrapped waveguide
- 15) Need light source: Beta Library → ebeam_gc-te 1310
↓
grating coupler
→ Array: 1 col, 4 rows, 127 μm pitch
- 16) Shift-O connects components