Chip 1 Design:

- 1 input to a splitter
    - 1 waveguide goes straight to next splitter
    - 1 waveguide goes into spiral paperclip

TE grating couplers facing to the right
L1 = 95.845+2474.91+190.005=2760.76
L2 = 120.025
L1-L2 = 2760.76-120.025 = 2640.735

So path length difference is **2.64 mm** as calculated to achieve 25GHz FSR