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Yices 2 Manual

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Introduction

This manual is an introduction to the logic, language, and architecture of the Yices 2 SMT solver. Yices is developed in SRI International's Computer Science Laboratory and is distributed free-of-charge for personnal use, under the terms of the Yices License 7. To discuss alternative license terms, please contact us at fm-license@csl.sri.com.

Yices can be downloaded at http://yices.csl.sri.com. The Yices website provides the latest release and information about Yices. For bug reports and questions about Yices, please contact us via the Yices mailing lists:

- To report a bug, send e-mail to yices-bugs@csl.sri.com.

 Please include enough information in your bug report to enable us to reproduce the problem.
- If you have any questions about Yices usage or installation, send e-mail to yices-help@csl.sri.com.

Yices 2 Language

Yices 2 specifications are written in a typed logic. The language is intended to be simple enough for efficient processing by the tool and expressive enough for most applications. The Yices 2 language is similar to the logic supported by Yices 1, but the most complex type constructs have been removed.

2.1 Type System

Yices 2 has a few built-in types for primitive objects:

- The arithmetic types int and real
- The boolean type bool
- The type (bitvector k) of bitvectors of size k, where k is a positive integer.

All these built-in types are *atomic*. The set of atomic types can be extended by declaring new *uninterpreted types* and *scalar types*. An uninterpreted type denotes a nonempty collection of objects with no cardinality constraint. A scalar type denotes a nonempty, *finite* set of objects. The cardinality of a scalar type is defined when the type is created.

In addition to the atomic types, Yices 2 provides constructors for tuple and function types. The set of all Yices 2 types can be defined inductively as follows:

- Any atomic type τ is a type.
- If n > 0 and $\sigma_1, \ldots, \sigma_n$ are n types, then $\sigma = (\sigma_1 \times \ldots \times \sigma_n)$ is a type. Objects of type σ are tuples (x_1, \ldots, x_n) where x_i is an object of type σ_i .
- If n > 0 and $\sigma_1, \ldots, \sigma_n$ and τ are types, then $\sigma = (\sigma_1 \times \ldots \times \sigma_n \to \tau)$ is a type. Objects of type σ are functions of domain $\sigma_1 \times \ldots \times \sigma_n$ and range τ .

By construction, all the types are nonempty. Yices does not have a specific type constructor for arrays since the logic does not distinguish between arrays and functions. For example, an array indexed by integers is simply a function of domain int.

Yices 2 uses a simple form of subtyping. Given two types σ and τ , let $\sigma \sqsubset \tau$ denote that σ is a subtype of τ . Then the subtype relation is defined by the following rules:

- $\tau \sqsubset \tau$ (any type is a subtype of itself)
- int

 real (the integers form a subtype of the reals)
- If $\sigma_1 \sqsubset \tau_1, \ldots, \sigma_n \sqsubset \tau_n$ then $(\sigma_1 \times \ldots \times \sigma_n) \sqsubset (\tau_1 \times \ldots \times \tau_n)$.
- If $\tau \sqsubset \tau'$ then $(\sigma_1 \times \ldots \times \sigma_n \to \tau) \sqsubset (\sigma_1 \times \ldots \times \sigma_n \to \tau')$.

For example, the type (int \times int) (pairs of integers) is a subtype of (real \times real) (pairs of reals).

Two types, τ and τ' , are said to be *compatible* if they have a common supertype, that is, if there exists a type σ such that $\tau \sqsubseteq \sigma$ and $\tau' \sqsubseteq \sigma$. If that is the case, then there exists a unique minimal supertype among all the common supertypes. We denote the minimal supertype of τ and τ' by $\tau \sqcup \tau'$. By definition, we then have

$$\tau \sqsubset \sigma \text{ and } \tau' \sqsubset \sigma \Rightarrow \tau \sqcup \tau' \sqsubset \sigma.$$

For example, the tuple types $\tau = (\texttt{int} \times \texttt{real} \times \texttt{int})$ and $\tau = (\texttt{int} \times \texttt{int} \times \texttt{real})$ are compatible. Their minimal supertype is $\tau \sqcup \tau' = (\texttt{int} \times \texttt{real} \times \texttt{real})$. The type $(\texttt{real} \times \texttt{real} \times \texttt{real})$ is also a common supertype of τ and τ' but it is not minimal.

2.2 Terms and Formulas

In Yices 2, the atomic terms include the boolean constants (true and false) as well as arithmetic and bitvector constants.

When a scalar type τ of cardinality n is declared, n distinct constant c_1, \ldots, c_n of type τ are also implicitly defined. In the Yices 2 syntax, this is done via a declaration of the form:

```
(define-type tau (scalar c1 ... cn))
```

An equivalent functionality is provided by the Yices API. The API allows one to create a new scalar type and to access n constants of that type indexed by integers between 0 and n-1 (check file include/yices.h for explanations).

The user can also declare *uninterpreted constants* of arbitrary types. Informally, uninterpreted constants of type τ can be considered like global variables, but Yices (in particular the Yices API) makes a distinction between *variables* of type τ and *uninterpreted constants*

of type τ . In the Yices API, variables are used to build quantified expressions and to support term substitutions. Free variables are not allowed to occur in assertions.

The term constructors include the common Boolean operators (conjunction, disjunction, negation, implication, etc.), an if-then-else constructor, equality, function application, and tuple constructor and projection. In addition, Yices provides an update operator that can be applied to arbitrary functions. The type-checking rules for these primitive operators are described in Figure 2.1, where the notation $t::\tau$ means "term t has type τ ".

There are no separate syntax or constructors for formulas. In Yices 2, a formula is simply a term of Boolean type.

The semantics of most of these operators is standard. The update operator for functions is characterized by the following axioms¹:

$$((\mathtt{update}\ f\ t_1\dots t_n\ v)\ t_1\dots t_n) \ = \ v$$

$$u_1\neq t_1\vee\dots\vee u_n\neq t_n\Rightarrow ((\mathtt{update}\ f\ t_1\dots t_n\ v)\ u_1\dots u_n) \ = \ (f\ u_1\dots u_n)$$

In other words, (update f $t_1 \dots t_n$ v) is the function equal to f at all points except (t_1, \dots, t_n) . Informally, if f is interpreted as an array then the update corresponds to "storing" v at position t_1, \dots, t_n in the array. Reading the content of the array is nothing other than function application: (f $i_1 \dots i_n)$ is the content of the array at position i_1, \dots, i_n .

The full Yices 2 language has a few more operators not described here, and it includes existential and universal quantifiers. We do not describe the type-checking rules for quantifiers here since Yices 2 does not have a solver for quantified formulas at this point.

2.3 Supported Theories

In addition to the generic operators presented previously, the Yices language includes the standard arithmetic operators and a rich set of bitvector operators.

2.3.1 Arithmetic

Arithmetic constants are arbitrary precision integers and rationals. Although Yices uses exact arithmetic, rational constants can be written using standard floating-point notation. Internally, Yices converts floating-point input to rationals. For example, the floating-point expression 3.04e-1 is converted to 304/1000.

The Yices language supports the traditional arithmetic operators (i.e., addition, subtraction, multiplication) with the exception that it does not allow division by a non constant, to avoid issues related to division by zero. For example, the expression (x+4y)/3 is allowed, but 3/(x+4y) is not. The arithmetic predicates are the usual comparison operators, including both strict and nonstrict inequalities.

¹These are the main axioms of the McCarthy theory of arrays.

Boolean Operators

Equality

$$\frac{t_1::\tau_1\quad t_2::\tau_2}{(t_1=t_2)::\mathsf{bool}} \ \ \mathsf{provided} \ \tau_1 \ \mathsf{and} \ \tau_2 \ \mathsf{are} \ \mathsf{compatible}$$

If-then-else

$$\frac{c :: \texttt{bool} \quad t_1 :: \tau_1 \quad t_2 :: \tau_2}{(\texttt{ite} \ c \ t_1 \ t_2) :: \tau_1 \sqcup \tau_2} \quad \texttt{provided} \ \tau_1 \ \texttt{and} \ \tau_2 \ \texttt{are compatible}$$

Tuple Constructor and Projection

$$\frac{t_1 :: \tau_1 \dots t_n :: \tau_n}{(\mathsf{tuple}\ t_1 \dots t_n) :: (\tau_1 \times \dots \times \tau_n)} \qquad \frac{t :: (\tau_1 \times \dots \times \tau_n)}{(\mathsf{select}_i\ t) :: \tau_i}$$

Function Application

$$\frac{f :: (\tau_1 \times \ldots \times \tau_n \to \tau) \quad t_1 :: \sigma_1 \ldots t_n :: \sigma_n \quad \sigma_1 \sqsubseteq \tau_1 \ldots \sigma_n \sqsubseteq \tau_n}{(f \ t_1 \ldots t_n) :: \tau}$$

Function Update

$$\frac{f :: (\tau_1 \times \ldots \times \tau_n \to \tau) \quad t_1 :: \sigma_1 \ldots t_n :: \sigma_n \quad v :: \sigma \quad \sigma_i \sqsubseteq \tau_i \quad \sigma \sqsubseteq \tau}{(\text{update } f \ t_1 \ldots t_n \ v) :: (\tau_1 \times \ldots \times \tau_n \to \tau)}$$

Figure 2.1: Primitive Operators and Type Checking

The language allows nonlinear polynomials but this is not fully supported by the tool at this time. Yices 2 can solve problems involving real and integer linear arithmethic, but it does not yet include a solver for nonlinear arithmetic.

2.3.2 Bitvectors

Yices supports all the bitvector operators defined in the SMT-LIB standard [RT06]. The most commonly used operators are listed in Table 2.1. They include bitvector arithmetic (where bitvectors are interpreted either as unsigned integers or as signed integers in two's complement representation), logical operators such as bitwise OR or AND, logical and arithmetic shifts, concatenation, and extraction of subvectors. Other operators are defined in the theory QF_BV of SMT-LIB (cf. http://combination.cs.uiowa.edu/smtlib); all of them are supported by Yices 2.

The semantics of all the bitvector operators is defined in the SMT-LIB 1.2 standard. Yices 2 follows the standard except for the case of division by zero. In SMT-LIB, the result of a division by zero is undefined but one must ensure that the division operators are functional. In other words, SMT-LIB does not specify the result of (bvudiv a b) if b is the zero vector, but (bvudiv a b) and (bvudiv a b) must be equal whenever a = c, even if b is the zero vector. Yices 2 uses a simpler semantics (inspired from the BTOR format [BBL08]):

• Unsigned Division: If b is the zero bitvector of n bits then

```
(bvudiv a b) = 0b111...1

(bvurem a b) = a
```

In general, the quotient (bvudiv a b) is the largest unsigned integer that can be represented on n bits, and is smaller than a/b, and the following idendity for all bitvectors a and b

```
a = (bvadd (bvmul (bvudiv a b) b) (bvurem a b))
```

• **Signed Division:** If b is the zero bitvector of n bits then

Operator and Type	Meaning	
$\texttt{bvadd} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))$	addition	
$\boxed{\mathtt{bvsub} :: ((\mathtt{bv}\ n) \times (\mathtt{bv}\ n) \to (\mathtt{bv}\ n))}$	subtraction	
$\boxed{\texttt{bvmul} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))}$	multiplication	
$ \texttt{bvneg} :: \texttt{bv} \ n) \to (\texttt{bv} \ n)) $	2's complement opposite	
$\boxed{\texttt{bvudiv} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))}$	quotient in unsigned division	
$\texttt{bvudiv} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))$	remainder in unsigned division	
$\boxed{\mathtt{bvsdiv} :: ((\mathtt{bv}\ n) \times (\mathtt{bv}\ n) \to (\mathtt{bv}\ n))}$	quotient in signed division	
	with rounding toward zero	
$\texttt{bvsrem} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))$	remainder in signed division	
	with rounding toward zero	
$\texttt{bvsmod} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))$	remainder in signed division	
	with rounding toward $-\infty$	
$\boxed{ \texttt{bvule} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to \texttt{bool} }$	unsigned less than or equal	
$ \texttt{bvuge} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to \texttt{bool} $	unsigned greater than or equal	
$\boxed{ \texttt{bvult} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to \texttt{bool} }$	unsigned less thanl	
$\texttt{bvugt} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to \texttt{bool}$	unsigned greater than	
$ \texttt{bvsle} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to \texttt{bool} $	unsigned less than or equal	
$ \texttt{bvsge} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to \texttt{bool} $	unsigned greater than or equal	
$\boxed{\texttt{bvslt} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to \texttt{bool}}$	unsigned less thanl	
$\texttt{bvsgt} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to \texttt{bool}$	unsigned greater than	
$\boxed{\texttt{bvand} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to (\texttt{bv} \ n))}$	bitwise and	
$\boxed{\mathtt{bvor} :: ((\mathtt{bv}\ n) \times (\mathtt{bv}\ n) \to (\mathtt{bv}\ n))}$	bitwise or	
$ \mathtt{bvnot} :: ((\mathtt{bv} \ n) \to (\mathtt{bv} \ n)) $	bitwise negation	
$\texttt{bvxor} :: ((\texttt{bv}\ n) \times (\texttt{bv}\ n) \to (\texttt{bv}\ n))$	bitwise exclusive or	
$\boxed{\texttt{bvshl} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to (\texttt{bv} \ n))}$	shift left	
$ \texttt{bvlshr} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to (\texttt{bv} \ n)) $	logical shift right	
$\boxed{ \texttt{bvashr} :: ((\texttt{bv} \ n) \times (\texttt{bv} \ n) \to (\texttt{bv} \ n)) }$	arithmetic shift right	
$\boxed{ \mathtt{bvconcat} :: ((\mathtt{bv}\ n) \times (\mathtt{bv}\ m) \to (\mathtt{bv}\ n + m)) }$	concatenation	
$ \mid \mathtt{bvextract}_{i,j}((\mathtt{bv}\; n) \to (\mathtt{bv}\; m)) $	extract bits i down to j	
	form a bitvector of size n	

Table 2.1: Bitvector Operators

Yices 2 Architecture

Yices 2 relies on a simpler language and type system than Yices 1. We have also completely redesigned the architecture to make Yices 2 easier to maintain and develop. The new architecture supports new features, such as the possibility to maintain several contexts in parallel.

3.1 Main Components

The Yices 2 software can be conceptually decomposed into three main modules:

Term Database Yices 2 maintains a global database in which all terms and types are stored. Yices 2 provides an API for constructing terms, formulas, and types in this database.

Context Management A context is a central data structure that stores asserted formulas. Each context contains a set of assertions to be checked for satisfiability. The context-management API supports operations for creating and initializing contexts, for asserting formulas into a context, and for checking the satisfiability of the asserted formulas. Several contexts can be constructed and manipulated independently.

Contexts are highly customizable. Each context can be configured to support a specific theory, and to use a specific solver or combination of solvers.

Model Management If the set of formulas asserted in a context is satisfiable, then one can construct a model of the formulas. The model maps symbols of the formulas to concrete values (e.g., integer or rational values or bitvector constants). The API provides functions to build and query models.

Figure 3.1 shows the top-level architecture of Yices 2, divided into the three main modules. Each context consists of two separate components: The *solver* employs a Boolean satisfiability solver and decision procedures for determining whether the formulas asserted

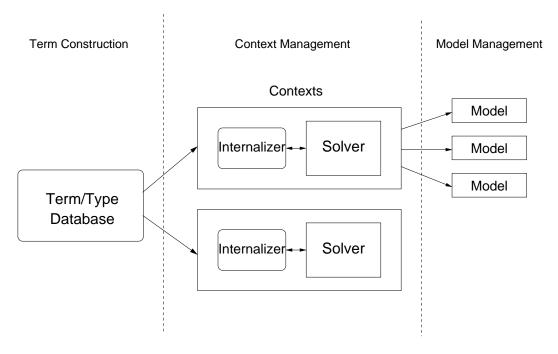


Figure 3.1: Top-level Yices 2 Architecture

in the context are satisfiable. The *internalizer* converts the format used by the term database into the internal format used by the solver. In particular, the internalizer rewrites all formulas in conjunctive normal form, which is used by the internal SAT solver.

3.2 Solvers

In Yices 2, it is possible to select a different solver (or combination of solvers) for the problem of interest. Each context can thus be configured for a specific class of formulas. For example, one can use a solver specialized for linear arithmetic, or use a solver that supports the full Yices 2 language. Figure 3.2 shows how the most general solver is built. A major component of all solvers is a SAT solver based on the Davis-Putnam-Logemann-Loveland (DPLL) procedure. The SAT solver is coupled with one or more so-called *theory solvers*. Each theory solver implements a decision procedure for a particular theory. Currently, Yices 2 includes four main theory solvers:

• The *UF Solver* deals with the theory of uninterpreted functions with equality¹. It implements a decision procedure based on computing congruence closures, similar to the Simplify system [DNS05].

¹UF stands for uninterpreted functions.

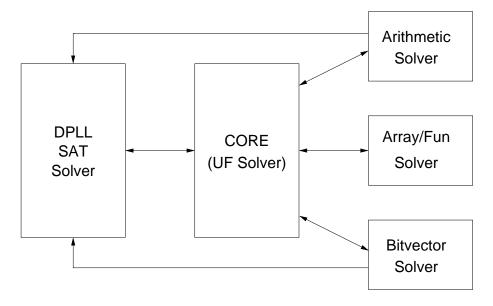


Figure 3.2: Solver Components

- The *Arithmetic Solver* deals with linear integer and real arithmetic. It implements a decision procedure based on the Simplex algorithm [DdM06a, DdM06b].
- The *Bitvector Solver* deals with the theory of bitvectors.
- The Array Solver implements a decision procedure for McCarthy's theory of arrays.

Yices 2 employs a modular solver architecture. It is possible to remove some of the components of Figure 3.2 to build simpler and more efficient solvers that are specialized for specific classes of formulas. For example, a solver for pure arithmethic can be built by directly attaching the arithmetic solver to the DPLL SAT solver. Similarly, Yices 2 can be specialized for pure bitvector problems, or for problems combining uninterpreted functions, arrays, and bitvectors (by removing the arithmetic solver).

Yices 2 combines several theory solver using the Nelson-Oppen method [NO79]. The UF solver is essential for this purpose; it coordinates the different theory solvers and ensures global consistency. The other solvers (for arithmetic, arrays, and bitvectors) communicate only with the central UF solver and never directly with each other. This property considerably simplifies the design and implementation of theory solvers.

yices

The Yices 2 distribution includes a tool for processing input written in the Yices 2 language. This tool is callled yices (or yices.exe in the Windows and Cygwin distributions). The syntax and the set of commands supported by yices are explained in the file doc/YICES-LANGUAGE included in the distribution. Seveval example specifications are also included in the examples/directory.

By default, the yices tool supports the combination of arithmetic, uninterpreted functions and arrays. It builds a context that includes the Simplex, UF, and Array solvers. This can be changed by giving command-line arguments to the tool. Try yices ——help for more details.

yices-smt

Another tool included in the distribution can process input written in the SMT-LIB notation. This tool is called <code>yices-smt</code> (or <code>yices-smt.exe</code>). It is included in the <code>bin</code> directory. Currently, this tool supports version 1.2 of SMT-LIB. Support for the more recent SMT-LIB 2 will be provided in future releases.

Yices API

The distribution includes a library and header files for embedding Yices in other software. The main header file is yices.h which includes all the API. The API functions are documented in this header file. More complete and detailed documentation on the Yices 2 API will be provided at the Yices website http://yices.csl.sri.com/.

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Bibliography

- [BBL08] R. Brummayer, A. Biere, and F. Lonsing. BTOR: Bit-Precise Modelling of Word-Level Problems for Model Checking. In *First International Workshop on Bit-Precise Reasoning*, pages 53–64, 2008. Available at http://fmv.jku.at/BrummayerBiereLonsing-BPR08.pdf.
- [DdM06a] Bruno Dutertre and Leonardo de Moura. A fast linear-arithmetic solver for DPLL(T). In *Computer-Aided Verification (CAV'2006)*, volume 4144 of *Lecture Notes in Computer Science*, pages 81–94. Springer Verlag, August 2006.
- [DdM06b] Bruno Dutertre and Leonardo de Moura. Integrating Simplex with DPLL(T). Technical Report SRI-CSL-06-01, Computer Science Laboratory, SRI International, May 2006. Available at http://yices.csl.sri.com/sri-csl-06-01.pdf.
- [DNS05] D. Detlefs, G. Nelson, and J. B. Saxe. Simplify: a Theorem Prover for Program Checking. *Journal of the ACM*, 52(3):365–473, May 2005.
- [NO79] G. Nelson and D. C. Oppen. Simplification by Cooperating Decision Procedures. ACM Transactions on Programming Languages and Systems, 1(2):245–257, 1979.
- [RT06] Silvio Ranise and Cesare Tinelli. The SMT-LIB Standard: Version 1.2. Technical report, SMT-LIB Initiative, 2006. Available at http://www.smtlib.org.