

[illegible]

PORT	AF0 SYS	AF1 TIM1 TIM2	AF2 TIM3 TIM4 TIM5	AF3 TIM9 TIM10 TIM11	AF4 I2C1 I2C2 I2C3	AF5 SPI1 SPI2 SPI4	AF6 SPI3 SPI4 SPI5	AF7 SPI3 USART1 USART2	AF8 USART6	AF9 I2C2 I2C3	AF10 OTG_FS	AF12 SDIO	ADC	配置										
PA	0	CH1 / ETR	CH1	CH1	CH1	MOSI	NSS	CTS RTS TX RX CK				CMD	ADC1_IN0	O	LED_R LED_G LED_B IMU_CSM IMU_SCK IMU_SDO IMU_SDI									
	1												ADC1_IN1	O										
	2												ADC1_IN2	O										
	3												ADC1_IN3	O										
	4	CH1 / ETR	CH1 CH2		NSS SCK MISO MOSI	NSS						ADC1_IN4	O	IMU_CSM IMU_SCK IMU_SDO IMU_SDI										
	5											ADC1_IN5	O											
	6											ADC1_IN6	O											
	7											ADC1_IN7	O											
	8	MCO1	CH1 CH2 CH3 CH4				CK TX RX					SOF	X	KEY UART_TX UART_RX EXT01 EXT02 SWDIO SWCLK										
	9											VBUS	X											
	10											ID	X											
	11											DM	X											
	12											DP	X											
	13	SDIO	CH1 / ETR				NSS	TX						X	SWDIO SWCLK									
	14	SCLK												X										
	15	JTDI	CH1 / ETR				NSS							O										
PB	0	CH2N CH3N	CH2	CH3 CH4			SCK NSS	RX		SDA	SDA	D0 D3	ADC1_IN8	X	IMU_CSB									
	1												ADC1_IN9	X										
	2													X										
	3													O										
	4	JTRST	CH1 CH2 CH3 CH4	CH1 CH2 CH3 CH4	SCK MISO MOSI	SCK MISO MOSI	TX RX		SDA	SDA	D0 D4 D5 D7		O											
	5	CH1 CH2 CH3 CH4													O									
	6														X									
	7														X									
	8	CH1			CH1	SCK	MOSI			SDA	SDA			O										
	9													O										
	10													O										
	11													X										
	12	CH2N CH3N	CH2	CH3 CH4		SCK	NSS	RX		SDA	SDA	D6 CK		X	IMU_SCL IMU_SDA IMU_RDY									
	13													X										
	14													X										
	15													X										
PC	13													X										
	14													X										
	15													X										