**Data sheet**

PRESETn input is asserted (‘1’), the APB4 interface is put into its initial reset state.

**Write/Read operation**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **PRESETn** | **PSEL** | **PENABLE** | **PWRITE** | **Function** |
| 0 | 1 | 1 | 0 | Read Access |
| 0 | 1 | 1 | 1 | Write Access |

**The PADDR is consist of 3 bit.**

|  |  |  |
| --- | --- | --- |
| **PADDR** | **Function** |  |
| 000 | Direction | 1->OUTPUT,  0->INPUT |
| 001 | SET | 1->ON  0->No effect |
| 010 | CLEAR | 1->OFF  0->No effect |
| 011 | OUT | - |
| 100 | IN | - |
| 101 | - | - |
| 110 | - | - |
| 111 | - | - |