

Assessment sub  
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NPTEL (https://swayam.gov.in/explorer?ncCode=NPTEL) » Compiler Design (course)



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# Thank you for taking the Week 1 : Assignment 1.

## Course outline

How does an  
NPTEL online  
course work?

Week 0 :

Week 1

- ☐ Lecture 01 :  
Introduction  
(unit?  
unit=17&lesson=18)
- ☐ Lecture 02 :  
Introduction  
(Contd.) (unit?  
unit=17&lesson=19)
- ☐ Lecture 03 :  
Introduction  
(Contd.) (unit?  
unit=17&lesson=20)
- ☐ Lecture 04 :  
Introduction  
(Contd.) (unit?  
unit=17&lesson=21)
- ☐ Lecture 05 :  
Introduction  
(Contd.) (unit?  
unit=17&lesson=22)

## Week 1 : Assignment 1

Your last recorded submission was on 2022-01-24, 11:45 Due date: 2022-02-09, 23:59 IST.

- 1) What role does intermediate code play? **1 point**
  - ☐ a) Retargeting code
  - ☐ b) Program analysis
  - ☐ c) Code optimization
  - ☒ d) Code check
- 2) What is the name of the phase in which the compiler scans the input sequence? **1 point**
  - ☒ a) lexical analysis
  - ☐ b) output scanner
  - ☐ c) structural analysis
  - ☐ d) syntax analysis
- 3) Which computer program takes a high-level language and translates it to assembly language? **1 point**
  - ☐ a) Linker
  - ☐ b) Assembler
  - ☒ c) Compiler
  - ☐ d) Interpreter
- 4) Compiler helps in debugging via **1 point**
  - ☐ a) Printing variable values
  - ☐ b) Slower execution of the program
  - ☐ c) Additional code embedding

Assessment submitted.  
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☐ Lecture 06 :  
Introduction  
(Contd.) (unit?  
unit=17&lesson=23)

☐ Lecture  
Materials  
(unit?  
unit=17&lesson=24)

☒ **Quiz: Week 1  
: Assignment  
1  
(assessment?  
name=135)**

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- ☒ d) None of the above options
- 5) Most of the programming language follows **1 point**
- ☐ a) dynamic scoping  
☒ b) static scoping  
☐ c) both static and dynamic scoping  
☐ d) Neither static nor dynamic scoping
- 6) Which temporary variables must be allocated for the best code execution speed? **1 point**
- ☒ a) CPU registers  
☐ b) Swap space  
☐ c) Main Memory  
☐ d) None of the above options
- 7) What is the name of the syntax analysis output? **1 point**
- ☐ a) Key tree  
☐ b) binary tree  
☒ c) parse tree  
☐ d) sparse tree
- 8) Converting a hardware description into actual circuitry is known as **1 point**
- ☒ a) HDL compilation  
☐ b) Circuit compilation  
☐ c) Silicon compilation  
☐ d) None of the other options
- 9) What are the results of the lexical analysis phase? **1 point**
- ☒ a) tokens  
☐ b) keyword  
☐ c) symbols  
☐ d) None of the above options
- 10) Which of the following is a result of a compiler? **1 point**
- ☐ a) Error messages  
☐ b) Warnings  
☐ c) Target code  
☒ d) All of the above options
- 11) Loops are the major targets for optimization since **1 point**
- ☒ a) Loop body is repeated several times  
☐ b) Condition check takes exceedingly large time  
☐ c) Loop may goto infinite execution  
☐ d) None of the other options

Assessment submitted.

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You may submit any number of times before the due date. The final submission will be considered for grading.

**Submit Answers**