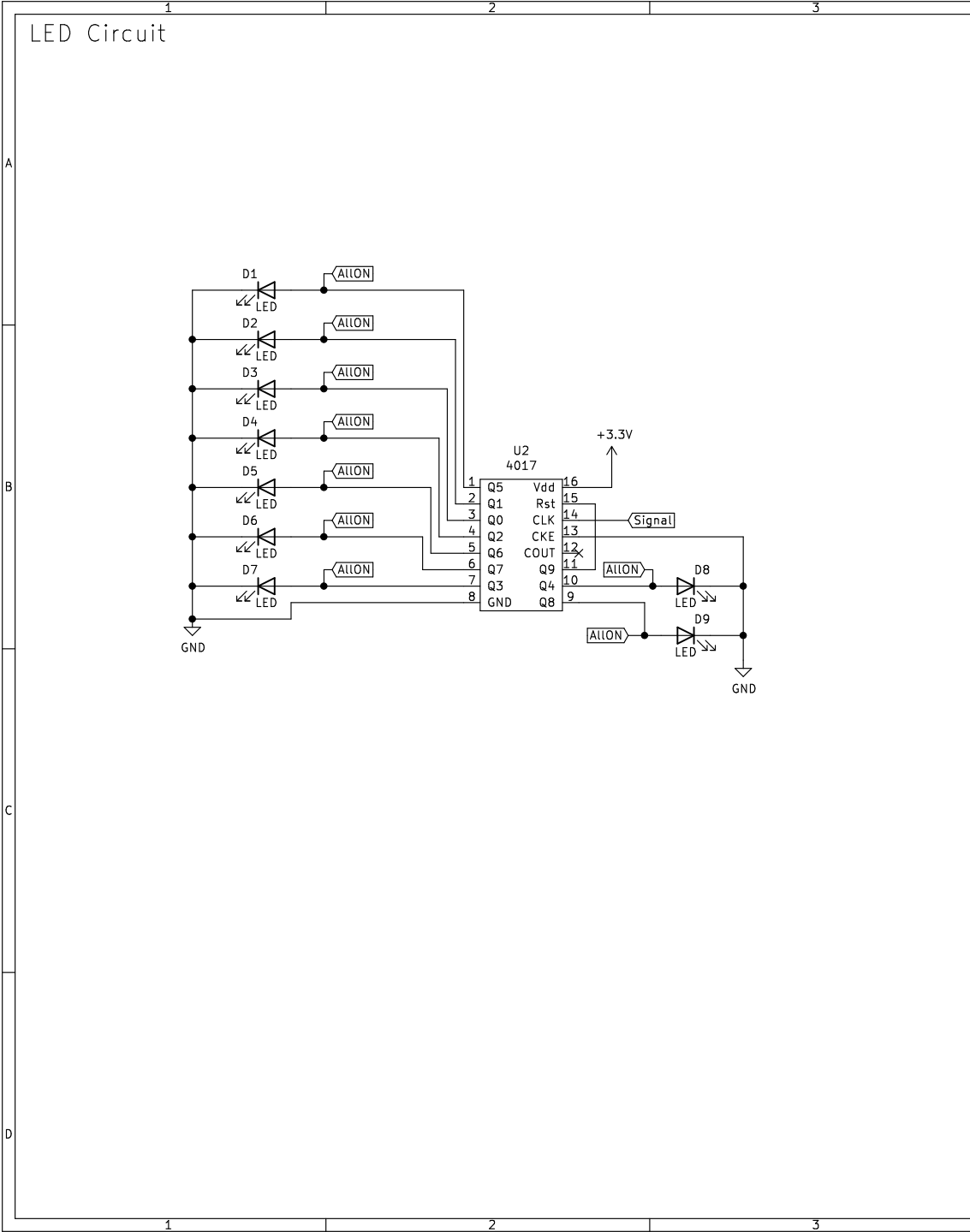
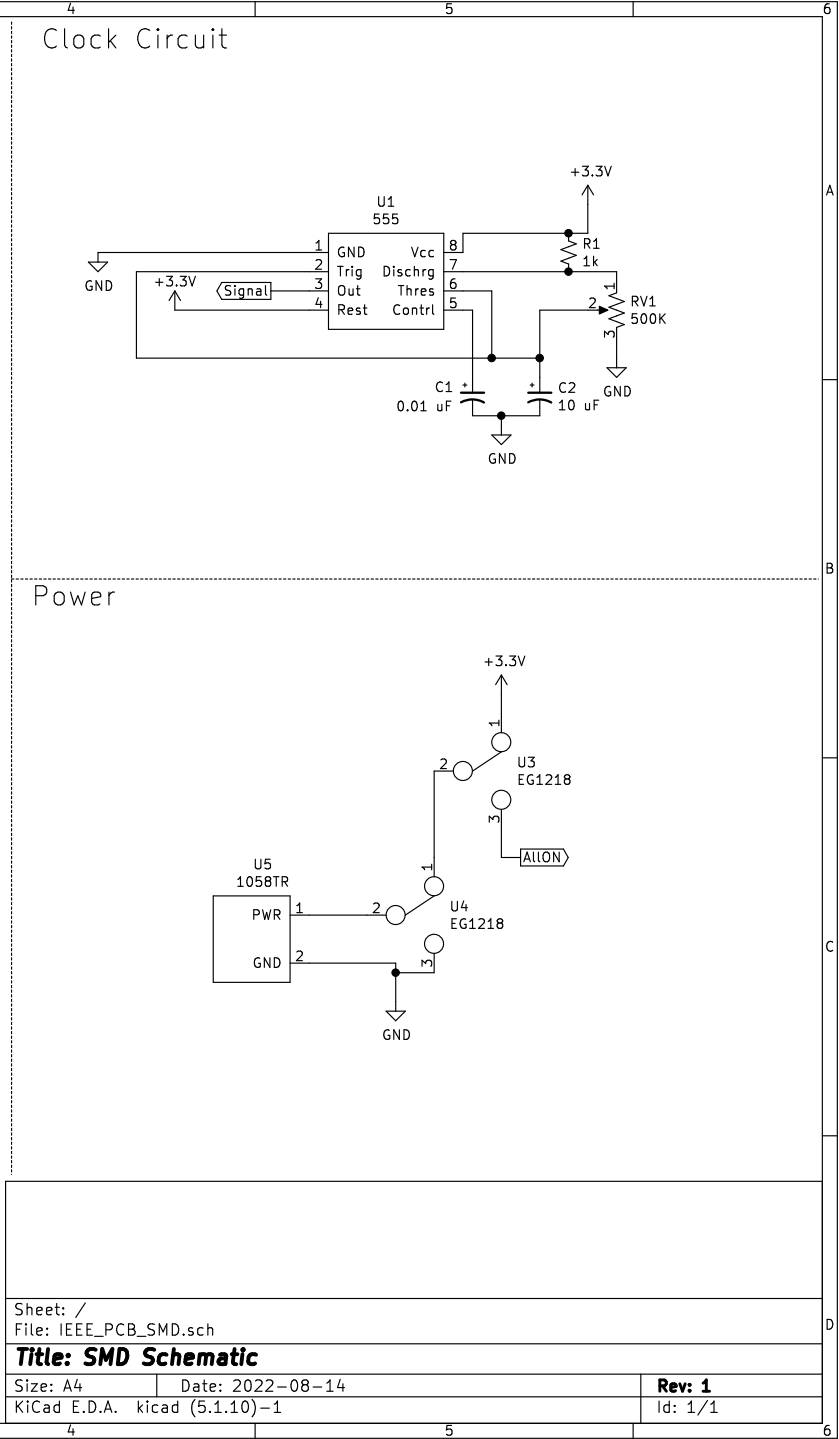


LED Circuit

The diagram illustrates a digital circuit using a 4017 decoder (U2) to control seven LEDs (D1-D7) and two additional LEDs (D8-D9). The 4017 decoder is configured with its Vdd (pin 16) connected to +3.3V and its Rst (pin 15) connected to GND. The CLK (pin 14) is connected to a Signal input. The Q1 (pin 2) through Q7 (pin 7) outputs are connected to the anodes of LEDs D1 through D7, respectively. The Q8 (pin 9) output is connected to the anode of LED D8. The Q9 (pin 11) output is connected to the anode of LED D9. The cathodes of all LEDs are connected to GND. The circuit is powered by a +3.3V supply.

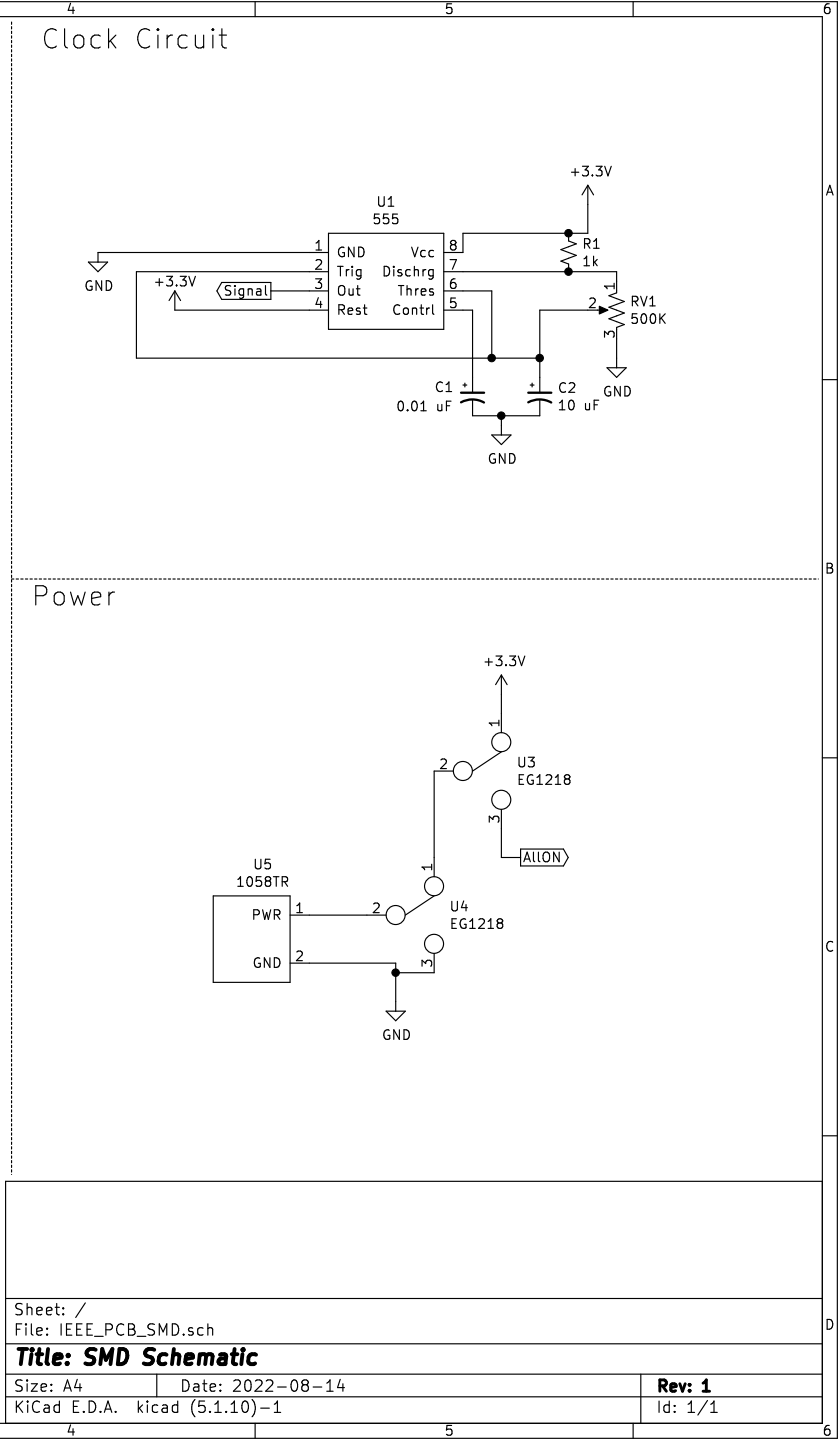
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Clock Circuit

Power

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