

Digital Circuit Lab — Lab 3 User Manual

Audio Recorder

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Github: <https://github.com/awinder0230/Digital-Circuit-Design-Lab>

• Usage

- Connect your microphone and speaker to the FPGA board.
- Record
 1. Switch SW[0] to REC.
 2. Press KEY[0] to start recording.
 3. You can press KEY[0] to pause the record and press again to continue.
 4. To stop recording, press KEY[0] to pause first and press KEY[1] to stop.
- Play
 1. Switch SW[0] to PLAY.
 2. Press KEY[0] to play the recorded audio.
 3. You can press KEY[0] to pause and press again to continue.
 4. To stop playing, press KEY[0] to pause first and press KEY[1] to stop.
 5. You can speed up the audio by pressing KEY[2] or slow down by pressing KEY[3]. The player supports up to 8x and down to 1/8x speed.
 6. In slow-forward replaying, you can enable linear interpolation by switching SW[1] to ON.

