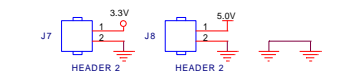
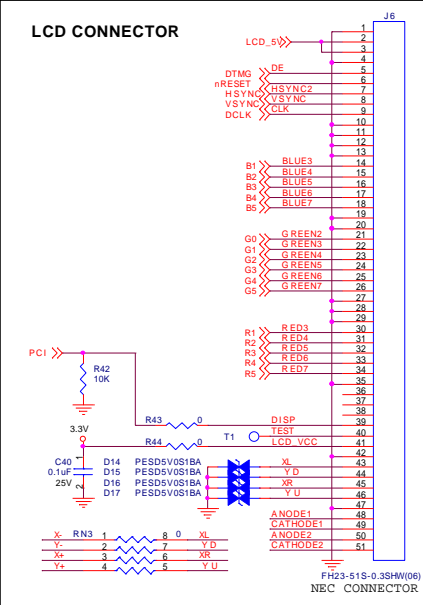
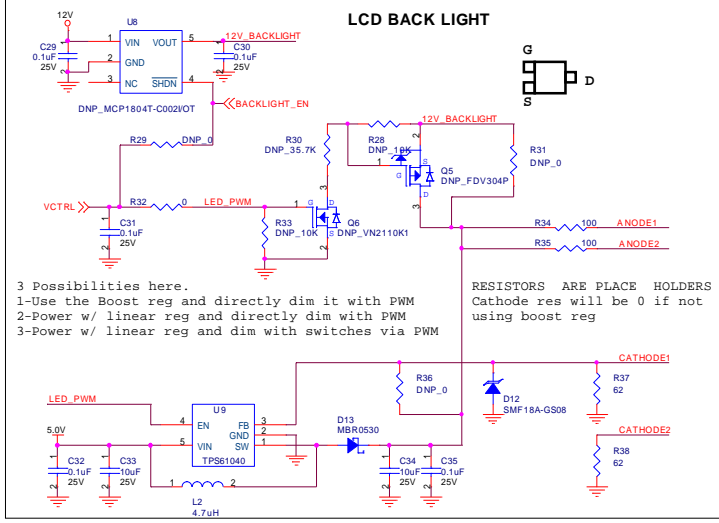
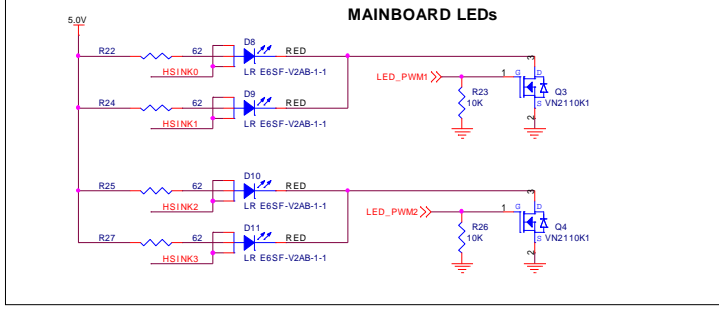
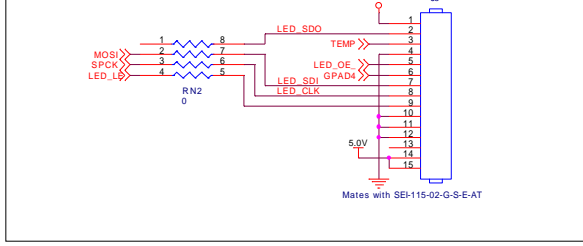
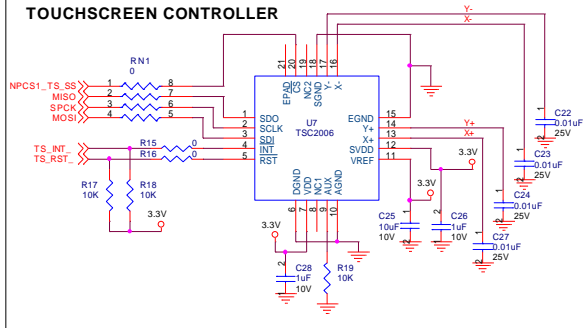
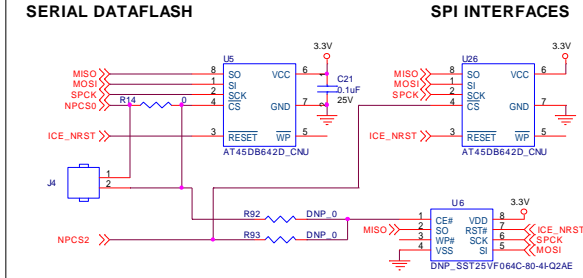
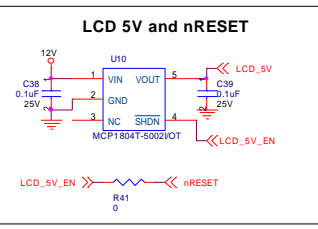
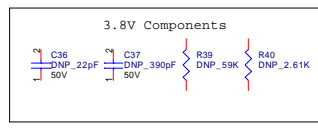
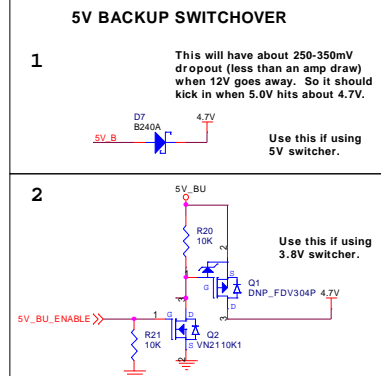
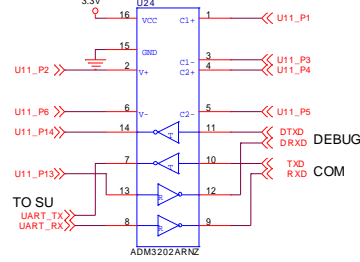
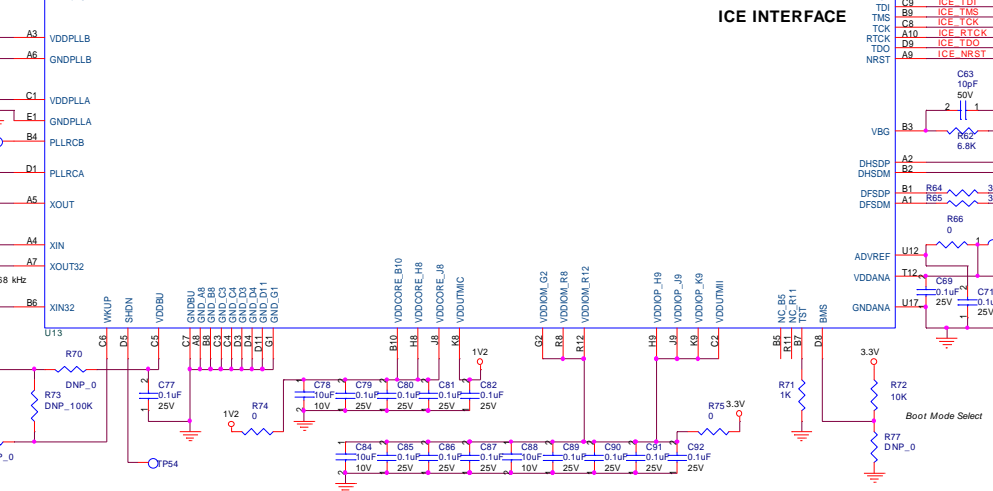
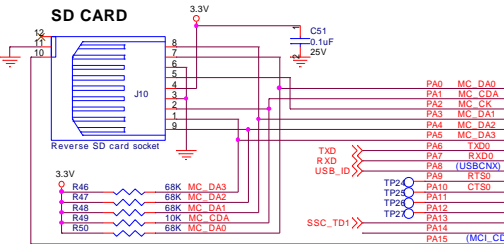


This should support the narrow and wide packages. This instance is a copy of the one on the processor page



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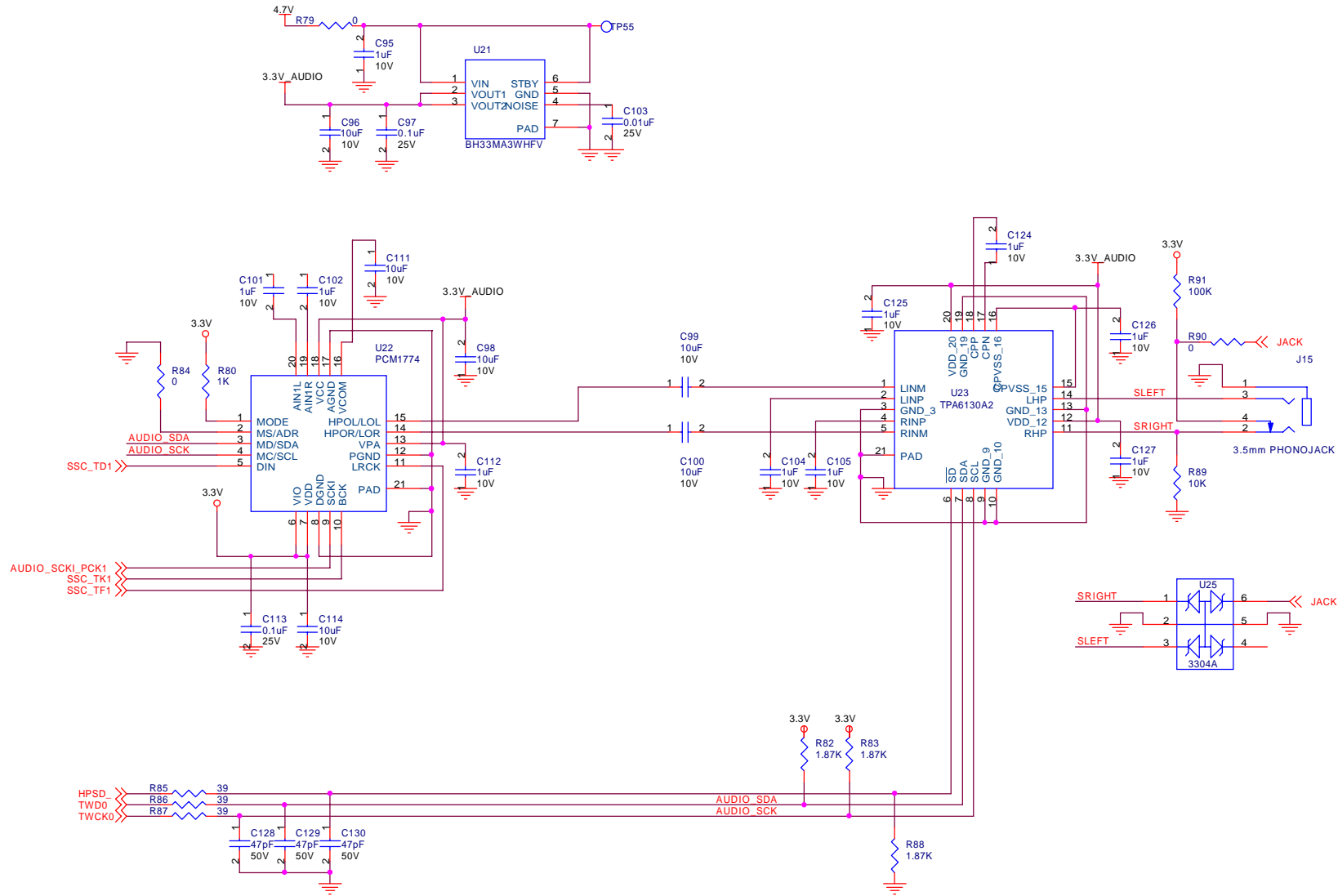
This should support the narrow and wide packages if possible.

[illegible]

The schematic diagram shows the ICNTRST chip (U15) connected to the ICNTRST board. The chip is a 3304A. The connections are as follows:

- ICNTRST Board:**
 - J11: 1 to 1, 2 to 2, 3 to 3, 4 to 4, 5 to 5, 6 to 6, 7 to 7, 8 to 8, 9 to 9, 10 to 10, 11 to 11, 12 to 12, 13 to 13, 14 to 14, 15 to 15, 16 to 16, 17 to 17, 18 to 18, 19 to 19, 20 to 20.
 - J12: 1 to 1, 2 to 2, 3 to 3, 4 to 4, 5 to 5, 6 to 6, 7 to 7, 8 to 8, 9 to 9, 10 to 10, 11 to 11, 12 to 12, 13 to 13, 14 to 14, 15 to 15, 16 to 16, 17 to 17, 18 to 18, 19 to 19, 20 to 20.
 - J13: 1 to 1, 2 to 2, 3 to 3, 4 to 4, 5 to 5, 6 to 6, 7 to 7, 8 to 8, 9 to 9, 10 to 10, 11 to 11, 12 to 12, 13 to 13, 14 to 14, 15 to 15, 16 to 16, 17 to 17, 18 to 18, 19 to 19, 20 to 20.
- ICNTRST Board:**
 - 3.3V: 3.3V
 - R53: 68K ICE_NTRST2
 - R54: 68K ICE_TDI
 - R55: 68K ICE_TMS
 - R57: 68K ICE_RTCK
 - R59: 1K ICE_NRST
 - U15: 3304A
 - ICNTRST Board: 1 to 1, 2 to 2, 3 to 3, 4 to 4, 5 to 5, 6 to 6, 7 to 7, 8 to 8, 9 to 9, 10 to 10, 11 to 11, 12 to 12, 13 to 13, 14 to 14, 15 to 15, 16 to 16, 17 to 17, 18 to 18, 19 to 19, 20 to 20.

[illegible]



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