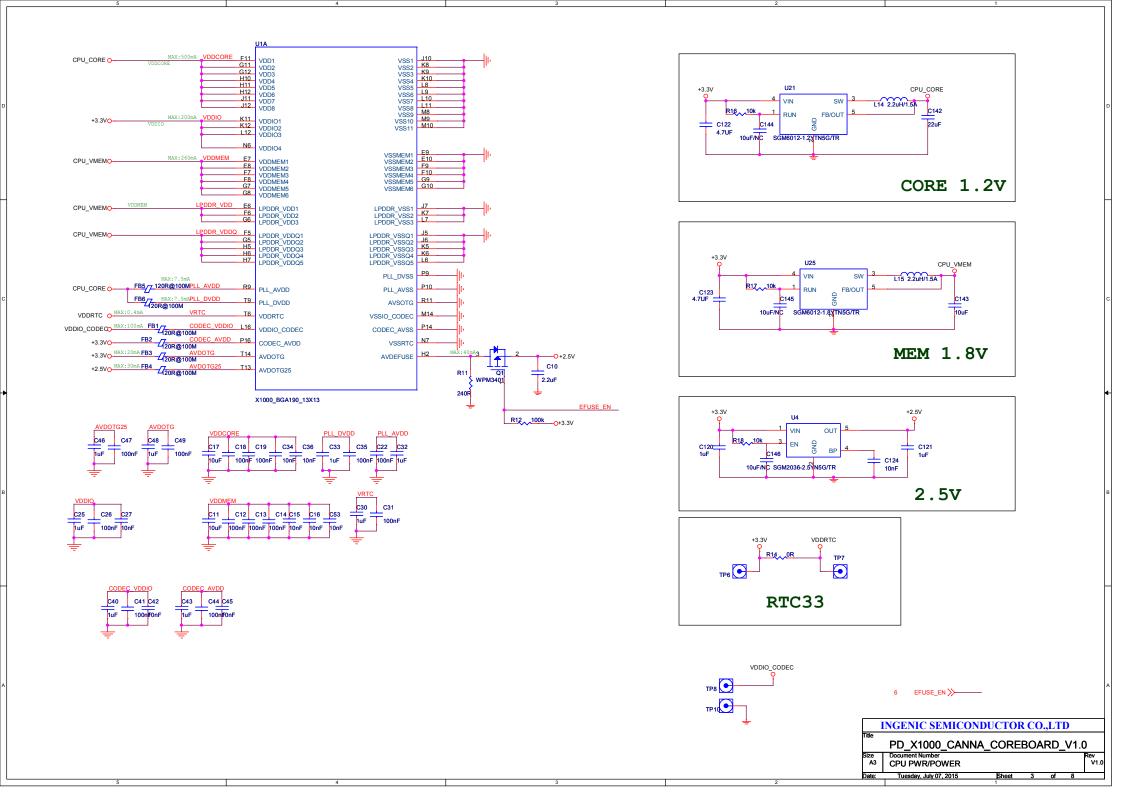
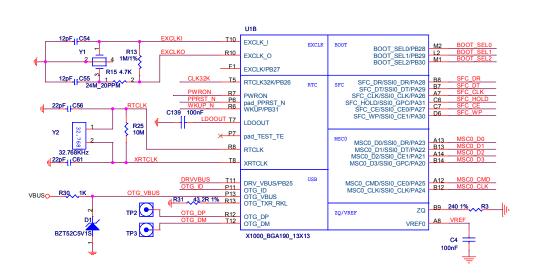
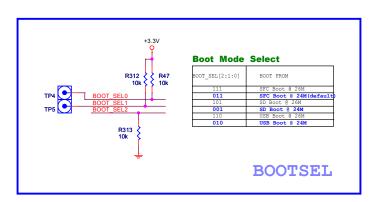
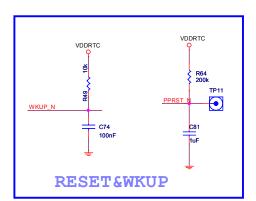


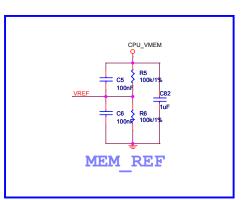
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	PD_X1000_CANNA_0	CORE	BOA	RD_	_V1.	.0
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A3	POWER BLOCK DIAGRAM					V1.0
Date:	Monday, June 29, 2015	Sheet	2	of	8	

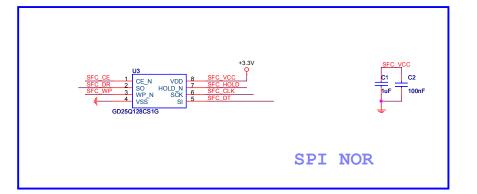


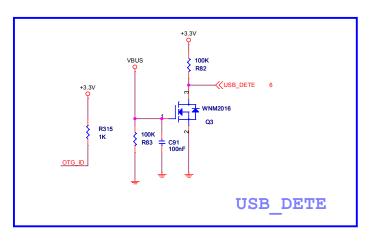


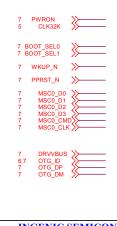








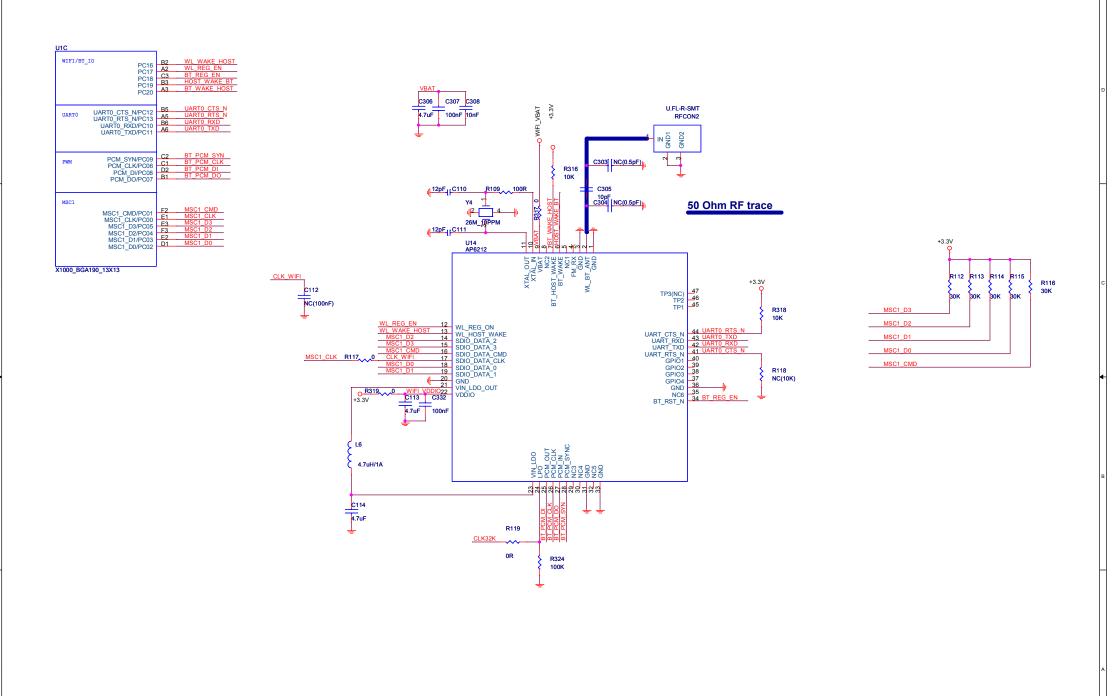




Thursday, July 02, 2015

MK1 MK3

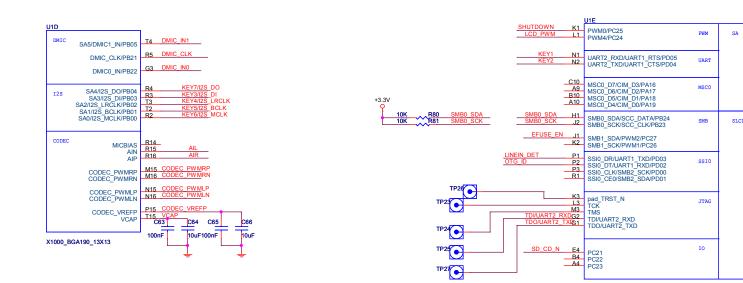
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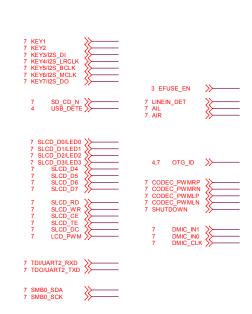


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CLK32K >>

Thursday, July 02, 2015





SA6/MAC_PHY_CLK/PWM3/PB06 SA7/MAC_CRS_DV/PB07 SA8/MAC_RXD1/PB08 SA9/MAC_RXD0/PB09

SA15/MAC_REF_CLK/PB15

\$A9/MAC_RXD0/PB09 \$A10/MAC_TXEN/PB10 \$A11/MAC_TXD1/PB11 \$A12/MAC_TXD0/PB12 \$A13/MAC_MD0C/PB13 \$A14/MAC_MD10/PB14 \$15/MAC_REF_CLK/PB15

SD1/SLCD_D1/SMB1_SDA/PA01
SD2/SLCD_D2/JART2_RXDPA02
SD3/SLCD_D3/JART2_TXDPA03
SD3/SLCD_D3/JART2_TXDPA03
SD4/SLCD_D3/JART1_TXDPA04
SD5/SLCD_D4/JART1_TXDPA04
SD5/SLCD_D5/JART1_TXDPA05
SD6/SLCD_D6/PA06
SD7/SLCD_D7/PA07
SD6/SLCD_D6/PA06
SD7/SLCD_D7/PA07
SD6/SLCD_D9/CIM_HSYN/PA09
SD9/SLCD_D10/CIM_SYN/PA04
SD1/SLCD_D10/CIM_SYN/PA04
SD1/SLCD_D11/CIM_MCLK/PA11
SD1/SLCD_D11/CIM_MCLK/PA11
SD1/SLCD_D11/CIM_D6/PA13
SD1/SLCD_D13/CIM_D6/PA13
SD1/SLCD_D14/CIM_D6/PA13
SD1/SLCD_D14/CIM_D6/PA14
SD1/SLCD_D14/CIM_D6/PA14
SD1/SLCD_D14/CIM_D6/PA14
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SD1/SLCD_D15/CIM_D4/PA15

WAIT/SLCD_DC/PB20

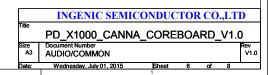
X1000_BGA190_13X13

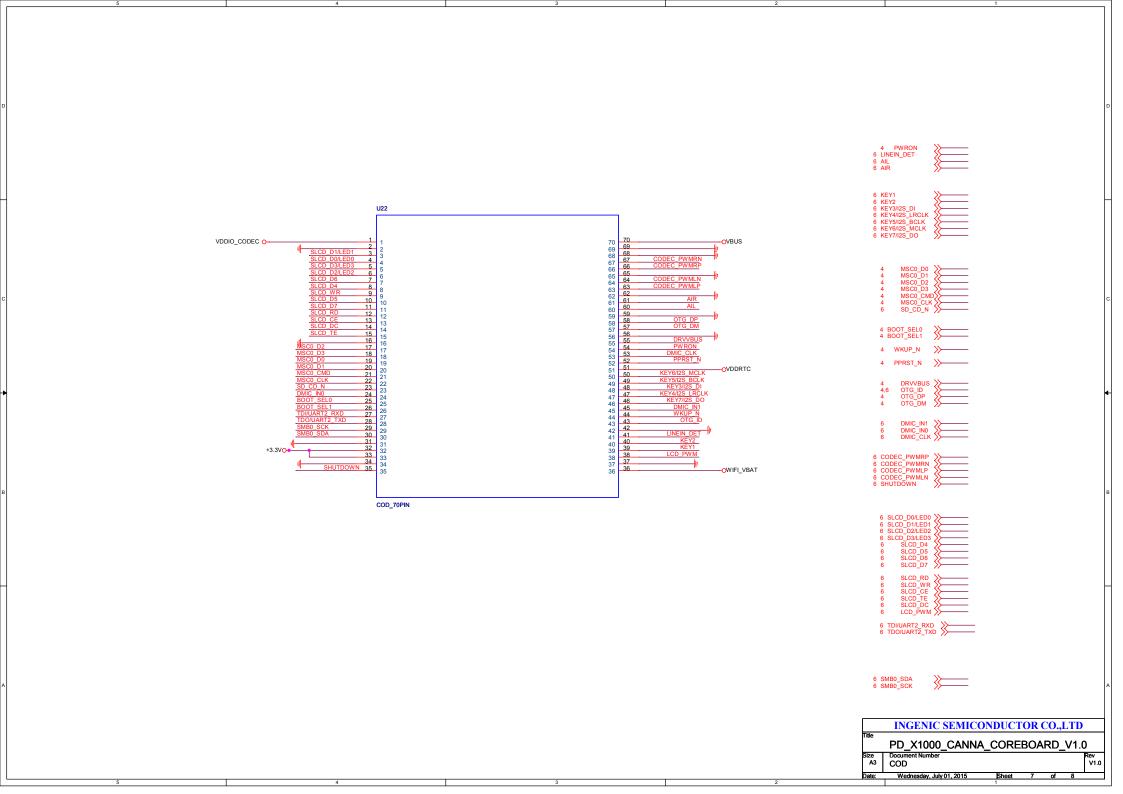
RD/SLCD_RD/PB16
WE/SLCD_WR/PB17
CS1/SLCD_CE/PB18
CS2/SLCD_TE/PB19
WAIT/SLCD_DC/PB20
WAIT/SLCD_DC/PB20
G14 SLCD_RD
D15 SLCD_WD
D16 SLCD_WD
D17 SLCD_WD

L15 L14

K16 K15

USB DETE





Data	Revision	Change
June.19 2015	Rev1.0	1. First released Revision
1		