**lqe'jg ljZjljBfno  
OlGhlgol/Gª cWoog ;+:yfg  
s]lGb|o SofDk; k'Nrf]s  
lzIfs sfo{ ;Dkfbg kmf/fd**

|  |  |
| --- | --- |
| **ljefu M Electronics and Computer Engineering** | **Semester : EVEN** |
| **lzIfssf] gfd y/ M Surendra Khadka** | **kb M Teacher** |
| **k|lt xKtf sIff ePsf] lbg M 6** |  |

:gfts tx sIff ljj/0f

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **qm=;+=** | **ljifo** | **sIff lsl;d** | **;+nUg lzIfs ;+Vof** | **lkl/o8** | **ljBfyL{ ;+Vof** |
| **1** | **Signal Analysis[BEL][L]III/II, BEL - AB** | **1** | **1** | **3** | **48** |
| **2** | **Signal Analysis[BEL][T]III/II, BEL - A** | **1** | **1** | **2** | **24** |
| **3** | **Signal Analysis[BEL][T]III/II, BEL - B** | **1** | **1** | **2** | **24** |
| **4** | **RF & Microwave Engineering [BEX][P]** | **3** | **3** | **3** | **24** |
| **5** | **Signal Analysis [BEX][P]** | **3** | **3** | **3** | **24** |
| **6** | **Basic Electronics Engineering [BCE][P]** | **3** | **3** | **3** | **24** |
| **7** | **Signal Analysis [BEL][P]** | **3** | **3** | **3** | **24** |
|  |  |  |  | **Total = 19 Periods** |  |

:gftsf]Q/ tx sIff ljj/0f

|  |  |  |  |
| --- | --- | --- | --- |
| **qm=;+=** | **ljifo** | **;+nUg lzIfs ;+Vof** | **q]ml86 cfj/** |
|  |  |  |  |

Gff]6 M -s\_ lkl/o8 M kf7|oqmddf pNn]v eP adf]lhd x'g]5 .  
 -v\_ sIff lsl;d eGgfn] **1,2,3** hgfpg' kg]{5 .  
 **1)**eGgfn] **Theory /Tutorial /B.E. Project /B.Arch. Thesis  
 2)** eGgfn] **Drawing /Design /Design Studio /Paper work** x'g] Nofj  
 **3)** eGgfn] **2** df pNn]v gePsf Nofjx?

|  |  |
| --- | --- |
| lzIfssf] x:tfIf/ M ========================================= | k|dfl0ft ug]{ |
| lzIfssf] gfd y/ M  **Surendra Khadka** | laefuLo k|d'v |
| ldtL M **2075-10-10** |  |

b|i6Jo M !\_ sIff ?l6g ;+nUg x'g''kg]{5 . @\_ **Elective Course** sf nflu ljBfyL{ ;+Vof $\* x'g]5 .  
 #\_ **Master/Ph.D.** sf] **Thesis** sf nflu of] kmd{ eg{ cfjZos 5}g .