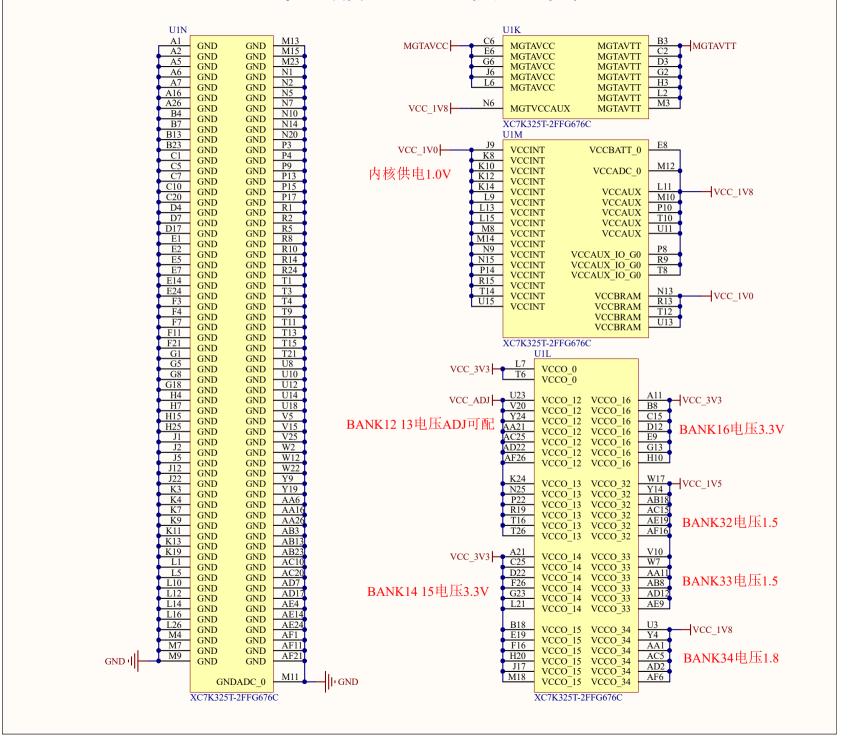
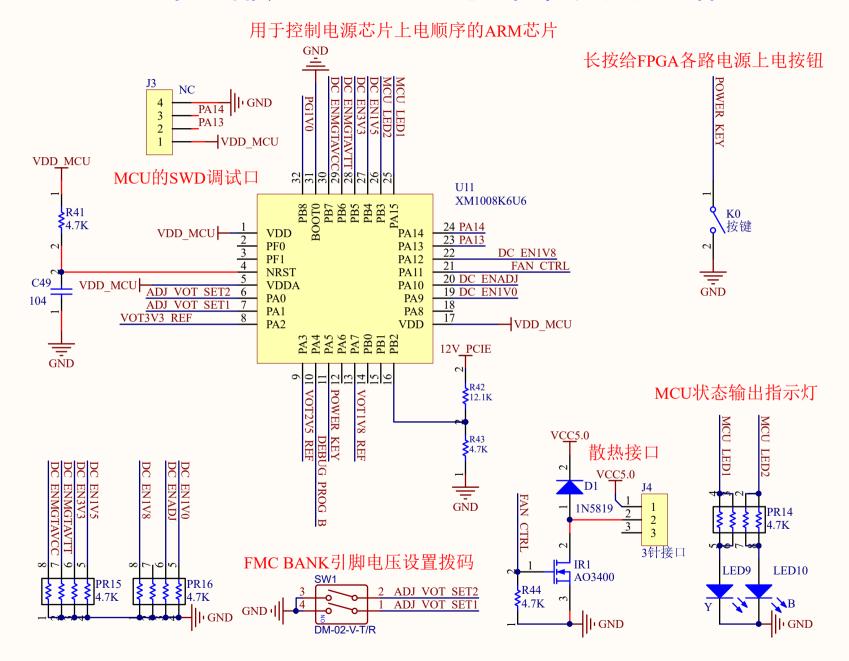


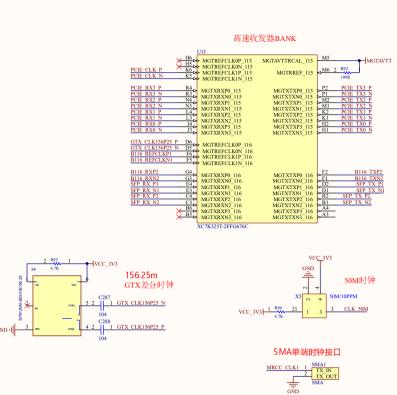
第二部分 FPGA供电引脚

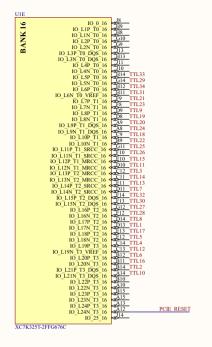


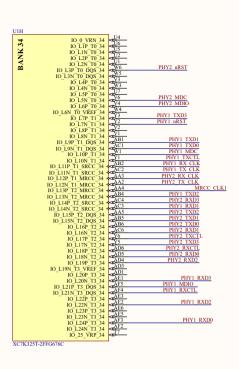
第三部分 MCU上电控制和风扇电路

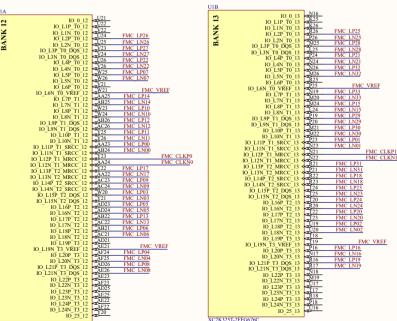


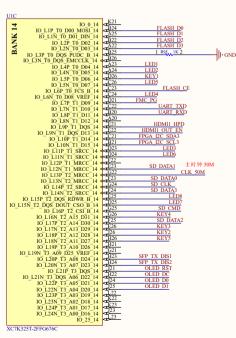
第四部分 FPGA的BANK

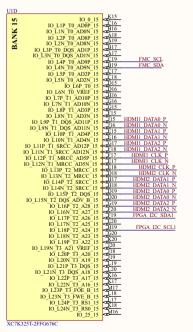




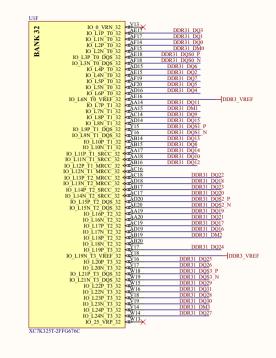


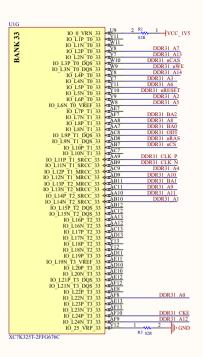


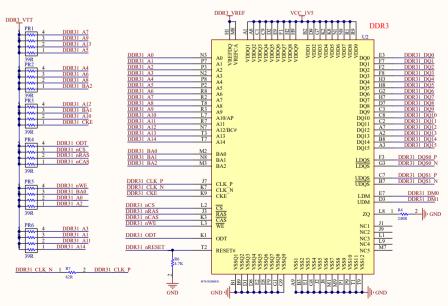




第五部分 DDR3/SD(TF)电路



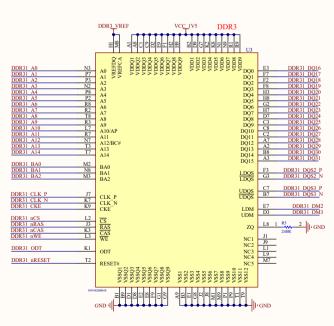




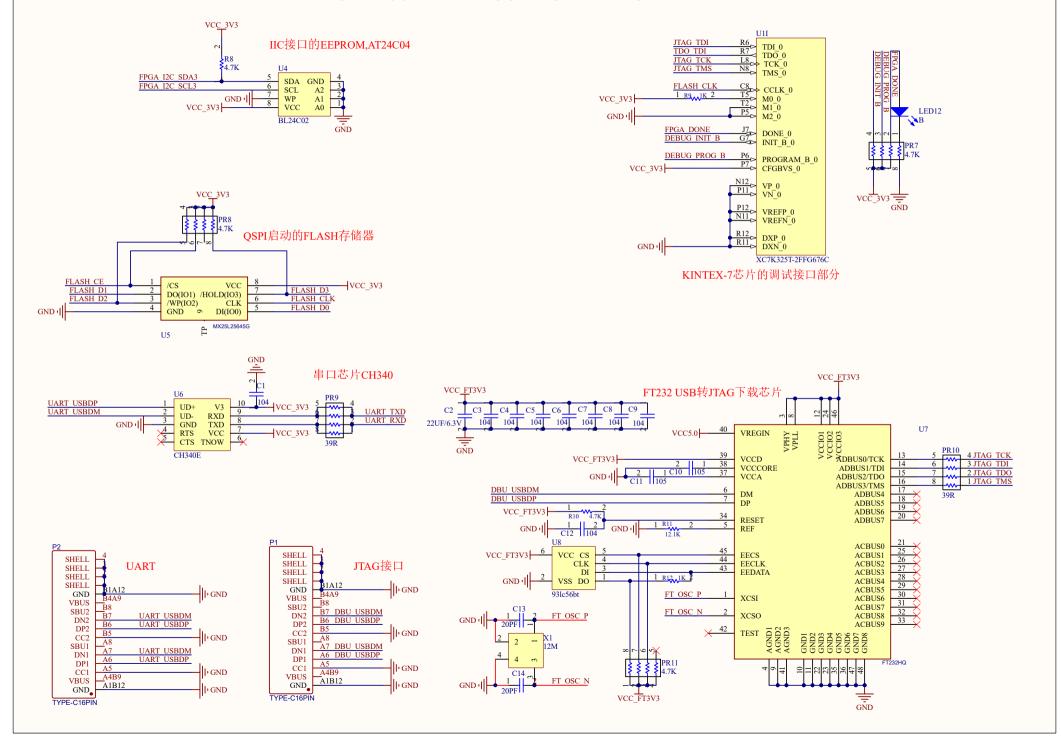
TF卡座

SD CLKVCC_3V3

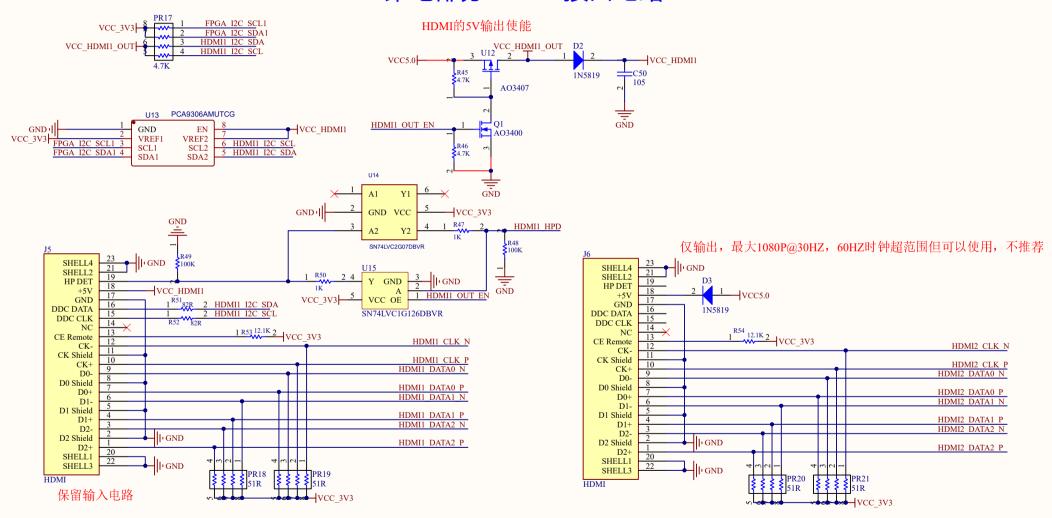
WWW



第六部分 JTAG\串口\FLASH\EEPROM

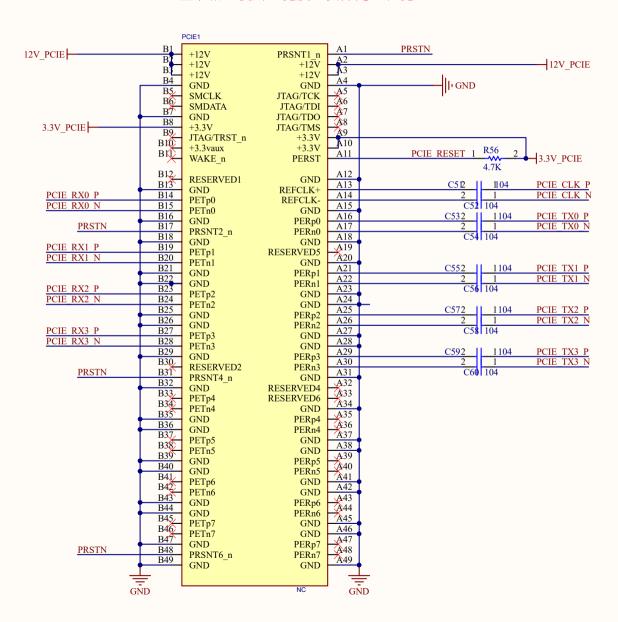


第七部分 HDMI接口电路

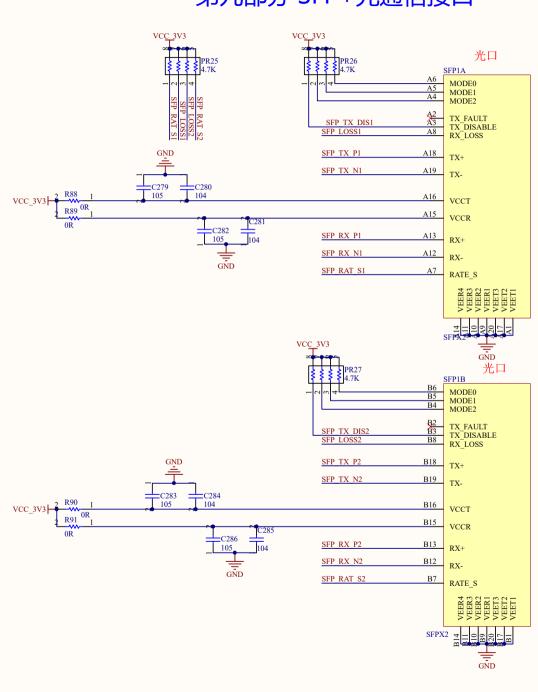


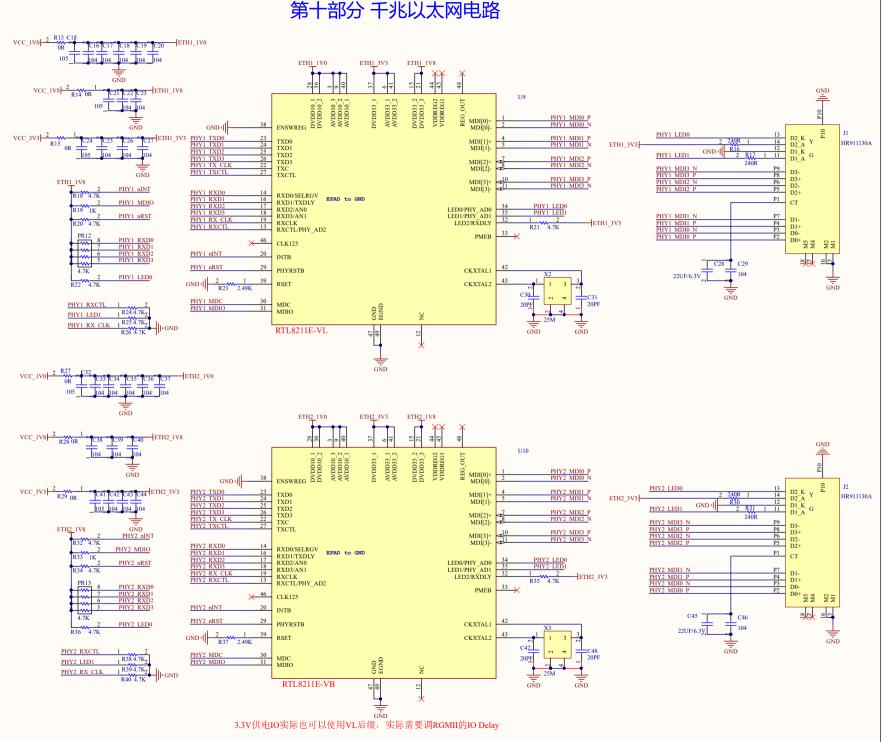
第八部分 PCIE接口电路

PCIE金手指,使用X8宽度,实际是X4位宽



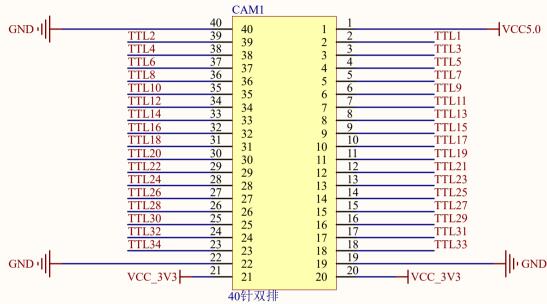
第九部分 SFP+光通信接口



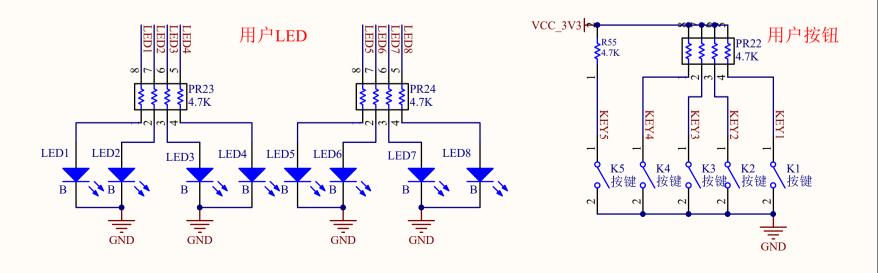


第十一部分 IO扩展接口和LED及按钮

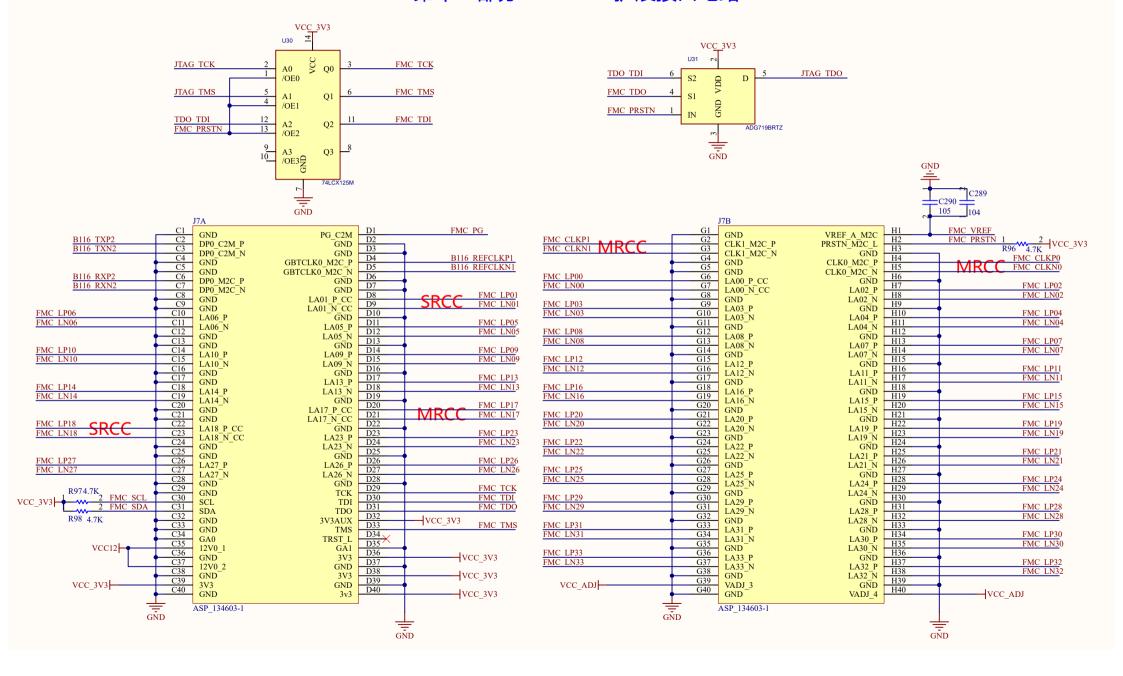




每个管脚直接查看开发板的扩展口丝印



第十二部分 FMC LPC扩展接口电路



第十三部份 PL的OLED显示模块

