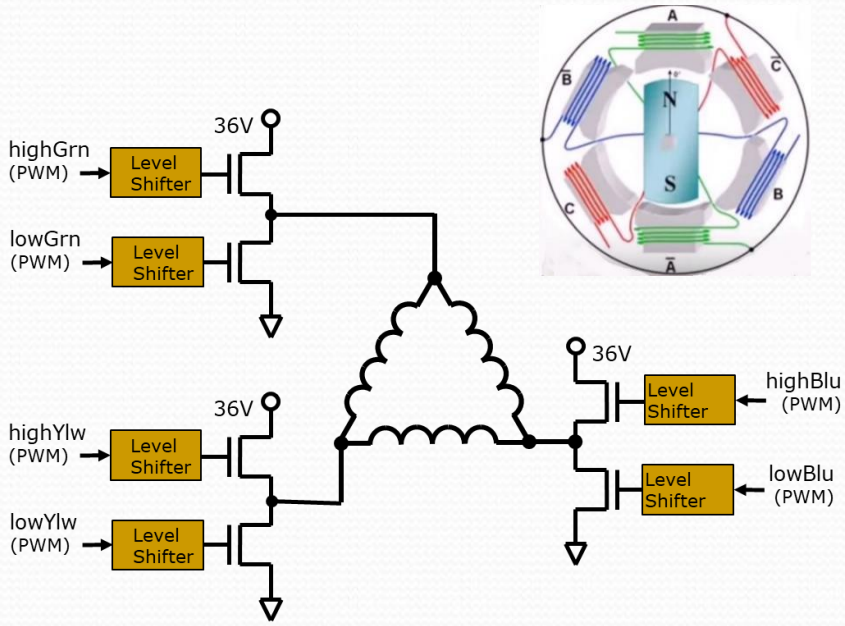
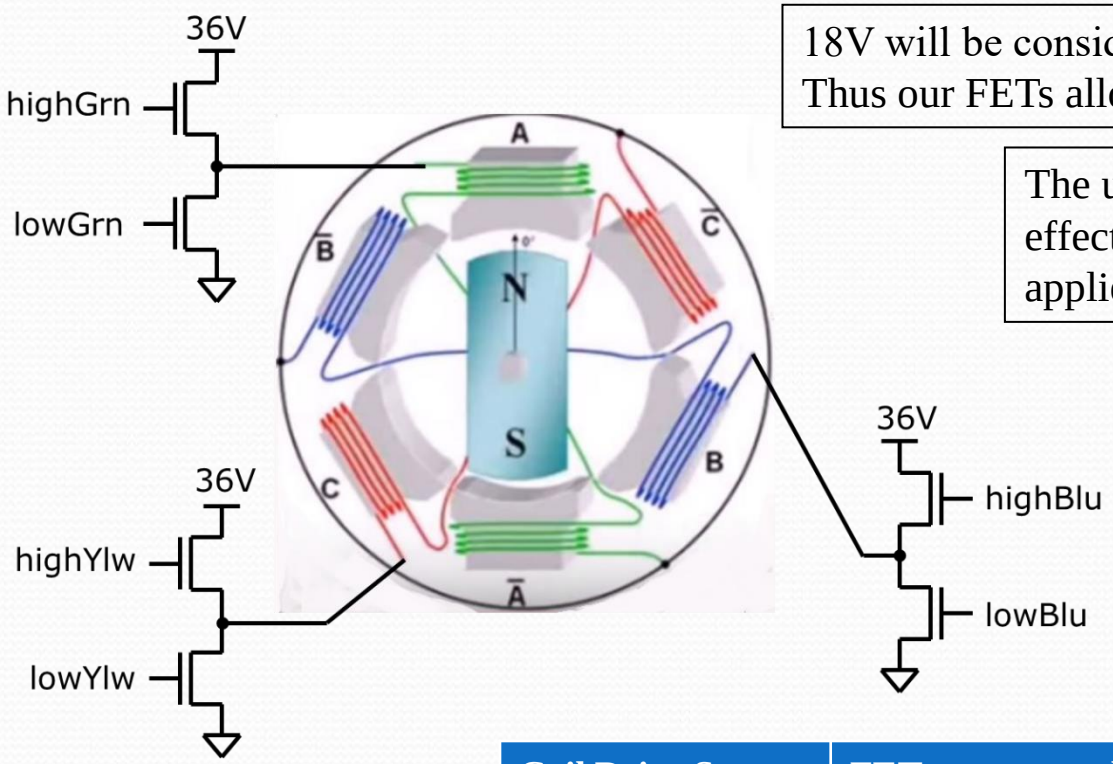
Exercise 12: brushless.sv

- You researched brushless motor drives in HW1…you might not remember much.
- We have 3 coil connections that we have to drive. We call them green, yellow, and blue. For any given coil we drive current in one direction, the opposite direction, or not at all.
- How do we know the proper coil drive at any given time? That is determined by the position of the rotor. We know the rotor position from the hall sensor inputs. Switching coil drive is referred to as commutation.



- **brushless.sv** will be the block that inspects the hall sensor signals and determines how to drive each coil. Each coil can be driven 1 of 4 ways: not driven (both high/low FETs off); driven "forward"; driven "reverse"; driven for dynamic braking (high FET off, low FET PWMed)
- Are the hall effect sensors synchronous to our clock domain? What does that mean?
- If you are curious why PWMing the lower FETs creates dynamic braking I think I can stumble through an explanation, but I suspect most of you lemmings will be content to just implement it as specified.

Exercise 12: brushless.sv



18V will be considered "virtual ground" for the coils. Thus our FETs allow us to drive positive or negative.

The use of PWM allows us to control the effective magnitude of the voltage applied across the coil. (hence speed/torque)

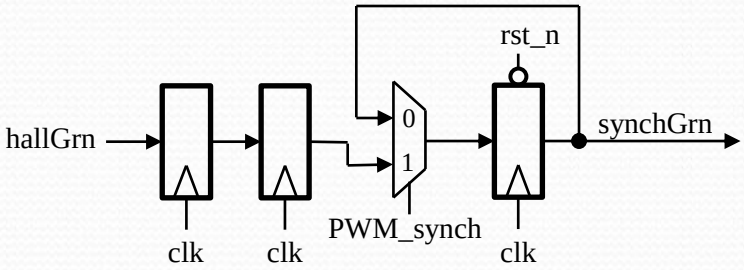
A PWM setting of 0x400 would be 50% and would represent driving a coil with 18V (virtual ground).

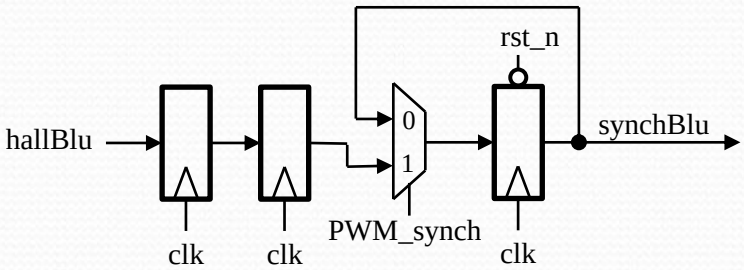
There are 4 possible states a coil can be driven. Regenerative braking is a special case indicated by **brake_n** signal being low.

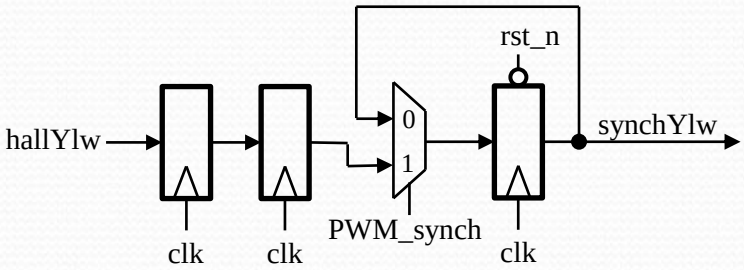
Coil Drive State:	FET gate controls:
Not driven (High Z)	Both high and low side low (both off)
Forward Current	High side driven with PWM, low side driven with ~PWM
Reverse Current	High side driven with ~PWM, low side driven with PWM
Regen Braking	High side low (off), low side driven with PWM

Exercise 12: brushless.sv (synchronizing & synchronizing)

- There are two forms of synchronizing we must concern ourselves with:
  - Synchronizing Hall sensors to our clock domain….ie. double flopping
  - Synchronizing commutation to our PWM cycle
    - Coil drive and commutation are determined by hall sensor readings
    - Hall sensor readings are not correlated to our PWM cycle *(change in hall sensor reading could happen anywhere in duty cycle of our PWM).*
    - We ideally would like to switch commutation when all power FETs are off *(i.e. when the PWM is in its deadtime)*
    - So we want to create a set of hall sensor signals that are correlated to our PWM. We will use the **PWM_synch** signal to synchronize hall readings to the PWM cycle *(and thus ensure commutation occurs during the deadband nonoverlap block enforces)*







The hall effect sensor wires are also green, yellow, & blue.

Infer the shown synchronizer circuits to form double synchronized signals for all 3 hall effect signals.

Exercise 12: brushless.sv (coil drive as function of hall readings)

Determining coil drive conditions from hall effect sensor readings:

- The hall effect sensors tell us the current position of the rotor
- The hall effect sensor wires are also green, yellow, and blue.
- Form a 3-bit vector:

  assign rotation_state = {synchGrn,synchYlw,synchBlu};

- The following table outlines how we drive the coils relative to **rotation_state**

rotation_state	3'b101	3'b100	3'b110	3'b010	3'b011	3'b001
coilGrn	for_curr	for_curr	High Z	rev_curr	rev_curr	High Z
coilYlw	rev_curr	High Z	for_curr	for_curr	High Z	rev_cur
coilBlu	High Z	rev_curr	rev_curr	High Z	for_curr	for_curr

- In the case of **brake_n == 1'b0** (braking) all coils are driven in the regenerative braking state with the high side FET off and the low side FET PWMing.

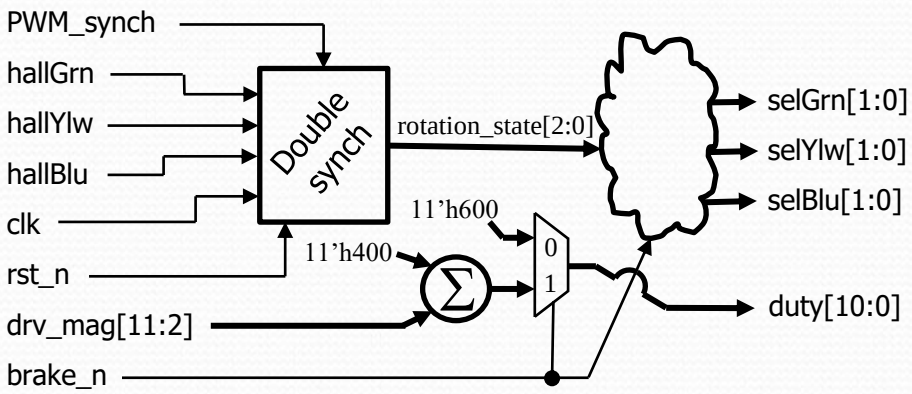
Exercise 12: brushless.sv

Signal:	Dir:	Description:
clk, rst_n	in	50MHz clock & asynch active low reset
drv_mag[11:0]	in	From PID control. How much motor assists (unsigned)
hallGrn,hallYlw, hallBlu	in	Raw hall effect sensors (asynch)
brake_n	in	If low activate regenerative braking at 75% duty cycle
PWM_synch	in	Used to synchronize hall reading with PWM cycle
duty[10:0]	out	Duty cycle to be used for PWM inside ***mtr_drv***. Should be 0x400+drv_mag[11:2] in normal operation and 0x600 if braking.
selGrn[1:0], selYlw[1:0], selBlu[1:0]	out	2-bit vectors directing how ***mtr_drv*** should drive the FETs. 00=>HIGH_Z, 01=>rev_curr, 10=>frwd_curr, 11=>regen braking

Code and test ***brushless.sv*** with the interface specified in this table.

Submit: **brushless.sv**.

In next exercise we will work on testing it.



Exercise 12: Testing brushless.sv

- If you are done coding **brushless.sv** and are thinking about how to test it consider this:
  - The outputs of **brushless.sv** feed the inputs to **mtr_drv.sv**.
  - We develop **mtr_drv.sv** in the next exercise (Exercise 13).
  - If you have time get started on Exercise 13 now.
  - When you are done with **mtr_drv.sv** implementation you can create a combined testbench **brushless_mtr_drv_tb.sv** that tests both units in combination.