Device ID	Vendor ID	00
Status register	Command register	01
Class code	Revision ID	02
BIST	Header Type	Latency Timer	Cache Size Line	03
Base address 0		04
Base address 1		05
Base address 2		06
Base address 3		07
Base address 4		08
Base address 5		09
CardBus CIS Pointer		10
Subsystem ID	Subsystem Vendor ID	11
Expansion ROM Base address		12
Reserved		13
Reserved		14
Max_Lat	Min_Gnt	Interrupt Pin	Interrupt Line	15

Required configuration registers