

- c) In terms of performance, RISC instructions sets are typically preferred over CISC instructions sets: compilers can produce better executable code for RISC and it should be easier to build a high frequency RISC processor. From the point of view of power consumption, how would you compare RISC and CISC instruction sets?
- d) Modern superscalar processors use speculative execution based on a number of prediction techniques (branch prediction, memory dependence prediction, etc). What is the likely impact of speculation on T, P, and E? Explain briefly.