

Fabian Pease, 1/29/01 1:59 PM -0800, Re: Quas Meeting Today!

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From: "Fabian Pease" <pease@cis.Stanford.EDU>  
To: "Diane Shankle" <shankle@ee.stanford.edu>  
Subject: Re: Quas Meeting Today!  
Date: Mon, 29 Jan 2001 13:59:21 -0800  
X-Priority: 3

Thanks Diane:

Here's my Question(s):

1. During the last 30 years digital circuitry has been increasingly doing tasks previously done with analog circuitry. Why?
2. List some of the parameters used to describe the technical performance of an analog-to-digital converter (ADC).
3. Outline how an ADC works. What happens when the 2 inputs to a comparator are the same?
4. What are some of the fundamental limits to ADC performance; for example does the Heisenberg uncertainty principle set a limit to the combination of high speed and high resolution? How might the various forms of noise limit speed and resolution?

Fabian

-----Original Message-----