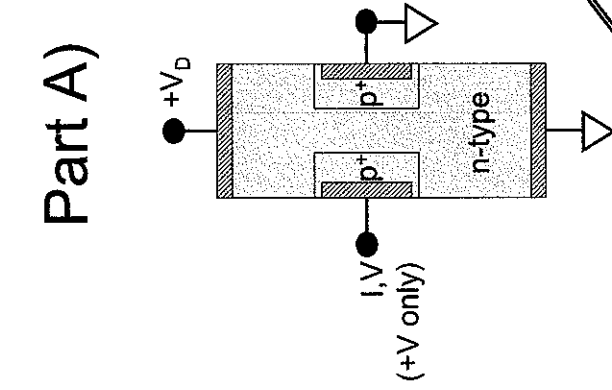


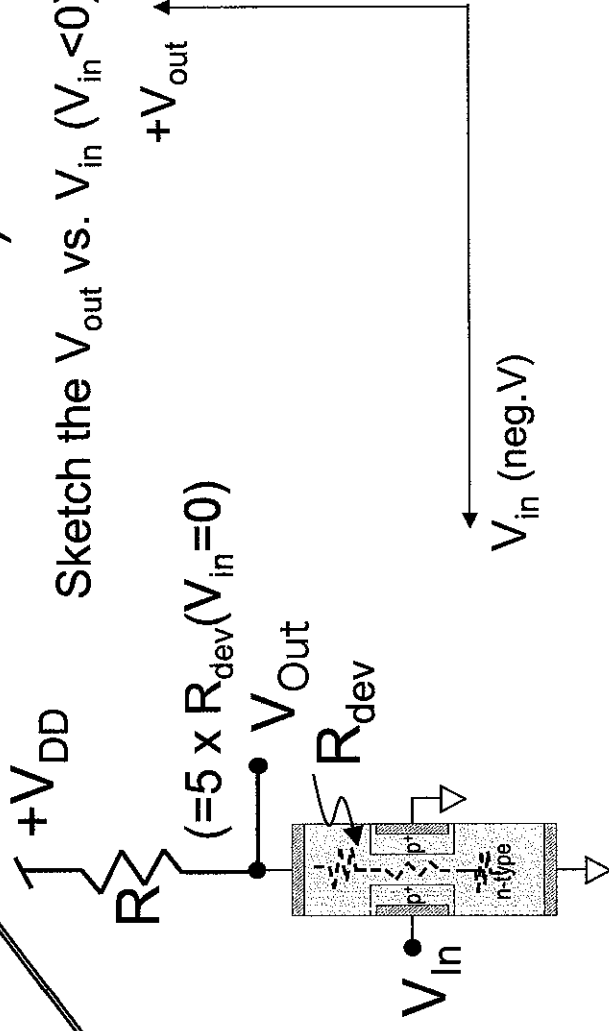
DUTION

Sketch the  $I_v$  vs.  $V_D$  for a fixed  $V_G > 0$



**Part B)**

Sketch the  $V_{out}$  vs.  $V_{in}$  ( $V_{in} < 0$ )



After discussion of Parts A) and B) there were follow-on questions (not included here) dealing with different terminal connections and bias conditions for Part A) and how to “optimized” the device performance in Part B)