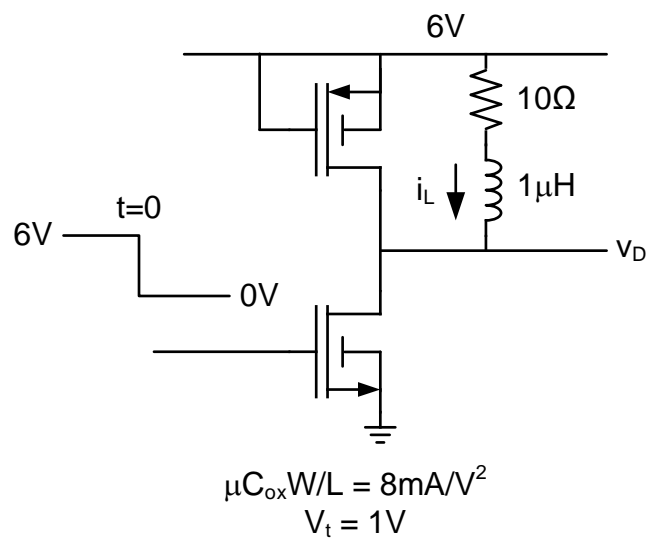


Name:

Stanford EE Quals 2016
Murmann

Sketch $i_L(t)$ and $v_D(t)$ versus time, starting at $t=0$. Ignore the capacitances of the MOSFETs and assume that they obey the ideal square law model. Assume that all transients before $t=0$ have settled.



The op-amps in the circuit below are ideal.

- Explain the function and operation of this circuit.
- Explain the why the designer sized $R_F/R_G = 4.167$ in this circuit.

