Perfect Branch Prediction vs. Perfect Data Cache

Assumptions

- · Single issue out-of-order processor, unlimited window size
- 10 cycle branch delay
- · Single cycle access, nonblocking cache, 1 word block size
- Memory accesses have 10% miss rate
- 100 cycle main memory access
- No structural hazards

```
increment X index
                                                                                                                                                                increment Y index
                                           initialize $83
                                                                                      50% mispredict
                                                                                                                                                                                             0% mispredict
                                                                                                                                                                              test if done
                                                                     X(i) == 0.0
                                                                                                                                   store Y(i)
              Y(i) = (X(i) == 0.0) ? X(i) : Y(i-1);
                                                                                                                     Y(i-1)
                                        addiu $s3, $s0, #400000
lw $f2, 0 ($s0)
c.eq.s$f0, $f2
for (i=0; i < 100,000; i++)
                                                                                                                                                            $s1, #4
                                                                                                                                                $80, $80, #4
                                                                                                                    $f2, -4($s1)
                                                                                                                                  $f2, 0($s1)
                                                                                                                                                                                             loop
                                                                                                                                                                $s1,
                                                                                     yi-1
                                                                                                                     yi-1: lw
```

Which performs better perfect branch prediction or perfect cache?