Robert Dutton
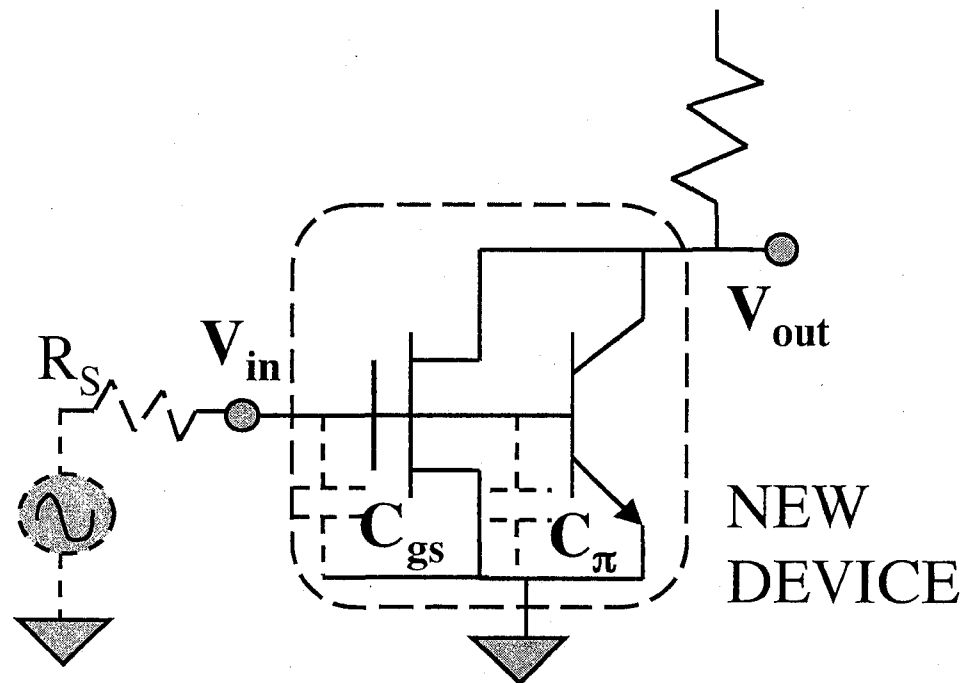
A device technologist has created a NEW DEVICE--part MOS, part BJT--with "drive current" given by: $I_{Drive}=I_D(MOS)+I_C(BJT)$. The schematic is as follows.

To achieve maximum small-signal (ac)voltage gain, at what operating point should we bias the NEW DEVICE? What is the ac gain and bandwidth?