

Date: Wed, 01 Mar 2006 22:45:39 -0800
To: Diane Shankle <shankle@ee.Stanford.EDU>
From: Simon Wong <wong@ee.Stanford.EDU>
Subject: Re: Reminder Quals Question 2006

Simon Wong,

Given the layout of a circuit, identify each layer, the transistor, connection and determine the circuit.

Answer : first circuit is a CMOS inverter, 2nd circuit is a CMOS cascode amplifier

At 08:58 AM 3/1/2006, you wrote:

Hi,

Reminder!

Please send me your Quals question.

Thanks and Enjoy Your Day,
Diane