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SESSION ID: PDSC-M02

Validating the Integrity of Computing Devices



MODERATOR: Nakia Grayson

IT Security Specialist

NIST/NCCoE

PANELISTS: Themistocles Chronis

Principal Consultant

Archer

Tom Dodson

Supply Chain Security Architect Intel Corporation

John Loucaides

SVP of Strategy Eclypsium

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Panel Group



Moderator



Nakia Grayson NIST IT Security Specialist

Panelist



Themistocles Chronis
Archer
Principal Consultant

Panelist



Tom Dodson
Intel
Supply Chain Security
Architect

Panelist



John Loucaides
Eclypsium
SVP of Strategy

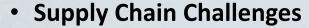






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Panel Discussion Topics



Supply Chain Security Goes Digital (Intel)

Supply Chain Threats and Risk Management

Risk at the Firmware Layer (Eclypsium Blog)

Framework for Improving Critical Infrastructure Cybersecurity, Version 1.1

Cybersecurity Supply Chain Risk Management Practices for Systems and Organizations

Industry Cyber Supply Chain Risk Management (C-SCRM) Collaborative Efforts

NIST Cybersecurity Supply Chain Risk Management Fact Sheet

Post-DETECT Remediation

Key Practices in Cyber Supply Chain Risk Management: Observations from Industry

NIST's NCCoE Supply Chain Assurance Project

NIST's NCCoE SP 1800-34, Validating the Integrity of Computing Devices Project Page



Call to Action/Key Takeaways



- Review NIST SP 1800-34 Volumes A, B, C based on your role in your organization
 - Provide feedback on Draft Practice Guide starting June 23rd until July 25th
 - Send project ideas to NCCoE Supply Chain Assurance Team at <u>supplychain-nccoe@nist.gov</u>
- Implement tools and technologies identified in NIST SP 1800-34
- Implement ongoing or continuous monitoring for an organization's devices
- Encourage your organization to work with original equipment manufacturers (OEMs) to ensure devices and components are genuine and haven't been tampered with
- Check out the Cybersecurity Supply Chain Risk Management Program site for additional information on other NIST work on C-SCRM
 - https://csrc.nist.gov/Projects/cyber-supply-chain-risk-management
 - National Initiative for Improving Cybersecurity in Supply Chains (NIICS)

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