HANDLING ADVANCED THREATS

SANS 2020 ONLINE SUMMIT



by Alexandre Borges



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- Speaker at DEF CON USA 2019
- Speaker at DEF CON USA 2018
- Speaker at DEF CON CHINA 2019
- Speaker at DC2711 (Johannesburg)
- Speaker at NO HAT 2019 (Italy)
- Speaker at HITB 2019 (Amsterdam)
- Speaker at CONFidence 2019 (Poland)
- Speaker at DevOpsDays BH 2019
- Speaker at BSIDES 2019/2018/2017/2016
- Speaker at H2HC 2016/2015
- Speaker at BHACK 2019/2018
- Researcher on Android/iOS Reversing,
 Rootkits and Digital Forensics.
- Referee on Digital Investigation: The International Journal of Digital

Forensics & Incident Response
HTTP://WWW.BLACKSTORMSECURITY.COM

Agenda:

- Introduction
- Reversing
- Anti-Reversing
- De-obfuscation

☐ Last conferences as speaker:

- BHACK 2019 (Belo Horizonte/Brazil)
- DC2711 (Johannesburg/South Africa)
- NO HAT Conference 2019 (Bergamo/Italy)
- DEF CON USA 2019 (Las Vegas / USA)
- CONFidence Conference 2019 (Krakow / Poland)
- DEF CON China 2019 (Beijing / China)
- HITB Security Conference 2019 (Amsterdam)
- BSIDES Sao Paulo 2019 (Sao Paulo / Brazil)
- DEF CON USA 2018 (Las Vegas / USA)
- Malwoverview Tool: https://github.com/alexandreborges/malwoverview

- During the reverse engineering of a malware sample, we need to understand few aspects on the threat:
 - Is the malware packed?
 - Are DLLs and functions resolved dynamically?
 - Are strings encrypted?
 - Are there any anti-forensic techniques such as anti-vm, antidebugging or anti-disassembly?
 - Is there any obfuscation technique being used?
- Unpacking malware is usually easy, but you might find sophisticated packers...

- Advanced threats are different from any daily malware because:
 - They don't use common packers.
 - Most of the time, they bring malicious device drives (rootkits).
 - Sometimes they try to compromise the platform (bootkits)
 - They can use 0-days to exploit the infrastructure and systems.
 - They bypass most of defenses and run under the radar.
 - C2 transmits beacons and data once per week with short duration.
 - They might implement tricks to prevent any memory acquisition.
 - Most certainly, there'll be anti-forensic techniques.
 - It's hard and take so much time to reverse it.
- lacktriangle If you have luck, so you'll have the opportunity to analyze them. oxines



- Most packed binaries can be unpacked using debuggers, breakpoints and dumping unpacked content from memory.
- Even when a binary uses customized packing techniques, it is still possible:
 - dumping the unpacked code from memory using Volatility.
 - fixing the ImageAddress field using few lines in Python its respective IAT using impscan plugin to analyze it in IDA Pro:
 - python vol.py -f memory.vmem procdump -p 2096 -D . --memory (to keep slack space)
 - python vol.py -f memory.vmem impscan --output=idc -p 2096

REVERSING



's'	.rdata:0040	0000000E	C (1	(null)
's'	.rdata:0040	00000007	С	(null)
's'	.rdata:0040	00000005	С	('8PW
's'	.rdata:0040	00000005	С	700PP
's'	.rdata:0040	00000012	С	```hhh\b\b\axppwpp\b\b
's'	.rdata:0040	0000000F	С	bad allocation
's'	.rdata:0040	00000011	С	=ÂUf>pëÂ?Mš\"\x1B_0\t_
's'	.rdata:0040	00000005	С	Úzz\\-
's'	.rdata:0040	00000006	С	ËCÁ!dÍ
's'	.rdata:0040	00000006	С	=ºUÈ;É
's'	.rdata:0040	00000005	С	Î/~ØÕ
's'	.rdata:0040	00000007	С	ãØòšÄ&\b
's'	.rdata:0040	00000007	С	Žf(ØÀPý
's'	.rdata:0040	00000009	С	a}A.{D ;Ì
's'	.rdata:0040	00000009	С	1ë!Fà\\î\x1BÜ
S'	.rdata:0040	00000005	С	@à_Ãç
's'	.rdata:0040	00000005	С	Ë6ÓàY
's'	.rdata:0040	0000000C	С	Qìë.ôóÏËSS%{
's'	.rdata:0040	00000007	С	Ù2K\v\$/Ø
's'	.rdata:0040	0000000A	С	!À7iD~(!9q
's'	.rdata:0040	0000000E	С	Nœ(ºW∼\n8YYhùœç
's'	.rdata:0040	80000000	С	uèEÏ Ïøå
's'	.rdata:0040	00000007	С	tî\aåIµV
's'	.rdata:0040	00000005	С	}pl#Y
's'	.rdata:0040	00000006	С	J[Ïf ã
's'	.rdata:0040	00000005	С	Y;sVĐ

- As we've mentioned previously, strings are one of first references.
- ❖ However, they are all encrypted and writing YARA rules using them could not be so interesting ☺

```
push
                                       ebp
                                       ebp, esp
                               mov
                                                                         Setup an exception framework
.text:00408B93
                               push
                                       OFFFFFFFFh
                               push
                                       offset SEH 408B90
text:00408B95
                               mov
                                       eax, large fs:0
                                                          Remember:
text:00408B9A
                                                          FS --> TEB --> TIB --> SEH
text:00408B9A
                                                          push Exception Handler (0xFFFFFFFF to end of handler list)
                                                        ; push next record
                                                        ; mov fs:[0], esp
                               push
                                       eax
                                            110h
text:00408BA1
                               sub
                                       esp,
text:00408BA7
                                               security cookie
                               mov
                                       eax,
                                       eax, ebp
                               xor
text:00408BAE
                                       [ebp+var 14], eax
                               mov
                               push
text:00408BB1
                                       ebx
                                       esi
                               push
                                       edi
                               push
text:00408BB3
                               push
text:00408BB4
                                       eax
text:00408BB5
                               lea
                                       eax, [ebp+var C]
                                       large fs:0, eax
                               mov
                                                                                    DLLs names seem to be "obfuscated" ©
                                       [ebp+var 10], esp
                               mov
text:00408BC1
                               push
                                       256
                                                        ; Size
                                       eax, [ebp+Dst]
                               lea
                               push
text:00408BCC
                                                        ; Val
text:00408BCE
                               push
                                                        ; Dst
                                       eax
                               call
                                                        ; void *memset(void *dest, int c, size t count);
text:00408BCF
                                       memset
text:00408BCF
                                                        ; Sets buffers to a specified character.
                               add
                                       esp, OCh
text:00408BD4
                               call
                                       ab resolve function addresses
text:00408BDC
                               test
                                       eax, eax
```

```
loc 408D35
                              jΖ
                              lea
                                       eax, [ebp+Dst]
text:00408BE4
                                       sub 408770
                               call
                                                                                          Old anti-debugger tricks...
                                       sub 409340
                              call.
                                       eax, eax
text:00408BF4
                               test
text:00408BF6
                              jnz
                                       loc 408D35
                                                        ; DWORD
text:00408BFC
                              push
                               call
                                       dword 4DA870
text:00408BFE
                                       edi, ds:SetUnhandledExceptionFilter; Enables an application to supersede the top-level
                              mov
                                                         exception handler of each thread of a process.
text:00408C04
                                                         After calling this function, if an exception occurs
                                                         in a process that is not being debugged, and the
text:00408C04
                                                         exception makes it to the unhandled exception filter,
text:00408C04
                                                         that filter will call the exception filter function
text:00408C04
                                                         specified by the lpTopLevelExceptionFilter parameter
text:00408C04
text:00408C04
                                                         LPTOP LEVEL EXCEPTION FILTER SetUnhandledExceptionFilter(
text:00408C04
                                                           LPTOP LEVEL EXCEPTION FILTER lpTopLevelExceptionFilter
text:00408C04
text:00408C04
                                       offset sub 408760 ; lpTopLevelExceptionFilter
text:00408C0A
                              push
                                       edi ; SetUnhandledExceptionFilter
                               call
                               call
                                       sub 409AB0
                               test
                                       al, al
text:00408C18
                              jΖ
                                       short loc 408C32
text:00408C1A
                                       sub 409B50
                               call
text:00408C1F
                               test
                                       al, al
                                       short loc 408C32
text:00408C21
                              jnz
                                       sub 4095C0
                               call
text:00408C28
                                       eax, 1
                              mov
                                       loc 408D37
text:00408C2D
                              jmp
```

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INTRODUCTION

```
text:00402D60 ab resolve function addresses proc near ; CODE XREF: ab possible decode fn 1+47 p
text:00402D60
                               = dword ptr -4
text:00402D60 var 4
text:00402D60
text:00402D60
                                       ebp
                               push
                                                                  DLL name resolution being executed before
text:00402D61
                                       ebp,
                                            esp
                               mov
text:00402D63
                               push
                                        ecx
                                                                  resolving function addresses.
text:00402D64
                                       ebx
                               push
                                       edi
                               push
                                       eax, eax
                               xor
text:00402D68
                                        [ebp+var 4], 0
                               mov
                               call
                                        ab decode dll names
text:00402D6F
                                        ebx, ds:GetModuleHandleA
text:00402D74
                               mov
text:00402D7A
                                                         ; lpModuleName GetModuleHandleA
                               push
                               call
                                              GetModuleHandleA
text:00402D7B
                                        ebx
text:00402D7D
                               mov
                                        edi, eax
text:00402D7F
                               test
                                       edi, edi
text:00402D81
                               İΖ
                                       loc 403400
text:00402D87
                                       esi
                               push
                               mov
                                        eax,
                                        ab decode dll names
text:00402D8D
                               call
text:00402D92
                                            ds:GetProcAddress
                               mov
text:00402D98
                               push
                                        eax
                                                         ; lpProcName GetProcAddress
                                                         ; hModule GetProcAddress
text:00402D99
                               push
                                        edi
text:00402D9A
                               call
                                       esi ; GetProcAddress
text:00402D9C
                                        dword 4DA824, eax
                               mov
text:00402DA1
                               test
                                        eax, eax
text:00402DA3
                                        loc 4033FF
                               jΖ
```



```
text:00405380
text:00405380 ab decode dll names proc near
                                                         ; CODE XREF: ab resolve function addresses+Ffp
                                                          ab resolve function addresses+2D↑p ...
text:00405380
                                        ds:ab dll resolver switch cases[eax*4]
text:00405380
                               jmp
                                                                                                      → Jump table
text:00405387
text:00405387
text:00405387 loc 405387:
                                                         ; CODE XREF: ab decode dll names † j
text:00405387
                                                          DATA XREF: .text:ab dll resolver switch cases to
text:00405387
                                        edx, edx
                               xor
                                       var 2, edx
text:00405389
                               cmp
text:0040538F
                               jnz
                                        short loc 405405
                                       var 4, 204C2044h
text:00405391
                               mov
                                       var 1, 50EA7350h
text:0040539B
                               mov
                                       var 3, 6A586B07h
                                                                    → DLL name in obfuscated representation.
text:004053A5
                               mov
                                       var 2, 620C7E1Bh
text:004053AF
                               mov
text:004053B9
                                       var 5, 17E21920h
                               mov
text:004053C3
                                        eax, 13h
                               mov
text:004053C8
                                        short loc 4053D0
                               jmp
text:004053C8
                               align 10h
text:004053CA
text:004053D0
                                                          CODE XREF: ab decode dll names+48<sup>†</sup>
text:004053D0 loc 4053D0:
                                                          ab decode dll names+5Djj
text:004053D0
text:004053D0
                                       cl, [eax+4DAF43h]
                               mov
                                        [eax+4DAF44h], cl
text:004053D6
                               xor
text:004053DC
                               dec
                                        eax
                                                                      Obfuscation procedure takes two slides
                                        short loc 4053D0
text:004053DD
                               jnz
text:004053DF
                                        ecx, ecx
                               xor
text:004053E1
```

```
; CODE XREF: ab decode dll names+83.j
text:004053E1 loc 4053E1:
text:004053E1
                                         eax, 4
                                 cmp
text:004053E4
                                 jb
                                         short loc 4053F9
                                         [eax+4DAF44h], dl
text:004053E6
                                 cmp
text:004053EC
                                         short loc 4053F5
                                 jnz
text:004053EE
                                         ecx, 1
                                 mov
text:004053F3
                                         short loc 4053F9
                                 jmp
text:004053F5
text:004053F5
text:004053F5 loc 4053F5:
                                                           ; CODE XREF: ab decode dll names+6C<sub>↑</sub>j
text:004053F5
                                         ecx, edx
                                 cmp
                                         short loc 4053FF
text:004053F7
                                 jΖ
text:004053F9
text:004053F9 loc 4053F9:
                                                           ; CODE XREF: ab decode dll names+64<sup>†</sup>
                                                            ; ab decode dll names+73<sub>1</sub>j
text:004053F9
text:004053F9
                                         byte ptr var 1[eax], dl
                                 mov
text:004053FF
                                                           ; CODE XREF: ab decode dll names+77 † j
text:004053FF loc 4053FF:
text:004053FF
                                 inc
                                          eax
text:00405400
                                         eax, 14h
                                 cmp
text:00405403
                                         short loc 4053E1
                                 jb
text:00405405
text:00405405 loc 405405:
                                                           ; CODE XREF: ab decode dll names+F<sub>↑</sub>j
text:00405405
                                         eax, 4DAF48h
                                 mov
text:0040540A
                                 retn
```

```
text:0040677C ab dll resolver switch cases dd offset loc 405387
text:0040677C
                                                      ; DATA XREF: ab decode dll names r
text:00406780
                             dd offset loc 40540B
                             dd offset loc 40548B
text:00406784
text:00406788
                                offset loc 40550B
text:0040678C
                             dd offset loc 40558B
text:00406790
                             dd offset loc 40560B
                                                                Each offset takes us to a different
text:00406794
                              dd offset loc 4056AB
text:00406798
                                offset loc 40573B
                                                                   switch case, which is a different
text:0040679C
                              dd offset loc 4057CB
                                                                   text:004067A0
                             dd offset loc 40585B
                                offset loc 4058FB
text:004067A4
                                offset loc 40599B
text:004067A8
text:004067AC
                                offset loc 405A2B
text:004067B0
                             dd offset loc 405ABB
                                offset loc 405B4B
text:004067B4
text:004067B8
                                offset loc 405BCB
text:004067BC
                                offset loc 405C3D
text:004067C0
                                offset loc 405CBB
text:004067C4
                                offset loc 405D4B
text:004067C8
                                offset loc 405DCB
text:004067CC
                                offset loc 405E4B
text:004067D0
                                offset loc 405ECB
text:004067D4
                                 offset loc 405F4B
text:004067D8
                              dd offset loc 405FCB
```



```
from binascii import *
     var_1 = ['50', '73', 'EA', '50']
     var_2 = ['1B', '7E', '0C', '62']
     var_3 = ['07', '6B', '58', '6A']
     var_4 = ['44', '20', '4C', '20']
     var 5 = ['20', '19', 'E2', '17']
     mylist = var 1 + var 2 + var 3 + var 4 + var 5
10
     def mydecrypt(hexdata):
         max = len(hexdata) - 1
12
13
         counter = max
         output = ""
14
15 ▼
        while(True):
             hexdata[counter] = ord(unhexlify(hexdata[counter])) ^ ord(unhexlify(hexdata[counter - 1]))
16
17
             counter -= 1
            if counter == 0:
18
                 break
19
20
         return hexdata
21
     final = mydecrypt(mylist)
23
     for x in range(0,4):
         final[x] = 0
24
     final1 = ''.join([chr(w) for w in final])
                                                                                         output: kernel32.dll
     print("The output is %s" % final1)
```

- Of course, we could try to improve and automatize the de-obfuscation of all functions names by using:
 - IDA Python: using IDA Python you can de-obfuscated function names and save them into the idb.
 - IDC: it's a bit more complicated, but very powerful.
- If your time is short, so you could try emulation tools such as Floss (https://github.com/fireeye/flare-floss) to decode possible obfuscated strings and create an IDA script to decorate the reversed code:
 - floss --no-static-strings -x malware.bin --ida=floss_ida.py

0x4DB0E4

0x402F5E

Module32Next

```
E:\malware samples>floss --no-static-strings -x malware.bin --ida=floss ida.py
. . .
Decoding function at 0x405380 (decoded 38 strings)
         Called At
Offset
                       String
                       Kernel32.dll
0x4DAF48
         0x402D6F
                       CloseHandle
0x4DAF5C 0x402D8D
0x4DAF70
         0x402DAE
                       CreateFileA
0x4DAF84
         0x402DC9
                       CreateMutexA
                       CreateToolhelp32Snapshot
0x4DAFAC
         0x402DE4
         0x402DFF
                       DeviceIoControl
0x4DAFCC
                       GetCurrentThread
0x4DAFE4 0x402E1A
         0x402E35
                       GetLongPathNameA
0x4DAFFC
                       GetModuleFileNameA
0x4DB014
         0x402E50
0x4DB030
         0x402E6B
                       GetNativeSystemInfo
0x4DB04C 0x402E86
                       GetProcessHeap
                       GetSystemInfo
0x4DB064
         0x402EA1
0x4DB07C
         0x402EBC
                       GetThreadContext
                       HeapAlloc
0x4DB094
         0x402ED7
0x4DB0A8
         0x402EF2
                       HeapFree
0x4DAF98
         0x402F0D
                       HeapReAlloc
                       IsBadReadPtr
0x4DB0B8
         0x402F28
                       Module32First
0x4DB0CC
         0x402F43
```

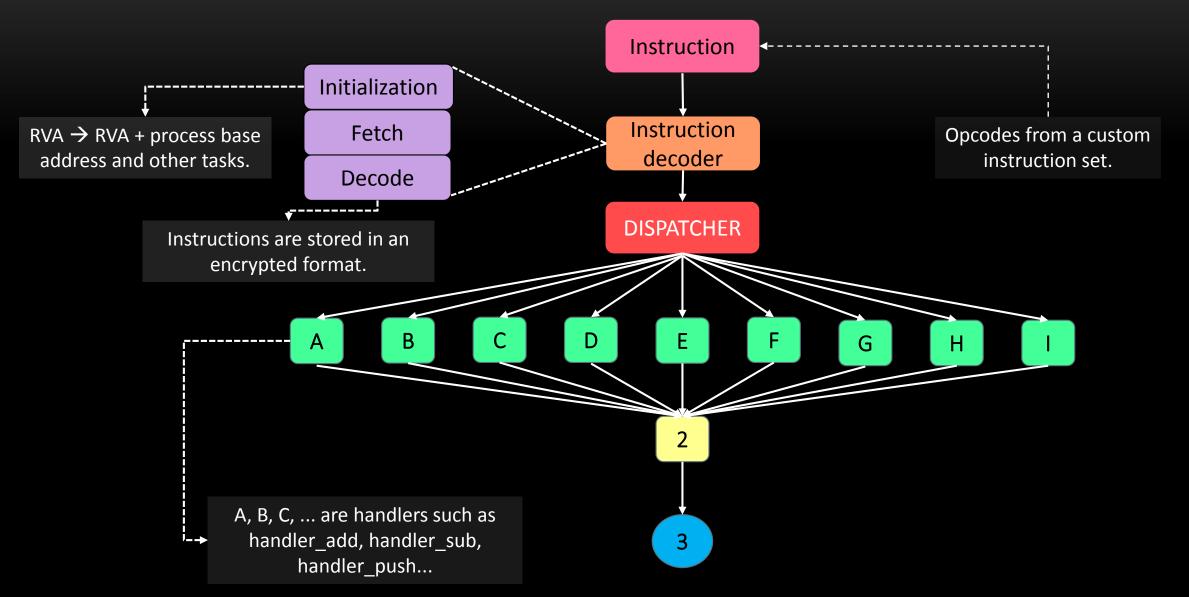


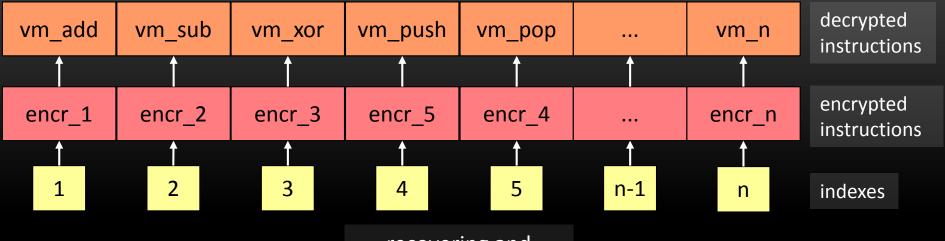
```
text:00405380 ab decode dll names proc near
                                                         ; CODE XREF: ab resolve function addresses+F<sub>↑</sub>p
                                                         ; ab resolve function addresses+2Dpp ...
text:00405380
                                        ds:ab dll resolver switch cases[eax*4]
text:00405380
text:00405387
text:00405387
text:00405387 loc 405387:
                                                         ; CODE XREF: ab decode dll names † j
                                                         ; DATA XREF: .text:ab dll resolver switch cases to
text:00405387
text:00405387
                                        edx, edx
                               xor
                                                          FLOSS: Kernel32.dll
text:00405389
                                        var 2, edx
                               cmp
                                        short loc 405405
text:0040538F
                               jnz
                                        var 4, 204C2044h
text:00405391
                               mov
                                                                                     Same result, of course. ©
text:0040539B
                                        var 1, 50EA7350h
                               mov
text:004053A5
                                        var 3, 6A586B07h
                               mov
                                                            FLOSS: Kernel32.dll
text:004053AF
                                        var 2, 620C7E1Bh
                               mov
                                       var 5, 17E21920h
text:004053B9
                               mov
                                        eax, 13h
text:004053C3
                               mov
text:004053C8
                                        short loc 4053D0
                               jmp
text:004053C8
                               align 10h
text:004053CA
text:004053D0
text:004053D0 loc 4053D0:
                                                         ; CODE XREF: ab decode dll names+48 j
text:004053D0
                                                          ab decode dll names+5D↓j
                                        cl, [eax+4DAF43h]
text:004053D0
                               mov
text:004053D6
                                        [eax+4DAF44h], cl
                               xor
text:004053DC
                               dec
                                        eax
text:004053DD
                               jnz
                                        short loc 4053D0
text:004053DF
                               xor
                                        ecx, ecx
```

- Advanced threats don't use standard tricks and there're many lots of facts about them:
 - They use similar techniques from modern packers such as Themida,
 Arxan, Agile .NET, Tigress, Obfuscator-LLVM and so on.
 - Most of them are focused on 64-bit code.
 - Obviously, almost all functions are removed from IAT. Remember that Themida packer usually keeps only TlsSetValue().
 - String encryption is also a common technique.

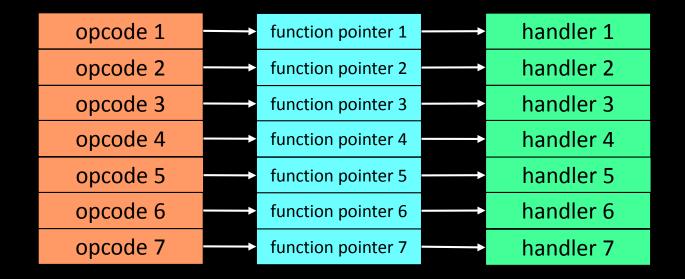
- Advanced threats are concerned to integrity:
 - They protect and check the memory integrity.
 - Thus, it is not possible to dump a clean executable from the memory (using Volatility, for example) because original instructions are not completely decoded in the memory.
 - There could be checksum functions verifing the integrity of the code itself. Therefore, any attempt of changing the code may break it up.
 - Additionally, advanced threats may use watermark to control the "ownership". This is the same technique used to program copyright.

- Many additional techniques are used:
 - There are also fake push instructions.
 - There are many dead and useless pieces of code.
 - There is some code reordering using unconditional jumps.
 - All obfuscators use code flattening.
 - Packers have few anti-debugger and anti-vm tricks. Weird anti-vm methods based on temperature, for example.





recovering and decrypting functions



function pointer table (likely encrypted)



- Constant unfolding: technique used by obfuscators to replace a contant by a bunch of code that produces the same resulting constant's value.
- Pattern-based obfuscation: exchange of one instruction by a set of equivalent instructions.
- Abusing inline functions.
- Anti-VM techniques: prevents the malware sample to run inside a VM.
- Dead (garbage) code: this technique is implemented by inserting codes whose results will be overwritten in next lines of code or, worse, they won't be used anymore.



- Code duplication: different paths coming into the same destination (used by virtualization obfuscators).
- Control indirection 1: call instruction \rightarrow stack pointer update \rightarrow return skipping some junk code after the call instruction (RET x).
- Control indirection 2: malware trigger an exception → registered exception is called → new branch of instructions.
- Anti-debugging: used as irritating techniques to slow the process analysis.

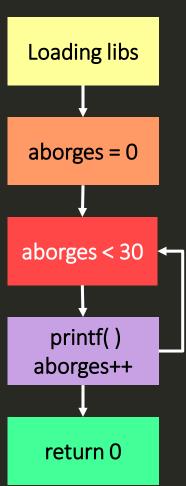
• Opaque predicate: Although apparently there is an evaluation (conditional jump: jz/jnz), the result is always evaluated to true (or false), which means an unconditional jump. Thus, there is a dead branch.

 Polymorphism: it is produced by self-modification code (like shellcodes) and by encrypting resources (similar most malware samples).





```
1 #include <stdio.h>
                                                             Loading libs
     int main (void)
                                                             aborges = 0
  6
          int aborges = 0;
          while (aborges < 30)</pre>
  8
                                                             aborges < 30
  9
               printf("%d\n", aborges);
10
               aborges++;
                                                               printf()
11
                                                              aborges++
12
13
          return 0;
                                                              return 0
14
 15
 16
```



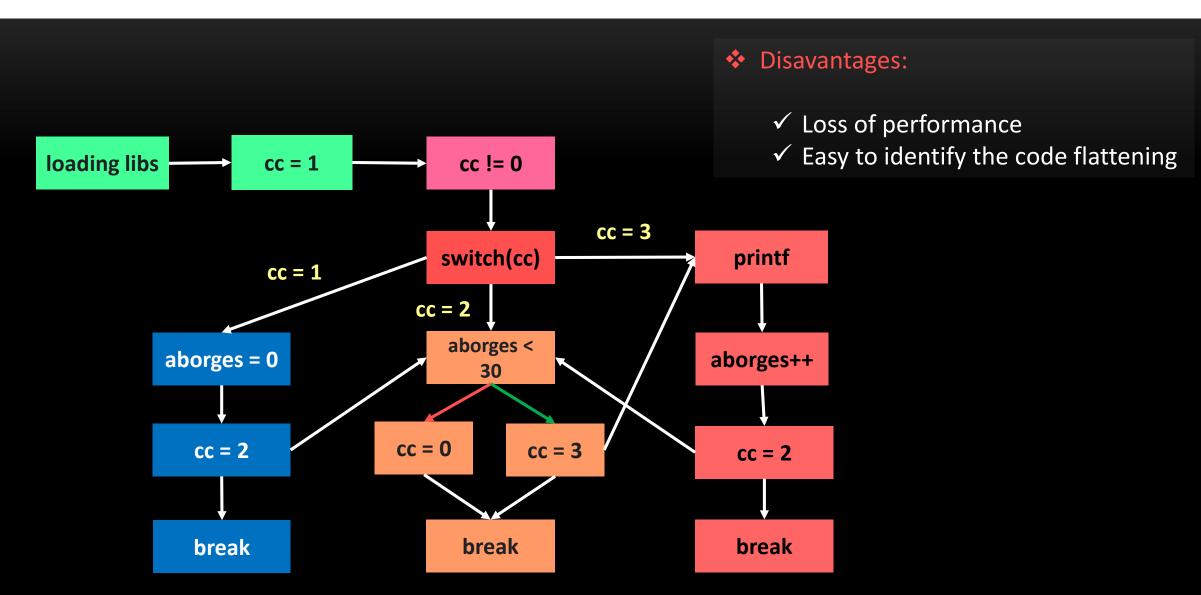
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```
; int cdecl main(int argc, const char **argv, const char **envp)
public main
main proc near
var 4= dword ptr -4
    unwind {
        rbp
push
        rbp, rsp
mov
sub
        rsp, 10h
        [rbp+var 4], 0
mov
        short loc 1160
jmp
                     <u></u>
```

Original Program

```
loc 1160:
                          [rbp+var 4], 1Dh
                  cmp
                  jle
                          short loc 1146
<u></u>
                                         eax, 0
                                 mov
loc 1146:
                                 leave
                                 retn
        eax, [rbp+var 4]
mov
        esi, eax
                                 ; } // starts at 1135
mov
        rdi, format
                                 main endp
lea
        eax, 0
mov
call
        printf
add
        [rbp+var 4], 1
```







- The obfuscator-llvm is an excellent project to be used for code obsfuscation. To install it, it is recommended to add a swap file first (because the linkage stage):
 - fallocate -I 8GB /swapfile; chmod 600 /swapfile
 - mkswap /swapfile; swapon /swapfile; swapon --show
 - apt-get install llvm-4.0
 - apt-get install gcc-multilib (install gcc lib support to 32 bit)
 - git clone -b llvm-4.0 https://github.com/obfuscator-llvm/obfuscator.git
 - mkdir build; cd build/
 - cmake -DCMAKE_BUILD_TYPE=Release -DLLVM_INCLUDE_TESTS=OFF ../obfuscator/
 - make -j7



Possible usages:

- ./build/bin/clang alexborges.c -o alexborges -mllvm -fla
- ./build/bin/clang alexborges.c -m32 -o alexborges -mllvm -fla
- ./build/bin/clang alexborges.c -o alexborges -mllvm -fla -mllvm -sub
- Where:
 - fla: Control Flow Flattening
 - sub: Instruction Substitution
 - bcf: Opaque Predicate



- A better option would be using Tigress.
- Download Tigress binary from https://tigress.wtf/download.html
- Install Tigress is pretty easy:
 - unzip tigress-3.1-bin.zip
 - Export the TIGRESS_HOME environment variable:
 - export TIGRESS HOME=/root/Downloads/tigress/3.1
 - Add the Tigress installation directory to the PATH variable:
 - export PATH=\$PATH:/root/Downloads/tigress/3.1



```
#include <stdio.h>
   #include "/root/Downloads/tigress/3.1/tigress.h"
3
   int main (void)
6
       int aborges = 0;
       while (aborges < 30)</pre>
10
            printf("%d\n", aborges);
            aborges++;
        return 0;
```



- To transform a C source using Tigress and, afterward, compile it:
 - tigress --Environment=x86_64:Linux:Gcc:4.6 --Transform=Flatten --Functions=main --out=aleborges_obfuscated.c aleborges_trigess.c
- There're many notes about the command above:
 - We should pick up one or more functions to be transformed. Of course, I've chosen the main() only for educational purposes.
 - The argument for Environment must be according to your environment (x86_64:Linux:Gcc:4.6, x86_64:Darwin:Clang:5.1, armv7:Linux:Gcc:4.6, armv8:Linux:Gcc:4.6)



ANTI-REVERSING

```
int main(int formal argc , char ** formal_argv , char ** formal_envp )
77
78
      int aborges ;
      int BARRIER 0 ;
79
      unsigned long 1 main next;
80
81
82
83
      megaInit();
84
     global argc = formal argc;
85
      global argv = formal argv;
                                                Program obfuscated with Tigress
      global envp = formal envp;
86
       BARRIER 0 = 1;
87
88
89
       1 \text{ main next} = 2UL;
90
      while (1) {
91
92
        switch ( 1 main next) {
93
        case 4: ;
94
        return (0);
95
        break;
96
        case 3:
        printf((char const */* restrict */)"%d\n", aborges);
```



ANTI-REVERSING

```
98
         aborges ++;
99
100
           1 main next = 0UL;
101
102
         break;
103
         case 0: ;
104 ▼
         if (aborges < 30) {
105
106
             1 main next = 3UL;
107
108 *
         } else {
109
110
             1 main next = 4UL;
111
112
113
         break;
114
         case 2:
115
         aborges = 0;
116
117
           1 main next = OUL;
118
119
         break;
120
121
122
123
124
     void megaInit(void)
125 ▼ {
```

Program obfuscated with Tigress (remaining part)

NDRE BORGES – MALWARE AND SECURITY RESEARCE

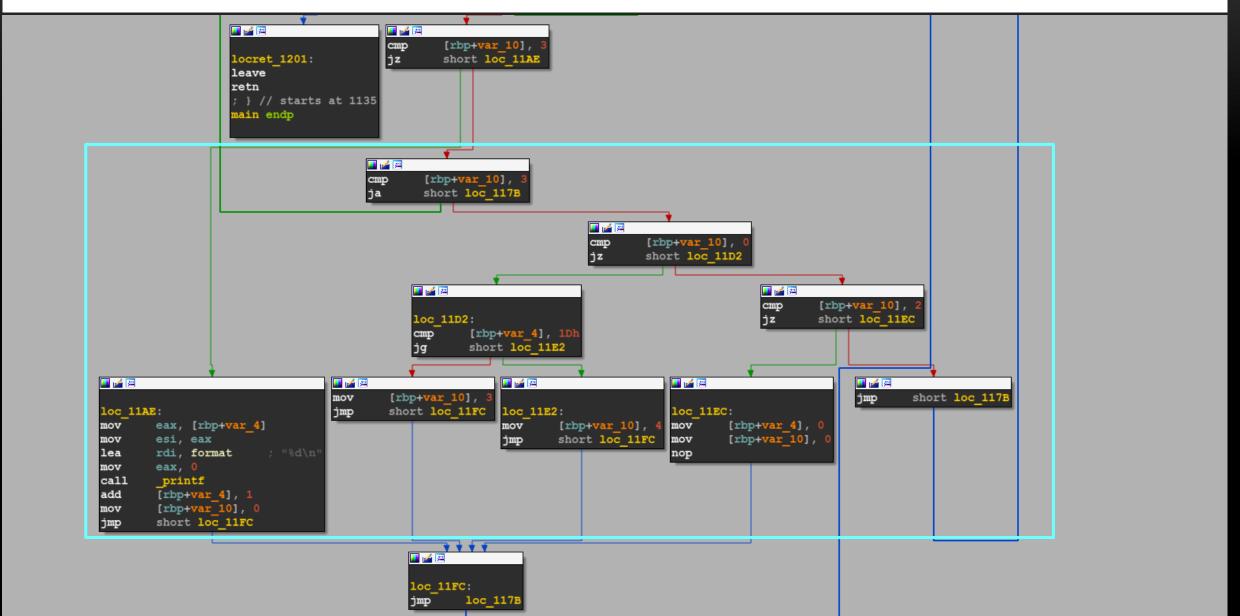


> ANTI-REVERSING

```
4 4
 int cdecl main(int argc, const char **argv, const char **envp)
public main
main proc near
  r 38= qword ptr -38h
    0= qword ptr
   24= dword ptr -24h
   14= dword ptr -14h
   10= qword ptr -10h
  r 4= dword ptr -4
                                Prologue and
   unwind {
push
       rbp
                                initial assignment
       rbp, rsp
sub
       [rbp+var 24], edi
       [rbp+var_30], rsi
       [rbp+var 38], rdx
       megaInit
call
       eax, [rbp+var 24]
       cs: global argc, eax
       rax, [rbp+var 30]
       cs: global argv, rax
       rax, [rbp+var 38]
mov
       cs: global envp, rax
       [rbp+var 14], 1
mov
       [rbp+var 10], 2
                 loc 117B:
                         [rbp+var_10],
                         short loc 11A7
                                                              Main dispatcher
   4
                              4
                                      [rbp+var 10],
                                     short loc 117B
    loc 11A7:
                              jа
           short locret 1201
```



> ANTI-REVERSING





ANTI-REVERSING

Simple opaque predicate and anti-disassembly technique

near ptr 0D0A8837h

```
.text:00401000 loc 401000:
                                           ; CODE XREF: _main+Fp
.text:00401000
                            push
                                    ebp
.text:00401001
                                   ebp, esp
                            mov
.text:00401003
                                   eax, eax
                            xor
.text:00401005
                                    short near ptr loc_40100D+1
                            jΖ
.text:00401007
                                   near ptr loc_40100D+4
                            jnz
.text:0040100D
.text:0040100D loc_40100D:
                                     ; CODE XREF: .text:00401005j
.text:0040100D
                                    ; .text:00401007j
```

imp

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.text:0040100D

ANTI-REVERSING

00401045 pop ecx

00401046 inc ecx

00401047 inc ecx

00401048 add ecx, 4

00401049 add ecx, 4

0040104A push ecx

0040104B

0040104C sub ecx, 6

0040104D dec ecx

0040104E dec ecx

0040104F jmp 0x401320

ret

Call stack manipulation:

■ Do you know what's happening here?

METASM

4 push ebx mov ebx, B9 sub eax, ebx pop ebx sub eax, B9 sub eax, 55 sub eax, 86 sub eax, 32 add eax,ecx add eax, ecx add eax, 86 add eax, 50 push edx sub eax, B9 add eax, 37 add eax, ecx → add eax, ecx → mov edx, 42 → push edx add eax, B9 inc edx push ecx dec edx mov ecx, 49 add edx, 77 mov edx, ecx add eax, edx pop ecx pop edx inc edx add edx, 70 How to reverse the obfuscation and, dec edx add eax, edx from stage 4, to return to the stage 1? ©

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pop edx

- METASM works as disassembler, assembler, debugger, compiler and linker.
- Key features:
 - Written in Ruby
 - C compiler and decompiler
 - Automatic backtracking
 - Live process manipulation
 - Supports the following architecture:
 - Intel IA32 (16/32/64 bits)
 - PPC
 - MIPS

- root@kali:~/github# git clone https://github.com/jjyg/metasm.git
- Include the following line into .bashrc file to indicate the Metasm directory installation:
 - export RUBYLIB=\$RUBYLIB:~/github/metasm
- Test metasm:
 - ruby -r metasm -e 'p Metasm::VERSION'
- You should see a number in the output. It's done ©



```
require "metasm"
    include Metasm
    mycode = Metasm::Shellcode.assemble(Metasm::Ia32.new, <<EOB)</pre>
    entry:
           push ebx
           mov ebx, 0xb9
           sub eax, ebx
           pop ebx
           sub eax, 0x55
16
           sub eax, 0x32
           add eax, ecx
18
           add eax, 0x50
19
           add eax, 0x37
20
           push edx
           push ecx
           mov ecx, 0x49
           mov edx, ecx
24
           pop ecx
           inc edx
26
           add edx, 0x70
           dec edx
28
           add eax, edx
29
           pop edx
30
           jmp eax
31
    E0B
```

Based on metasm.rb file and Bruce Dang's code.

This instruction was inserted to make the eax register evaluation easier. ©



```
33
                                                                      initialize and disassemble code
34
    addrstart = 0
    asmcode = mycode.init disassembler
                                                                      since the beginning (start point).
    asmcode.disassemble(addrstart)
36
    conference di = asmcode.di at(addrstart)
37
    conference = conference di.block
38
    puts "\n<!!!> Blackstorm Security 2020:\n "
40
    puts conference.list +
                                                                      list the assemble code
41
    conference.list.each{|aborges|+
                                                                      initialize the backtracking engine
        puts "\n<!!!> #{aborges.instruction}"
43
44
        back = aborges.backtrace binding()
        v = back.values
         k = back.keys
46
          = k.zip(v)
        puts "Our data flow follows below:\n"
48
         j.each do |mykeys, myvalues|
                                                                     determines which is the final
           puts "Processing: #{mykeys} ==> #{myvalues}"
50
                                                                     instruction to walk back from there
51
52 ▼
           if aborges.opcode.props[:setip]
             puts "\nOur control flow follows below:\n"
             puts ">>> #{asmcode.get xrefs x(aborges)}"
54
           end
56
        end
```



```
58
                                                  Backtracing from the last instruction
59
    addrstart2 = 0
    asmcode2 = mycode.init disassembler
                                                                                                  Logs the sequence of
    asmcode2.disassemble(addrstart2)
                                                                                                   backtraced instructions.
    dd = asmcode2 block at(addrstart2)
62
    final = asmcode2.ge\bar{t} x\bar{r}efs x(dd.l\bar{i}st.last).first
63
    puts "\n[+] final output: #{final}"
64
65
    values = asmcode2.backtrace(final, dd.list.last.address, {:log => backtracing log = [] , :include start => true})
66
67
    backtracing log.each{|record|}
      case type = record.first
68
69 ▼
      when :start
        record, expression, addresses = record
70
        puts "[start] Here is the sequence of expression evaluations #{expression} from 0x#{addresses.to s(16)}\n"
72
73 ▼
      when :di
        record, new, old, instruction = record
74
75
        puts "[new update] instruction #{instruction},\n --> updating expression once again from #{old} to #{new}\n"
76
77
78
79
    effective = backtracing log.select{|y| y.first==:di}.map{|y| y[3]}.reverse
80
81
    puts "\nThe effective instructions are:\n\n
82
    puts effective
83
                                                                                   Shows only the effective instructions,
84
85
                                                                                   which really can alter the final result.
86
```



```
root@kali:~# ./metasmtest.rb
<!!!> Blackstorm Security 2020:
0 push ebx
 mov ebx, 0b9h
6 sub eax, ebx
  pop ebx
9 sub eax, 55h
Och sub eax, 32h
Ofh add eax, ecx
11h add eax, 50h
14h add eax, 37h
17h push edx
18h push ecx
19h mov ecx, 49h
1eh mov edx, ecx
20h
    pop ecx
21h inc edx
22h add edx, 70h
25h dec edx
26h add eax, edx
28h pop edx
```

Remember: this is our obfuscated code. ©

```
<!!!> push ebx
 Our data flow follows below:
 Processing: esp \implies esp-4
 Processing: dword ptr [esp] \Longrightarrow ebx
 <!!!> mov ebx, 0b9h
Our data flow follows below:
 Processing: ebx \implies 0b9h
<!!!> sub eax, ebx
 Our data flow follows below:
 Processing: eax ⇒ eax-ebx
 Processing: eflag_z \Longrightarrow (((eax80ffffffffh)-(ebx80ffffffffh))80ffffffffh)=0
 Processing: eflag_s \Longrightarrow ((((eax80ffffffffh)-(ebx80ffffffffh))80ffffffffh)>>1fh)\ne0
Processing: ertag_s → ((((caxos.
Processing: eftag_c ⇒ (eax&0fffffffh)<(ebx&0ffffffh)
Processing: eftag_o ⇒ ((((eax&0fffffffh)>>1fh)≠0)=(!(((ebx&0fffffffh)>>1fh)≠0)))&f(((eax&0ffffffffh))age of the context of the co
 <!!!>> pop ebx
Our data flow follows below:
 Processing: esp \implies esp+4
 Processing: ebx \implies dword ptr [esp]
 <!!!> sub eax, 55h
Our data flow follows below:
 Processing: eax \implies eax-55h
```

```
Our control flow follows below:
>>> [Expression[:eax]]
[+] final output: eax
[start] Here is the sequence of expression evaluations eax from 0×29
[new update] instruction 26h add eax, edx,
 → updating expression once again from eax to eax+edx
[new update] instruction 25h dec edx,
 → updating expression once again from eax+edx to eax+edx-1
[new update] instruction 22h add edx, 70h,
 → updating expression once again from eax+edx-1 to eax+edx+6fh
[new update] instruction 21h inc edx,
 → updating expression once again from eax+edx+6fh to eax+edx+70h
[new update] instruction 1eh mov edx, ecx,
 → updating expression once again from eax+edx+70h to eax+ecx+70h
[new update] instruction 19h mov ecx, 49h,
 → updating expression once again from eax+ecx+70h to eax+0b9h
[new update] instruction 14h add eax, 37h,
 → updating expression once again from eax+0b9h to eax+0f0h
[new update] instruction 11h add eax, 50h,
 → updating expression once again from eax+0f0h to eax+140h
[new update] instruction 0fh add eax, ecx,
 → updating expression once again from eax+140h to eax+ecx+140h
[new update] instruction Och sub eax, 32h,
 → updating expression once again from eax+ecx+140h to eax+ecx+10eh
```

```
[new update] instruction 9 sub eax, 55h,
 → updating expression once again from eax+ecx+10eh to eax+ecx+0b9h
[new update] instruction 6 sub eax, ebx,
→ updating expression once again from eax+ecx+0b9h to eax-ebx+ecx+0b9h
[new update] instruction 1 mov ebx, 0b9h,
 → updating expression once again from eax-ebx+ecx+0b9h to eax+ecx
                                                                           Game over ©
The effective instructions are:
1 mov ebx, 0b9h
6 sub eax, ebx
9 sub eax, 55h
0ch sub eax, 32h
Ofh add eax, ecx
11h add eax, 50h
                              Output originated from backtracing_log.select command (in reverse)
14h add eax, 37h
19h mov ecx, 49h
1eh mov edx, ecx
21h inc edx
22h add edx, 70h
25h dec edx
26h add eax, edx
```

MIASM

- MIASM is one of most impressive framework for reverse engineering, which
 is able to analyze, generate and modify several different types of programs.
- MIASM supports assembling and disassembling programs from different platforms such as ARM, x86, MIPS and so on, and it also is able to emulate by using JIT.
- Therefore, MIASM is excellent to de-obfuscation.
- Installing MIASM (python 2.7.x):
 - git clone https://github.com/serpilliere/elfesteem.git elfesteem
 - cd elfesteem/



- python setup.py build
- python setup.py install
- apt-get install clang texinfo texi2html
- apt-get remove libtcc-dev
- apt-get install llvm
- **cd** ...
- git clone http://repo.or.cz/tinycc.git
- cd tinycc/
- git checkout release 0 9 26
- ./configure --disable-static
- make
- make install



- pip install llvmlite
- pip install future
- apt-get install z3
- apt-get install python-pycparser
- pip install pyparsing
- cd ..
- git clone https://github.com/cea-sec/miasm.git
- cd miasm
- python setup.py build
- python setup.py install
- cd test/
- python test all.py

```
root@kali:~/github/miasm/test# python2.7 test_all.py
[LLVM] Python'llvmlite' module is required for llvm tests
Z3 and its python binding are necessary for TranslatorZ3.
TEST/ARCH msp430/arch.py
TEST/ARCH ppc32/arch.py
TEST/ARCH x86/arch.py
TEST/ARCH aarch64/arch.py
DONE msp430/arch.py 0s
TEST/ARCH x86/sem.pv python
DONE ppc32/arch.py 0s
TEST/ARCH x86/unit/mn_strings.py python
DONE x86/sem.py python 1s
TEST/ARCH x86/unit/mn stack.py gcc
DONE x86/unit/mn_strings.py python 1s
TEST/ARCH x86/unit/mn_daa.py gcc
DONE x86/unit/mn_daa.py gcc 3s
TEST/ARCH x86/unit/mn_das.py gcc
DONE x86/unit/mn_stack.py gcc 3s
TEST/ARCH x86/unit/mn_int.py gcc
DONE x86/unit/mn_int.py gcc 1s
TEST/ARCH x86/unit/mn_pshufb.py gcc
DONE x86/unit/mn_pshufb.py gcc 1s
TEST/ARCH x86/unit/mn_psrl_psll.py gcc
```



- Before proceding with MIASM, we need to create a binary containing our code, so we need an assembler and Keystone is great.
- Keystone Engine acts an assembler and:
 - Supports x86, Mips, Arm and many other architectures.
 - It is implemented in C/C++ and has bindings to Python, Ruby, Powershell and C# (among other languages).
- Installing Keystone:
 - root@kali:~/Desktop# wget https://github.com/keystoneengine/keystone/archive/0.9.1.tar.gz



- root@kali:~/programs# cp /root/Desktop/keystone-0.9.1.tar.gz .
- root@kali:~/programs# tar -zxvf keystone-0.9.1.tar.gz
- root@kali:~/programs/keystone-0.9.1# apt-get install cmake
- root@kali:~/programs/keystone-0.9.1# mkdir build ; cd build
- root@kali:~/programs/keystone-0.9.1/build# apt-get install time
- root@kali:~/programs/keystone-0.9.1/build# ../make-share.sh
- root@kali:~/programs/keystone-0.9.1/build# make install
- root@kali:~/programs/keystone-0.9.1/build# ldconfig
- root@kali:~/programs/keystone-0.9.1/build# tail -2 /root/.bashrc

export RUBYLIB=\$RUBYLIB:~/programs/metasm export LD_LIBRARY_PATH=\$LD_LIBRARY_PATH:/usr/local/lib

```
#include <stdio.h>
    #include <keystone/keystone.h>
                                                                                                   instructions from the
     #define SANS "push ebx; mov ebx,0xb9; sub eax,ebx; pop ebx; sub eax,0x55; sub
     #eax,0x32; add eax,ecx; add eax,0x50; add eax,0x37; push edx; push ecx; mov
     #ecx,0x49; mov edx,ecx; pop ecx; inc edx; add edx,0x70; dec edx; add eax.edx;
                                                                                                    original obsfuscated code
     #pop edx"
    int main(int argc, char **argv)
                                                                                                    Creating a keystone engine
10 ▼
11
        ks engine *keyeng; ←
12
        ks err keyerr = KS ERR ARCH;
13
         size t count;
14
        unsigned char *encode;
15
         size t size;
                                                                                                    Assembling our instructions
         keyerr = ks open(KS ARCH X86, KS MODE 32, &keyeng);
17
                                                                                                    using Keystone engine.
18 ▼
        if (keyerr != KS ERR OK) {
             printf("ERRO\overline{R}: A fail has ocurred while calling ks open(), quit\n");
19
20
             return -1;
21
22
        if (ks_asm(keyeng, SANS, 0, &encode, &size, &count)) { printf("ERROR: A fail has occurred while calling ks_asm() with count = %lu, error code = %u\n", count, ks_errno(keyeng)); } else { size_t i;
23
24 ▼
25
             for (i=0; i < size; i++) {
26
27
                 printf("%02x ", encode[i]);
28
29
                                                                                                    Freeing memory and closing
30
31
        ks free(encode);
                                                                                                    engine.
         ks close(keyeng);
32
33
34
         return 0;
```

63



> DEOBFUSCATION

```
root@kali:~/conference# more Makefile
.PHONY: all clean
KEYSTONE_LDFLAGS = -lkeystone -lstdc++ -lm
all:
        ${CC} -o conference2020 conference2020.c ${KEYSTONE_LDFLAGS}
clean:
        rm -rf *.o conference2020
root@kali:~/conference#
root@kali:~/conference# make
cc -o conference2020 conference2020.c -lkeystone -lstdc++ -lm
root@kali:~/conference#
root@kali:~/conference# ./conference2020
53 bb b9 00 00 00 29 d8 5b 83 e8 55 83 e8 32 01 c8 83 c0 50 83 c0 37 52 51 b9 49 00 00 00
89 ca 59 42 83 c2 70 4a 01 d0 5a rootakali:~/conference#
root@kali:~/conference#
root@kali:~/conference# ./conference2020 | xxd -r -p - > conference2020.bin
root@kali:~/conference#
root@kali:~/conference#|hexdump -C conference2020.bin
00000000 53 bb b9 00 00 00 29 d8 5b 83 e8 55 83 e8 32 01
                                                             |S.....).[ ..U..2.
00000010 c8 83 c0 50 83 c0 37 52 51 b9 49 00 00 00 89 ca
                                                              ... P .. 7RQ.I....
                                                              YB .. pJ .. Z
00000020 59 42 83 c2 70 4a 01 d0 5a
00000029
```



```
: Binary file
                ; Format
                 ; Base Address: 0000h Range: 0000h - 0029h Loaded length: 0029h
                                 .686p
                                 · mmx
                                 .model flat
                                 segment byte public 'CODE' use32
                seg000
                                 assume cs:seg000
                                 assume es:nothing, ss:nothing, ds:nothing, fs:nothing, gs:nothing
                                 push
                                         ebx
                                         ebx, 0B9h
                                 mov
                                 sub
                                         eax, ebx
                                         ebx
                                 pop
                                         eax, 55h
                                 sub
                                 sub
                                         eax, 32h
                                 add
                                         eax, ecx
                                 add
                                         eax, 50h
                                 add
                                         eax, 37h
                                 push
                                         edx
                                                            IDA Pro confirms: it's our content ©
                                 push
                                         ecx
                                         ecx, 49h
                                 mov
                                         edx, ecx
                                 mov
                                         ecx
                                 pop
                                 inc
                                         edx
                                 add
                                         edx, 70h
                                 dec
                                         edx
                                 add
                                         eax, edx
                                 pop
                                         edx
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```

```
from colorama import init, Fore, Back, Style
                                                               Open the binary file generated through
    from miasm.analysis.binary import Container
                                                               Keystone. The Container provides the byte
    from miasm.analysis.machine import Machine
                                                               source to the disasm engine.
    from miasm.jitter.csts import PAGE READ, PAGE WRITE
         open("/root/conference/conference2020.bin") as fdesc:
        cont=Container.from stream(fdesc)
                                                                         Instantiates the assemble engine
                                                                         using the x86 32-bits architecture.
    offset = 0
    conferencemach=Machine('x86 32')
                                                                         Initialize and run the Symbolic
    conferencedis=conferencemach.dis engine(cont.bin stream)
11
    asmcfg = conferencedis.dis multiblock(offset)
                                                                         Execution Engine, setting all
    ira = conferencemach.ira(conferencedis.loc db)
                                                                         registers to an initial value.
14
    ircfg = ira.new ircfg from asmcfg(asmcfg)
16
    open('out.dot', 'w').write(ircfg.dot())
17
    from miasm.ir.symbexec import SymbolicExecutionEngine
18
    symb = SymbolicExecutionEngine(ira,conferencemach.mn.regs.regs init)
    symbolic pc = symb.run at(ircfg, 0, step=True)
20
    print(Fore.YELLOW + "\nThe final value of EAX register is: %s" %
21
    symb.symbols[conferencemach.mn.regs.EAX]
                                                                           Print the final value of EAX.
    print (Fore.RESET)
```

```
root@kali:~# python conference_symbolic.py
                                               more
[WARNING]: not enough bytes in str
[WARNING ]: cannot disasm at 29
[WARNING ]: not enough bytes in str
[WARNING ]: cannot disasm at 29
Instr PUSH
                  EBX
Assignblk:
ESP = ESP + -0 \times 4
032[ESP + -0 \times 4] = EBX
R12
                    = R12_init
                    = MM2_init
MM2
FS
                    = FS_init
                    = XMM8_init
8MMX
                    = AL init
AL
                    = float_c3_init
float_c3
                    = ST(0)_init
ST(0)
                    = EAX_init
EAX
                    = cf init
cf
R8W
                    = R8W init
                    = float_st6_init
float_st6
                    = MM1_init
MM1
```



```
EBX, 0×B9
Instr MOV
Assignblk:
EBX = 0 \times B9
                    = R12 init
R12
MM2
                    = MM2_init
FS
                    = FS_init
                    = XMM8 init
8MMX
                    = AL_init
AL
float_c3
                    = float_c3_init
ST(0)
                    = ST(0)_init
                    = EAX_init
EAX
cf
                    = cf_init
                    = R8W_init
R8W
float_st6
                    = float_st6_init
                    = MM1 init
MM1
pf
                    = pf_init
R15B
                    = R15B_init
XMM10
                    = XMM10 init
SP
                    = SP_init
                    = zf_init
zf
                    = XMM15_init
XMM15
                    = BL_init
BL
```

```
DR3
                    = DR3 init
R14
                    = R14 init
                    = XMM11_init
XMM11
                    = CX init
CX
R13B
                    = R13B_init
                    = DR2_init
DR2
                    = RCX_init
RCX
i_d
                    = i d init
XMM1
                    = XMM1 init
RSI
                    = RSI init
R8
                    = R8 init
DI
                    = DI init
                    = RAX init
RAX
float_st4
                    = float_st4_init
                    = XMM6_init
XMM6
                    = R15 init
R15
MM7
                    = MM7 init
SPL
                    = SPL_init
@32[ESP init + 0×FFFFFFF8] = ECX init
@32[ESP_init + 0×FFFFFFFC] = EDX_init
```

- I've skipped a very long output, which shows all instruction being execute. We are interested in the final value of EAX. ⓒ
- If you want, view the graph:
 - apt-get install graphviz
 - apt-get install xdot
 - xdot out.dot

The final value of EAX register is: EAX_init + ECX_init← Finally.... ☺



Closing thoughts and Acknowledgments...

- Honestly, I didn't even scratch the surface of this topic....
- There're tons of obfuscation techniques and de-obfuscation/reversing techniques to explain... it would take one or two entire weeks...and probaly you wouldn't to know about it... ②

- I'd like to thank SANS for the event and, in special, my friend Stephen Sims for the invite. ©
- And, of course, I'd like to thank you (the audience) for attending my talk.



Security Researcher

- Speaker at DEF CON USA 2019
- Speaker at DEF CON USA 2018
- Speaker at DEF CON CHINA 2019
- Speaker at DC2711 (Johannesburg)
- Speaker at NO HAT 2019 (Italy)
- Speaker at HITB 2019 (Amsterdam)
- Speaker at CONFidence 2019 (Poland)
- Speaker at DevOpsDays BH 2019
- Speaker at BSIDES 2019/2018/2017/2016
- Speaker at H2HC 2016/2015
- Speaker at BHACK 2018
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THANK YOU FOR ATTENDING MY TALK ©

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