

Buttons, Switches, LEDs

SHEET 2

TRIGGER

SHEET 3

USB3.0

SHEETS 4 & 5

FMC CONNECTORS

SHEETS 6-8

FPGA POWER & GROUND

SHEETS 16 & 17

XILINX FPGA
SHEETS 9-15

FPGA Decoupling

SHEETS 18 & 19

POWER SEQUENCER

SHEET 20

POWER SUPPLIES

SHEETS 21-26

MECHANICAL
SHEET 27

DNI MEANS DO NOT INSTALL THIS PART

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**TEXAS INSTRUMENTS**

Title	<i>TSW14DL3200</i>
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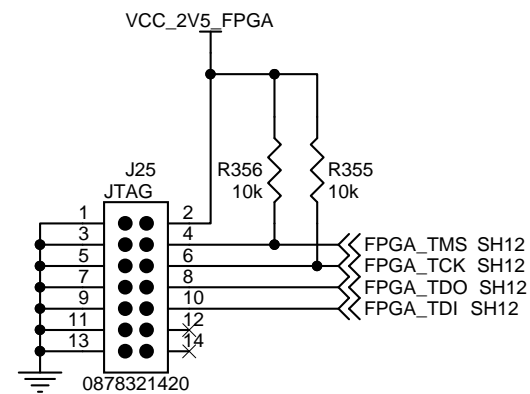
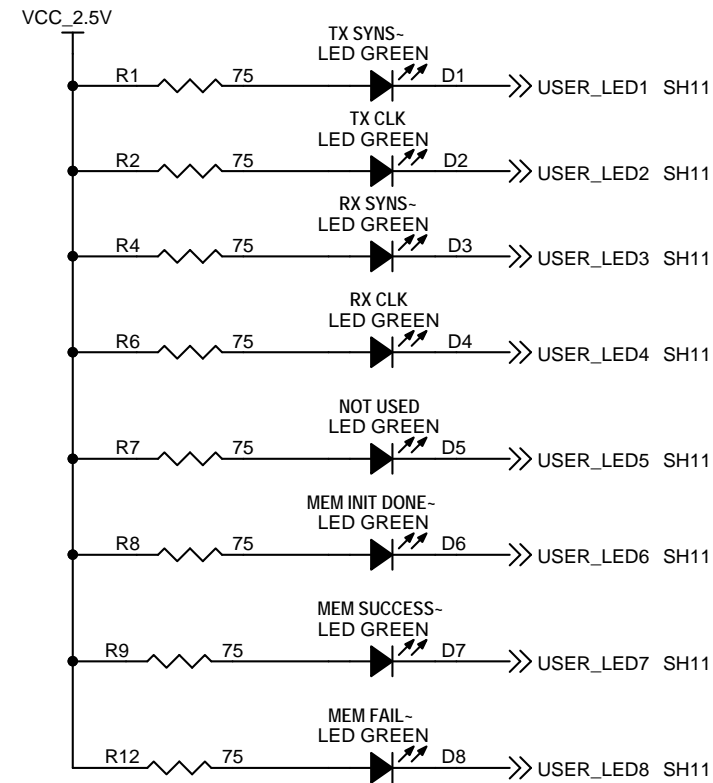
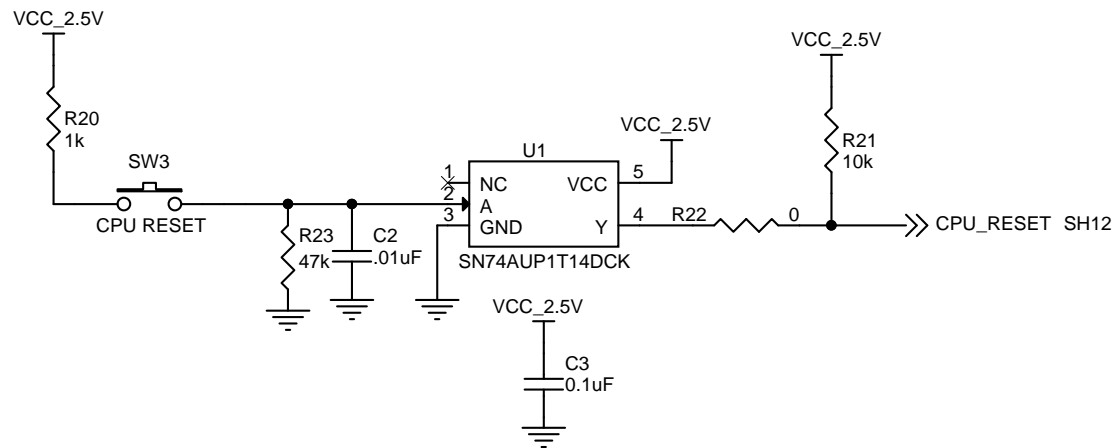
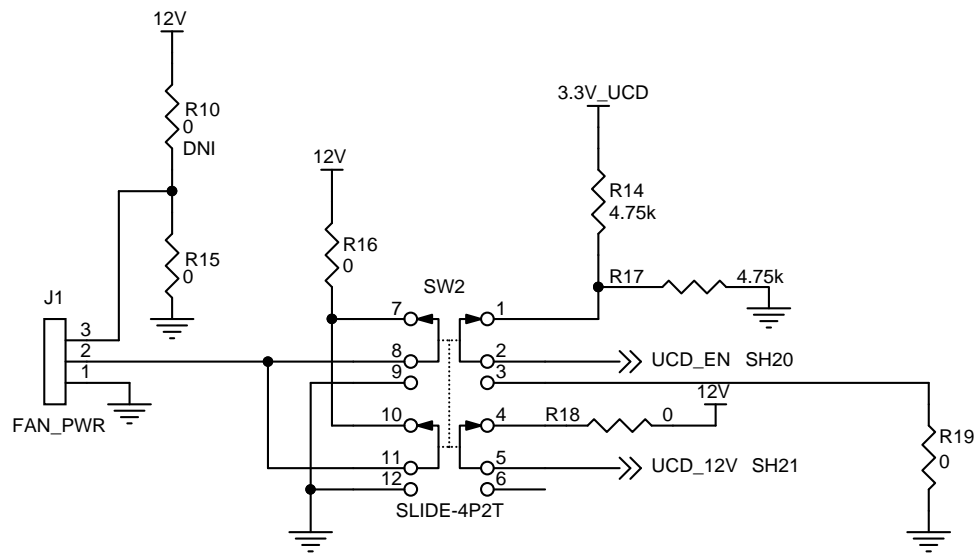
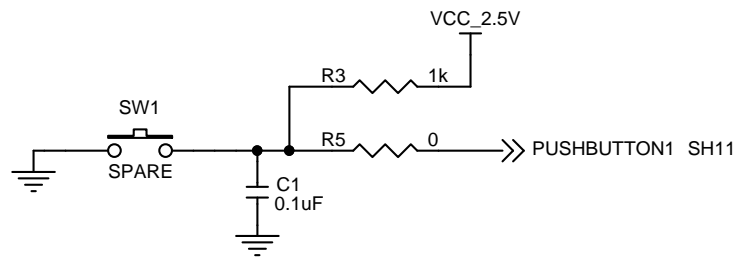
Drawn: KRYPTON 09/2018

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B	DC036

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Engineer: J SETON / K CHAN 09/2018

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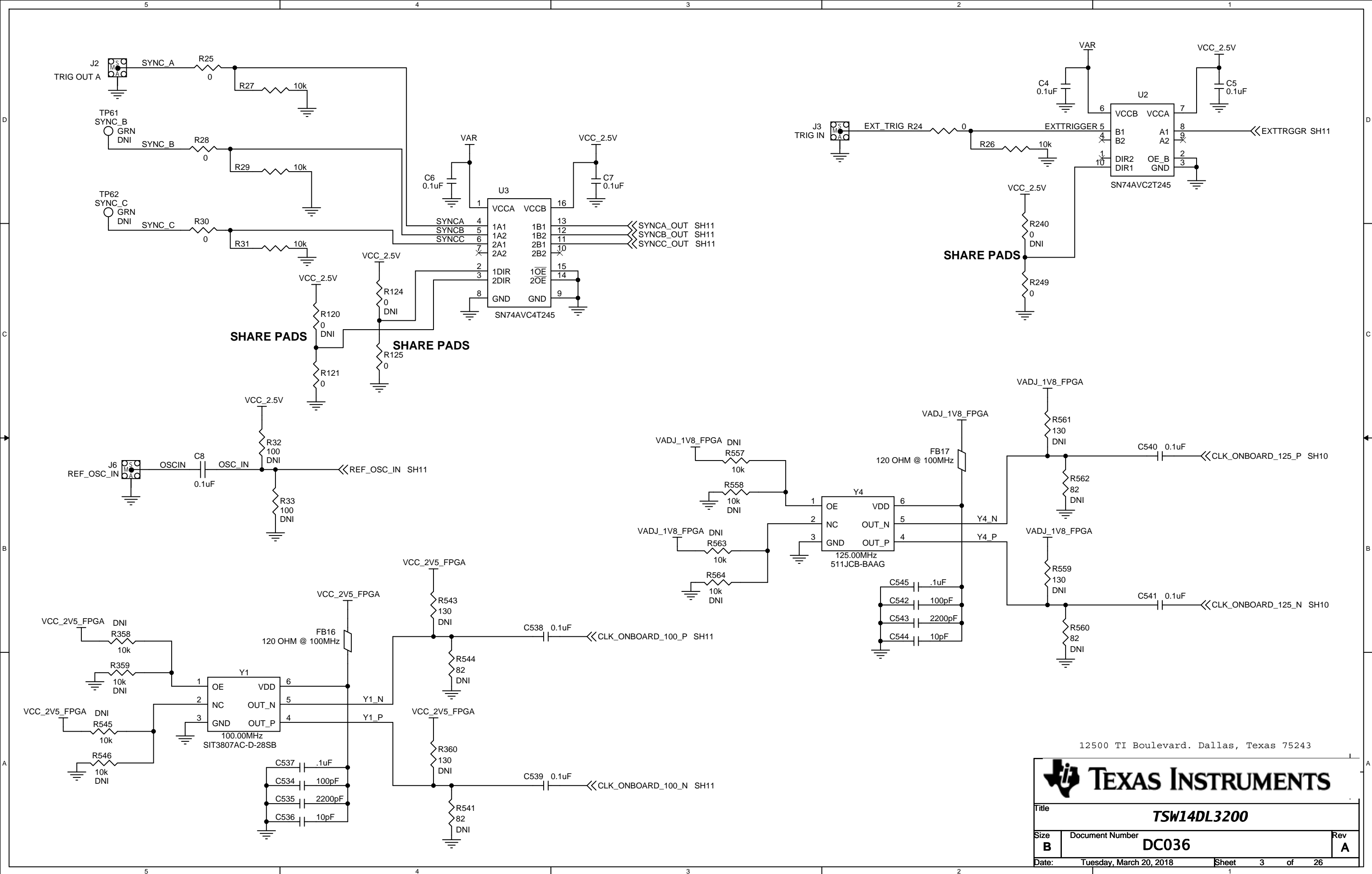


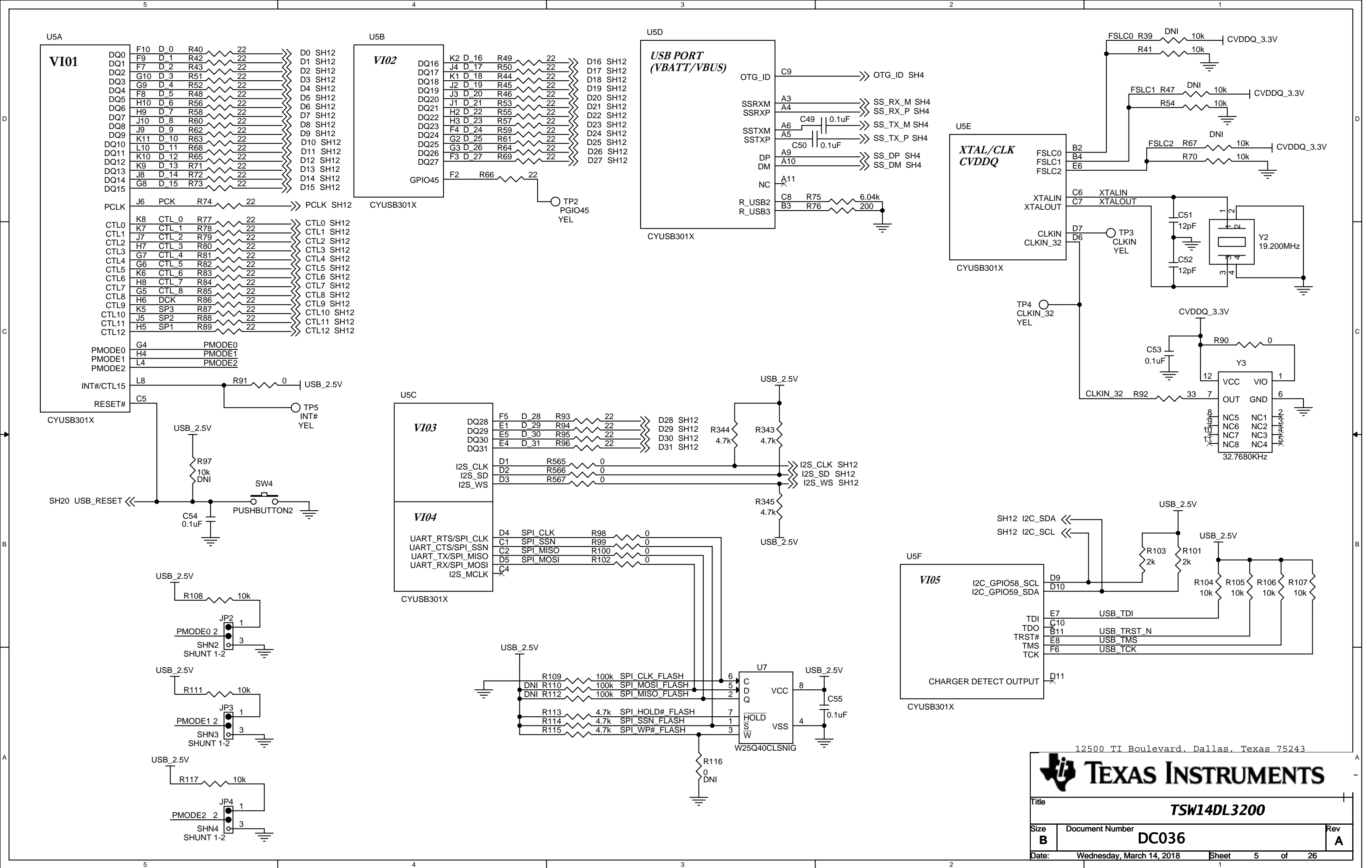
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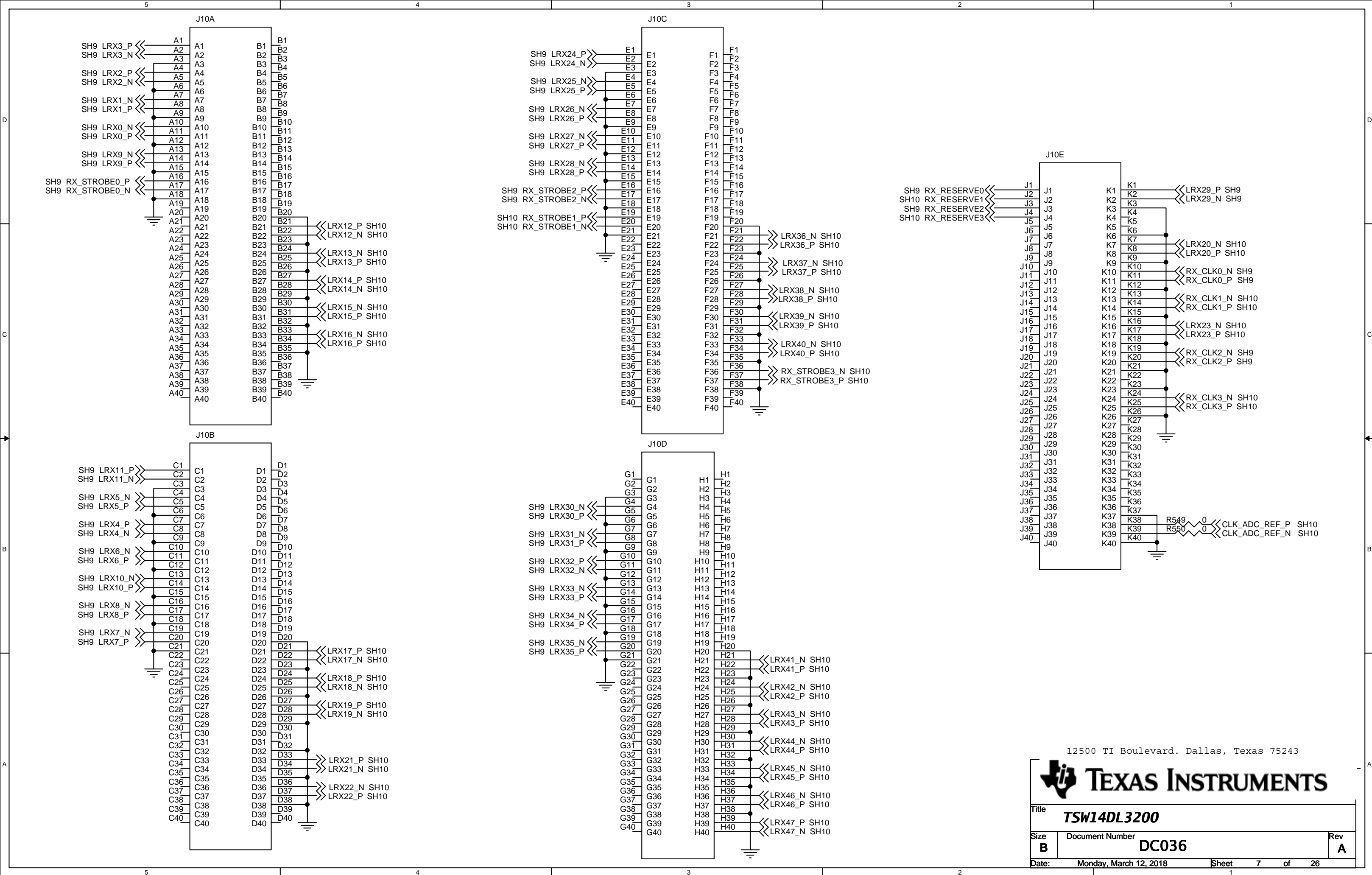
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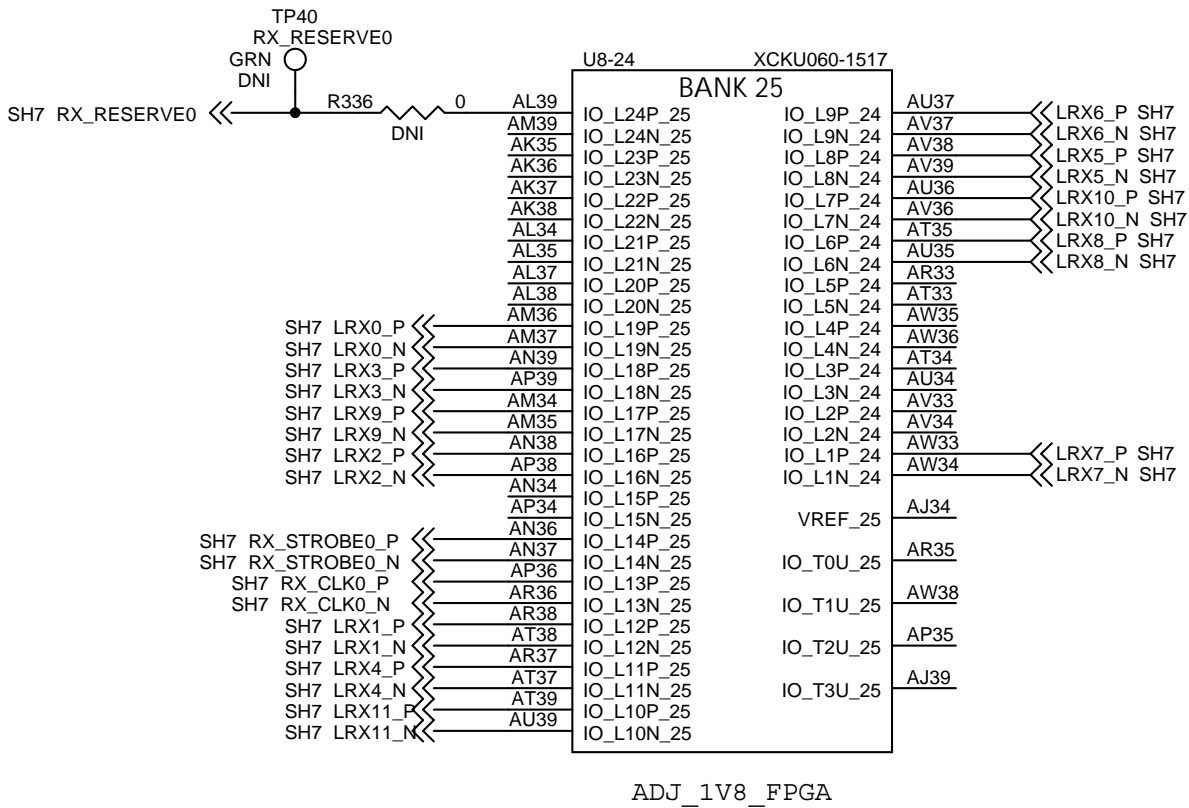
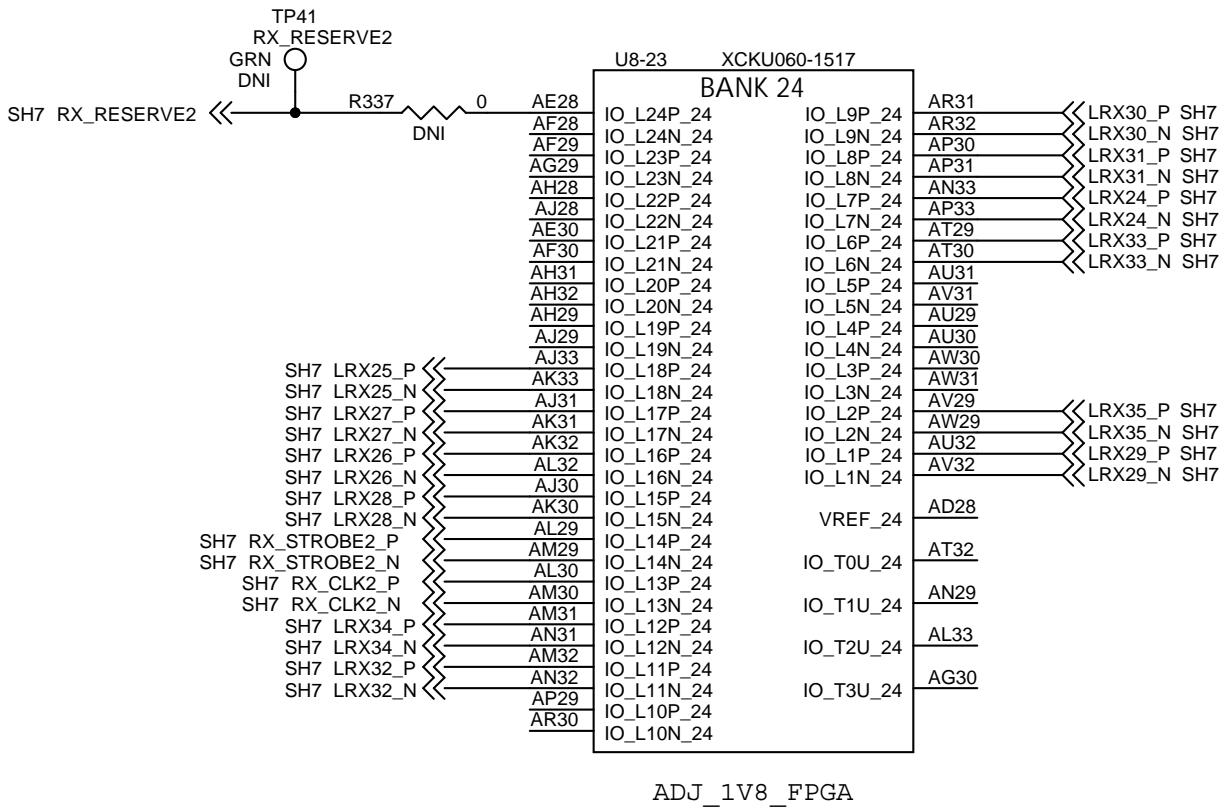
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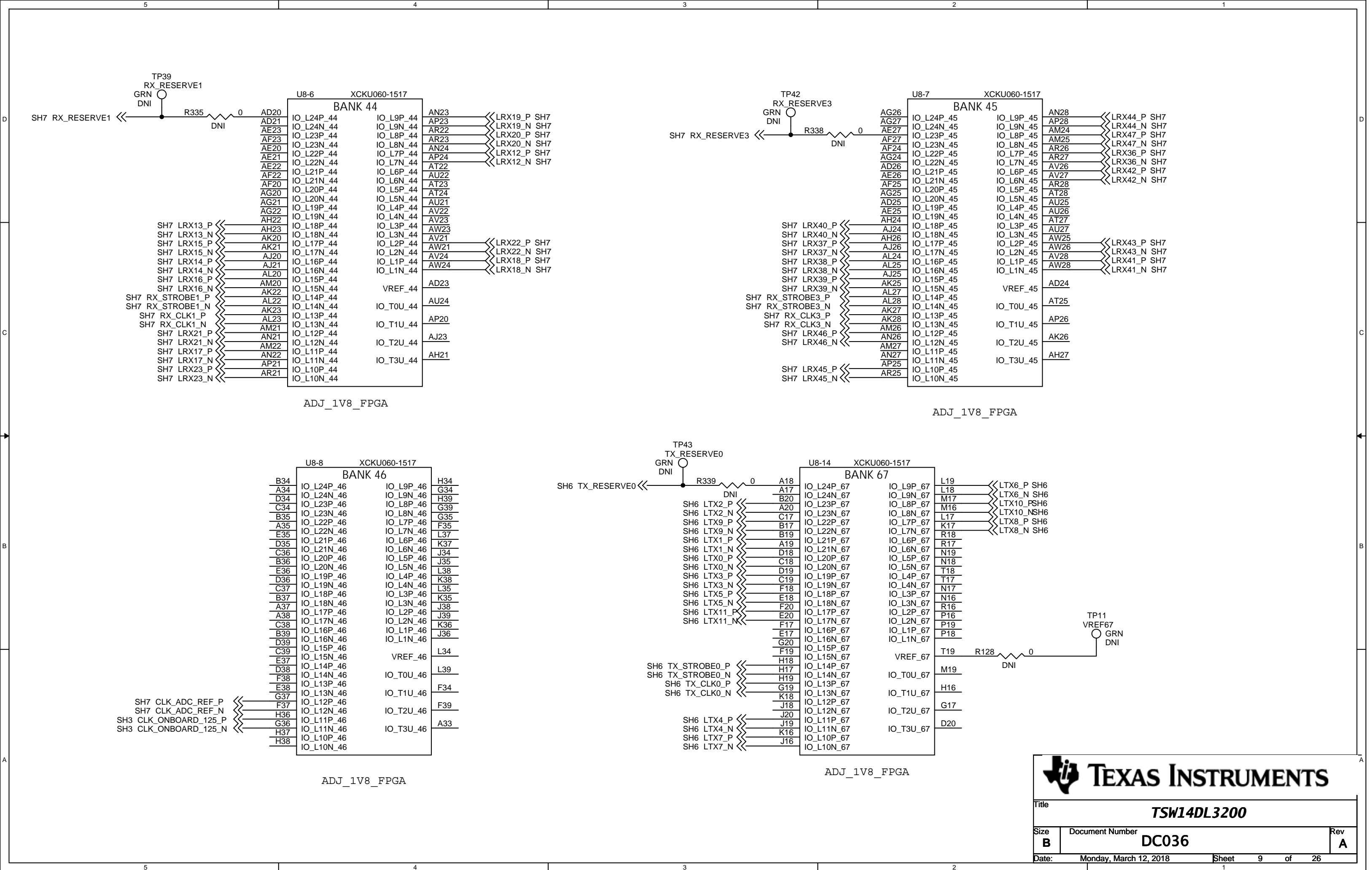
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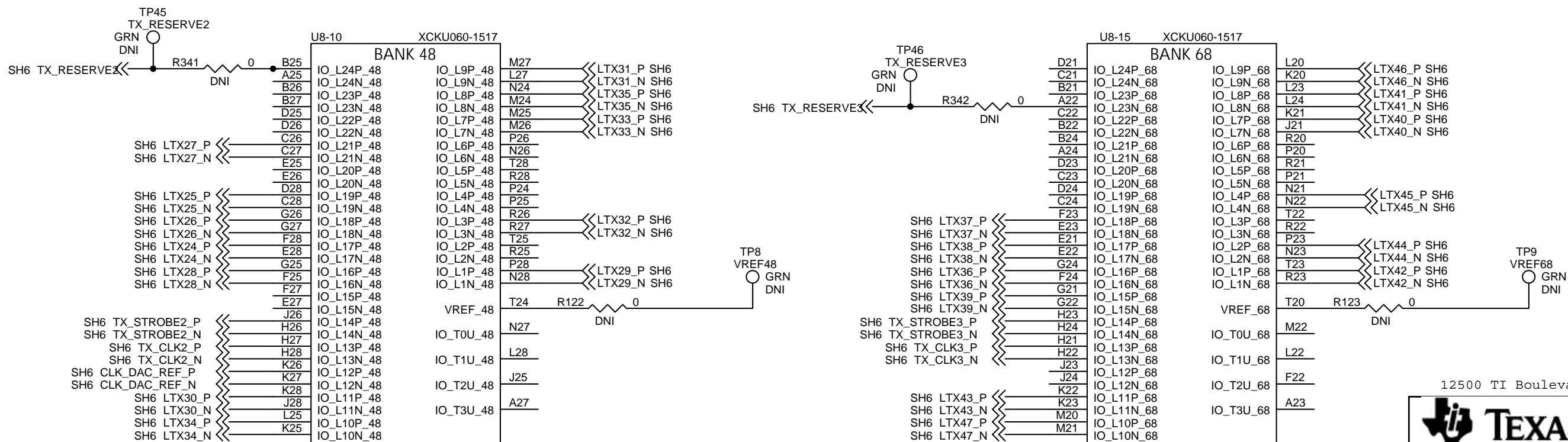
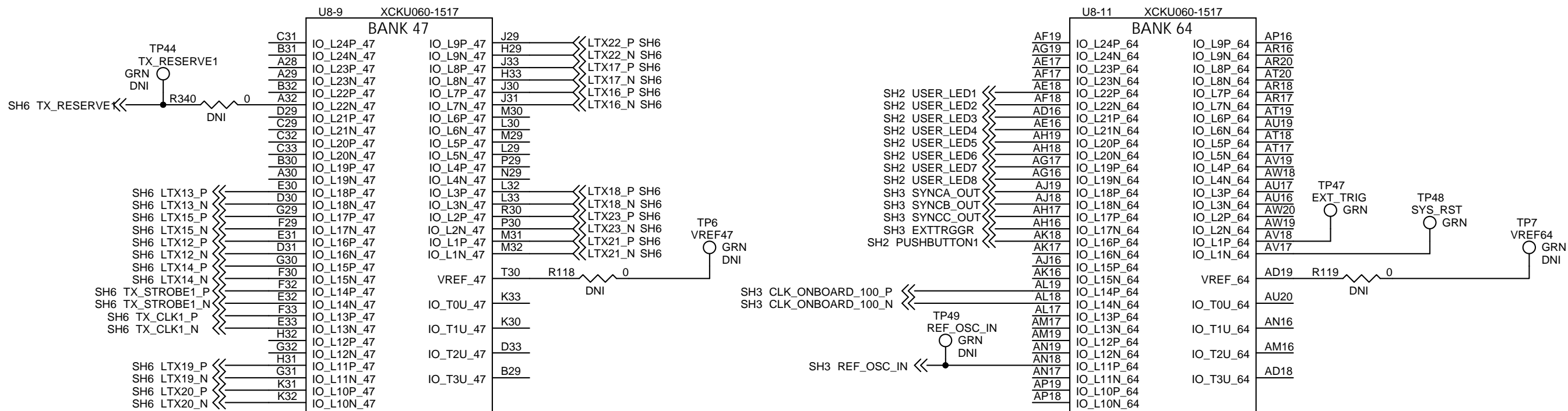
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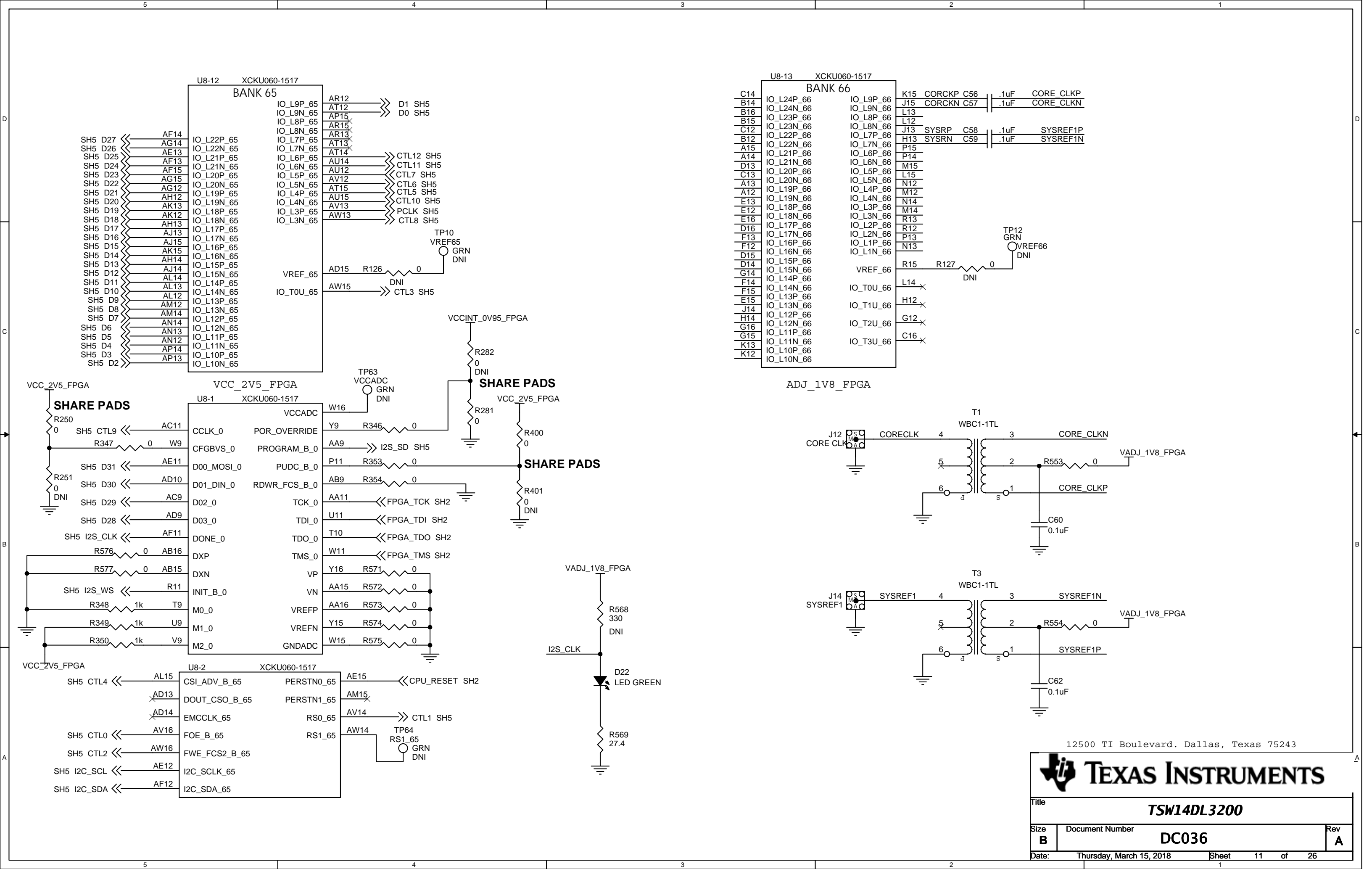


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QUAD 127	
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<div><div>Y36</div><div>Y37</div></div>	<div><div>MGTHTXP2_127</div><div>MGTHRXN2_127</div></div> <div><div>MGTHRXN2_127</div><div>MGTHRXN2_127</div></div>
<div><div>AA34</div><div>AA35</div></div>	<div><div>MGTHTXP1_127</div><div>MGTHRXN1_127</div></div> <div><div>MGTHRXN1_127</div><div>MGTHRXN1_127</div></div>
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<div><div>V32</div><div>V33</div></div>	<div><div>MGTRREFCLK1P_127</div><div>MGTRREFCLK1N_127</div></div> <div><div>MGTRREFCLK1N_127</div><div>MGTRREFCLK1N_127</div></div>
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<div><div>T32</div><div>T33</div></div>	<div><div>MGTRREFCLK0P_128</div><div>MGTRREFCLK0N_128</div></div> <div><div>MGTRREFCLK0N_128</div><div>MGTRREFCLK0N_128</div></div>


U8-18XCKU060-1517	
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<div><div>AT10</div><div>AT9</div></div>	<div><div>MGTRREFCLK0P_224</div><div>MGTRREFCLK0N_224</div></div> <div><div>MGTRREFCLK0N_224</div><div>MGTRREFCLK0N_224</div></div>

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U8-20XCKU060-1517	
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TSW14DL3200

Size

B

Document Number

DC036

Rev

A

Date:

Monday, March 12, 2018

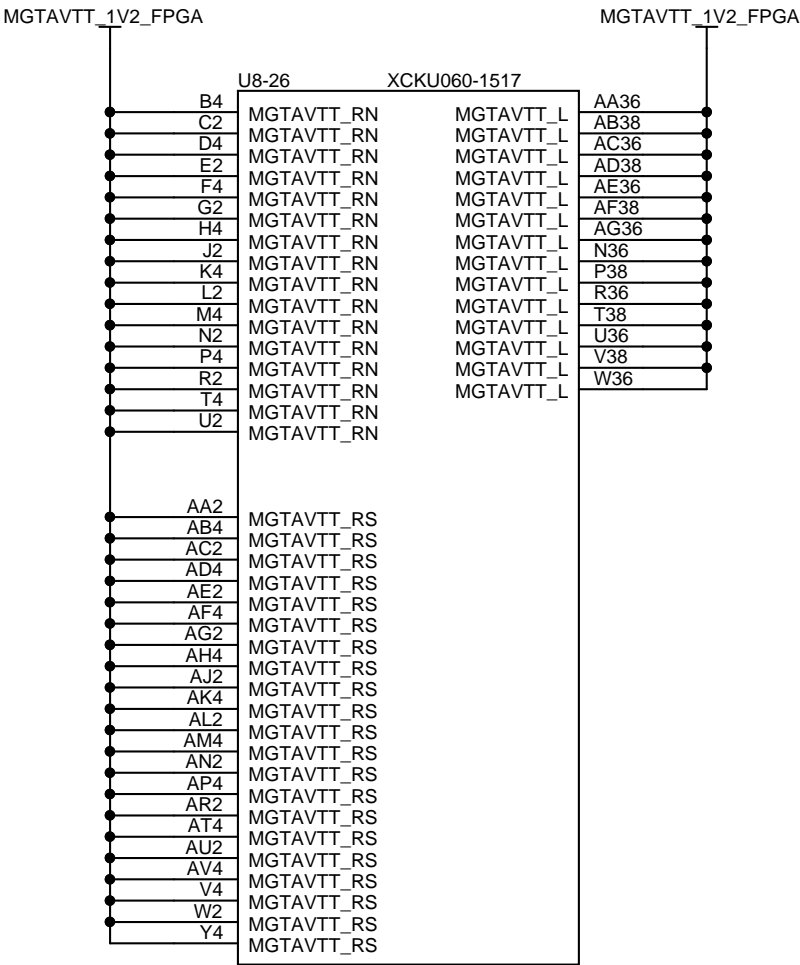
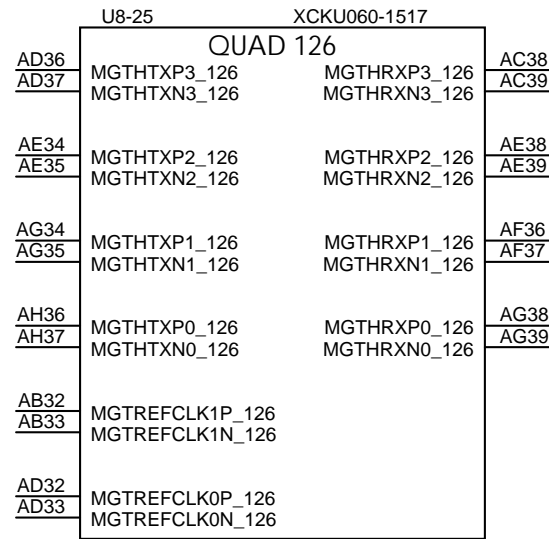
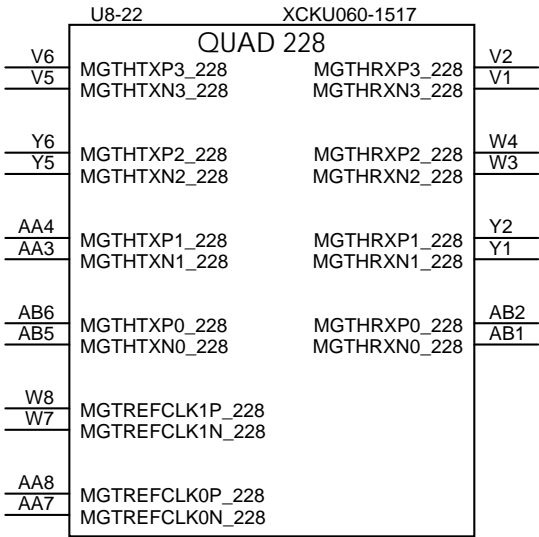
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of

26

RX = MEZANNE TO CARRIER
TX = CARRIER TO MEZANNE



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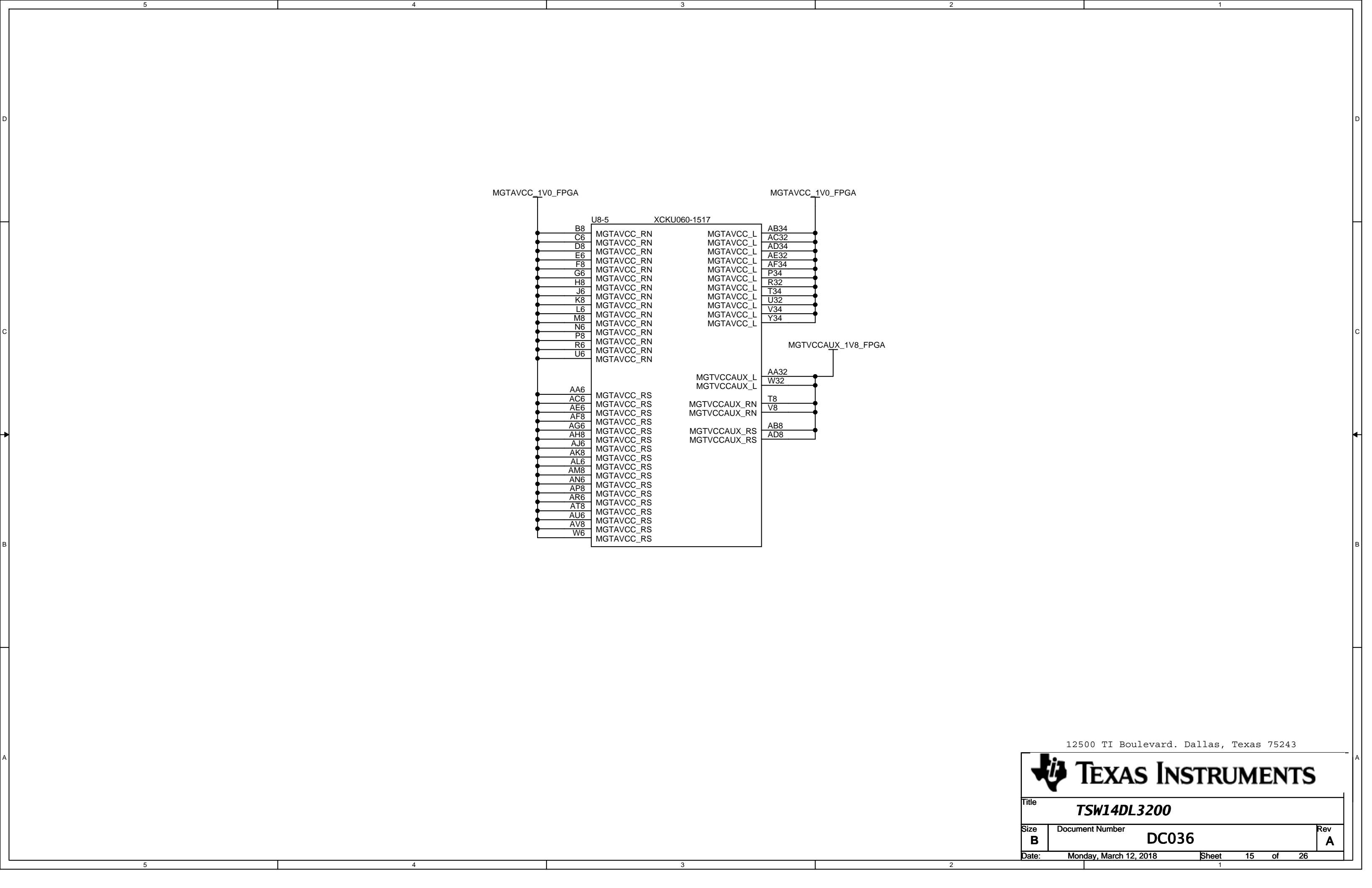
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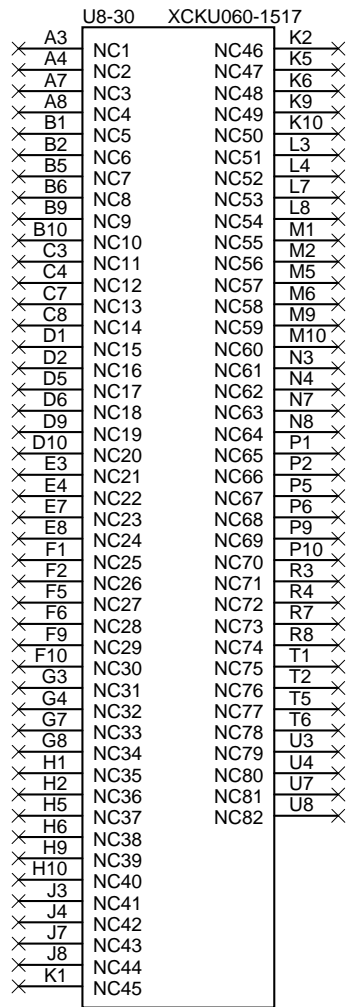
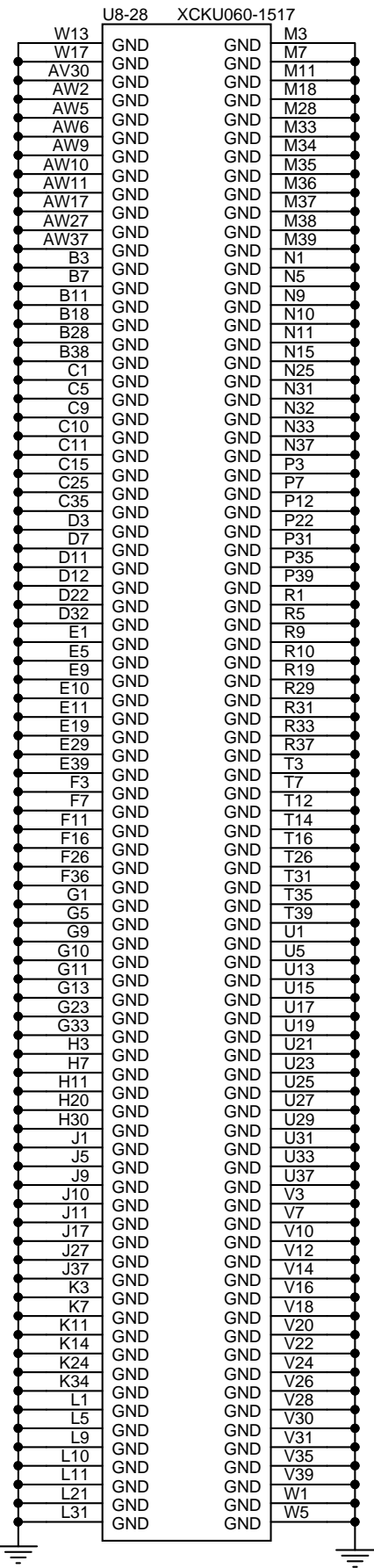
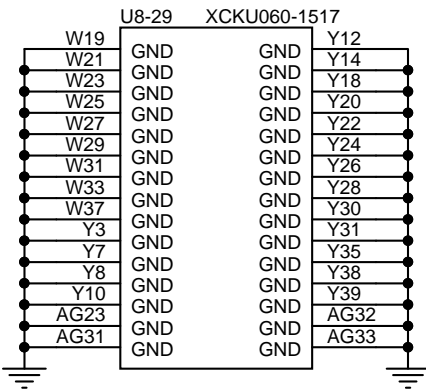
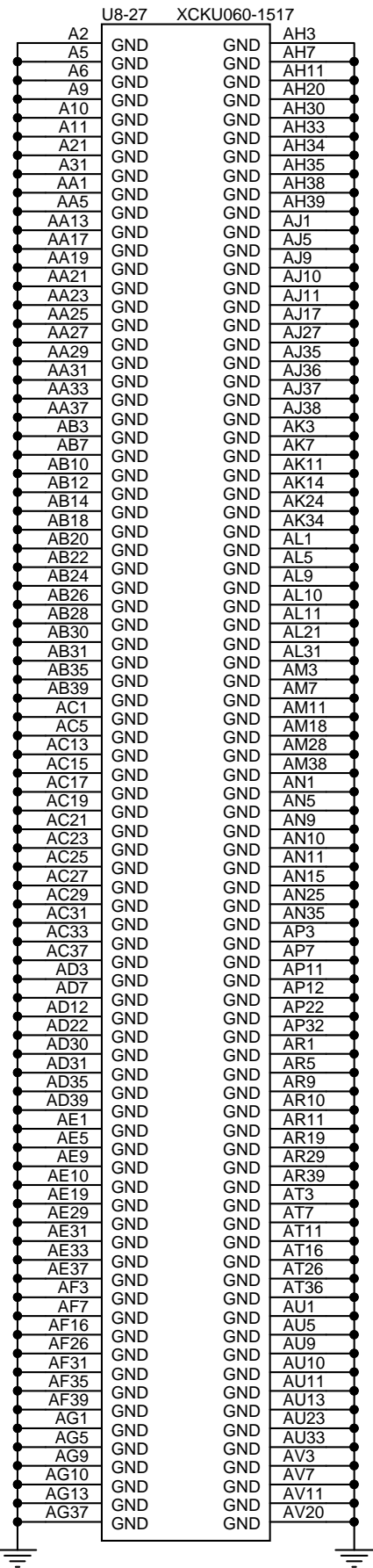
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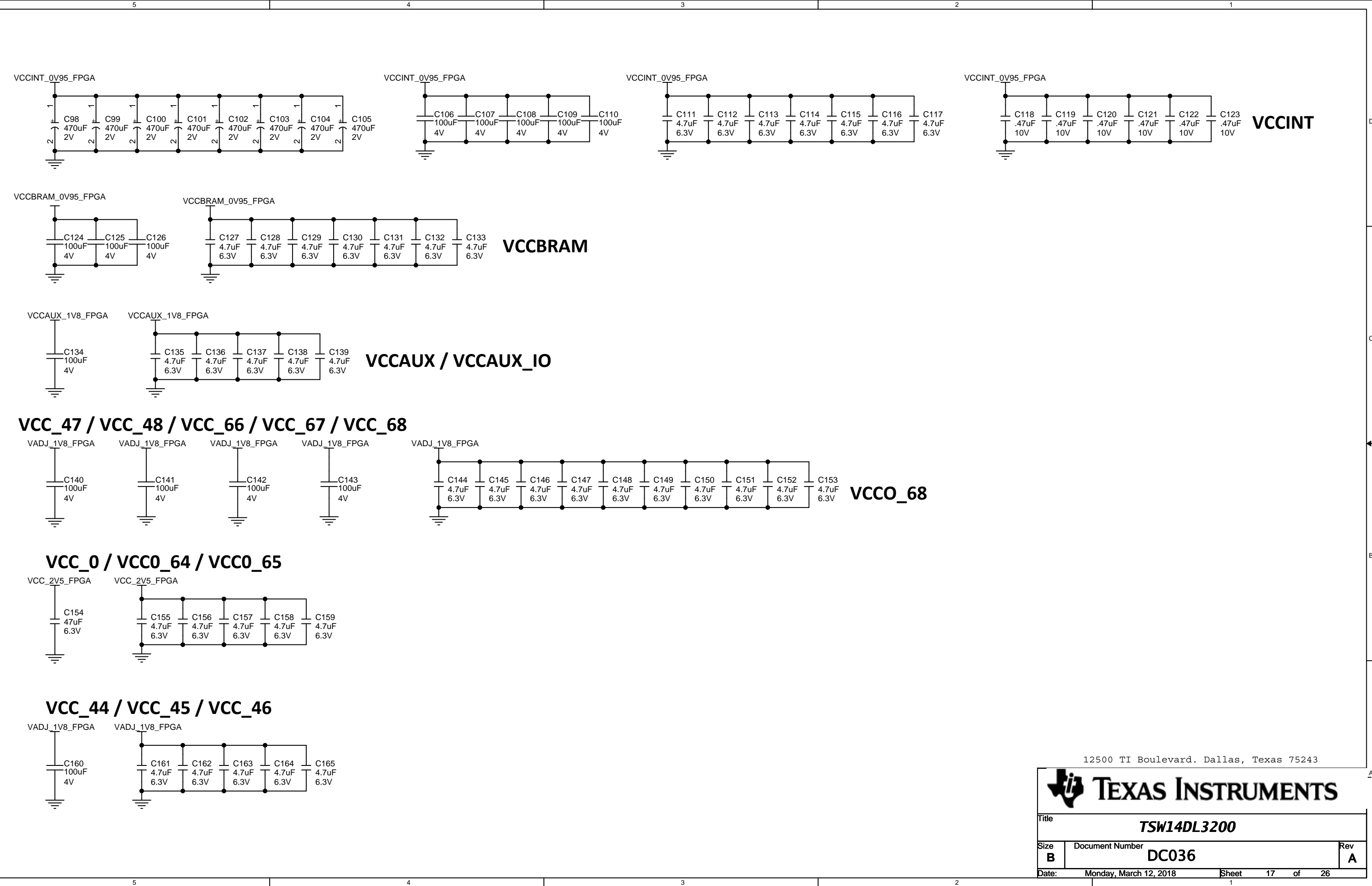
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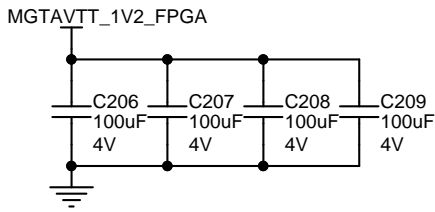
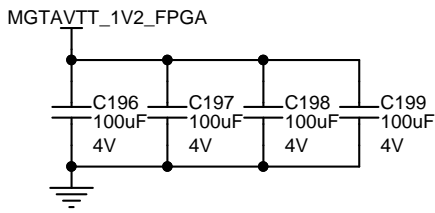
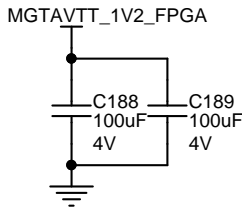
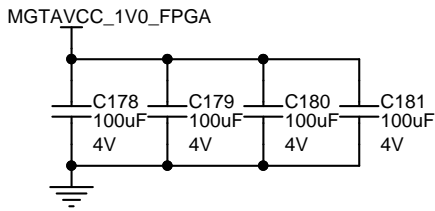
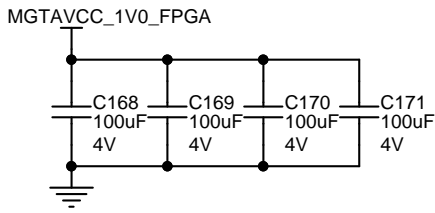
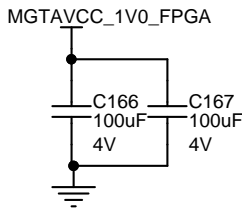


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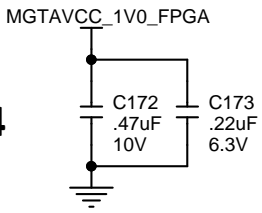


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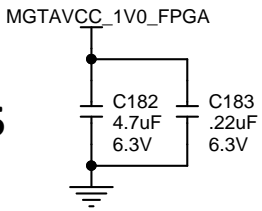




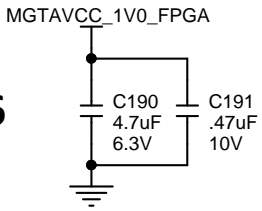
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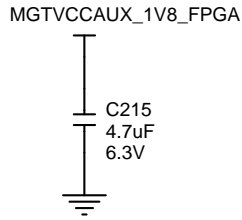
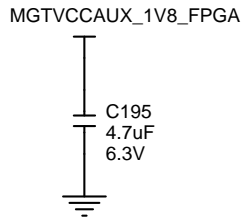
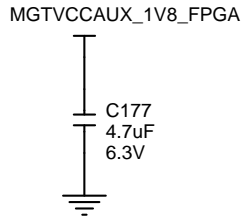
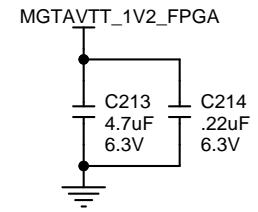
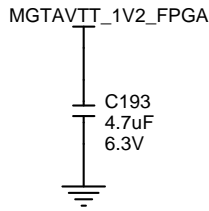
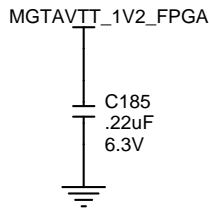
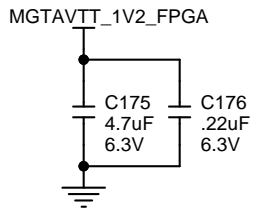
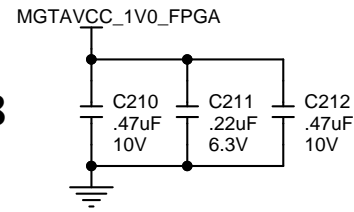
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QUAD 226



QUAD 228

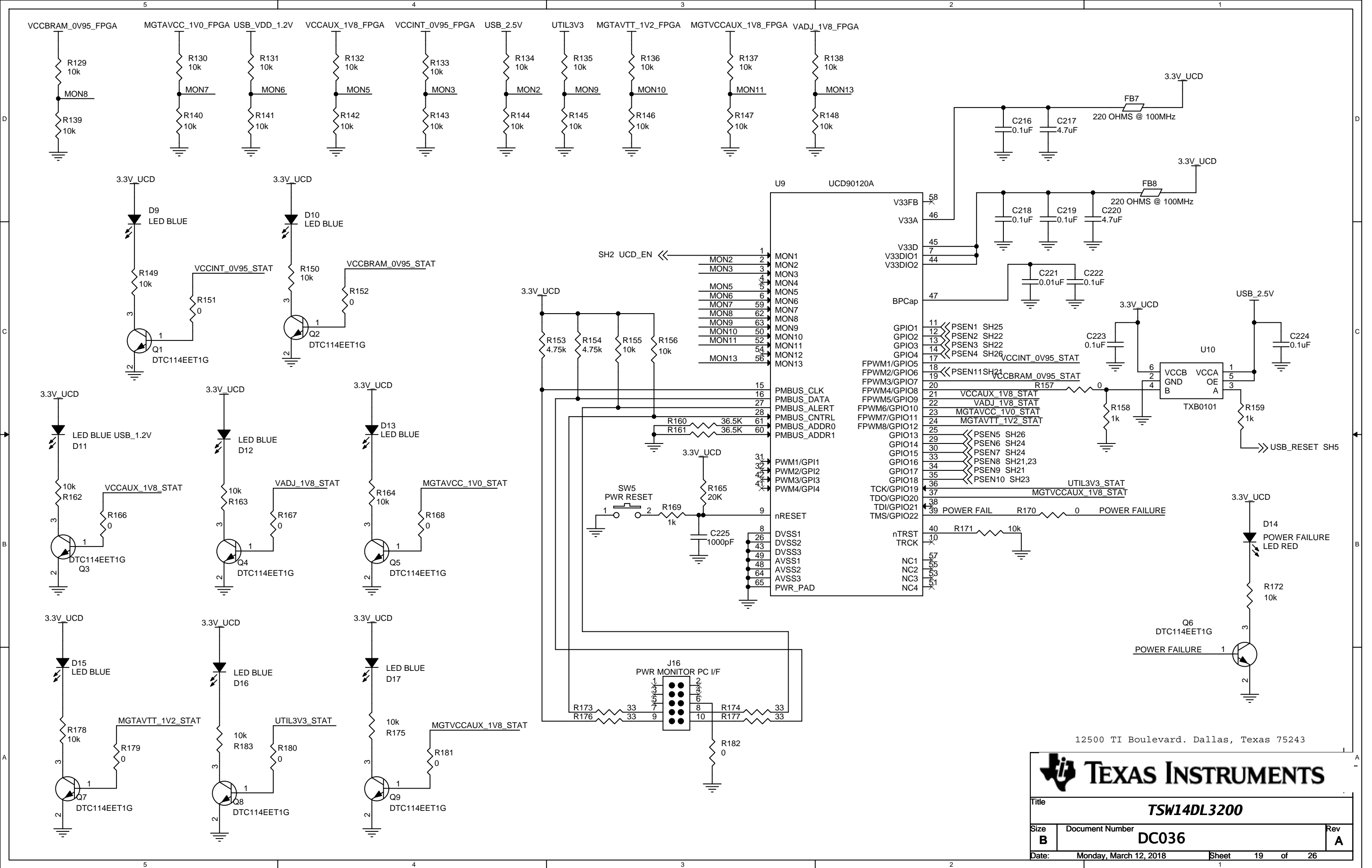


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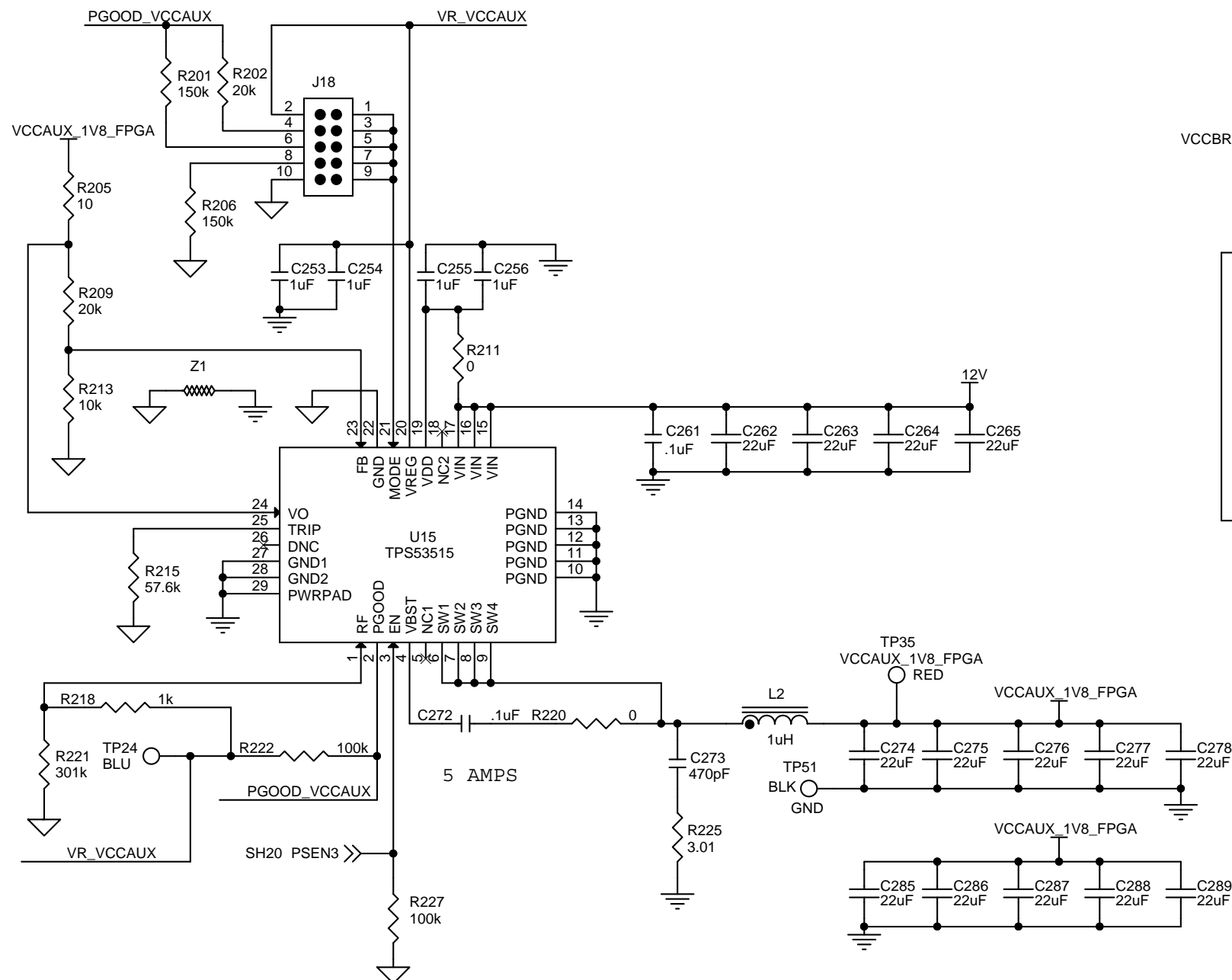


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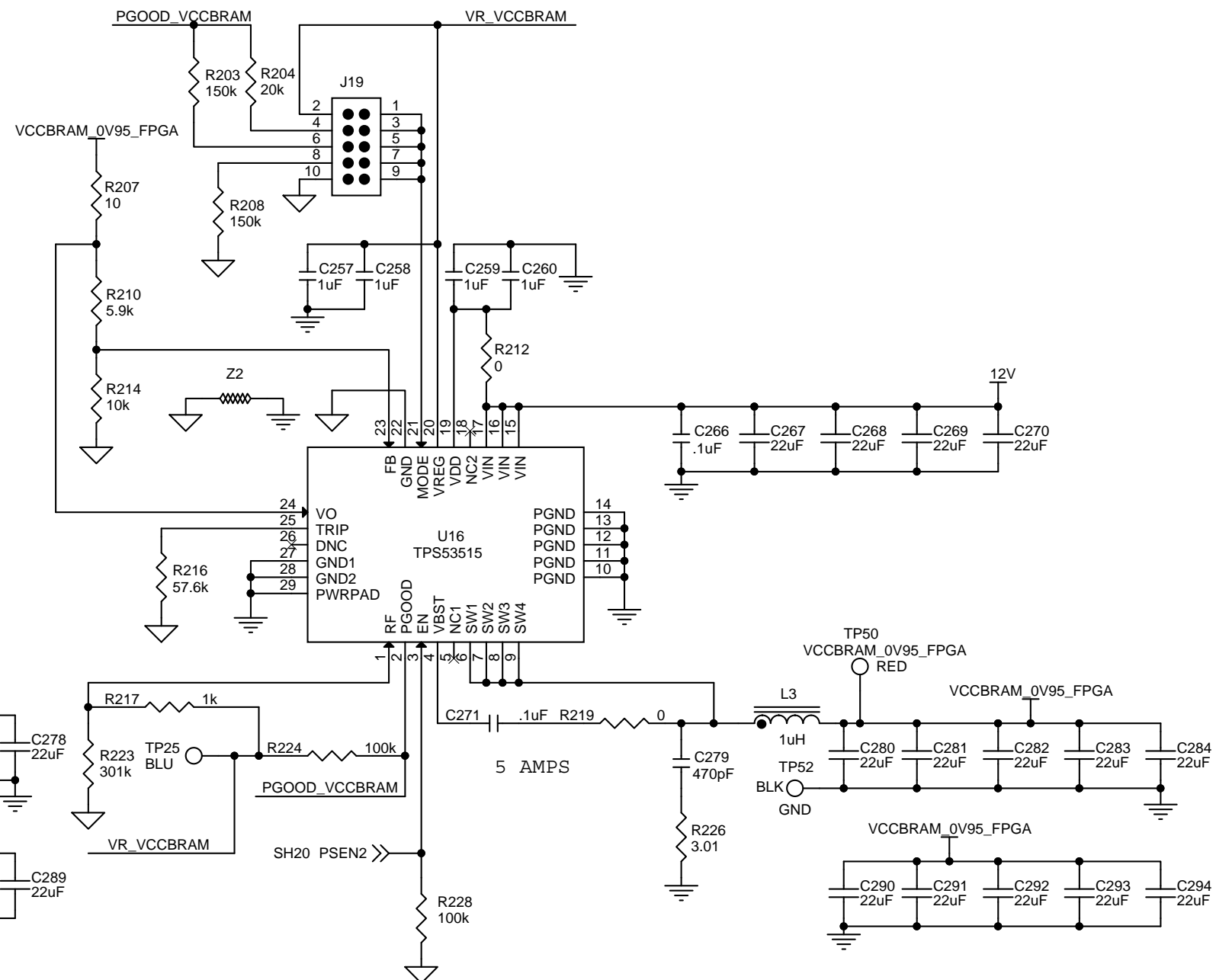
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VCCAUX FPGA 1V8



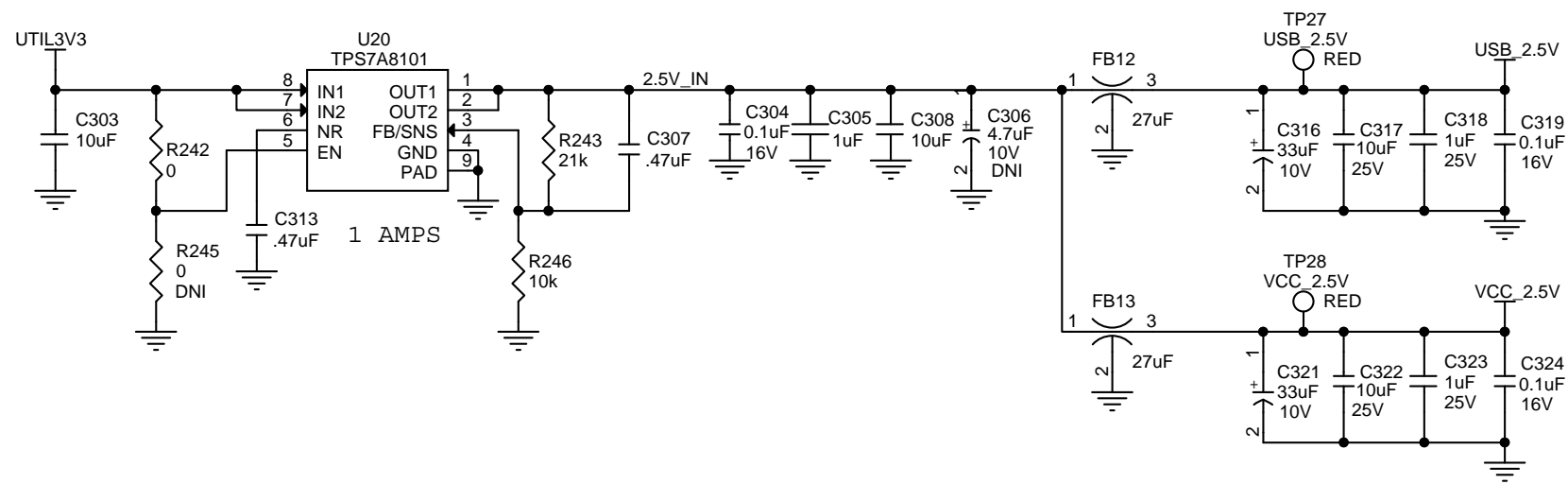
VCCBRAM FPGA 0V95



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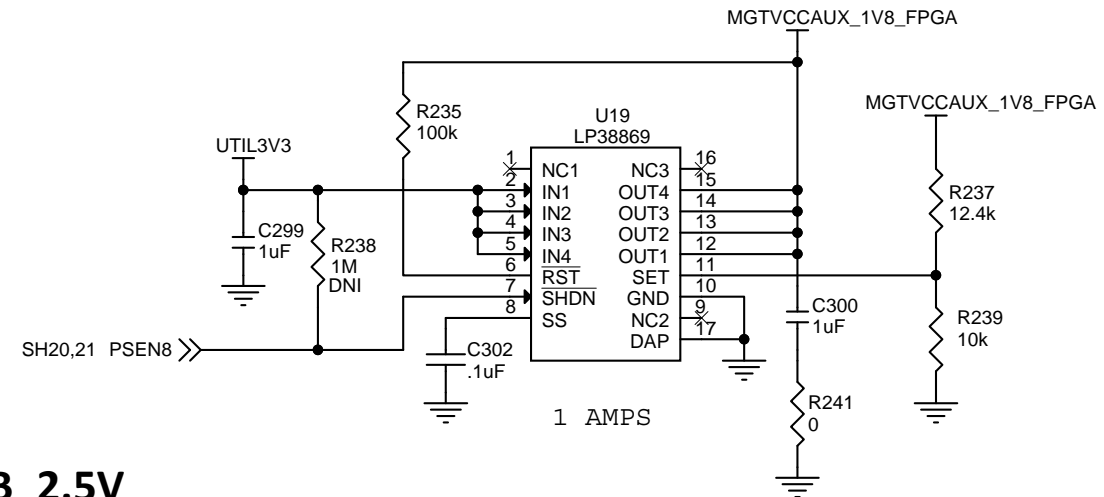
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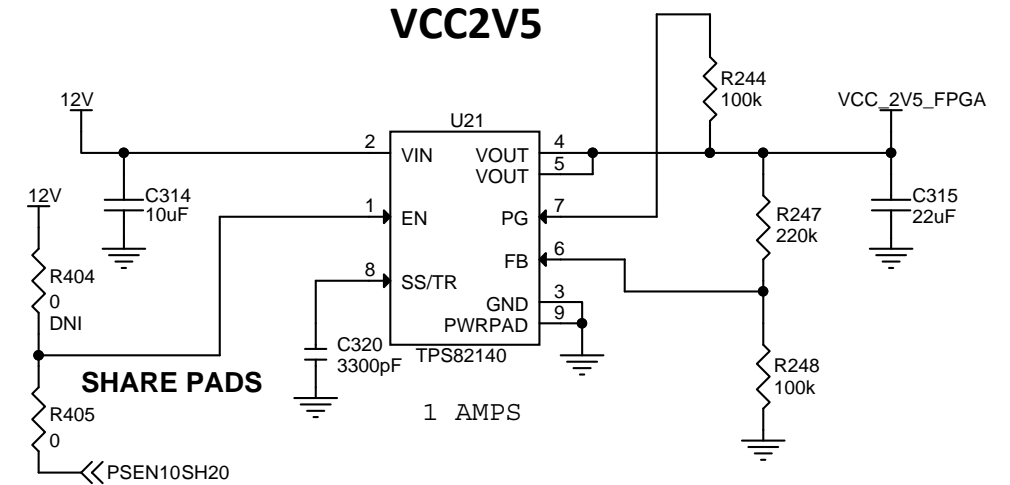
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VCC2V5

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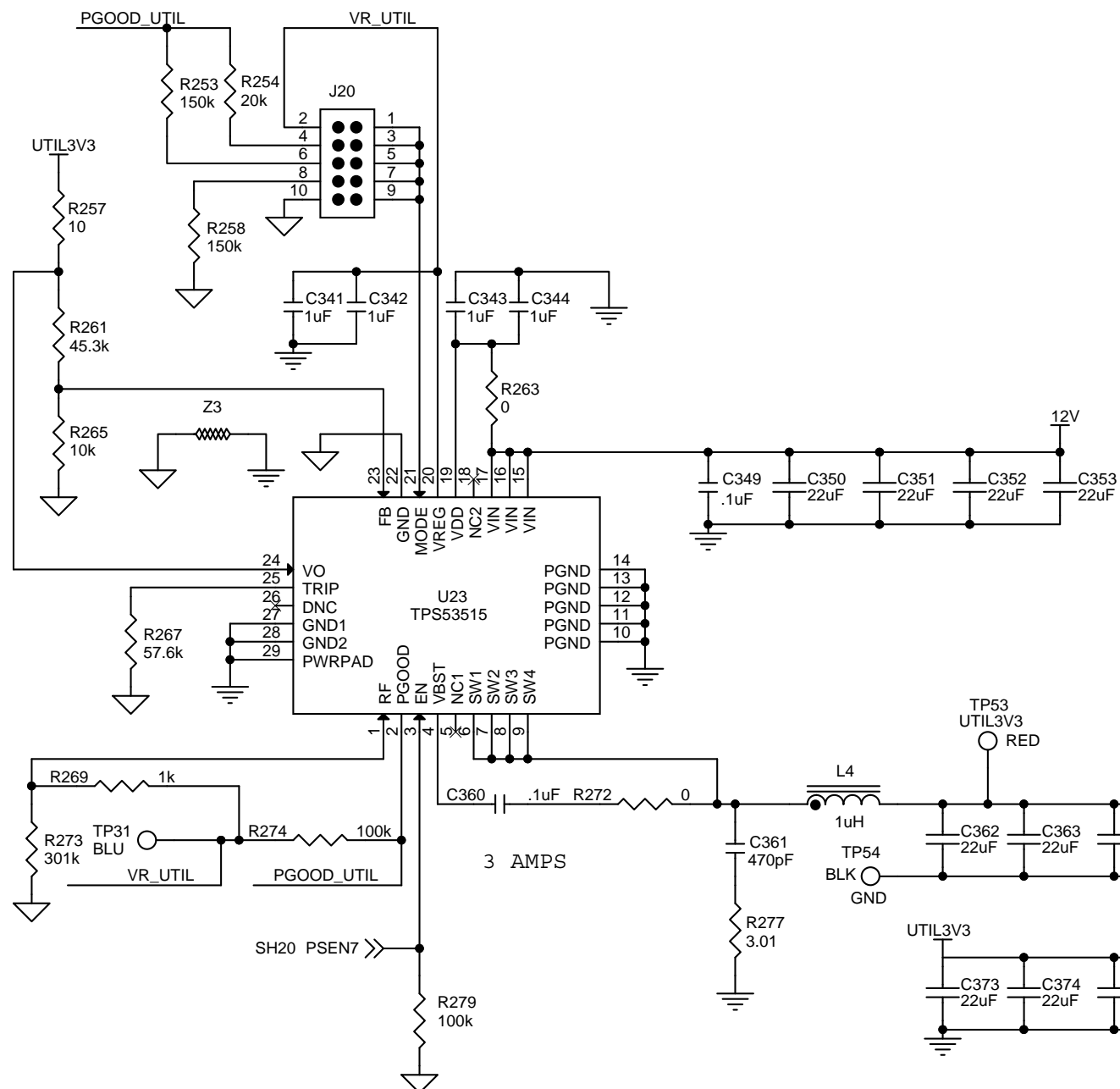


VCC2V5

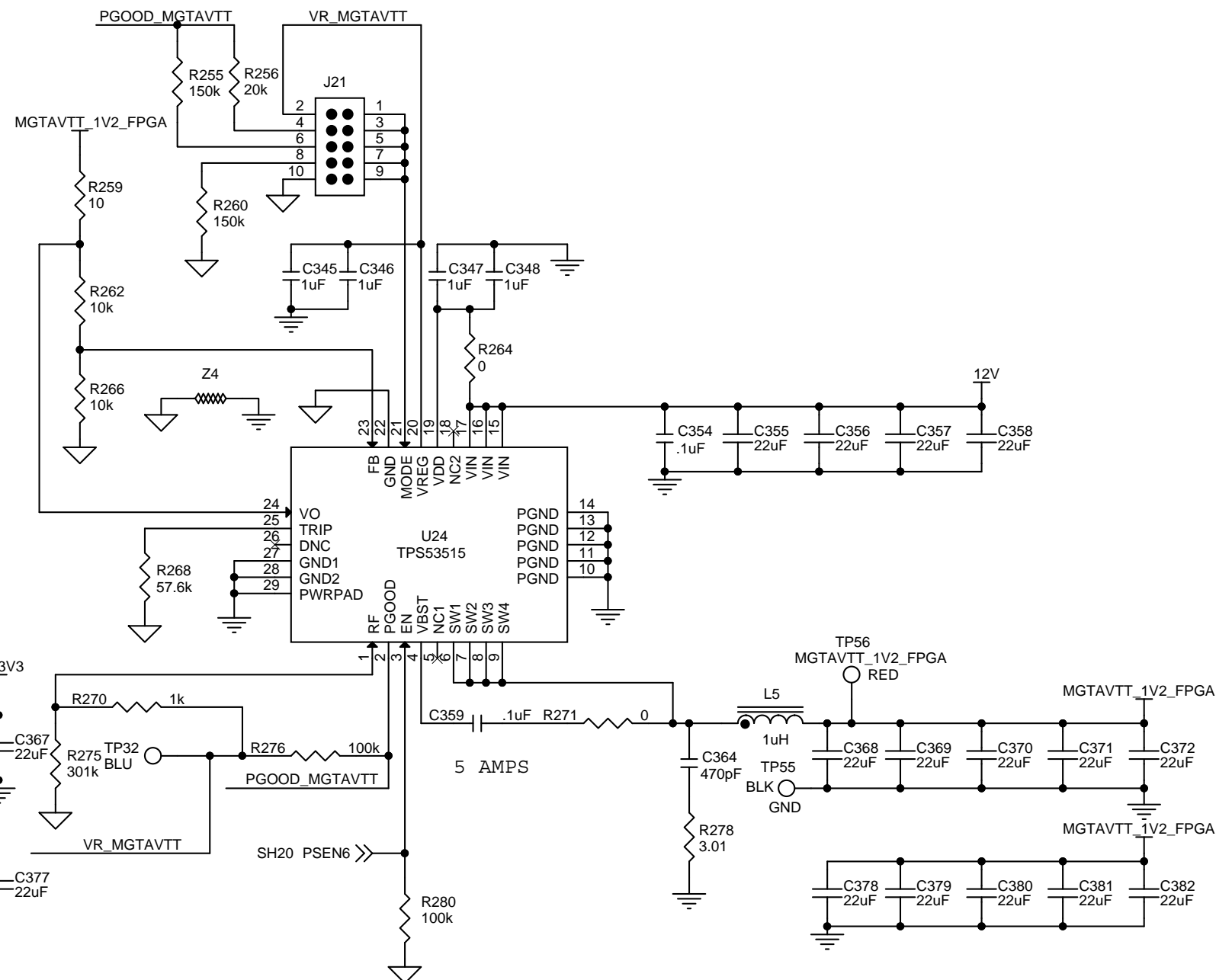


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UTIL 3V3



MGTAVTT FPGA 1V2



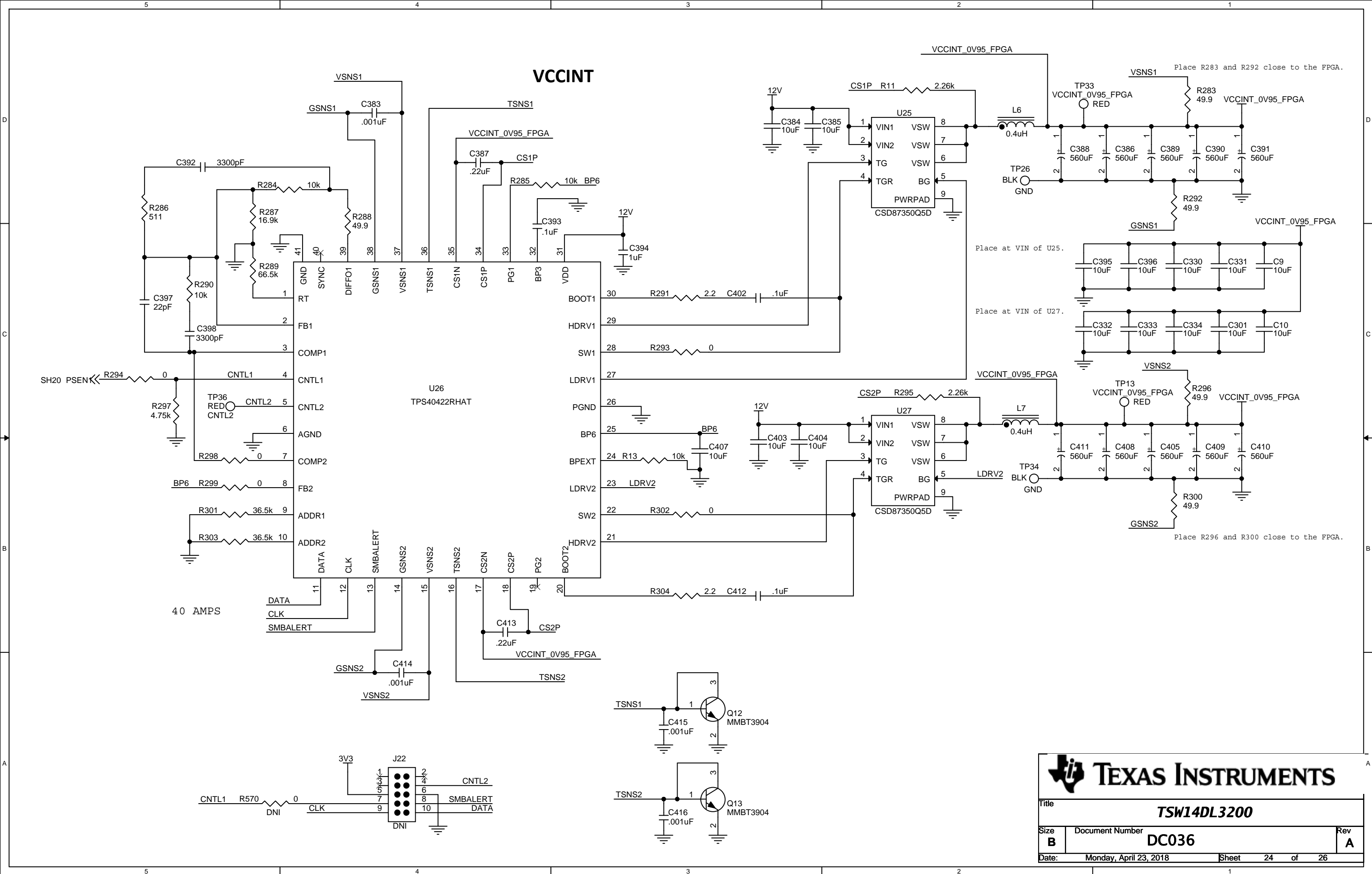
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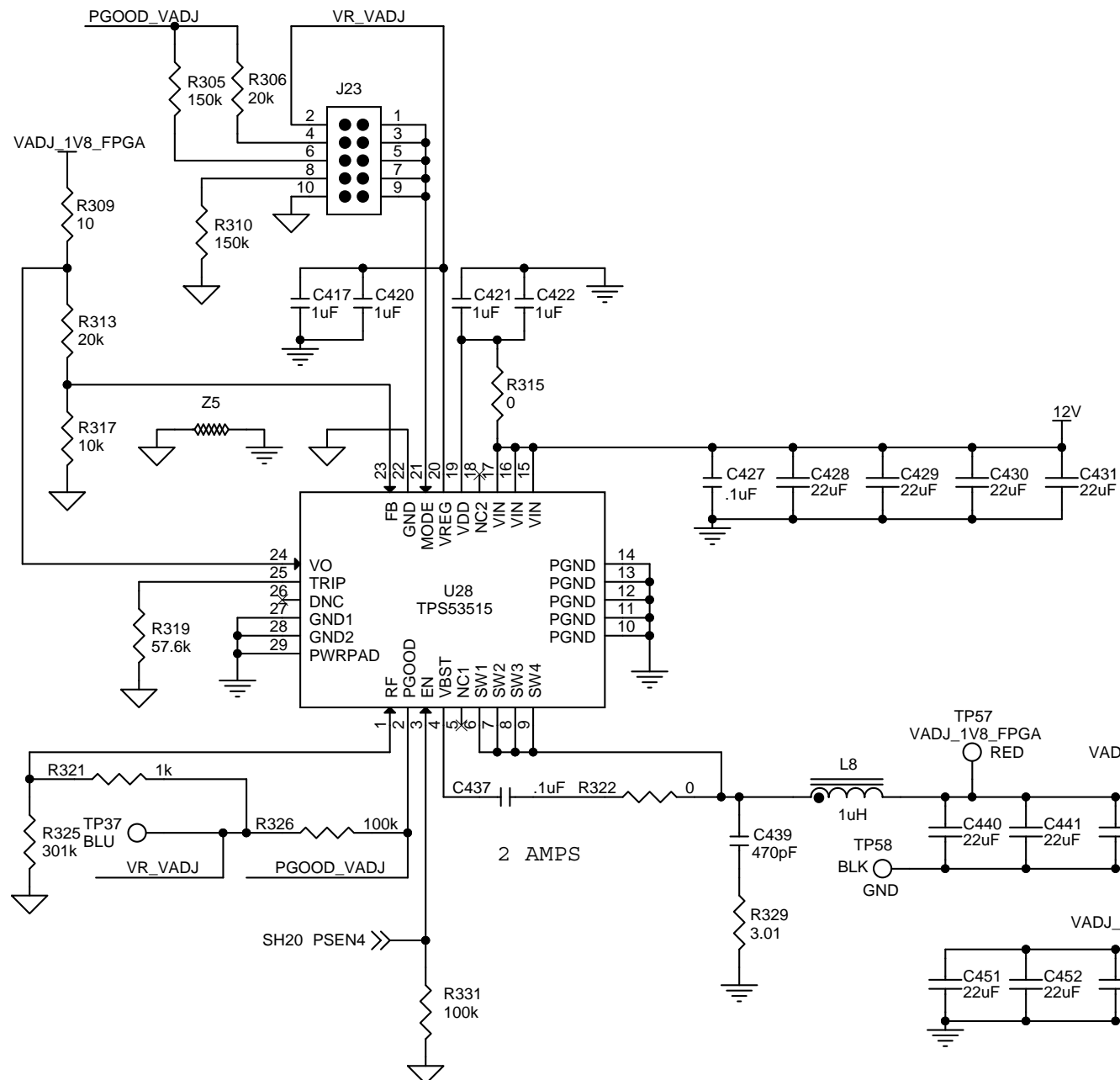
Title	<i>TSW14DL3200</i>
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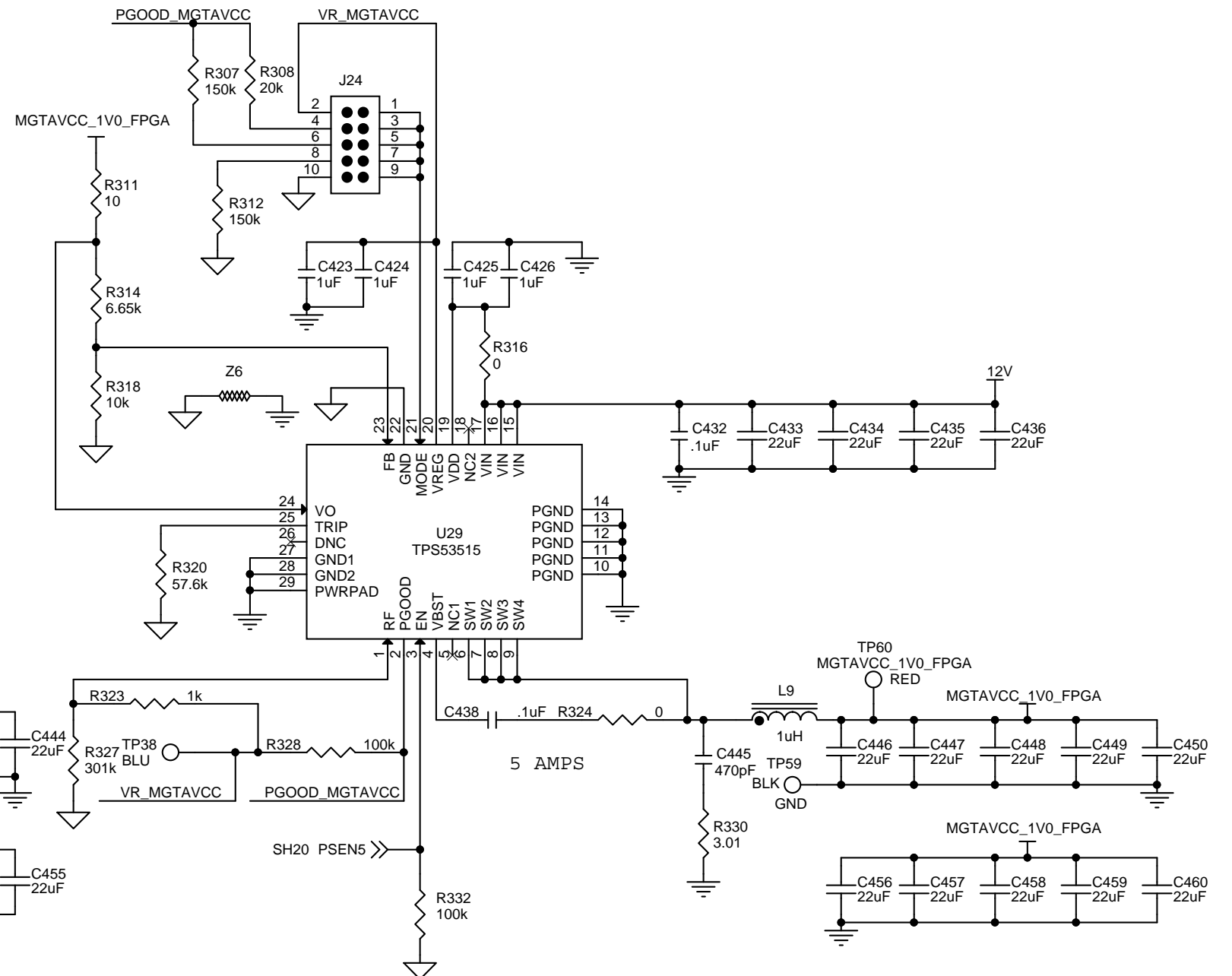
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VADJ FPGA 1V8



MGTAVCC FPGA 1V0



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FOR THE FPGA

Z_HEATSINK1

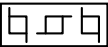


HEATSINK ASSEMBLY

MECHANICAL

PCB1

BARE BOARD, TSW14DJ3200



SCREW PANHEAD 4-40 x 3/4"

Z_SCREW1



SCREW PANHEAD 4-40 x 3/4"

Z_SCREW2



SCREW PANHEAD 4-40 x 3/4"

Z_SCREW3



SCREW PANHEAD 4-40 x 3/4"

Z_SCREW4



STANDOFF ALUM HEX 4-40 x 1 1/4"

Z_STANDOFF1



STANDOFF ALUM HEX 4-40 x 1 1/4"

Z_STANDOFF2



STANDOFF ALUM HEX 4-40 x 1 1/4"

Z_STANDOFF3



STANDOFF ALUM HEX 4-40 x 1 1/4"

Z_STANDOFF4

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TEXAS INSTRUMENTS

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