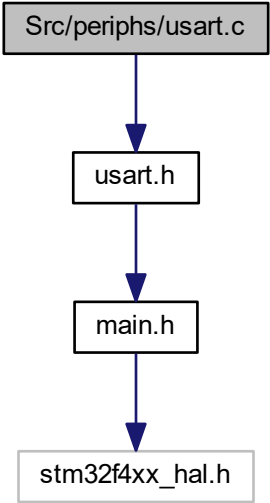


Src/periphs/usart.c



```
graph TD; A[Src/periphs/usart.c] --> B[usart.h]; B --> C[main.h]; C --> D[stm32f4xx_hal.h];
```

usart.h

main.h

stm32f4xx_hal.h