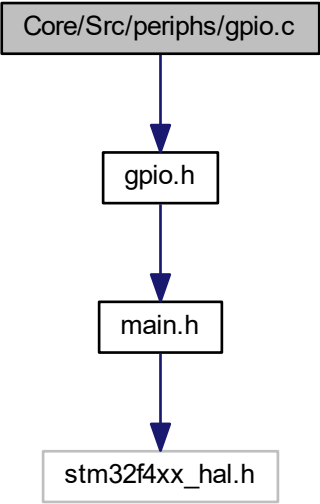


Core/Src/periphs/gpio.c



```
graph TD; A[Core/Src/periphs/gpio.c] --> B[ gpio.h ]; B --> C[ main.h ]; C --> D[ stm32f4xx_hal.h ]
```

gpio.h

main.h

stm32f4xx\_hal.h