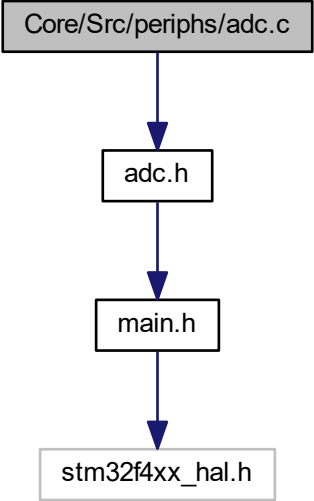


Core/Src/periphs/adc.c



```
graph TD; A[Core/Src/periphs/adc.c] --> B[adc.h]; B --> C[main.h]; C --> D[stm32f4xx_hal.h];
```

adc.h

main.h

stm32f4xx\_hal.h