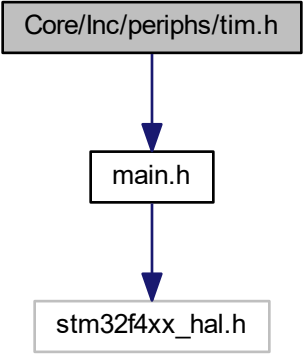


Core/Inc/periphs/tim.h



```
graph TD; A[Core/Inc/periphs/tim.h] --> B[main.h]; B --> C[stm32f4xx_hal.h];
```

main.h

stm32f4xx\_hal.h