



Block 7 internal peripherals 0xe000 0000

OXOFFF FFFF 512-mByte Block 6

fsmc

not used 0xc000 0000 512-mByte OXUTT TIT Block 5

registers 0%a000 0000 512-mByte 0x9fff ffff Block 4

fsmc Banks 384 0x8000 0000 0x7fff ffff 512-mB∨t∈ Block 3

fsmc Banks 1&2 0x6000 0000 0x5fff ffff 512-mByte

Block 2 peripherals

0x4000 0000 0x3fff ffff 512-mByte Block 1

spam

0x2000 0000 512-mByte 0x1fff ffff Block o

0x0000 0000

code

reserved 0x2002 0000 - 0x3fff ffff 0x2001 c000 - 0x2001 ffff (16 kB aliased by bit-banding)

sram 0x2000 0000 - 0x2001 bfff

option bytes reserved

reserved

system memory + otp reserved

> ccm data ram (64 kB data spam)

reserved flash reserved

aliased to flash, System memory or sram depending on the boot pins 0x1fff c008 - 0x1fff ffff 0x1fff c000 - 0x1fff c007 0x1fff 7a10 - 0x1fff 7fff

0x1, fff 0000 - 0x1 fff 7a0f 0x1001,0000 - 0x1ffe ffff 0×1000 0000 - 0×1000 ffff

0x0810 0000 - 0x0fff ffff 0x0800 0000 - 0x080f, ffff 0x0010 0000 - 0x07ff ffff

0x0000 0000 - 0x000f ffff

reserved

ahB3

ahB2

reserved

ah<sub>B1</sub>

reserved

apB2 high speed

reserved

apb1 low speed