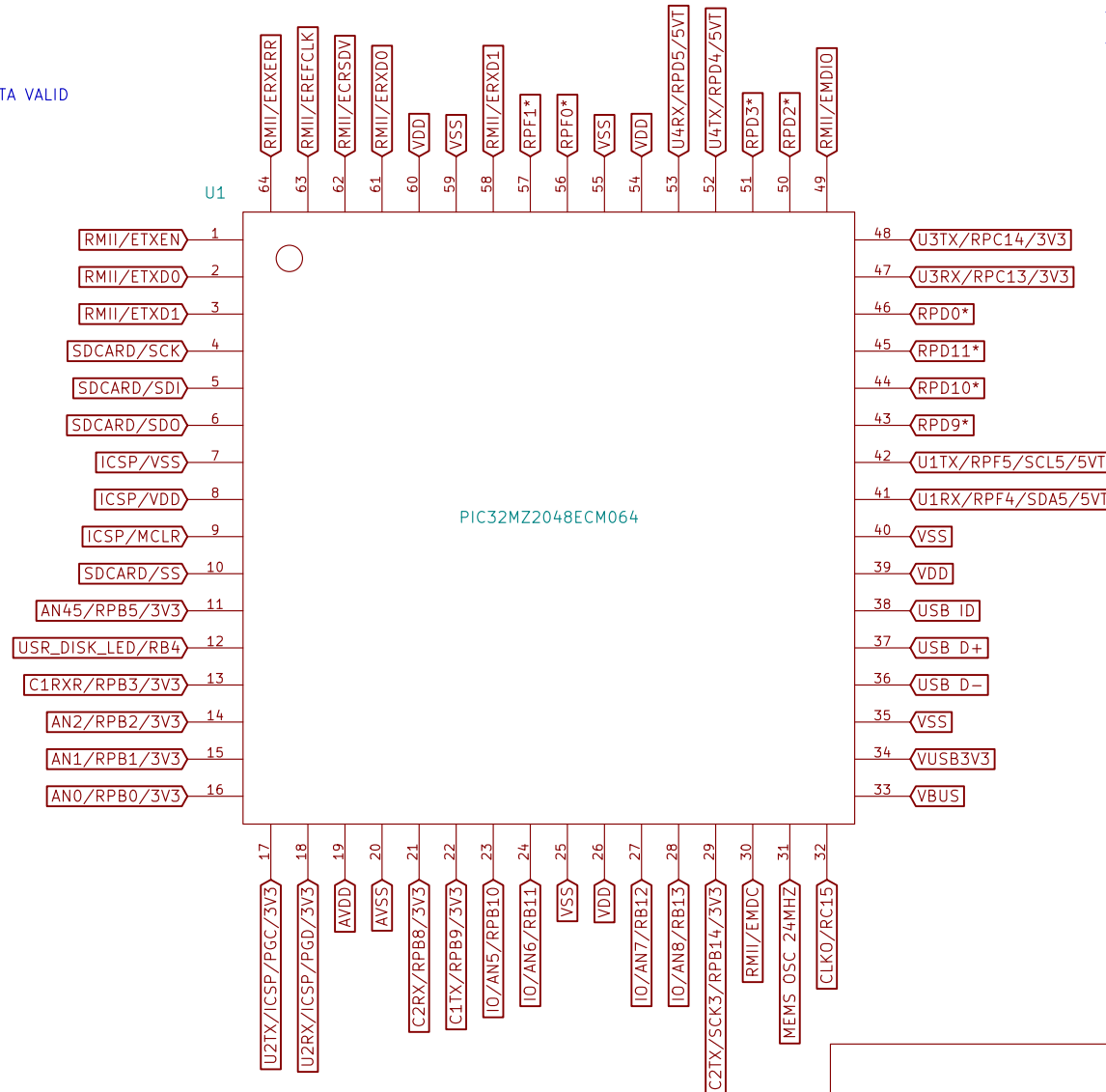


RMII INTERFACE  
FMIEN = 0, FETHIO = 1  
EMDC MANAGEMENT CLOCK  
EMDIO MANAGEMENT I/O  
ETXEN TRANSMIT ENABLE  
ETXD0 TRANSMIT DATA 0  
ETXD1 TRANSMIT DATA 1  
EREFCLK REFERENCE CLOCK  
ECRSDV CARRIER SENSE - RECEIVE DATA VALID  
ERXD0 RECEIVE DATA 0  
ERXD1 RECEIVE DATA 1  
ERXERR RECEIVE ERROR  
SMSC LAN8720 PHY

\* MII pins that are not used by the Ethernet module during RMII operation are not released, and therefore, lower priority functions on these pins are not available in this mode. However, higher priority functions on these pins, such as EBI and analog inputs (for ADC and Comparators), can still be used.

UART\_BUF\_SIZE 1024\* CHECK



Depending on the revision of silicon, a crystal oscillator cannot be used as the input to the Primary Oscillator (OSC1/OSC2 pins).  
Work around  
For Revision A3 and A4 silicon:  
Use an external clock or an internal FRC.

4x UART / SPI  
1x PHY ETHERNET 100M  
2x CAN 2.0B  
X AN  
X IO  
X PWM  
X OC  
1x FS USB 2.0

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**Title: PINOUT PREVIEW PIC32MZ2048ECM064**

Size: A4 Date: 20 mar 2015  
KiCad E.D.A. eeschema (2015-01-16 BZR 5376)-product

Rev:  
Id: 1/1