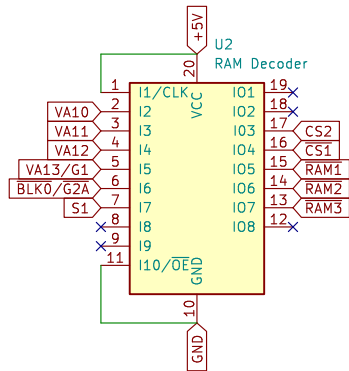
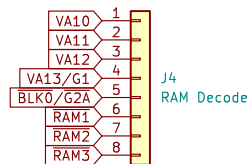


Generates the Chip Select and Output Enable signals for the 6264 RAM.  
Replaces the 74LS138 RAM decoder.

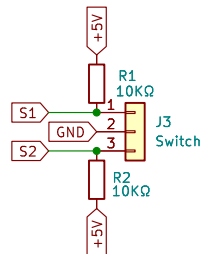


Signals to/from RAM decoder (74LS138)

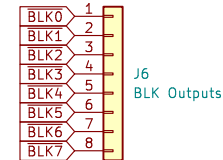
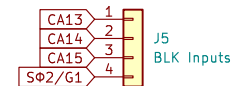


Switch between unexpanded, +3KB expanded or +24KB expanded

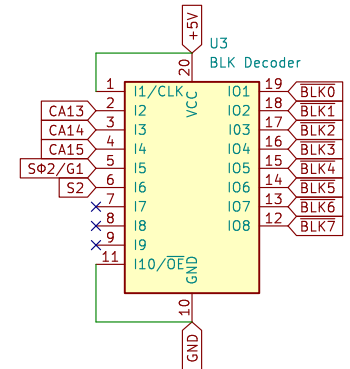
S1+S2=high -> unexpanded  
S1=low, S2=high -> +3KB  
S1=high, S2=low -> +24KB



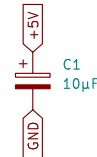
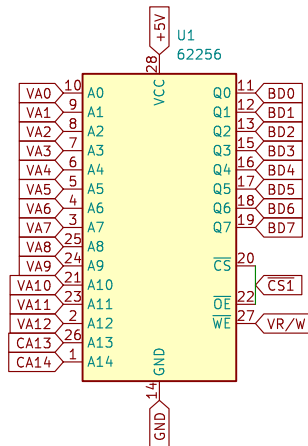
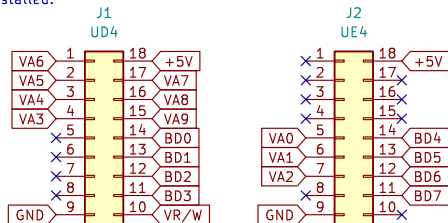
Signals to/from BLK decoder (74LS138)



Generates the BLK select signals.  
Replaces the 74LS138 RAM decoder.



Original 2114 RAM chips are removed from the VIC.  
Two are replaced with IC sockets so daughterboard can be installed.



[www.youtube.com/@Brfff](https://www.youtube.com/@Brfff)

Brett Hallen

Sheet: /

File: VIC20\_Internal\_32KB\_RAM.kicad\_sch

**Title: Commodore VIC-20 32KB RAM Expansion**

Size: A4

Date: 13-May-2025

Rev: A

KiCad E.D.A. 9.0.0

Id: 1/1