Genera	اد	Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Name of the boar	d Bubblegum96		(Limer 0)			
	Email address (primary)	d Bubblegum96) hzhang@ucrobotics.com) yvonne.wang@actions-semi.com					
	Target Specification	n CE v1.0					
	Form Factor	CE v1.0 (standard) - 85 x 54 mm	+/- 0.25mm error margin allowed				Using 3D templates for the appropriate form-factor
				Validation			
Docum	nentation Schematic published	Response Yes	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Text Description
Board: Board	Reference Manual published	Yes	Board reference manual should follow templat	te			
SoC te	chnical reference published	Yes	SoC TRM published as per guidelines	Fail	Technical reference manual is sp	a Pass	
Memo	ry and Storage	Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Onboard DRAM (GB)) 2	CE: >= 0.5GB; EE: >= 1GB or DIMM				
		d microSDHC	EE: Mandatory if no onboard DRAM				
	SD Card Boot from SD Card	d microSDHC	CE: Mandatory		#VALUE!		
			CE: optional, only if SD card is not bootable; EE	8			
	Boot ROM Boot ROM size (MB)	1 No	mandatory CE: 8MB (optional), EE: 64 MB (mandatory)		#VALUE!		
	Onboard Flash storage	e Yes	CE: 8MB (optional), EE: 64 MB (mandatory) Optional				
0	nboard Flash storage size (GB))	Optional 8 Optional				
	SATA interface	None	Optional				
				Validation			
Netwo	wifi protoco Bluetooth 4.0 LE	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description
	Wifi protoco	il 802.11 g/n F Yes					
	Ethernel	t No	EE: mandatory				
Debug	ging	Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description
	UART	Response I No	Comments EE: micro-B USB in specified location	,,		(
u	ART 1 (LS expansion interface)	Yes	default mandatory UART CE: optional; EE: NA				
u	ART 2 (LS expansion interface)		CE. optional; EE: NA				
	4 User LEDs						
	WIFI LED BT LED	O No					
	BT LED	Yes	Optional				
USB		Response	Comments	Validation Results (Linera)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Port 1 - Host (Protocol)	USB 3.x	EE: Host Port 1 & 2 are stacked connectors				
	Port 1 - Host (Connector)) Type A					
	Port 2 - Host (Protocol)	USB 2.x	EE: Host Port 1 & 2 are stacked connectors				
	Port 2 - Host (Connector) Port 3 - Slave (Protocol)) USB 2.x					
	Port 3 - Slave (Connector)) micro-AB (for OTG)					
	Port 4 - Host (Protocol)) USB 2.x					
	Port 4 - Host (Connector) Other					
				Validation	Linaro comments		
Displa		Response	Comments CE: Audio cupport for at least 1 channel in	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Display Type		CE: Audio support for at least 1 channel is mandatory				
LUD	Display Connector	r Type A (full-size)					
Print	I DSI port on HS expansion bus MIPI DSI lanes	110	4				
Camer	ъ	Resnonse	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description
Cumer	MIPI CSI-2 ports	Response	(Review) CE: Port 1 should be on CSI0 port (1-4 2 lanes). CSI1 port supports 1-2 lanes	Results (Ellier 0)	Linus o commency	Timet Resolts (Emarcy	, selection text beautiful.
	MIPI CSI 2 ports	•	2 lanes). CSI1 port supports 1-2 lanes				
				Validation			
Audio			Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Test Description
	Audio channels (BT)		CE: IZS/PCM channel should be provided on LS 2 expansion interface				
	Audio channels (Display))	2				
				Validation			
DC Por	wer	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Test Description
	Power source	e DC Power Jack (8-18V)					
				Validation	Linaro comments		
Button	ns / Switches / Jumpers Power on/off	Response	Comments Mandatory	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Test Description
	Power on/off Hard reset	t Yes	Mandatory Mandatory				
	Auto power up	Through Switch	Mandatory				
	USB mode change	n NA	Optional, to change mode of the OTG port				
				Validation			
Expan	sion header Low-speed expansion header	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description
			Signals to be exposed as per spec	se .			
	High-speed expansion header	Yes	CE: mandatory; EE: NA, Signals to be exposed a per spec				
				Validation			
Miscel	llaneous External Fan	Response	Comments	Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description
	External Far	n e None	EE: location specified but optional				
	PCle	· · · · · · · · · · · · · · · · · · ·	L				
				Validation	Linaro comments		
Softwa	Kernel oit tree UD	Response L git://git.linaro.org/people/paul.liu/vendor-kernel.g	Comments	Results (Linaro)	Linaro comments	rinal Results (Linaro)	Validation Test Description
	Kernel version	n v3.10					
	Kernel compliance Leve	l Other	Level definitions as per spec	Fail	3.10 doesn't correspond to the l	25	
			According to the ones				
	ootloader bits are open source	No	According to the spec, everything after SoC reset should be available as open source				
All Di	stage bootloader git tree URI	L git://git.linaro.org/people/paul.liu/vendor-uboot.g	e.g. UEFI/EDK2 or u-boot				
Last	Fastboot protocol support User can update bootloader						
Last	User can update bootloader bility to unbrick the board w/o special hardware						
Last							
Last		Needs usersnare binary blobs					
Last							
Last	GPU acceleration Camera ISP	Needs userspace binary blobs					
Last	GPU acceleration Camera ISP						
Last	GPU acceleration Camera ISP	P Needs userspace binary blobs P NA a Needs userspace binary blobs					
Last	GPU acceleration Camera ISF DSP Multimedia	NA a Needs userspace binary blobs		Validation			
Last	GPU acceleration Camera ISF DSF Multimedia	P NA a Needs userspace binary blobs Response	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Text Description
Last	GPU acceleration Camera ISP DSF Multimedia are Functionality Boot to Graphical U ks acceleration usino Soc GPU ks acceleration usino Soc GPU	Response I Yes J Yes	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Text Description
Possib Softwa	GPU acceleration Camera ISP DSF Multimedia are Functionality Boot to Graphical U ics acceleration using SoC GPU.	NA Needs userspace binary blobs Response If Yes J Yes	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)) Validation Text Description
Possit Software Graph	GPU acceleration Camera ISP DSF Multimedia are Functionality Boot to Graphical U ks acceleration usino Soc GPU ks acceleration usino Soc GPU	NA Needs userspace binary blobs Response If Yes J Yes	Comments	Validation Results (Linaro)	Linaro comments	Final Results (Linaro)	Validation Text Description

Audio playback support for AV media files via AZDP Bluetooth and display audio Yes			
Networking using all onboard interfaces and USB ethernet interfaces Yes			
Software power shutdown and reboot options Yes			
Appropriate activity LEDs (e.g. Bluetooth, WiFi) Yes			
Additional storage is usable: eMMC (if fitted), SD, SATA, USB Yes			