

To Due

**CONN\_8 P1**

- 1 RFU1
- 2 IOREF
- 3 RESET
- 4
- 5
- 6
- 7
- 8 AM-VIN

**CONN\_10 P5**

- 1 SCL1
- 2 SDA1
- 3 AREF
- 4
- 5 D13
- 6 D12
- 7 D11
- 8 D10
- 9 D9
- 10 D8

**CONN\_8 P2**

- 1 A0
- 2
- 3
- 4
- 5
- 6
- 7 A7
- 8

**CONN\_8 P3**

- 1 A8
- 2 A9
- 3 A10
- 4 A11
- 5 A12
- 6 A13
- 7 A14
- 8 A15

**CONN\_8 P6**

- 1 D7
- 2
- 3
- 4
- 5
- 6
- 7
- 8 D0

**CONN\_8 P7**

- 1 D14
- 2
- 3
- 4
- 5
- 6
- 7
- 8 D21

**CONN\_18X2 P4**

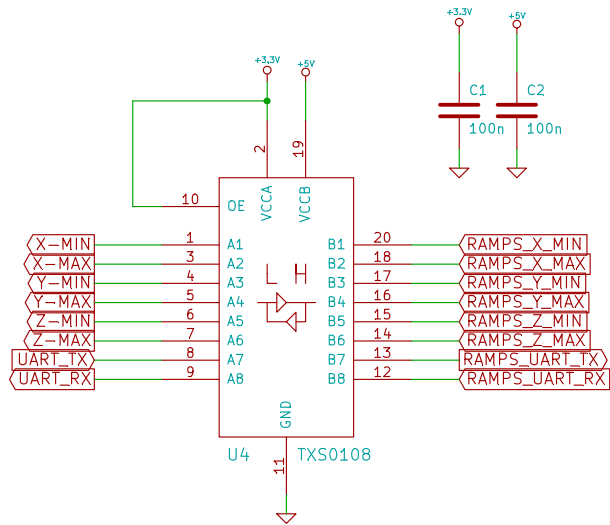
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**CONN\_3X2 P8**

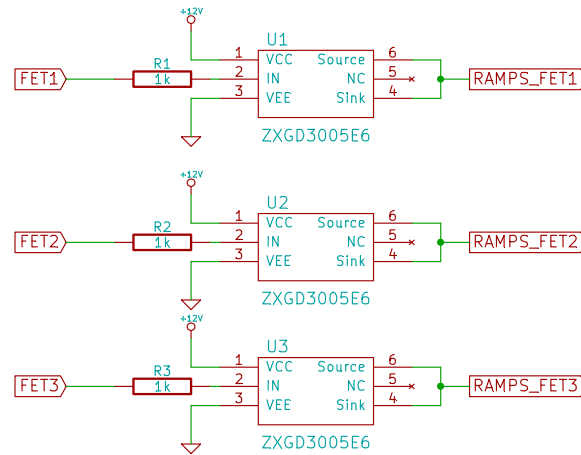
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**Note:** unused Digital and Analog pins are passed straight through to RAMPs

[illegible]



3.3V/5V Level Translation



Gate drivers

File: RIB_interface.sch		
Sheet: /Interfacing/		
<b>Title:</b>		
Size: A4	Date: 2 feb 2013	Rev:
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