Time	1	0 sec 200	sec 300	) sec 400 s	ec 500	sec 600 sec	700 ss	ec 800 séc	90 se	t 100 set	1100 sec	1200 se	d 1300 se	d 1400 sec	1500 se	c 1600 sec
clk=0																
Program Counter {																
PC_NEXT[15:0]=xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx	xxxxxxxxx+	000000000000000000		(00000000000000000000000000000000000000		(0000000000001100		(0000000000010000		X0000000000010100		(0000000000011000		0000000000000000		0000000000+
} Program Counter																
Instructions {																
RD[15:0]=0000000000000000	00000000000	00000 (000010	1100110000	000100101101	0000	0010001011010	000	0011001011010	000	0000101100110	000	000000010100	0010	000000000000000000000000000000000000000	0011	0000101100+
} Instructions																
Register File {																
A1[2:0]=000	000	(110		<b>X</b> 010						110		000				110
A2[2:0]=000	000	100		<b>X</b> 011						100		101		000		100
A3[2:0]=000	000	101		001						101		000				101
RD1[15:0]=00000000000000000	00000000000	00000 (000000	0000000110	000000000000000000000000000000000000000	0010					00000000000000	110	000000000000000000000000000000000000000	0000			0000000000+
RD2[15:0]=00000000000000000	00000000000	00000 (000000	0000000100	<b>X</b> 0000000000000	0011					00000000000000	100	0000000000000	1010	00000000000000000	0000	0000000000+
WD3[15:0]=00000000000000000	00000000000	00000 (000000	0000001010	1111111111111	1111	00000000000000	010	(00000000000000	011	00000000000001	010	000000000000000000000000000000000000000	0000			0000000000+
WE3=1																
} Register File																
ALU {																
Result[15:0]=00000000000000000	00000000000	00000 (000000	0000001010	1111111111111	1111	00000000000000	010	(00000000000000	011	00000000000001	010	000000000000000000000000000000000000000	0000			0000000000+
} ALU																
Control Unit {																
ALUOp[1:0]=00	00											11		00		
ALUSrc=0																
Jump_EN=0																
MemRead=0																
MemToReg=0																
MemWrite=0																
RegDst=1																
RegWrite=1																
op[2:0]=000	000											010		<b>X</b> 011		000
} Control Unit																
Data Mem {																
A[15:0]=0000000000000000	00000000000	00000 (000000	0000001010	1111111111111	1111	00000000000000	010	00000000000000	011	00000000000000000001	010	000000000000000000000000000000000000000	0000			0000000000+
RD[15:0]=0000000000000000	00000000000	00000														