

# **APP N05-3**

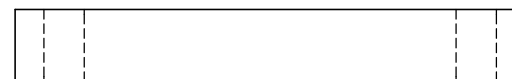
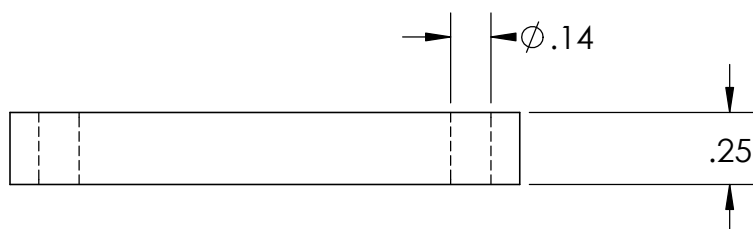
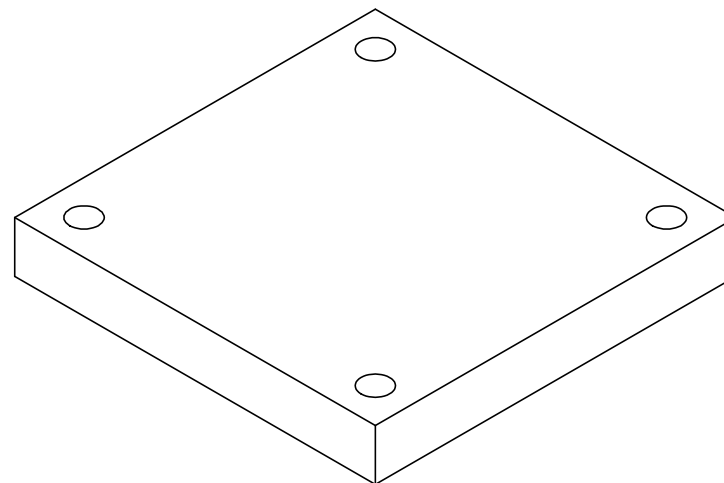
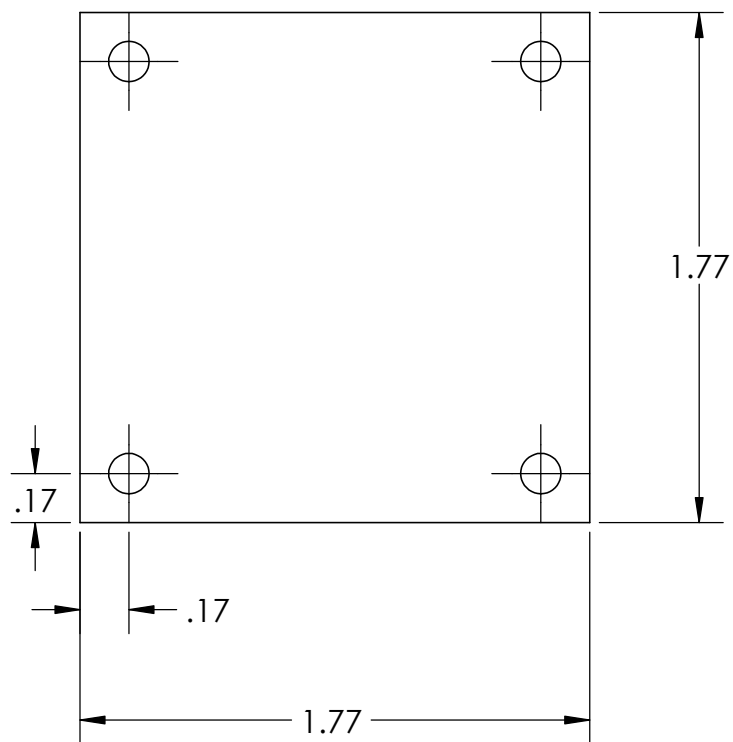
Engineering 1282H

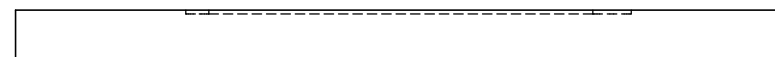
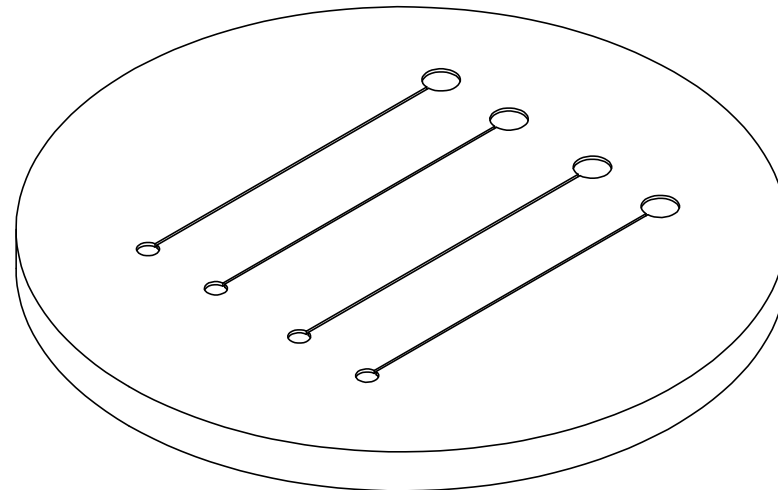
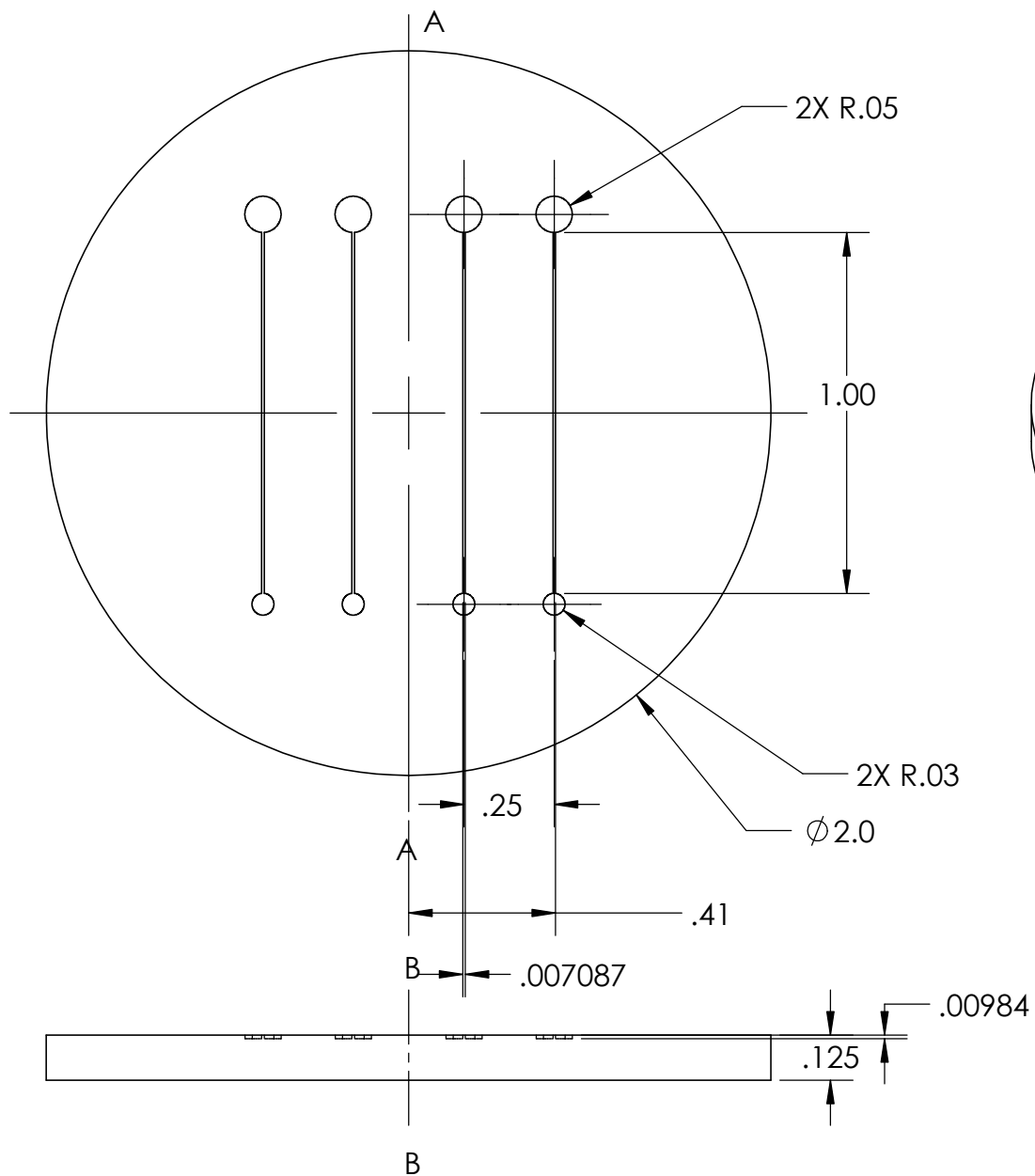
Spring 2020

**Team 4**

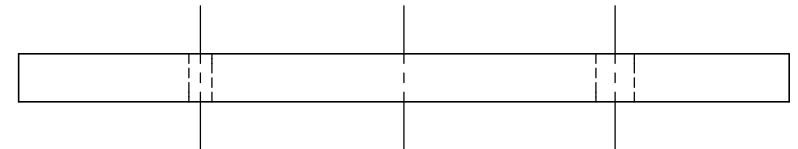
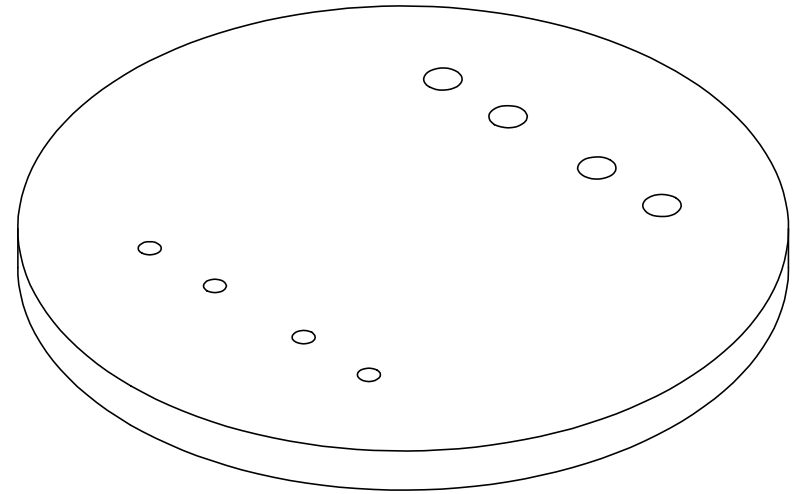
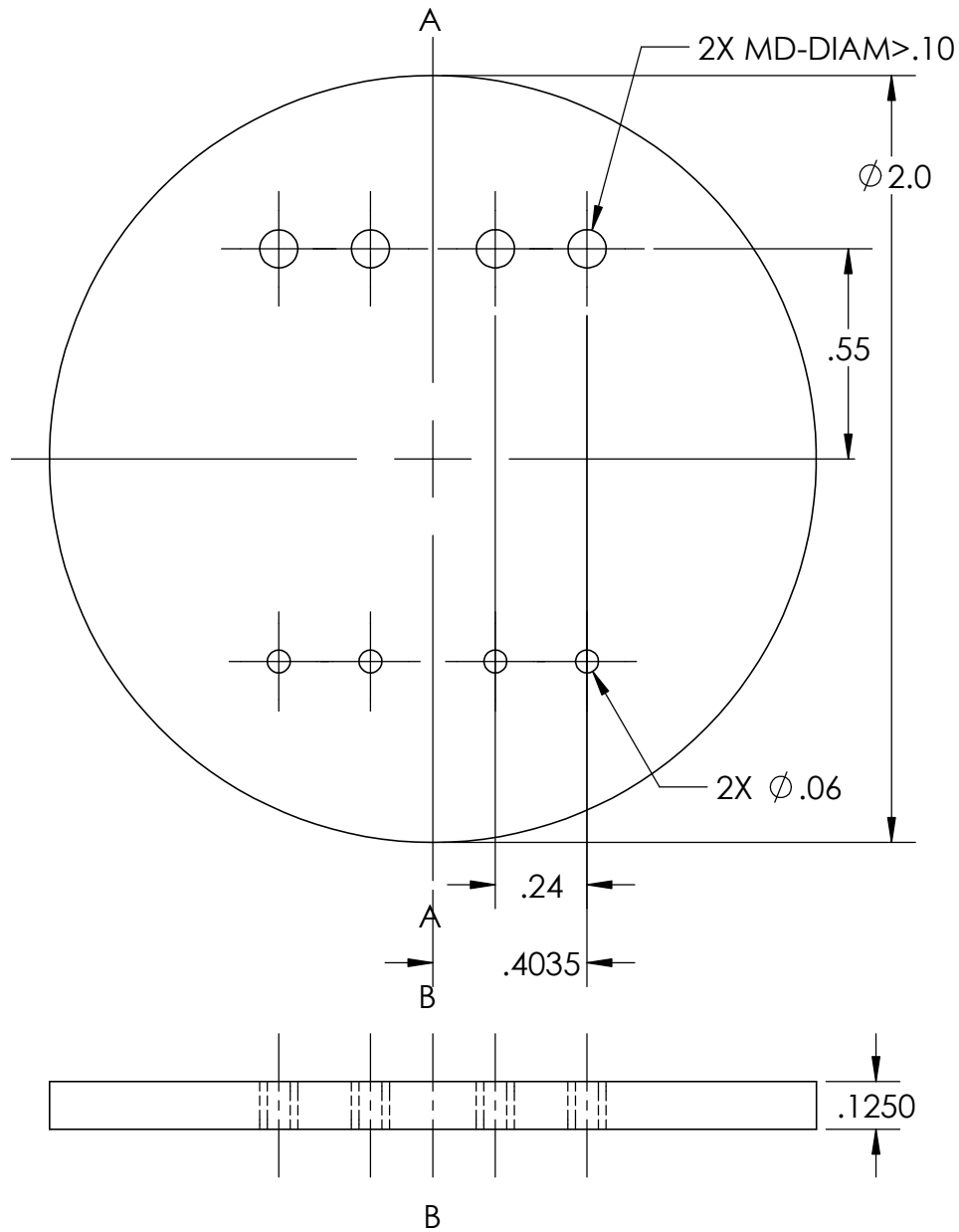
Parke 12:40 PM

Date of Submission: 2/20/20

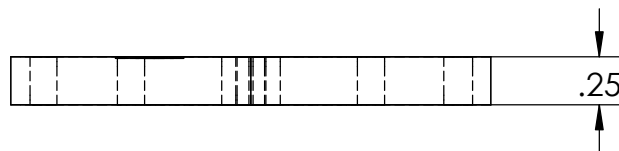
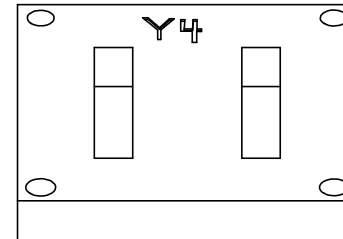
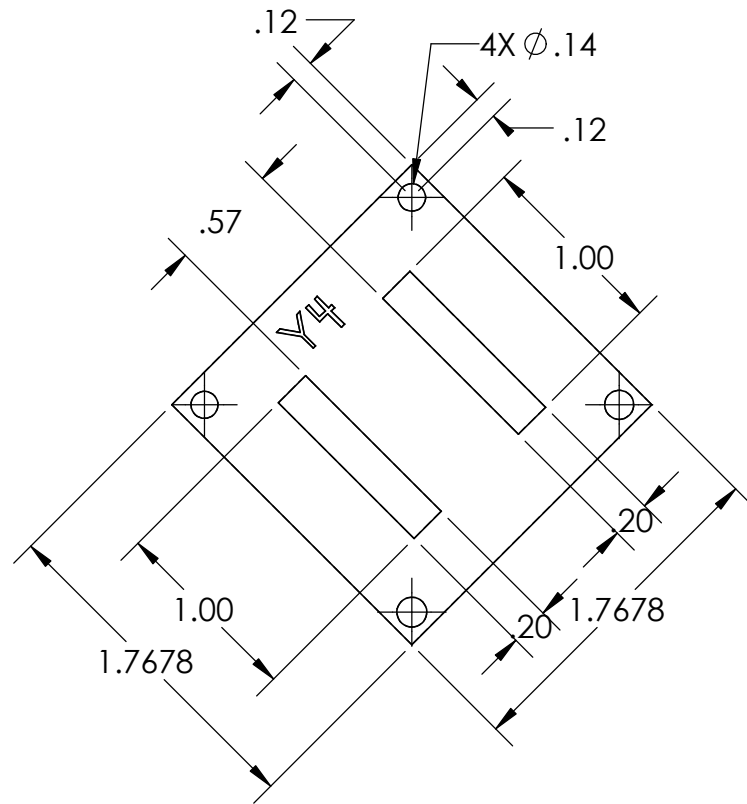


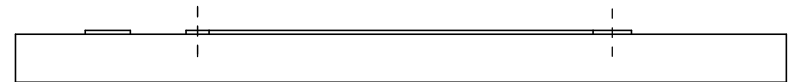
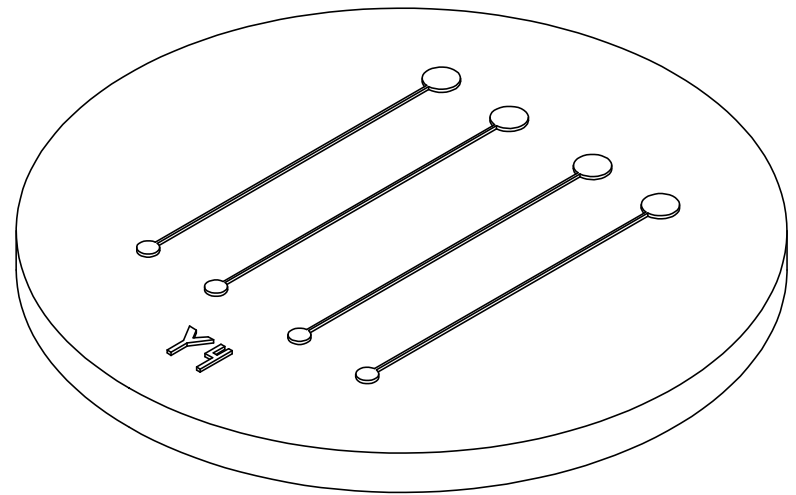
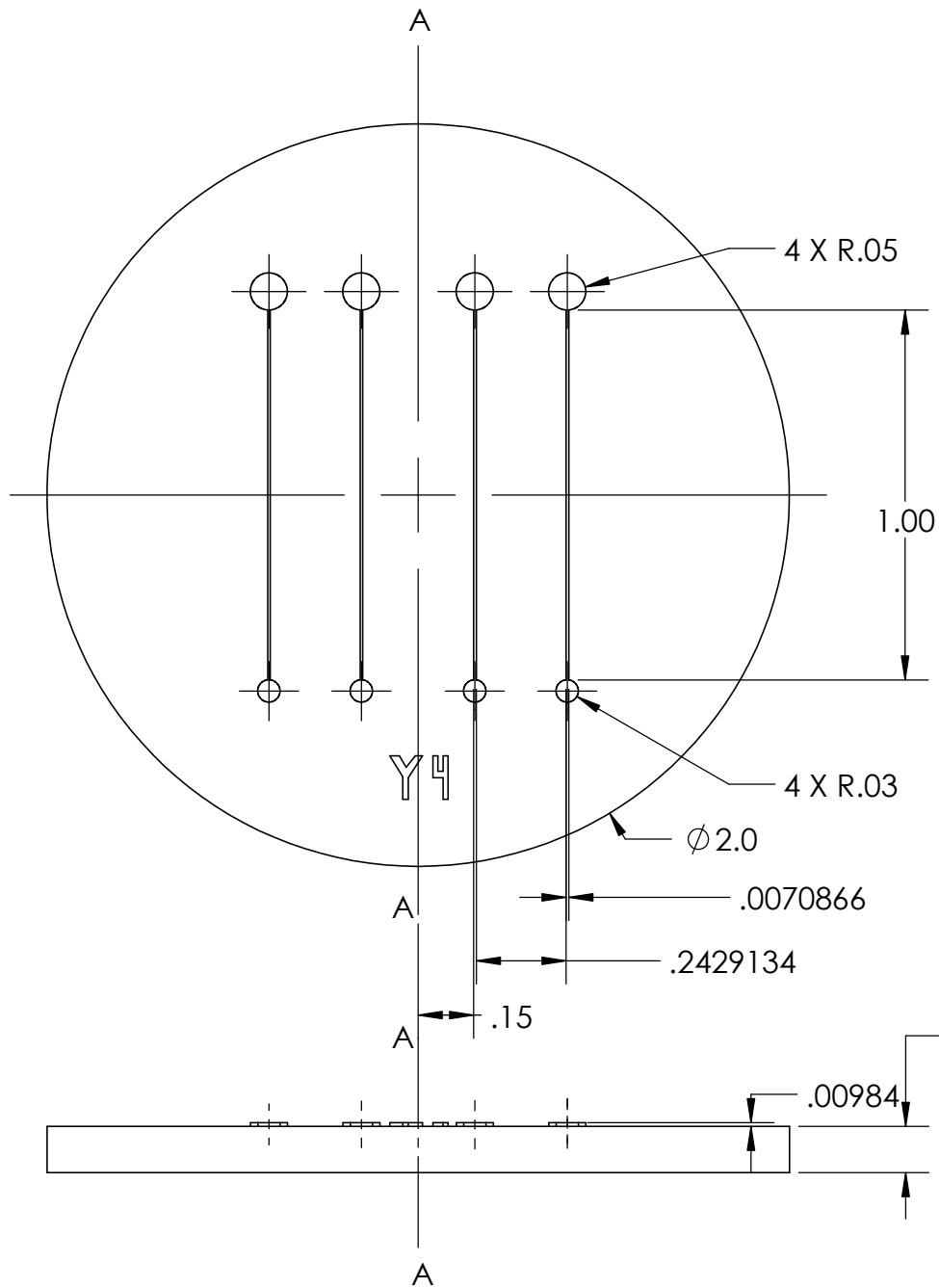


Chip is Symmetric along lines A-A and B-B



The device is symmetric along the lines A-A and B-B





Sysmmetry can be assumed about Axis A-A excluding team ID