

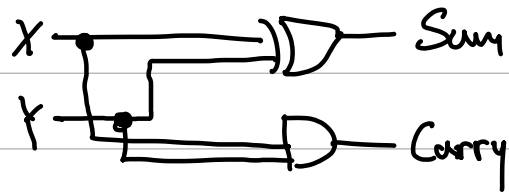
4. Combinational Logic

Adder

X	Y	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

$$\text{Sum} = X \oplus Y$$

$$\text{Carry} = X \cdot Y$$

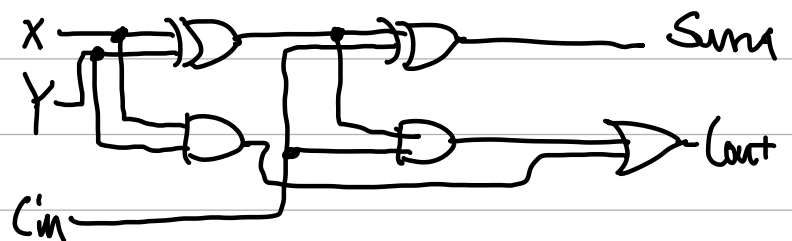


half adder

X	Y	C _{in}	Sum	C _{out}
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

$$\text{Sum} = X \oplus Y \oplus C_{in}$$

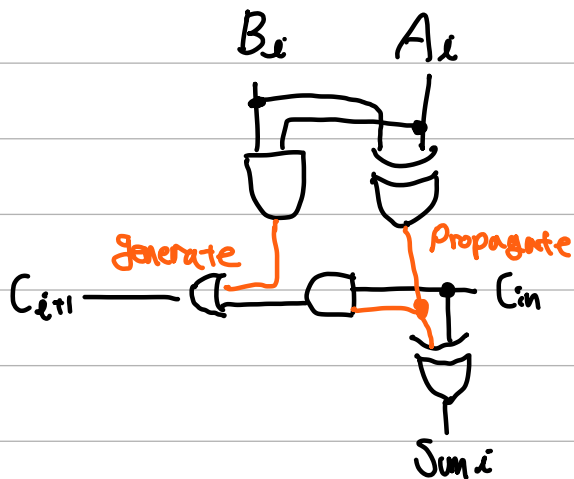
$$\text{Cout} = (X \oplus Y) \cdot C_{in} + X \cdot Y$$



이 Adder는 overflow 발생 가능!

이전 4bit Adder를 만들었을 때 Cout이 1이라면 결과가 5bit

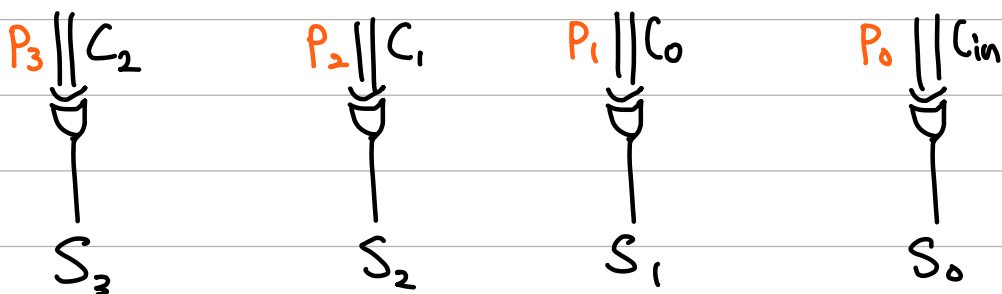
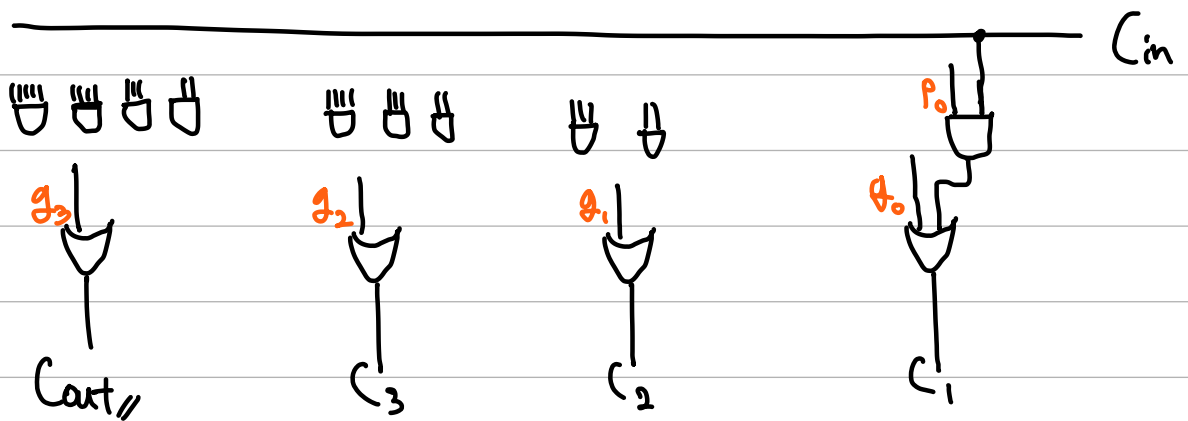
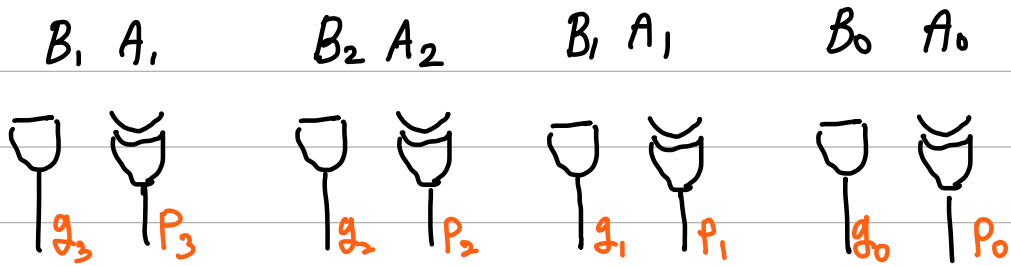
4bit lookahead adder



$$C_{i+1} = g_i + P_i C_i$$

⋮

Carry를 두단계 만에 생성 가능!



Subtraction

빠른 대신 Signed 된 수끼리 더하는 방식!

signed 1's complement

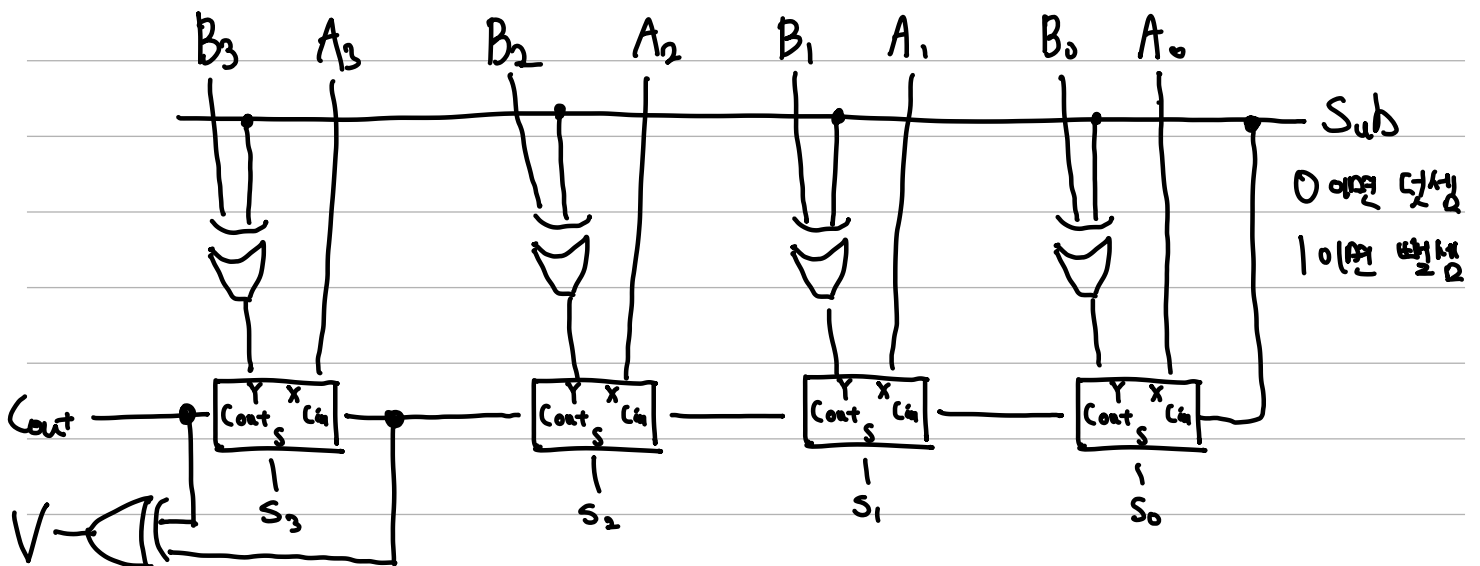
$$\begin{array}{r} 0111 \quad (+7) \\ + 1011 \quad (-4) \\ \hline 10010 \\ + \quad 1 \quad \text{Carry Out을 다시 더해준다!} \\ \hline 0011 \quad (+3) \end{array}$$

Signed 2's complement

$$\begin{array}{r} 0111 \quad (+7) \quad 7 - 4 \\ + 1100 \quad (-4) \\ \hline 10011 \quad (+3) \end{array}$$

Carry Out 무시

Adder - Subtractor



V for signed overflow : $\left\{ \begin{array}{l} \text{둘다 0, 0 인 수를 더했는데 1이 나옴.} \\ \text{둘다 1, 1 인 수를 더했는데 0이 나옴.} \end{array} \right.$

+ C_{in} Sum C_{out}
0 0 1 1 0 \rightarrow 부족 오류

0 1

1 0

1 1 0 0 1 \rightarrow 부족 오류

$(C_{out} \oplus C_{in}) = 0$ 이면 정상!
1 이면 오류!