# Security R&D Projects using LLVM







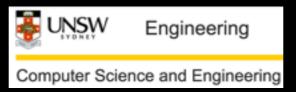
























































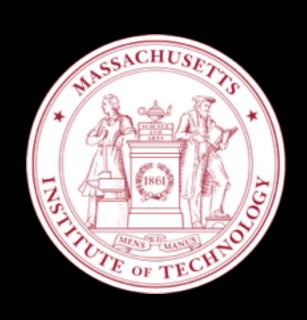
# Vellvm: Verified LLVM

- Model syntax and semantics of LLVM IR so as to...
- Reason about code expressed in IR in order to...
- Prove properties about LLVM passes in Coq
- OCaml extracted, ran unit tests validating model
- Prior work: reasoning on Instruction Combiner pass
- Recent work: reasoning on FastTrack (TSan)
  - Toward proof of implementation: 18900 LoC in proofs









#### Hybrid static/dynamic "sanitizers"

G	AddressSanitizer	<ul><li>RT lib replaces malloc/free/etc</li><li>UAF, UAR</li></ul>	<ul> <li>"asan-module" ModulePass</li> <li>"asan" FunctionPass</li> <li>Taint/poison memory</li> <li>Analyze those points on use</li> <li>Instruction Visitor collect return loc's (UAR)</li> </ul>
G	MemorySanitizer	<ul><li>Detect reads of uninit memory</li><li>Subset of valgrind</li></ul>	<ul> <li>"msan" FunctionPass</li> <li>Instrument each function to taint a few bits of app memory</li> <li>Instruction Visitor does much of the work to add propagation elements</li> </ul>
G	ThreadSanitizer	Detect race conditions & deadlocks	<ul> <li>"tsan" FunctionPass</li> <li>Instrument load/store insts to track read and writes to mem</li> </ul>
G	DataFlowSanitizer	Allows for create your own dynamic taint flow analysis	"DFSan"
/USec	DangSan	Detect UAF (at scale)	instruments programs written in C or C++ to invalidate pointers whenever a block of memory is freed
/USec	TypeSan	Check casts in C++ code	

### Dynamic analysis: fuzzing

Project	Action	LLVM Usage
<b>G</b> LibFuzzer	<ul> <li>Dynamic analysis</li> <li>Evolutionary fuzzer engine</li> <li>In-process, coverage guided</li> <li>Wrap code to make target functions</li> </ul>	<ul> <li>Target code instrumented with DFSan and instruction tracing</li> <li>Taint applied to every byte of input</li> </ul>
American Fuzzy Lop (AFL)	<ul> <li>Dynamic analysis</li> <li>Genetic Algorithm based Fuzzer</li> <li>Instruments code for guided coverage</li> </ul>	<ul> <li>LLVM mode option allows for instrumentation via IR</li> <li>Implemented as a ModulePass</li> <li>Uses IRBuilder</li> </ul>
lafindel's compare splitter	<ul> <li>Improve getting pass multi-byte compares by splitting them</li> <li>See "Circumventing Fuzzing" in projects appendix listing</li> </ul>	<ul> <li>Split compares module pass</li> <li>Split switch statements module pass</li> <li>Work toward strcmp, memcmp's</li> </ul>
TokenCap	<ul><li>Similar to above</li><li>Find tokens (magic values) etc</li><li>Use that to feed your fuzzing</li></ul>	Implemented as a ModulePass

## Static Program Analysis

Project	Action	LLVM Usage
Static Value Flow  Iter Science and Engineering (SVF)	<ul> <li>Static program analysis</li> <li>Andersen's Alias Analysis</li> <li>Interprocedural value flow</li> <li>Build your own program analysis</li> </ul>	<ul> <li>Uses LLVM as means for all analysis</li> <li>Lifting to simplifying auto-analysis</li> <li>Decomposing some GetElementPtr uses</li> </ul>
IKOS	<ul> <li>Static Program analysis</li> <li>find buffer overflows, etc)</li> <li>Abstract Interpretation</li> </ul>	<ul> <li>Function pass that lifts to ARBOS IR (a text based rep)</li> <li>Module pass that determines read-only globals and <i>lowers</i> them to constants</li> <li>Lower select to 3 BB and a phi node</li> </ul>
fInfer	<ul> <li>Static Program analysis</li> <li>Memory leaks</li> <li>Null deref</li> <li>Resource leak</li> <li>Build-time analysis</li> </ul>	Clang static analyzer plugin

# Lifting MC to IR

Project	Action	LLVM Usage
Mc Sema, remill	<ul> <li>Binary patching + instrumentation</li> <li>Reverse engineering</li> <li>symbolic exec</li> <li>Bug finding</li> <li>Retargeting</li> <li>Worth reading the CGC blog post &amp;&amp;</li> </ul>	<ul> <li>Control flow reproduction in IR</li> <li>IR code generation</li> <li>MC to IR translation</li> <li>compilable</li> </ul>
rev.ng	<ul> <li>Reverse engineering</li> <li>Symbolic concolic execution</li> <li>Worth reading Ilvm devmtg slides &amp;&amp;</li> </ul>	<ul><li>Translation from QEMU to IR</li><li>IR code generation</li></ul>
fdc	<ul> <li>Reverse engineering</li> <li>Output C-like pseudocode with IR</li> </ul>	<ul><li>IR code generation</li><li>MC to IR translation</li></ul>
(Pfl s2e	<ul> <li>Reverse engineering</li> <li>Bug finding</li> <li>Symbolic or relaxed execution</li> <li>path analysis</li> </ul>	Uses Klee for symbolic exec

### FM Verification

Project	Action	LLVM Usage
Software Analysis Workbench (SAW) galois	<ul> <li>Verification: mode/code equivalence</li> <li>Translate code to formal model</li> <li>Use SMT/SAT solvers to prove equiv</li> <li>(C/C++) bitcode &lt;-&gt; java &lt;-&gt; cryptol</li> </ul>	<ul> <li>LLVM symbolic simulator:</li> <li>haskell based xlate to symbolic IR</li> </ul>
Divine	<ul> <li>Verification: Explicit state model checker</li> <li>Explores attempting to hit asserts</li> <li>Implements DIVINE VM</li> </ul>	<ul> <li>"divine cc prog.c" will generate prog.bc</li> <li>DiVM symexec's IR</li> </ul>
arnegie Iellon Iniversity	<ul> <li>Verification: Model checking integral assertions</li> <li>intra-procedural</li> </ul>	<ul> <li>Inject calls in C for assume and assert style checking</li> <li>See end possible projects slide</li> <li>Lifts to CRAB (abstract rep) for reasoning on values</li> </ul>
Ilstar	<ul> <li>Verification Model checking</li> <li>Pre Post condition checking</li> <li>Translates to coreStar for sym.exec</li> </ul>	<ul><li>Ingests Bitcode</li><li>Translates it to coreStar</li></ul>

#### Translate IR to Formal Language

Project	Action	LLVM Usage
IImc VERSITY WENTE.	<ul> <li>Verification: Transform to Labeled Transition System (LTS)</li> <li>Map IR to PINS interface of LTS_min</li> </ul>	<ul> <li>Ilmc git is empty</li> <li>Thesis available with code discussion &amp;&amp;</li> </ul>
pmGen	<ul> <li>Verification: Translation IR to Promela</li> <li>Process or Protocol Meta Language Verification Modeling Language</li> <li>Promela is checked in SPIN</li> </ul>	Uses IR reader, but does not use pass API.
Smack institute	<ul> <li>Verification: Translation to Boogie (IVL)</li> <li>Checks user-supplied assestions</li> </ul>	<ul> <li>Translates LLVM to Boogie</li> <li>Can inject into bcVERIFIER_assert()</li> </ul>

### Application Hardening

	Project	Action	LLVM Usage
UNIVERSITY OF CAMBRIDGE	ecurity-Oriented Analysis of Application ograms (SOAAP)	<ul> <li>Help with (fine grained) app compartmentalization</li> <li>Semi-automated static/dynamic analysis</li> <li>Act as an aide to development</li> </ul>	<ul> <li>Pass manager</li> <li>Passes analyzing code and compare with injected annotations (policy)</li> </ul>
	Temporally hanced Security ogic Assertions (TESLA)	<ul> <li>Developer specify temporal properties</li> <li>Auto-gen runtime checks for the properties</li> <li>"previously", "eventually"</li> <li>Borrowed from model checking</li> </ul>	<ul><li>Injection of tesla_ routines</li><li>Injection of runtime checks</li></ul>
UNIVERSITY OF CAMBRIDGE	ausal, Adaptive, Distributed, & Efficient Tracing estem (CADETS)	<ul> <li>Improve tracing and audit implementations based on lessons learned from TrustedBSD Audit and DTrace</li> </ul>	TBD
ILLINOIS UNIVERSITY OF ILLINOIS AT URBANA CHAMPAIGN	SAFECode	<ul><li>Code hardening</li><li>Static analysis for runtime safety</li><li>Goal to reduce runtime checks</li></ul>	<ul> <li>Virtual instruction set is LLVM IR + more</li> <li>Compile to that and code is analyzed / verified</li> <li>Then translated to proper MC</li> </ul>
ÉCOLE POLYTECHNIQUE FÉDÉRALE DE LAUSANN	ASAP	<ul> <li>Optimizing "hardened" code</li> <li>Analyzes runtime checks to determine which may be safely removed</li> </ul>	<ul> <li>Implements cost calculators as module passes</li> <li>Show use of SmallPtrSet container</li> <li>Metadata node use</li> </ul>

#### General Code Hardening & SW Resilience

Project	Action	LLVM Usage
KULFI UNIVERSITY OF UTAH	<ul><li>Software resiliency analysis</li><li>Instruction level fault injection</li></ul>	Module pass that injects static corruption and dynamic corruption
Return-less-code	<ul> <li>Code hardening</li> <li>Investigate eliminating ROP via no returns</li> <li>Generated return-less FreeBSD kernel</li> </ul>	<ul> <li>Iterate through Instructions finding call/ret and replacing</li> <li>Implement backend (MC)</li> </ul>
PRESAGE	<ul> <li>Software resiliency</li> <li>Protect structured address computations against soft errors (e.g. bit flips from alpha particles)</li> </ul>	<ul> <li>Translate GetElementPtr array accesses to have dependencies that broken are a signal of soft error</li> <li>Read paper for gory details on GEP</li> </ul>
Obfuscator-LLVM	<ul><li>Code obfuscation</li><li>Tamper-proofing</li></ul>	<ul> <li>Operator substitution is FunctionPass</li> <li>Bogus CFG injection is FunctionPass</li> <li>splits basic blocks</li> <li>introduce bogus loops</li> </ul>

### Many small, one-off passes or tools as well...

Project	Action	LLVM Usage
uarkslab kryptonite	<ul> <li>Code obfuscation</li> </ul>	Implemented function pass to transform IR
Passes from QuarksLab	<ul><li>Example passes:</li><li>ObfuscateZero</li><li>Others</li></ul>	OZ is BasicBlockPass
whole program IIvm	Help merge multiple bitcode files into one	

 https://github.com/roachspray/opcde2017/blob/ master/projects.md

Please send me additions