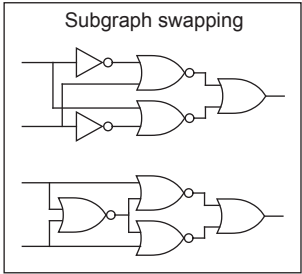
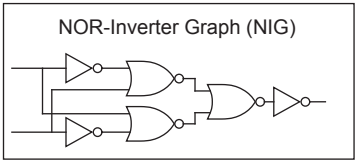
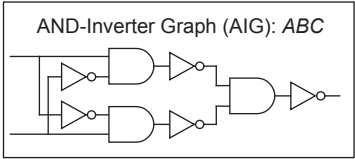


Truth table

A	B	out
0	0	0
0	1	1
1	0	1
1	1	0



Final wiring diagram