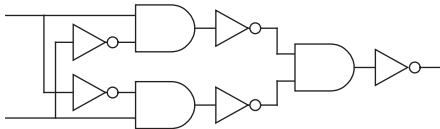


Truth table

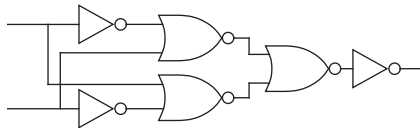
A	B	out
0	0	0
0	1	1
1	0	1
1	1	0



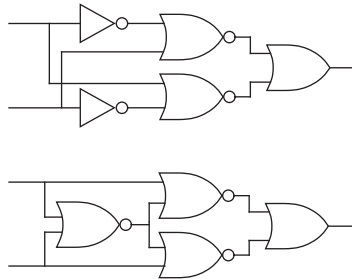
AND-Inverter Graph (AIG): ABC



NOR-Inverter Graph (NIG)



Subgraph swapping



Final wiring diagram