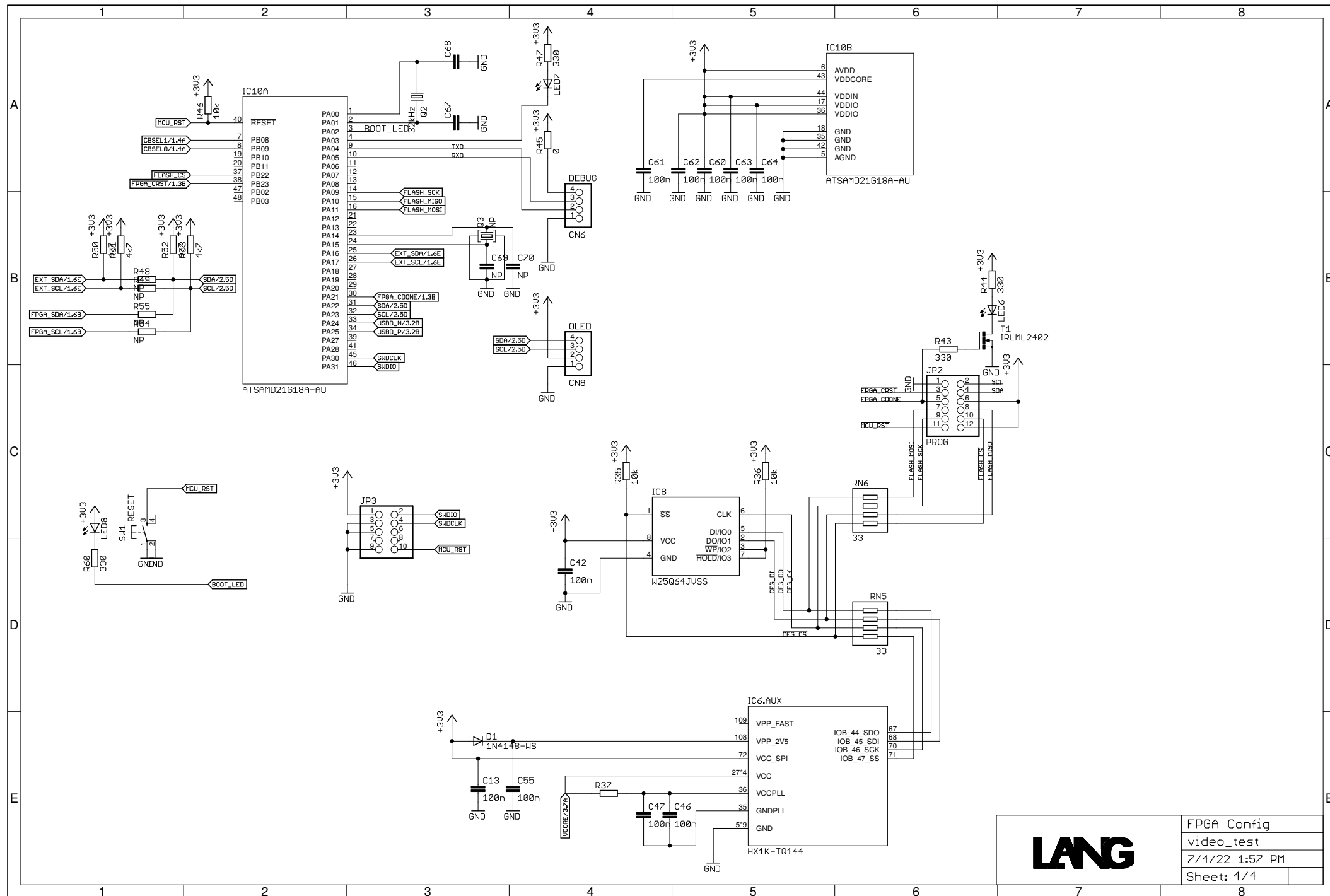


LANG

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|----------------|
| Power & Clock |
| video_test |
| 7/4/22 1:57 PM |
| Sheet: 3/4 |



LANG

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| FPGA Config |
| video_test |
| 7/4/22 1:57 PM |
| Sheet: 4/4 |