



Systems and Internet
Infrastructure Security

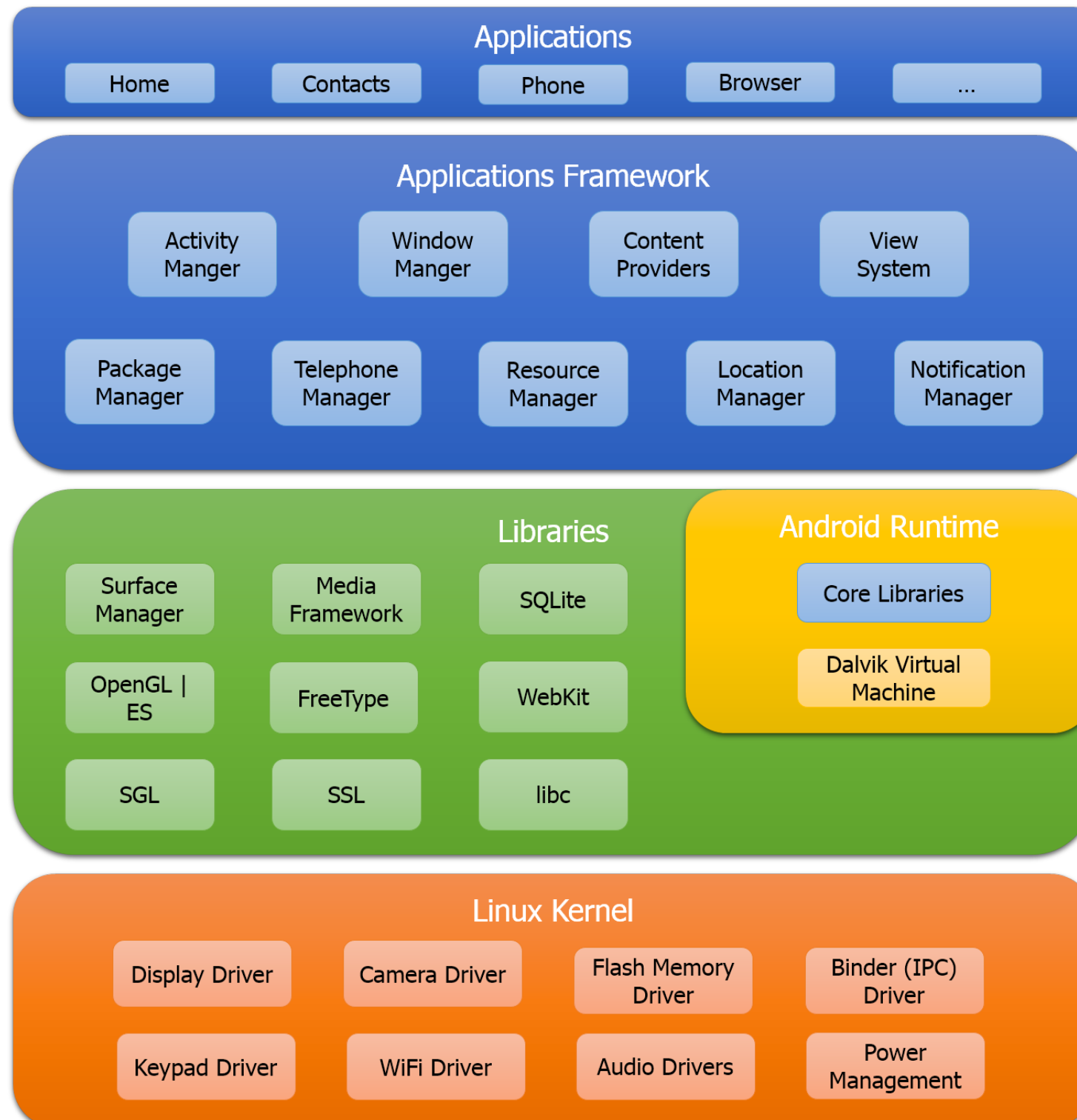
Network and Security Research Center
Department of Computer Science and Engineering
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SPROBE: Enforcing Kernel Code Integrity on the TrustZone Architecture

Xinyang Ge, Hayawardh Vijayakumar, and Trent Jaeger

May 17th, 2014

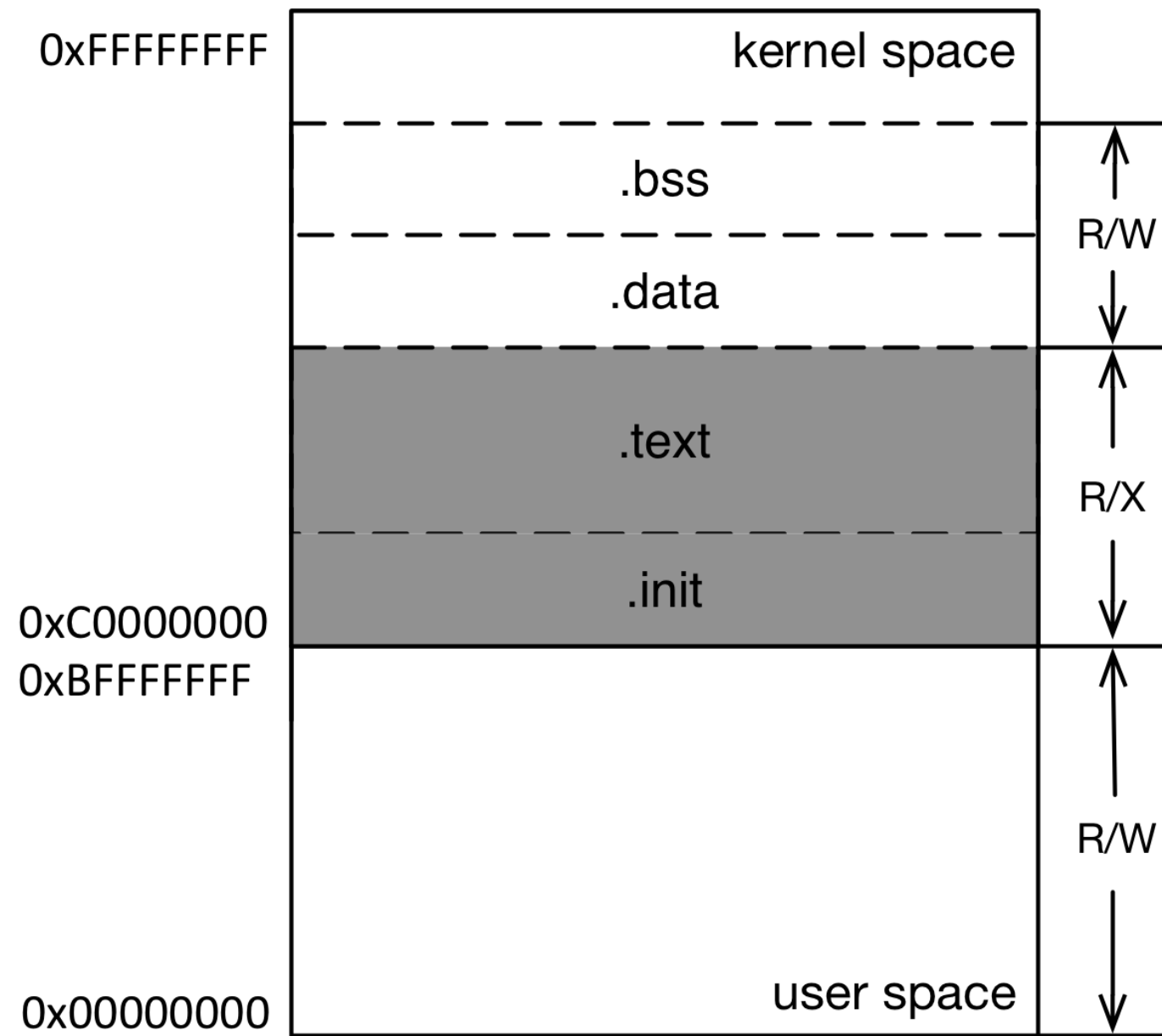
Problem



- Kernel rootkits are now becoming a serious threat to smartphone operating systems (e.g., Android)
 - ▶ CVE-2011-1823: an **integer overflow** bug in a daemon process on Android 3.0 enables an adversary to gain root privilege and install a kernel rootkit
 - ▶ Motivation: Protect the kernel code integrity despite of the presence of kernel rootkits

- $W \oplus X$ Protection
 - ▶ Background: eXecute-Never (XN) bit
 - ▶ A virtual memory page cannot be set as writable and executable at the same time
- Privileged eXecute-Never
 - ▶ Prohibit user code from executing in the kernel

Preliminary Defenses



- Code-Reuse Attacks
 - ▶ ret2libc attack
 - ▶ return-oriented programming
- Attack Vectors
 1. Attacks that modify the initial VM layout
 2. Attacks that remain the initial VM layout

Type #1 Attacks

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 - ▶ Enable execution over code pages in the user space

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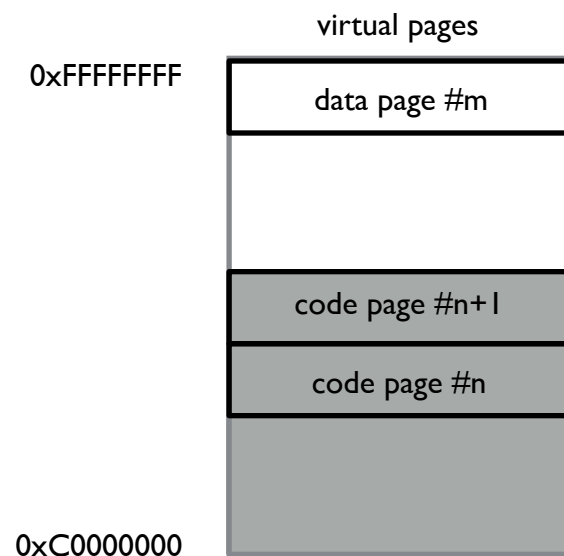
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 - ▶ Modify page table entries in place
 - ▶ Enable execution over code pages in the user space
 - ▶ Disable the MMU

Type #2 Attacks

- Goal: (1) Remap a code page to a physical frame containing data or (2) map a data page to a physical frame containing code

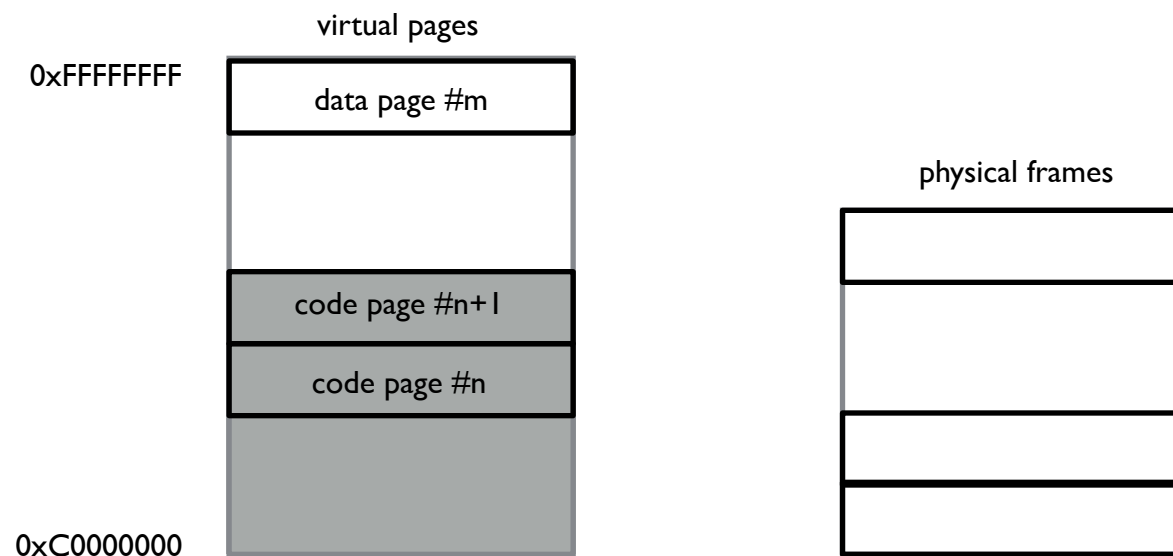
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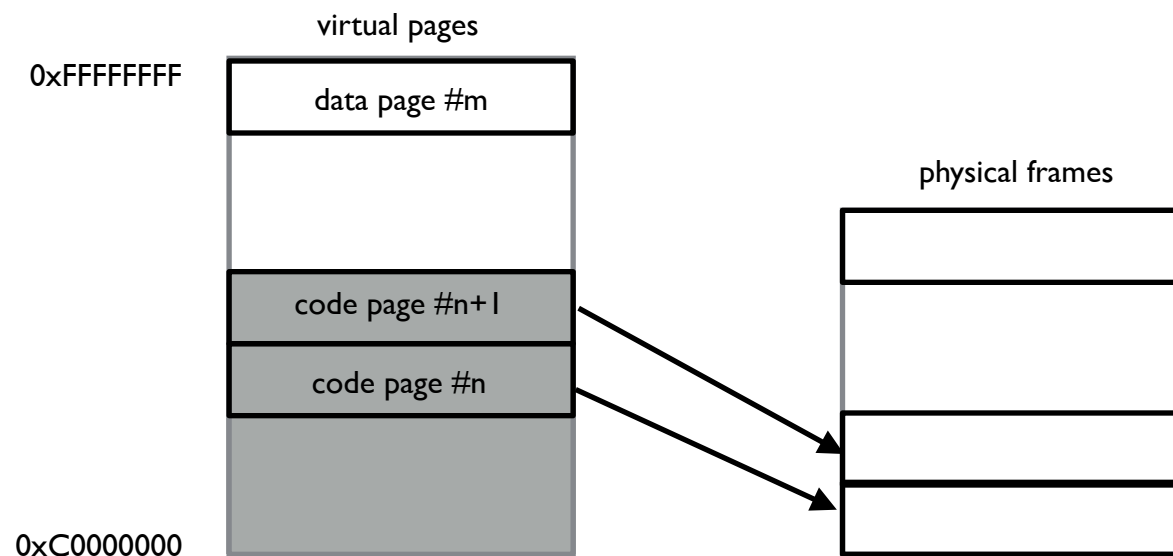
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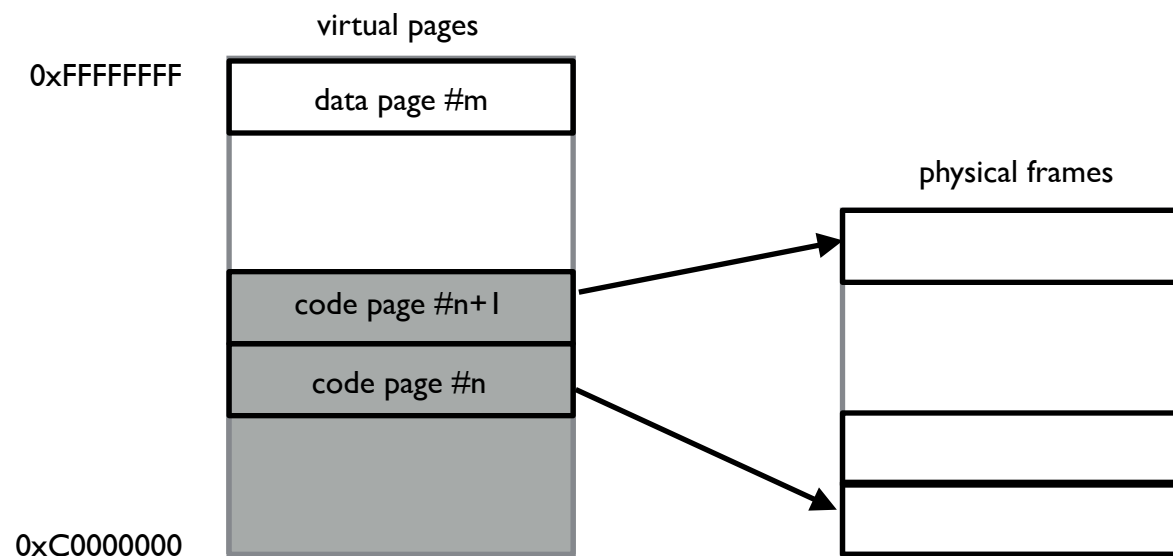
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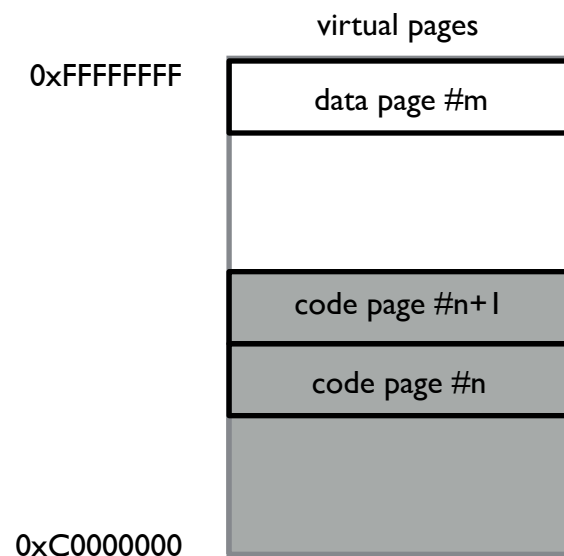


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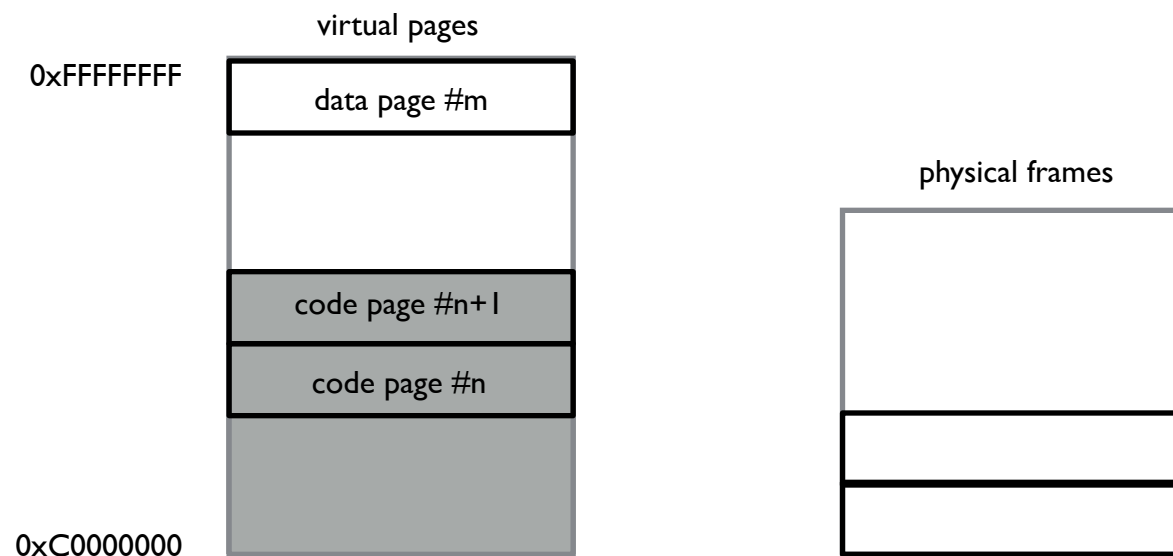
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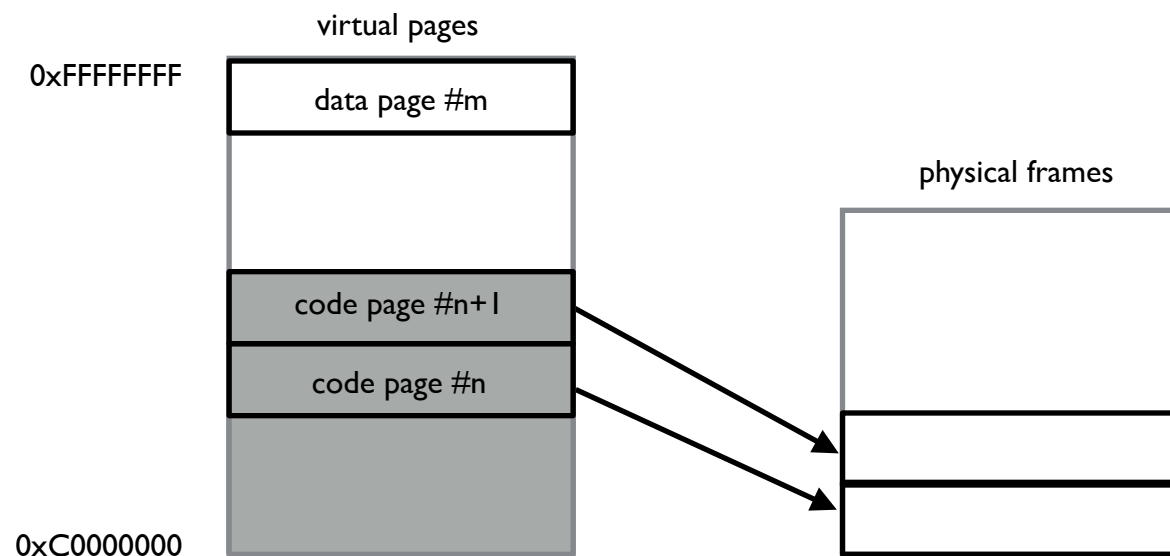
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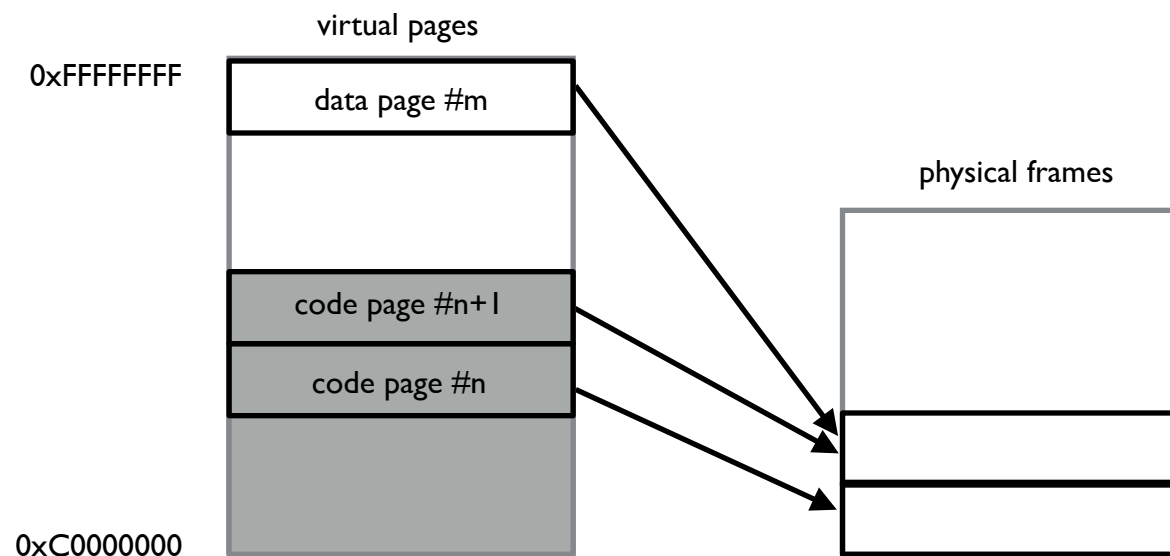
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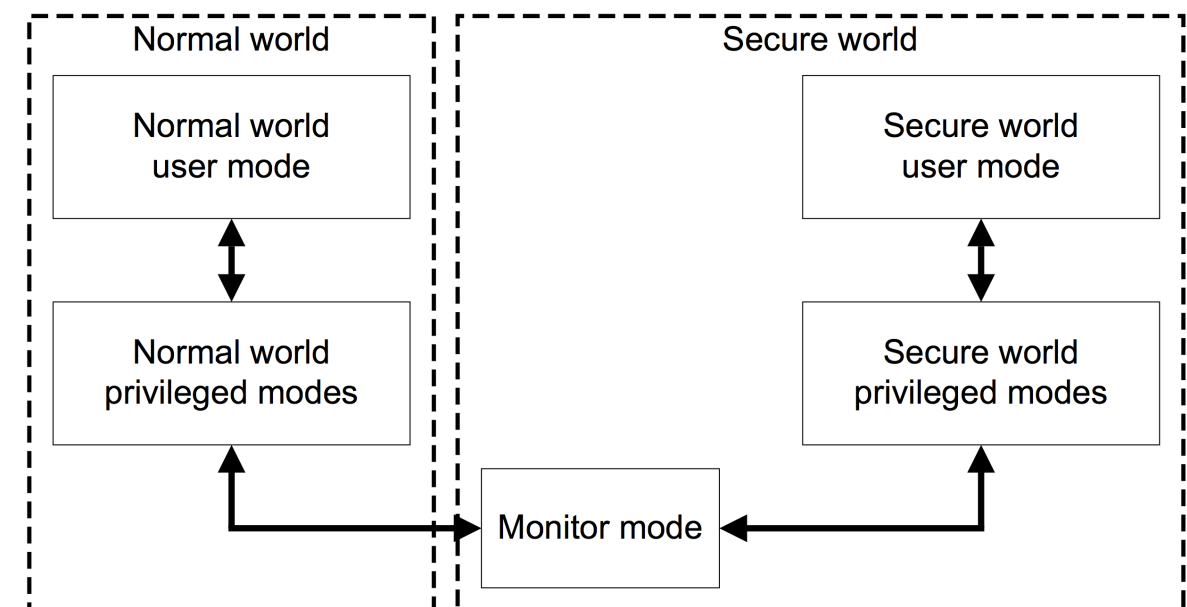
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Prevent both types of attacks and limit the adversary to approved kernel code on the TrustZone

Background: TrustZone

- Resources are partitioned into two distinct worlds
 - physical memory, interrupts, peripherals, etc.
- Each world has its autonomy over its own resources
- Secure world can access normal world resources, but *not* vice versa
- Run in time-sliced fashion



SPROBE Mechanism

- We need an instrument mechanism that enables the secure world to be notified upon events of its choice in the normal world

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Normal World	
push	{r1-r3}
stmia	sp!, r10
...	
mov	pc, lr

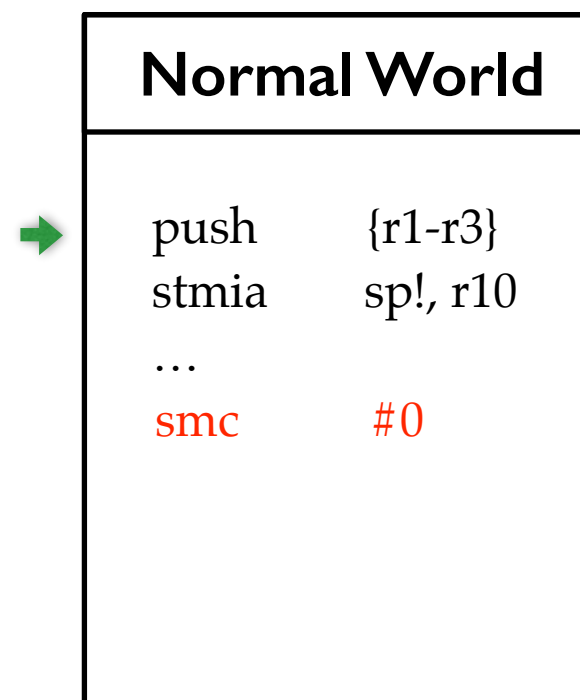
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push	{r1-r3}
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...	
smc	#0

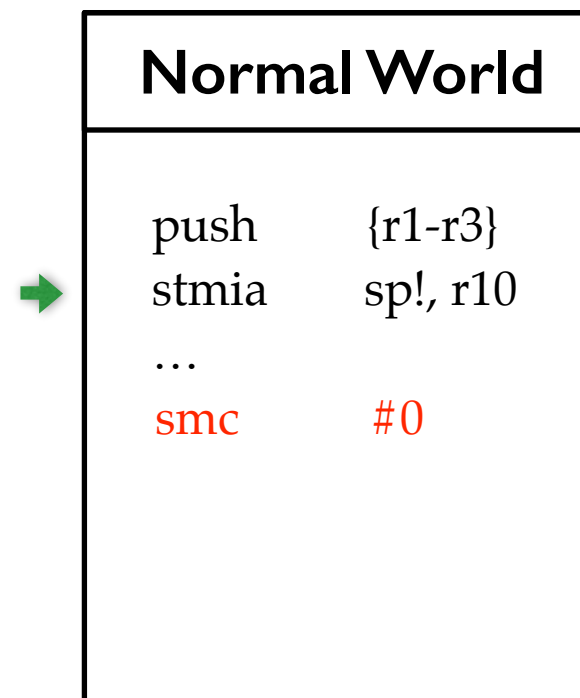
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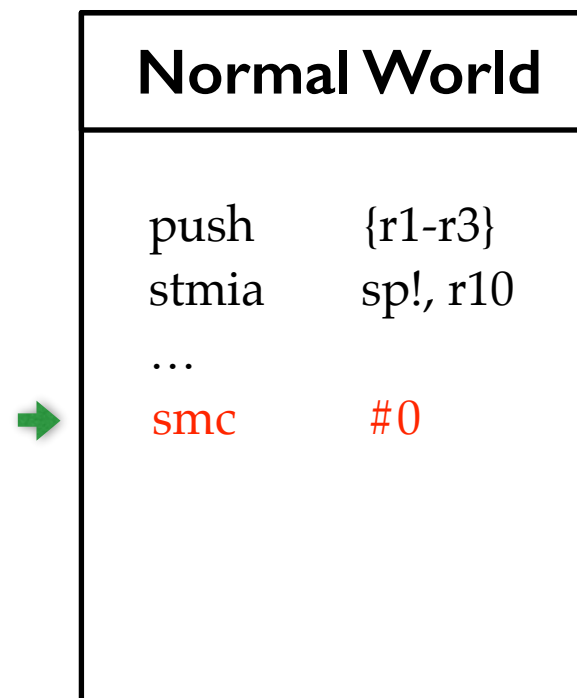
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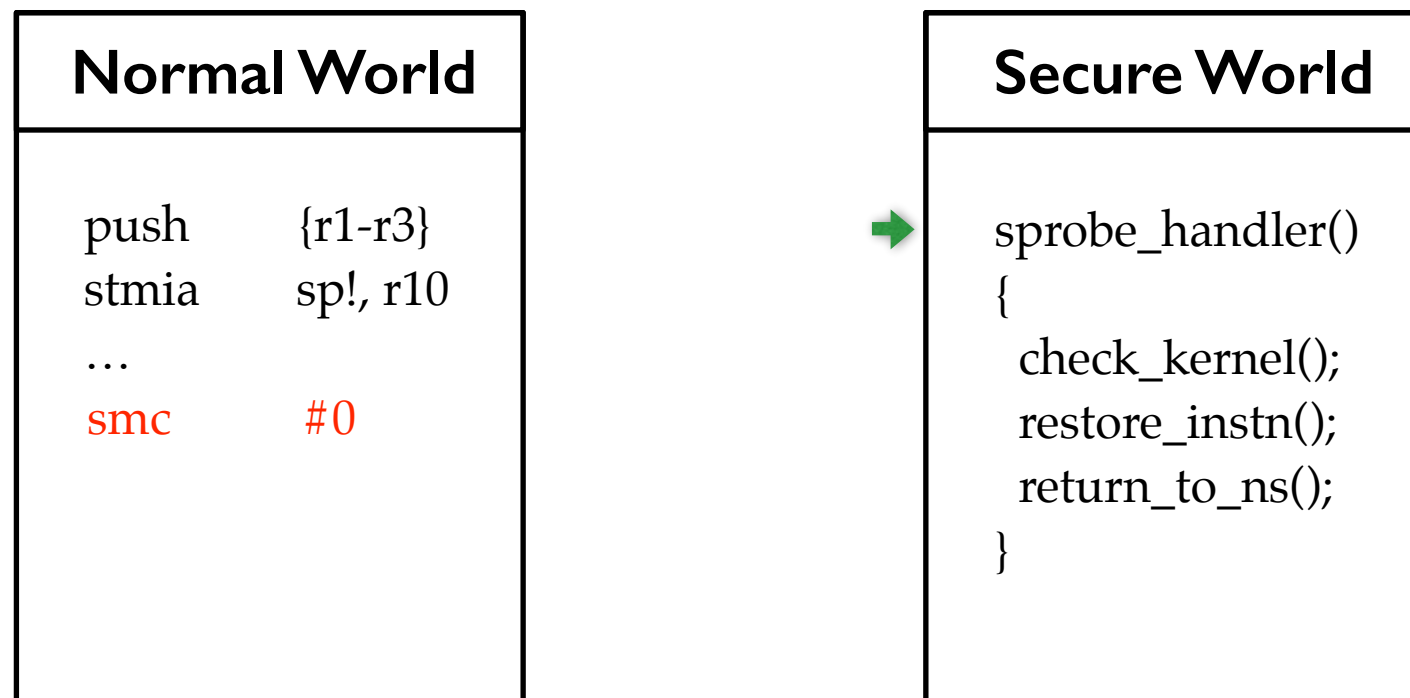
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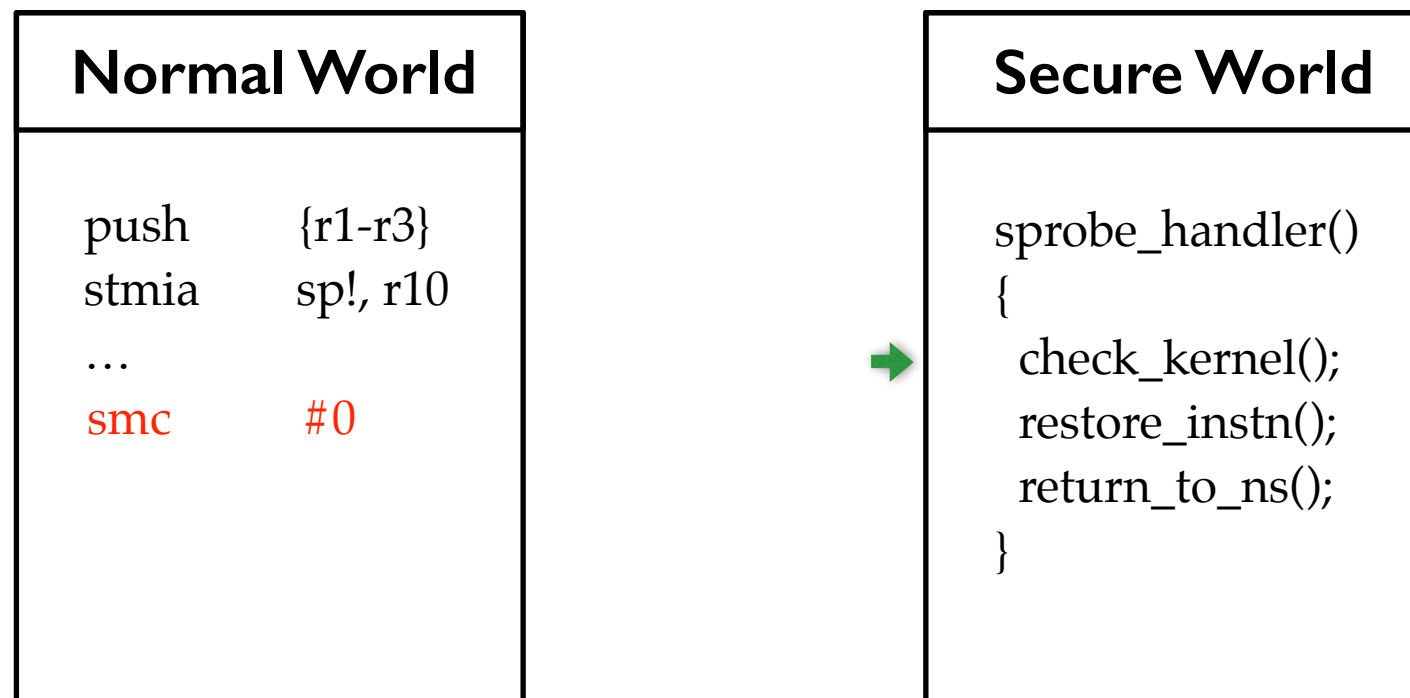
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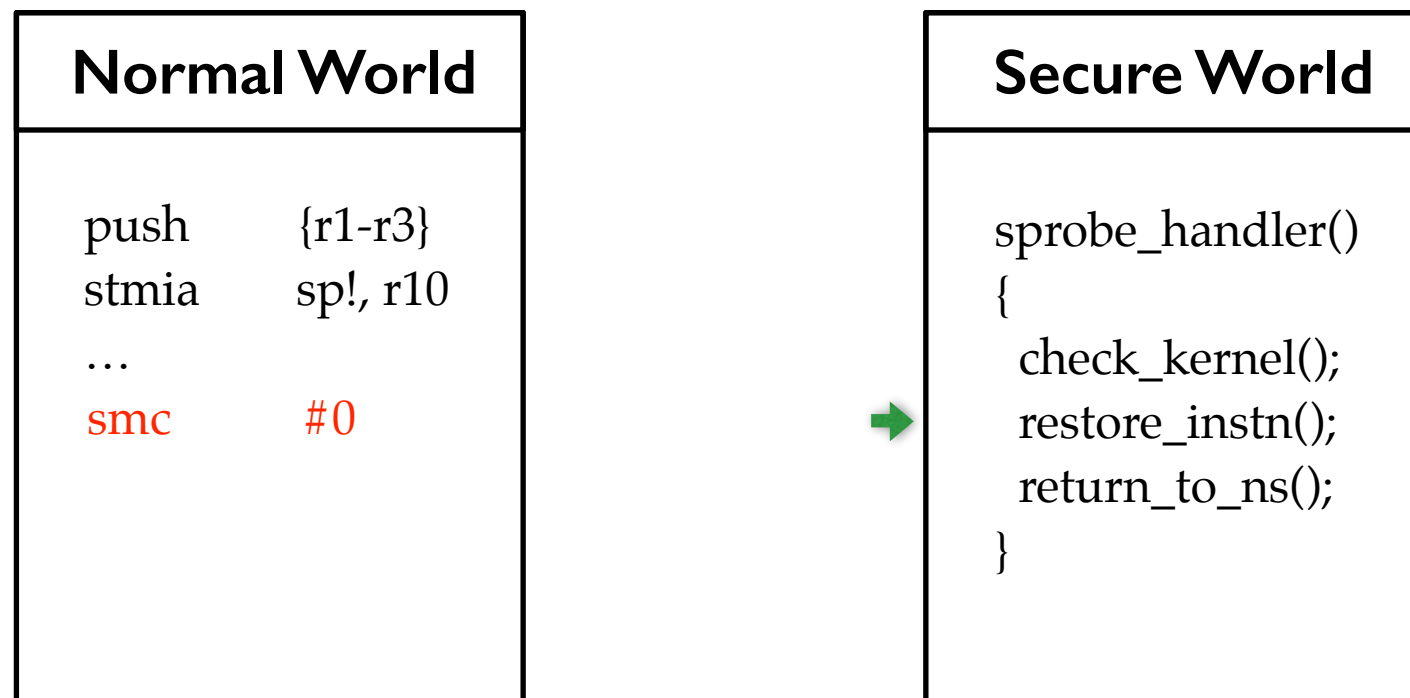
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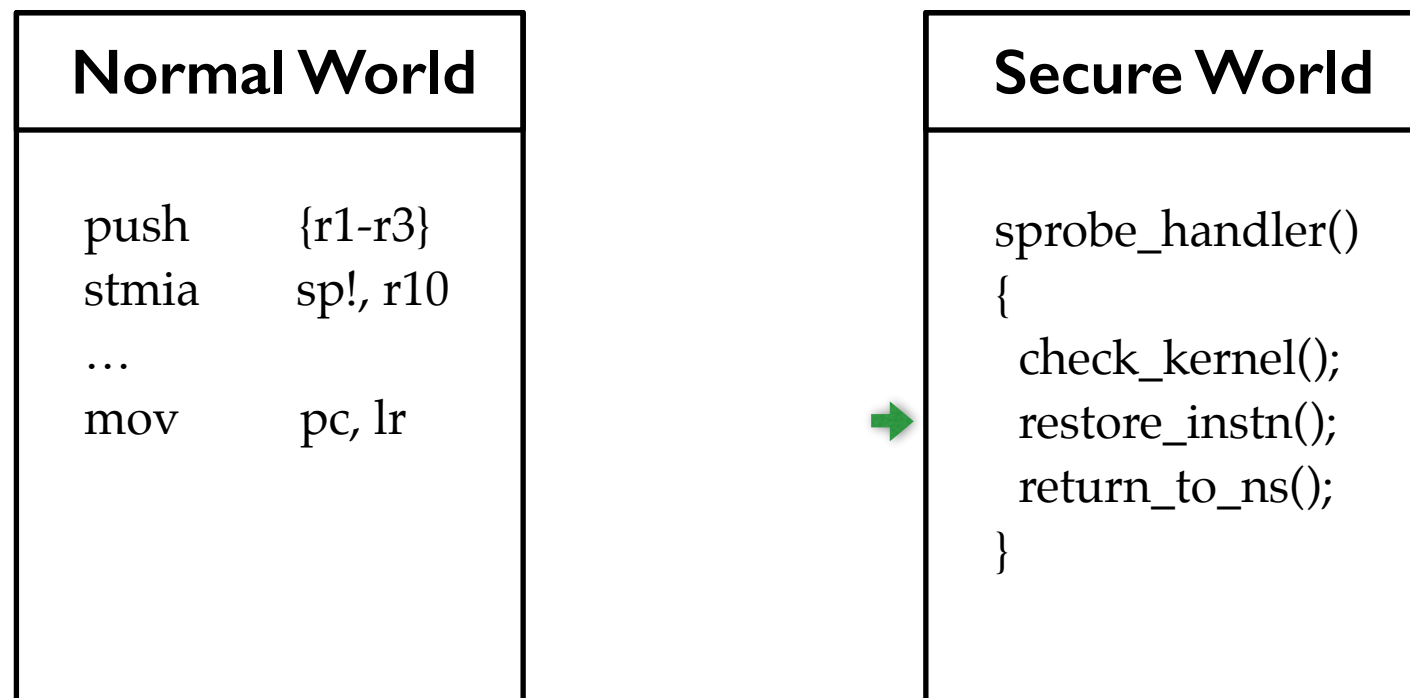
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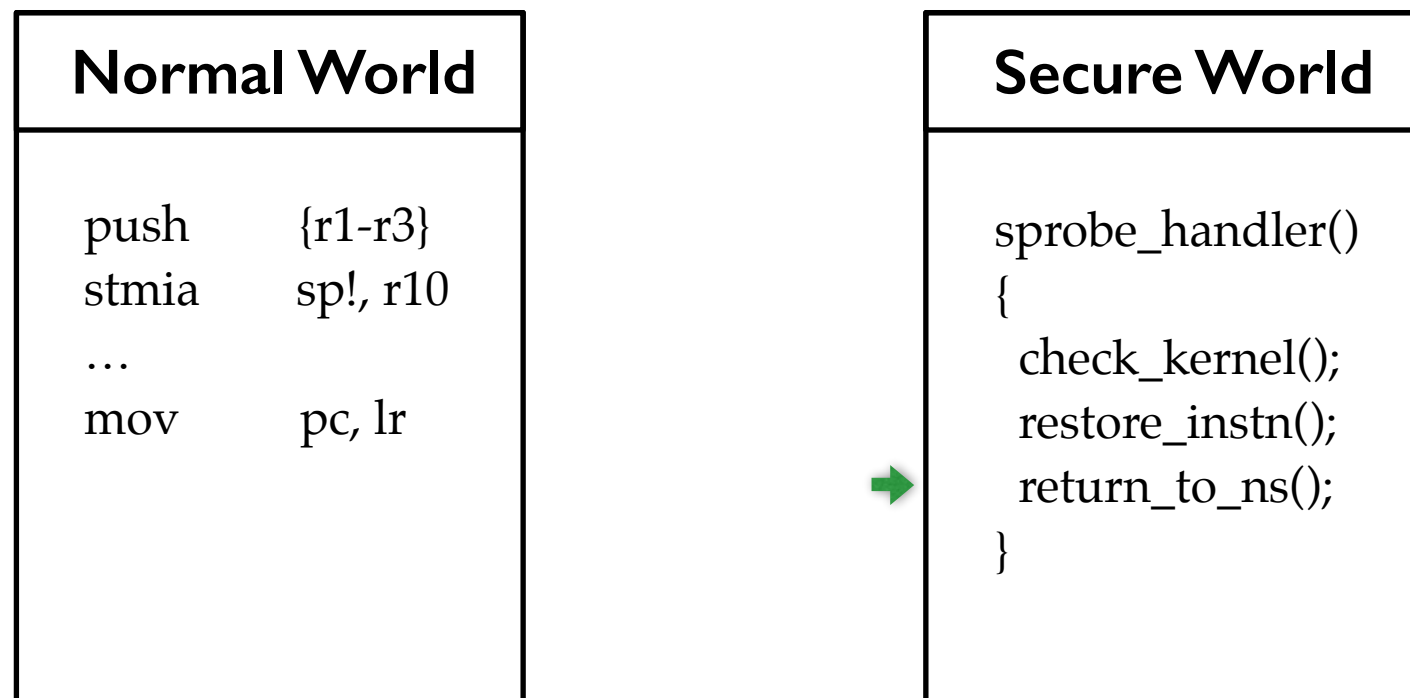
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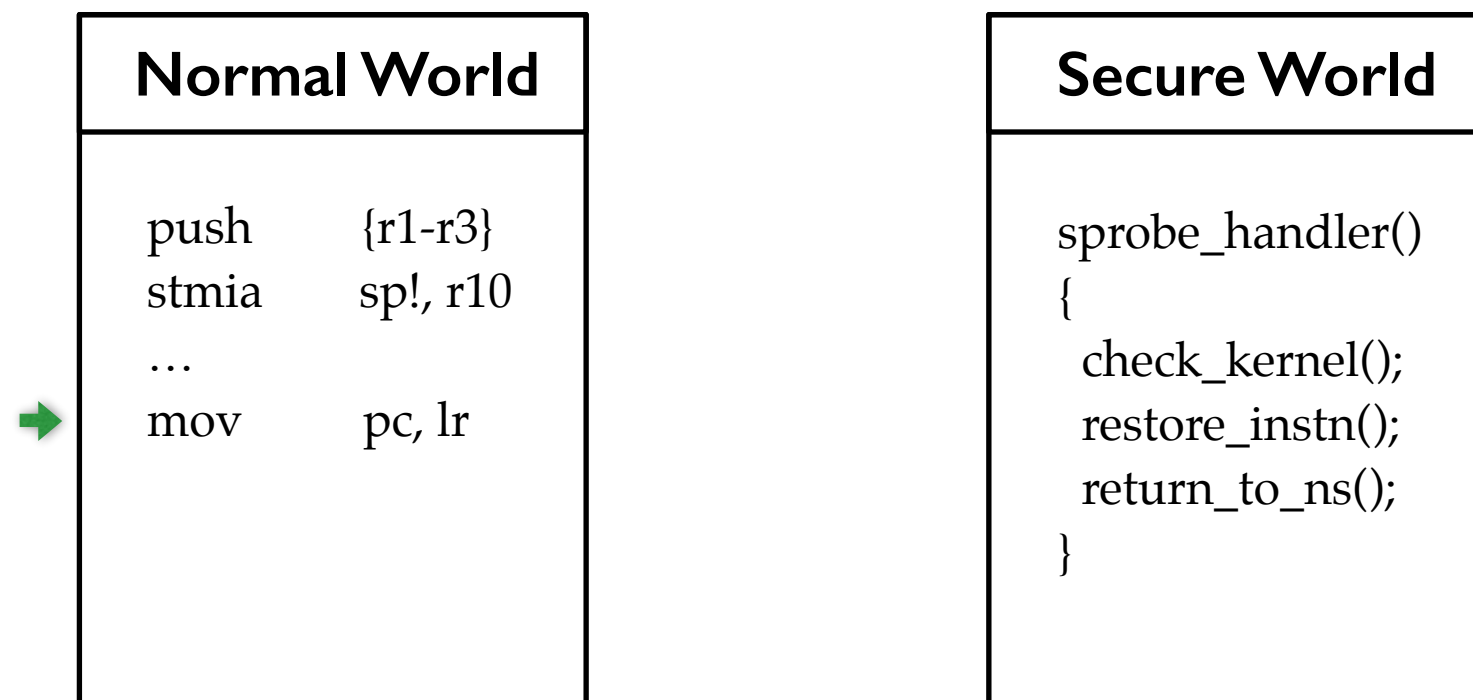
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SPROBE Placement

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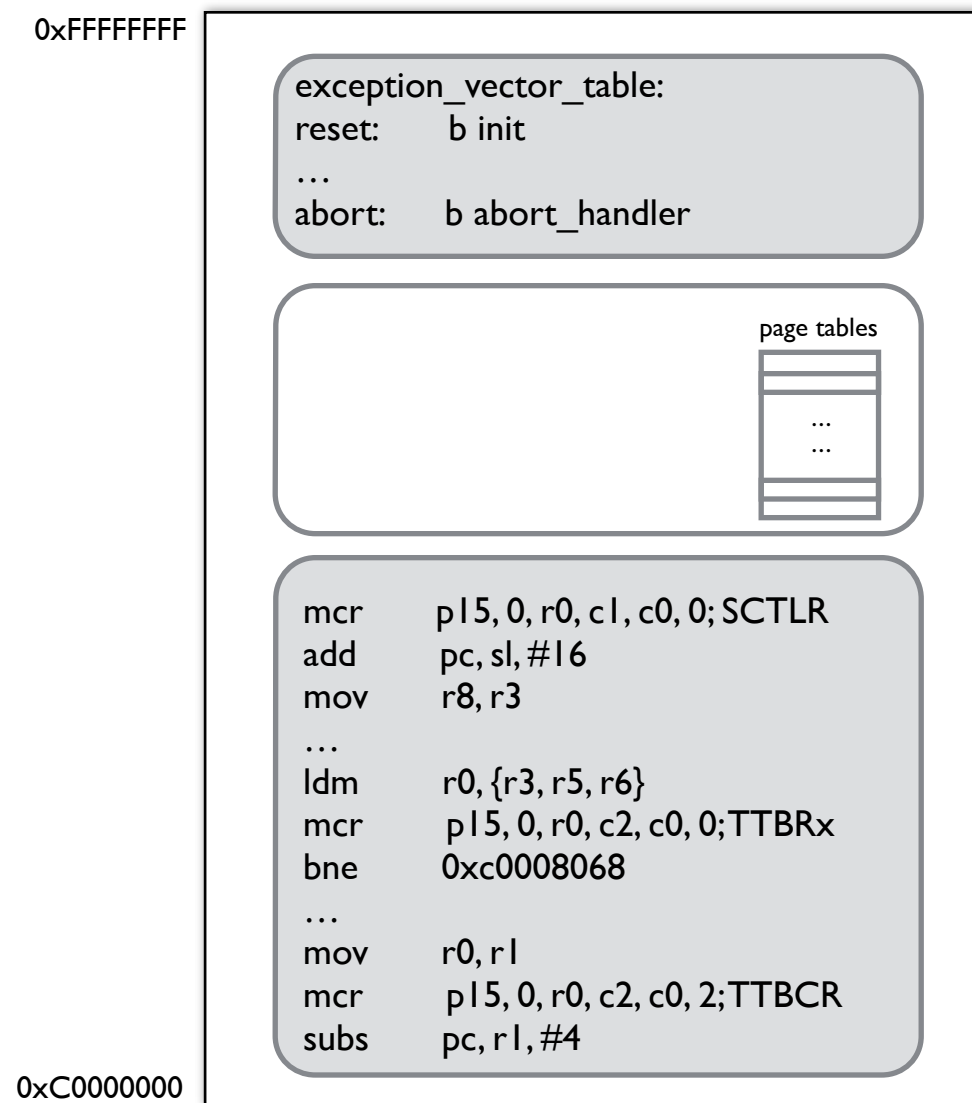
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 - ▶ Modify page table entries in place
 - **write-protect the whole page tables** and instrument the first instruction in page fault handler

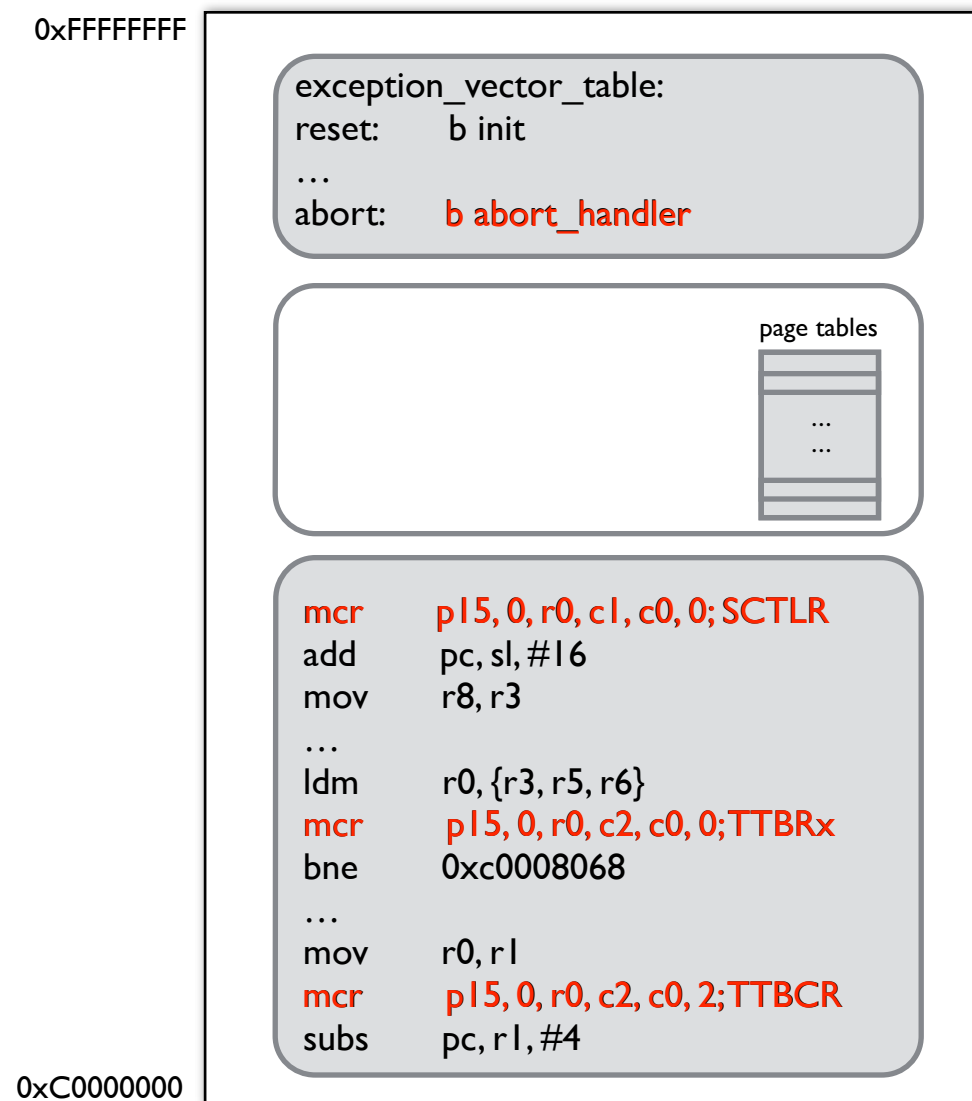
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Normal World Kernel Space



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Normal World Kernel Space



- Environment setup
 - ▶ Linux 2.6.38 in the normal world
 - ▶ Fast Models 8.1 for emulation
- Types of SPROBES
 - ▶ Type #1: 6 SPROBES for enforcing $W \oplus X$ protection
 - ▶ Type #2: 4 SPROBES for monitoring page table root
 - ▶ Type #3: 1 SPROBE for monitoring page table configuration
 - ▶ Type #4: 1 SPROBE for monitoring page table entries

- Def. Hit Frequency: the average number of instructions elapsed between two contiguous SPROBE hits

SPROBE Type	1	2	3	4
Hit Frequency	N/A	313,836	N/A	85,982
Overhead	0	1.8%	0	6.5%

Q & A