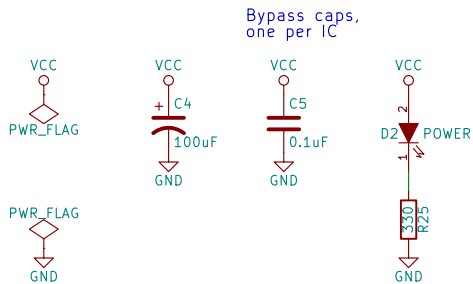
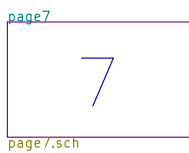
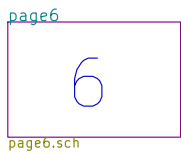
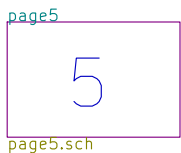
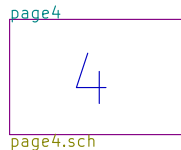
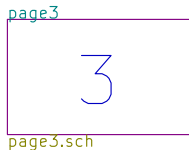
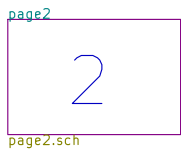
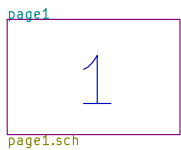


68000 Single Board Computer  
from  
"Microprocessor Systems Design" by Alan Clements  
Modified by Jeff Tranter



Top Level Schematic

Jeff Tranter

Sheet: /

File: ts2.sch

**Title: TS2 68000 Single Board Computer**

Size: A

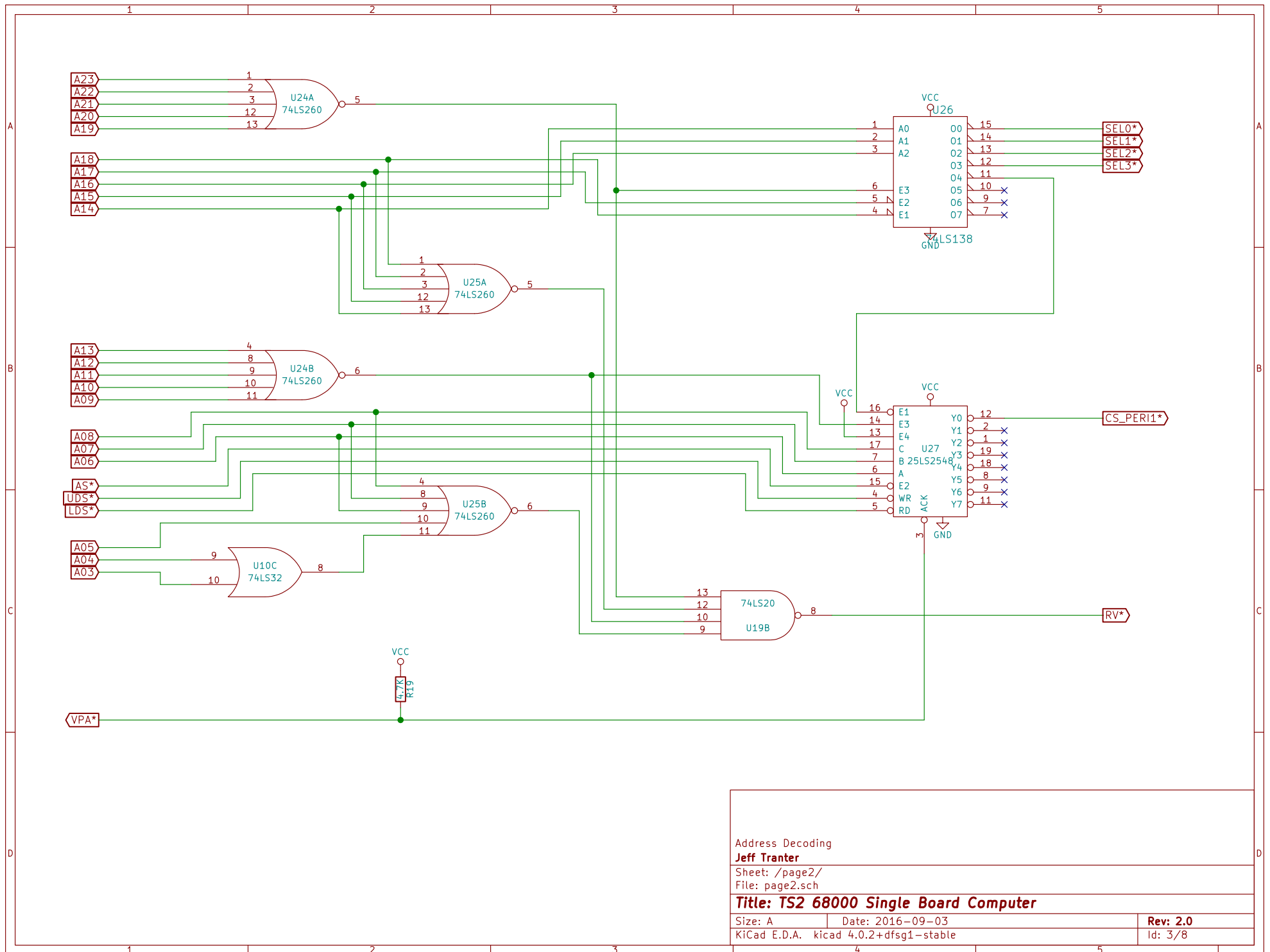
Date: 2016-09-03

Rev: 2.0

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Id: 1/8





Address Decoding

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File: page2.sch

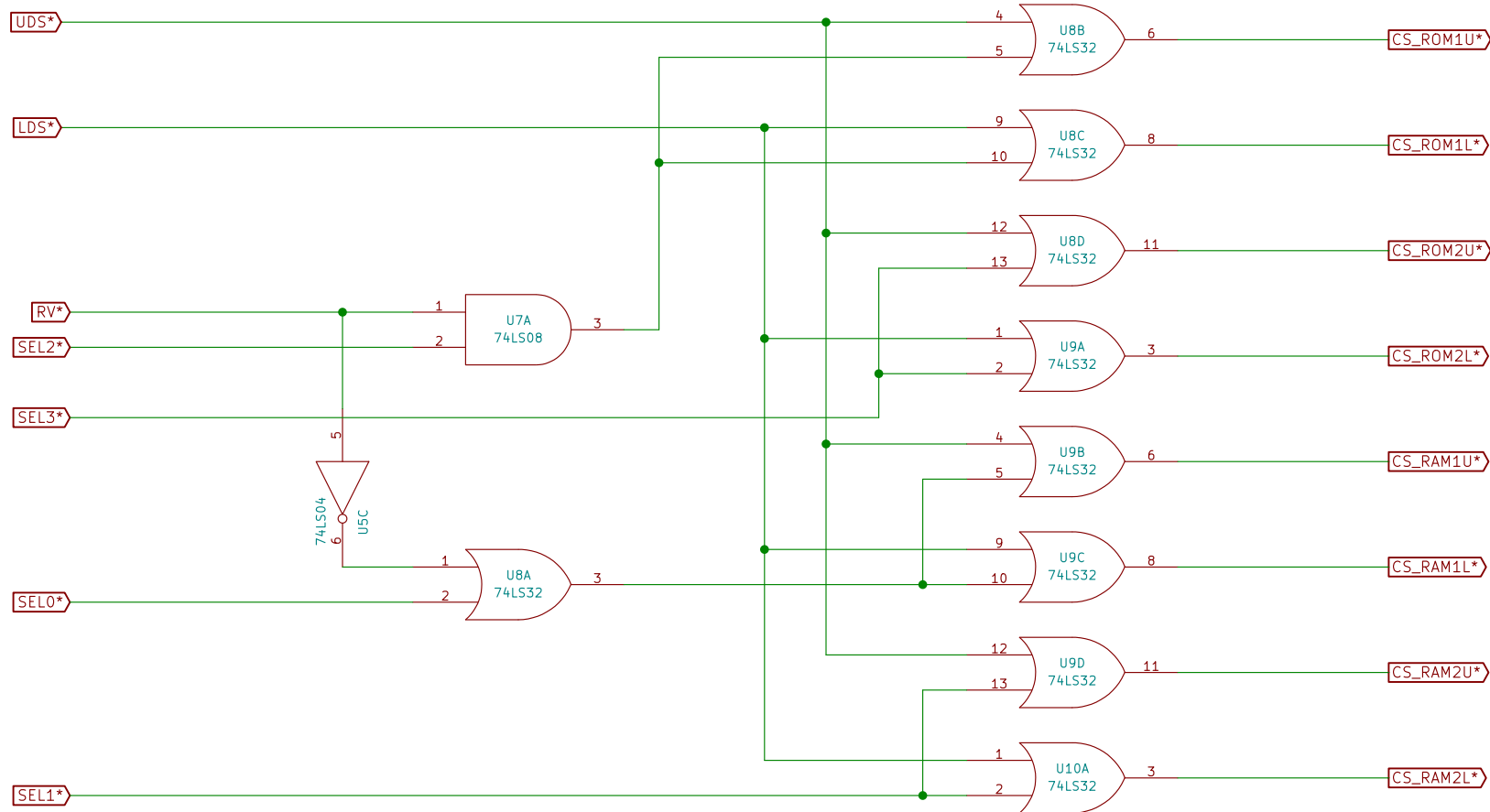
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Size: A Date: 2016-09-03

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Rev: 2.0

Id: 3/8



RAM and ROM Address Select

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File: page3.sch

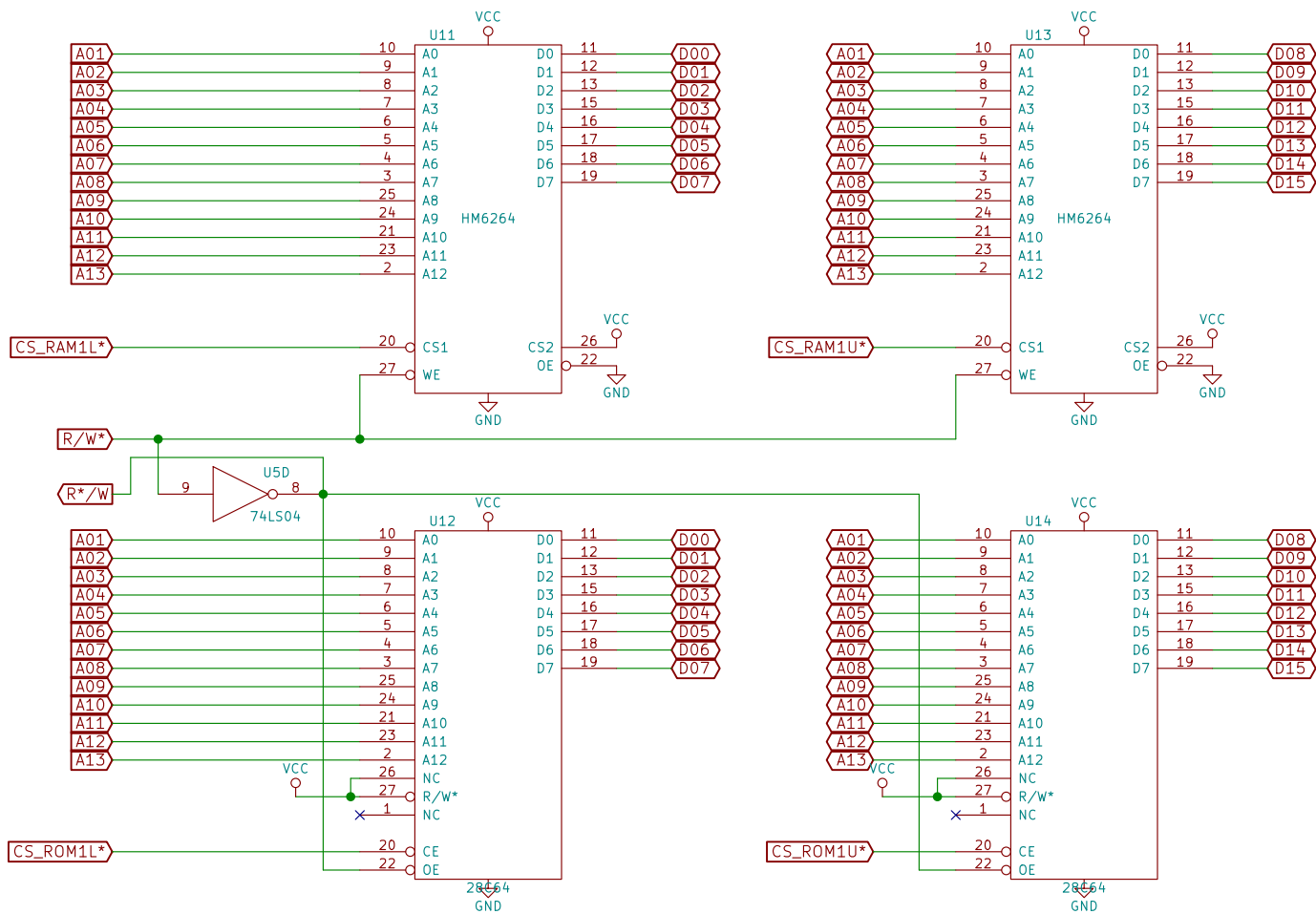
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Size: A Date: 2016-09-03

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.0

Id: 4/8



RAM and ROM (1 of 2)

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File: page4.sch

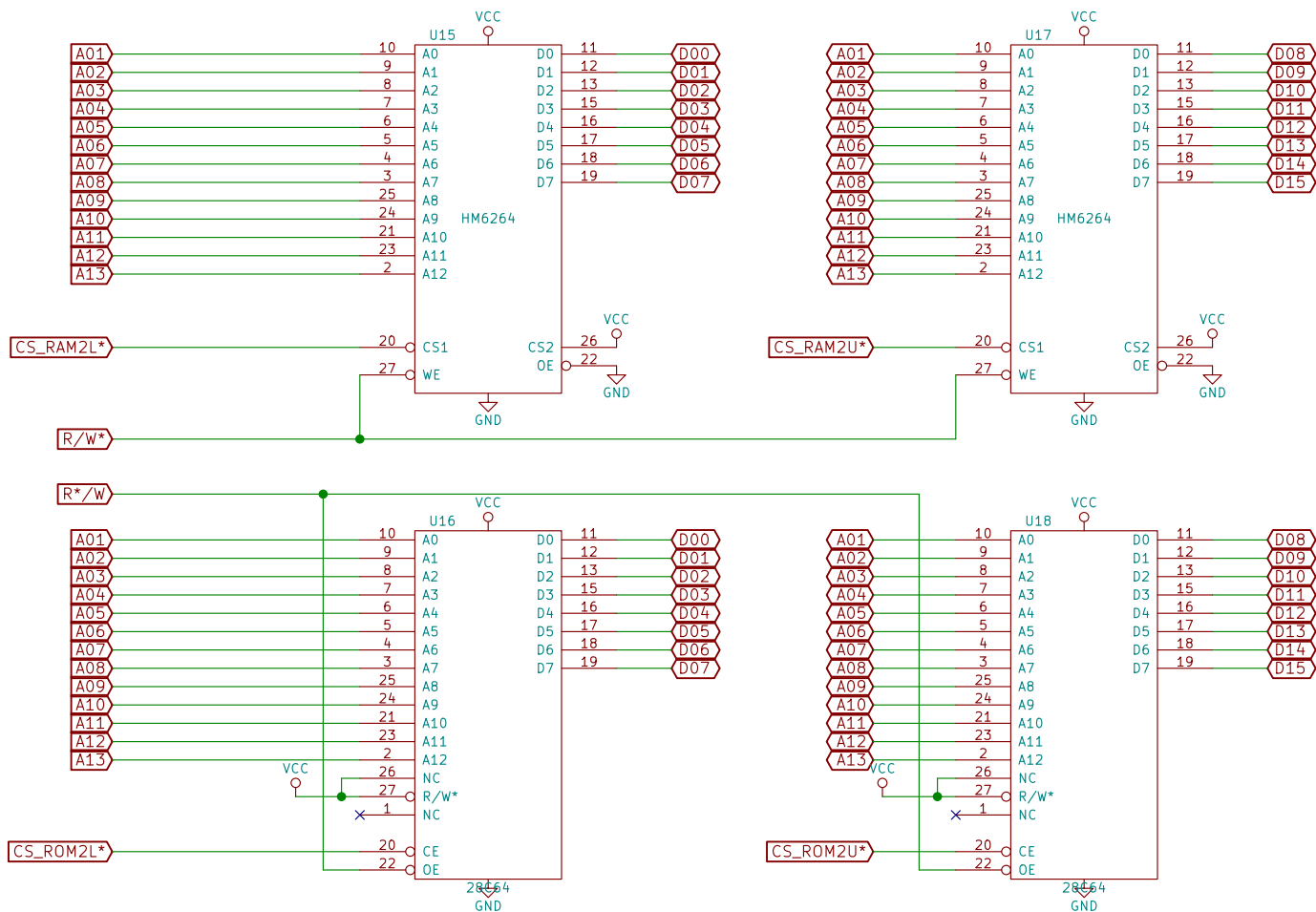
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Size: A Date: 2016-09-03

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.0

Id: 5/8



RAM and ROM (2 of 2)

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Sheet: /page5/

File: page5.sch

**Title: TS2 68000 Single Board Computer**

Size: A Date: 2016-09-03

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.0

Id: 6/8

