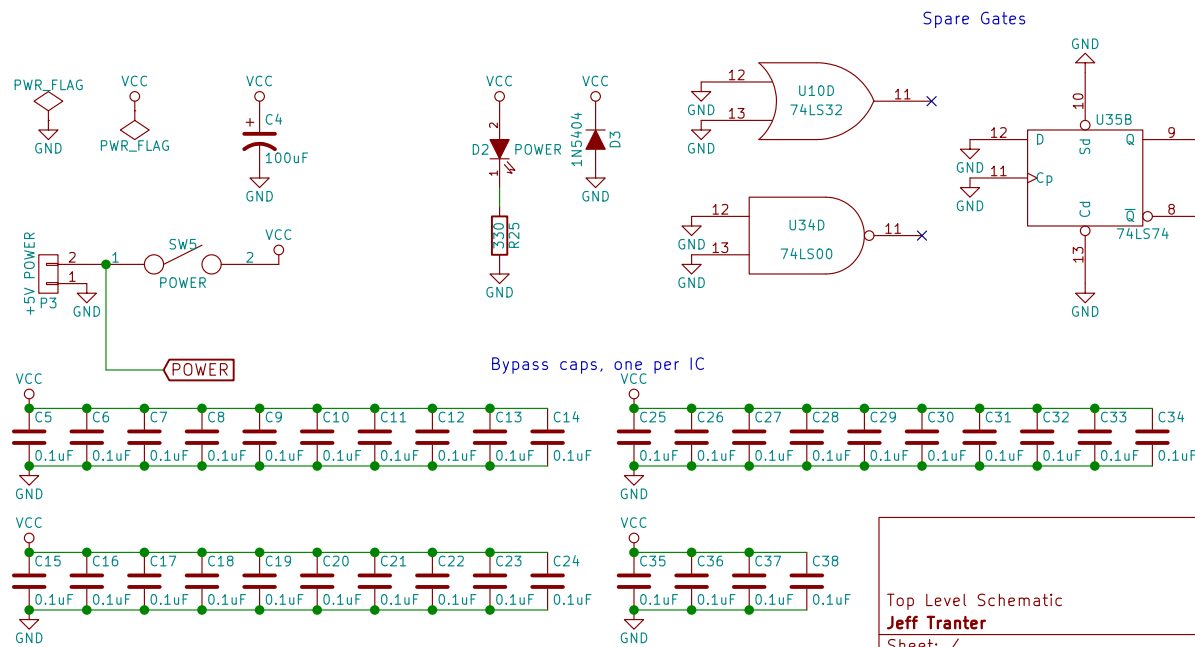
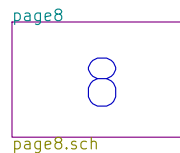
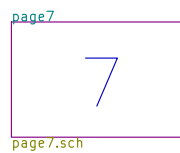
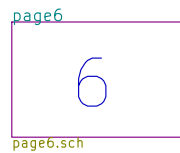
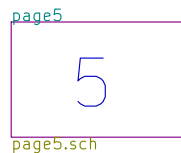
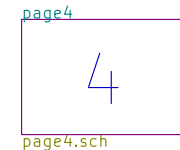
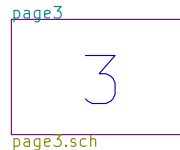
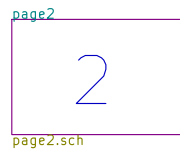
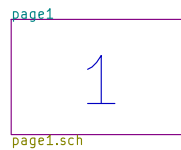


# 68000 Single Board Computer

## from

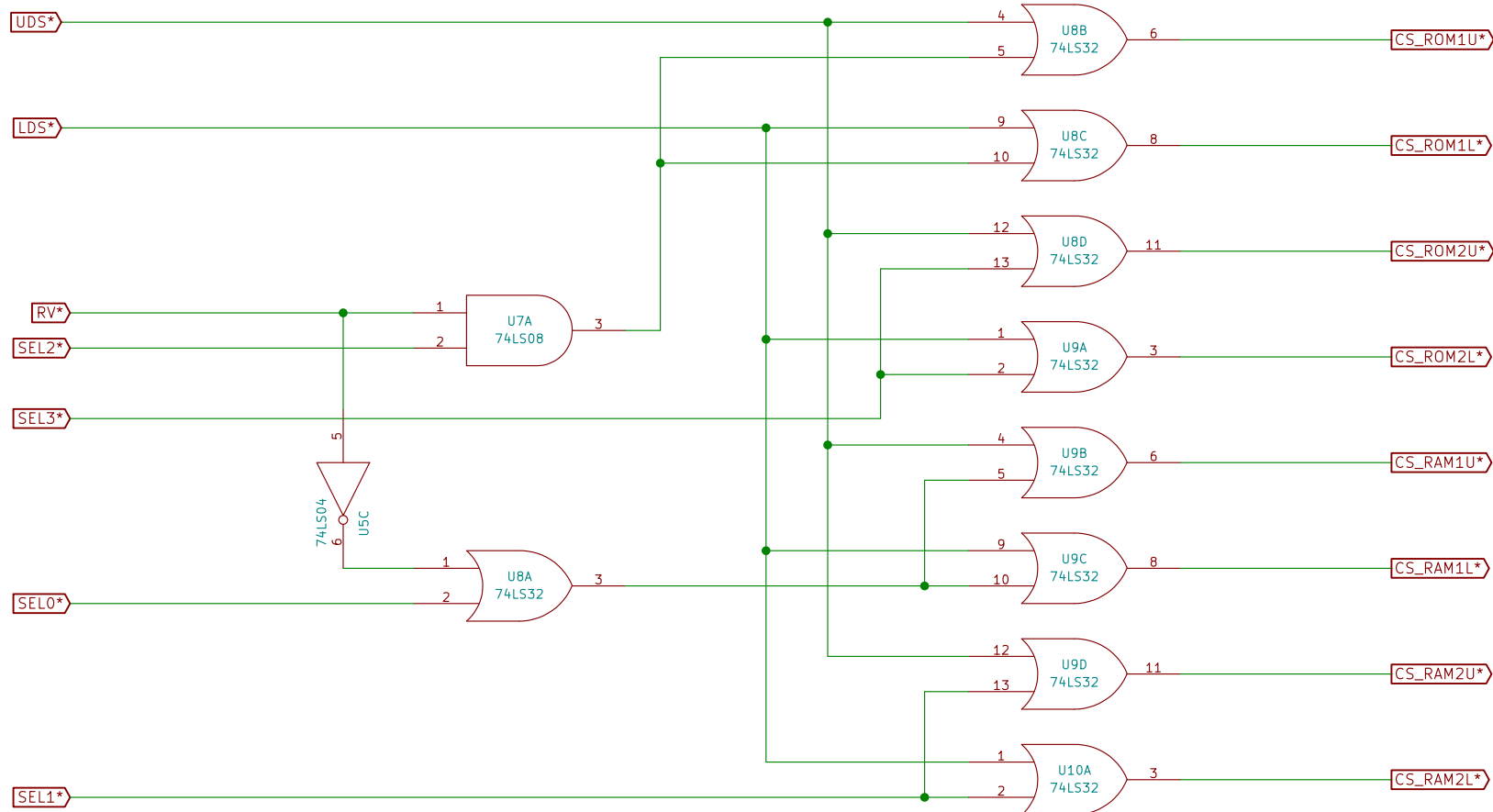
### "Microprocessor Systems Design" by Alan Clements

#### Modified by Jeff Tranter









RAM and ROM Address Select

**Jeff Tranter**

Sheet: /page3/

File: page3.sch

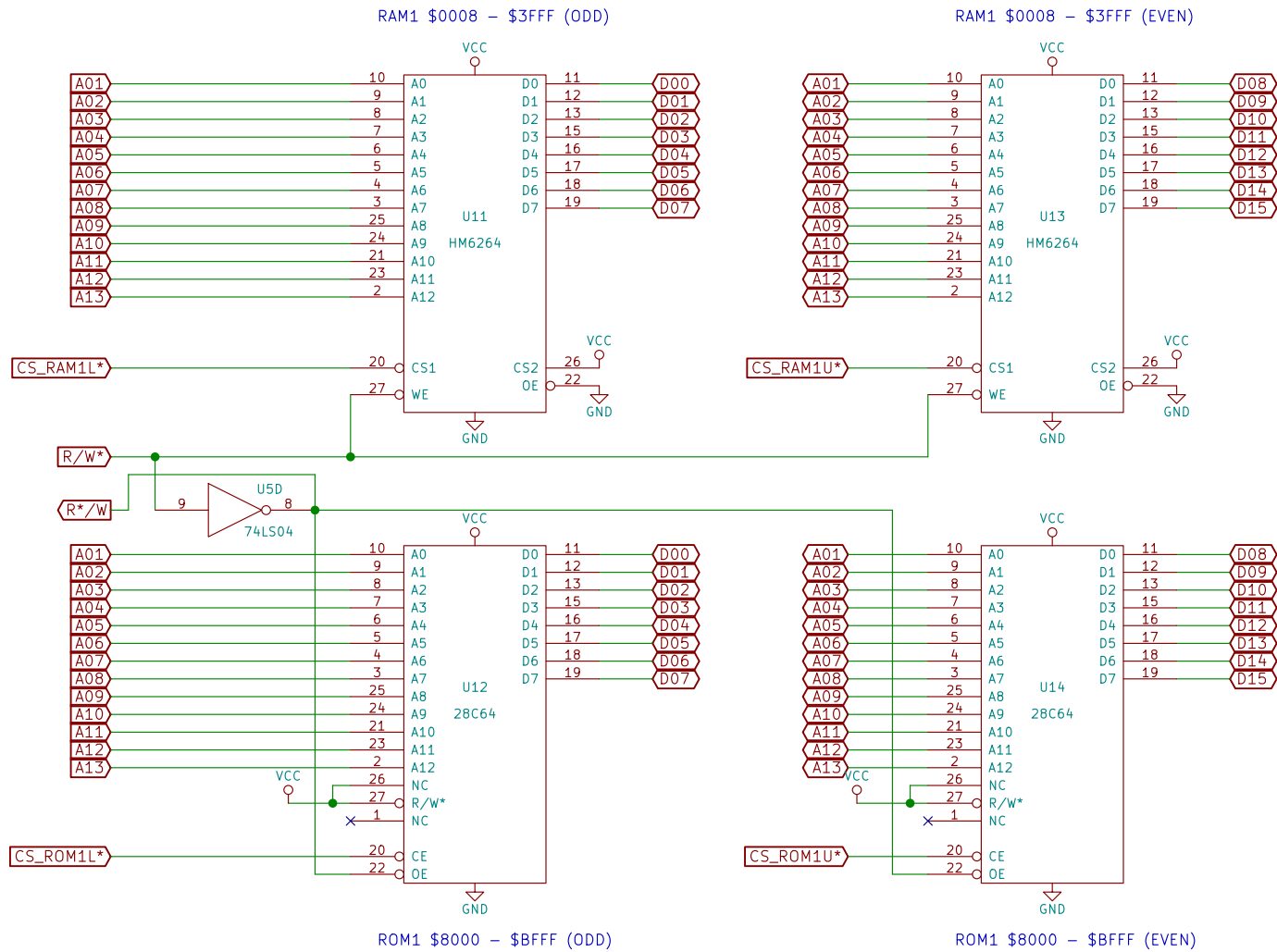
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Size: A Date: 2017-01-23

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**Rev: 2.0**

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RAM and ROM (1 of 2)

Jeff Tranter

Sheet: /page4/

File: page4.sch

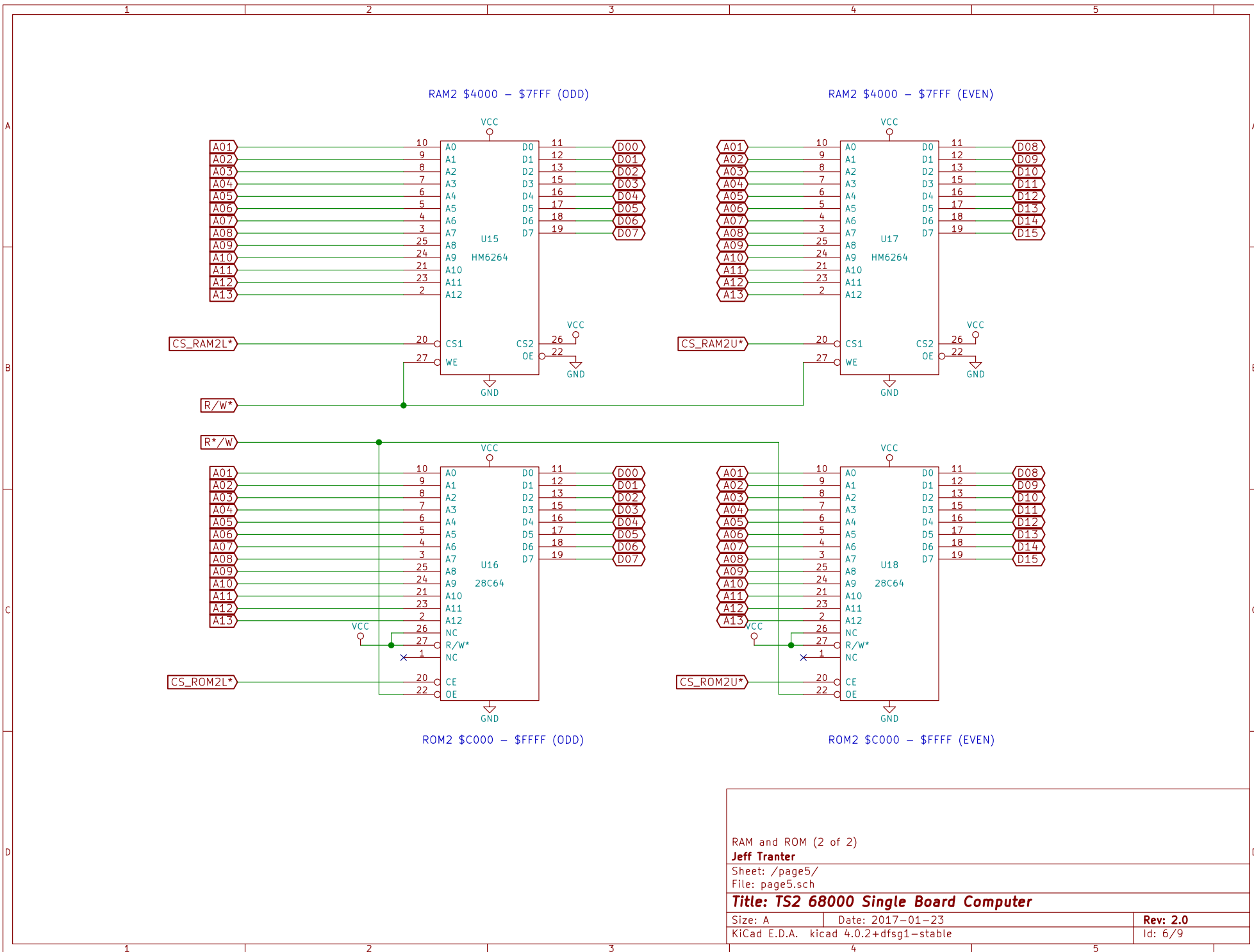
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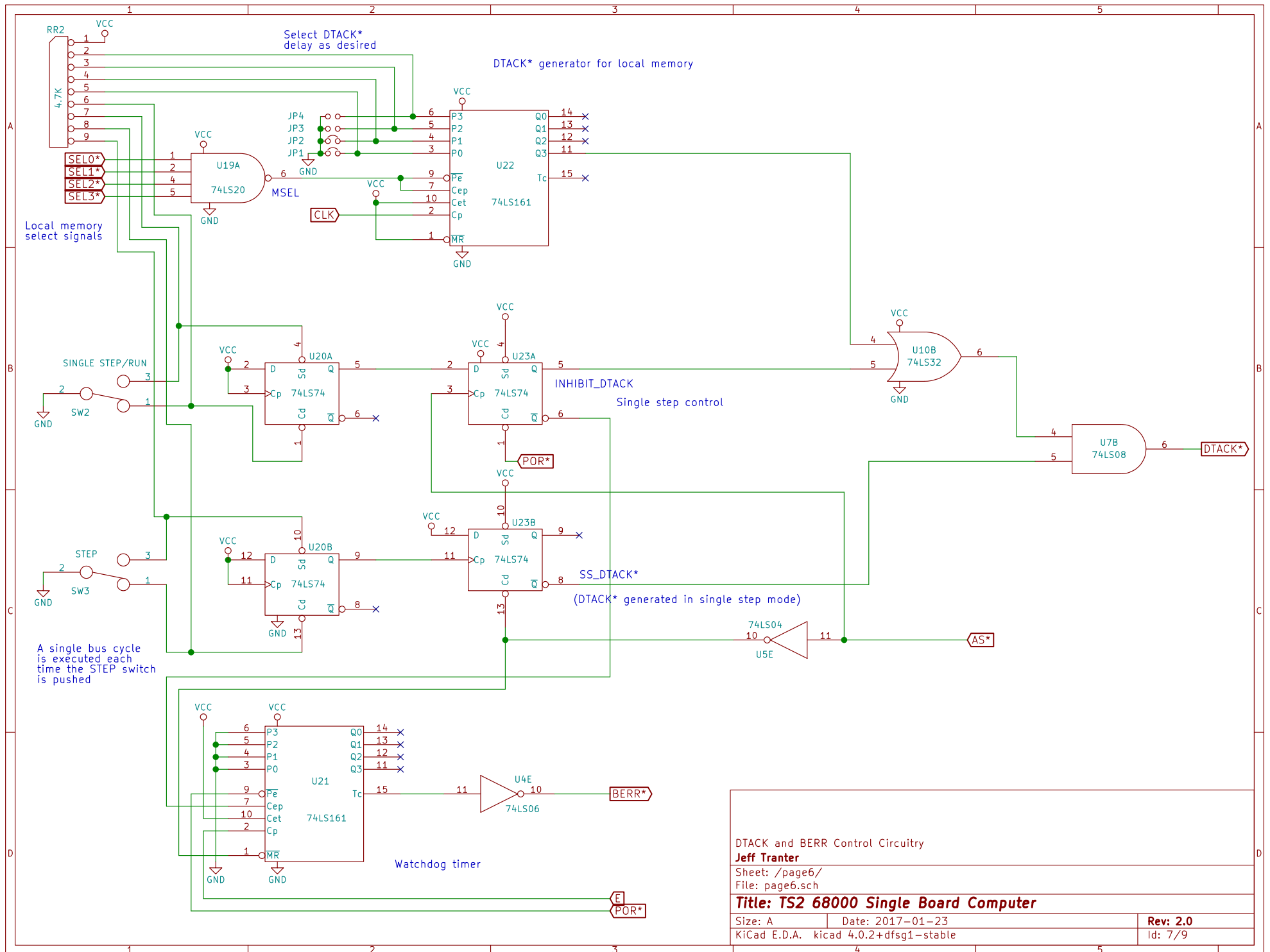
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DTACK and BERR Control Circuitry

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Sheet: /page6/

File: page6.sch

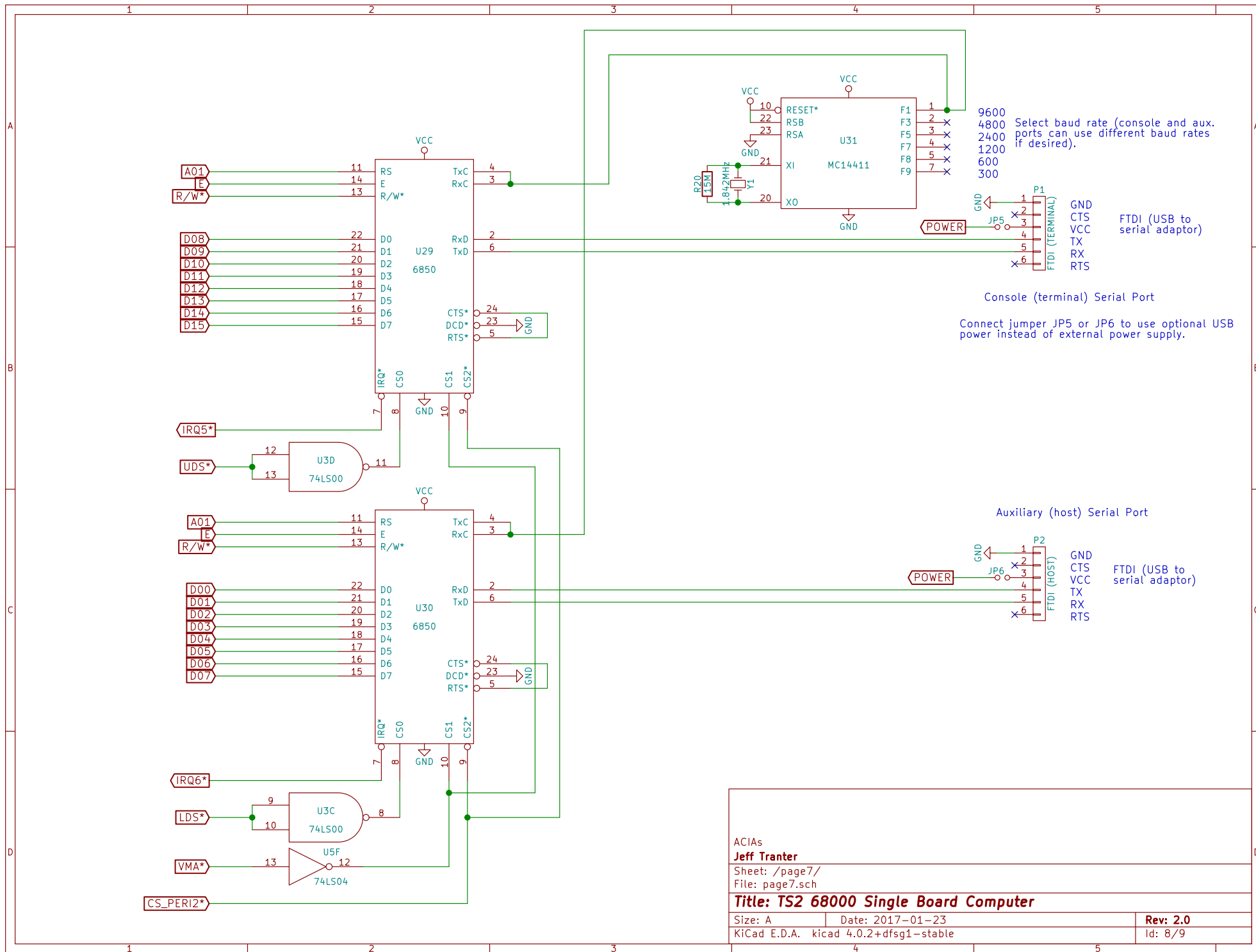
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Rev: 2.0

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ACIAs

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Sheet: /page7/

File: page7.sch

**Title: TS2 68000 Single Board Computer**

Size: A

Date: 2017-01-23

Rev: 2.0

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Id: 8/9



