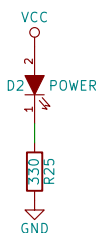
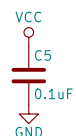
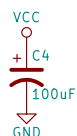
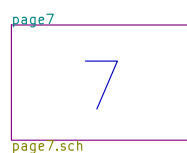
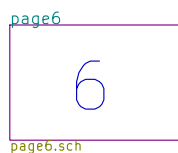
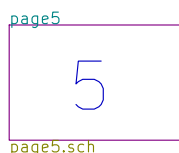
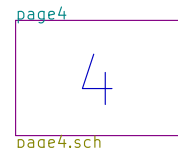
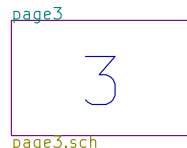
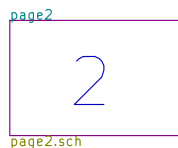
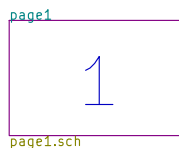
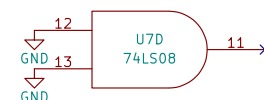
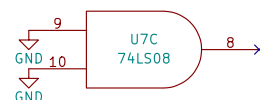
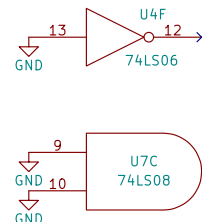


68000 Single Board Computer

from "Microprocessor Systems Design" by Alan Clements Modified by Jeff Tranter



Bypass caps,
one per IC



Spare Gates

Top Level Schematic

Jeff Tranter

Sheet: /

File: ts2.sch

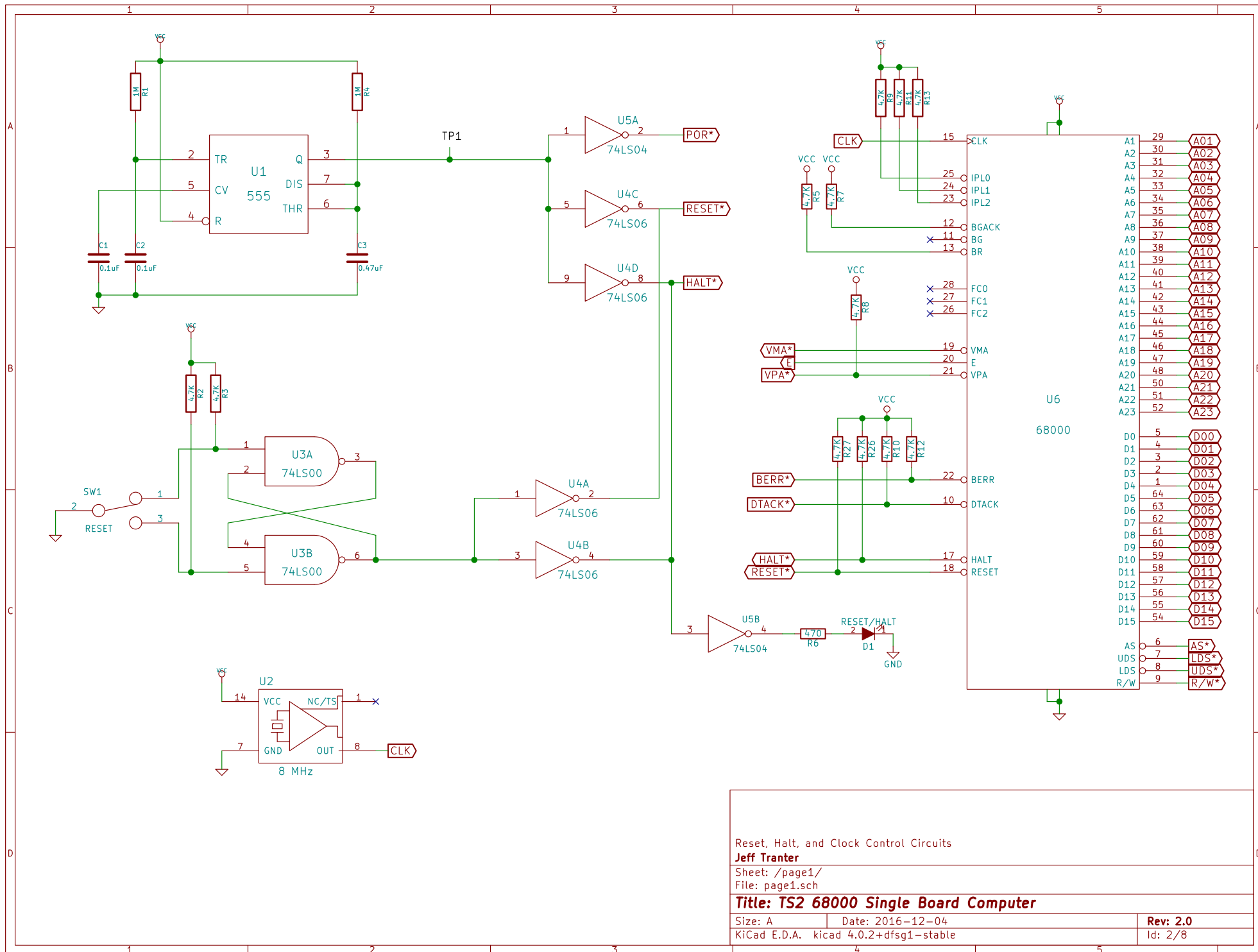
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Size: A Date: 2016-12-04

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.0

Id: 1/8



Reset, Halt, and Clock Control Circuits

Jeff Tranter

Sheet: /page1/

File: page1.sch

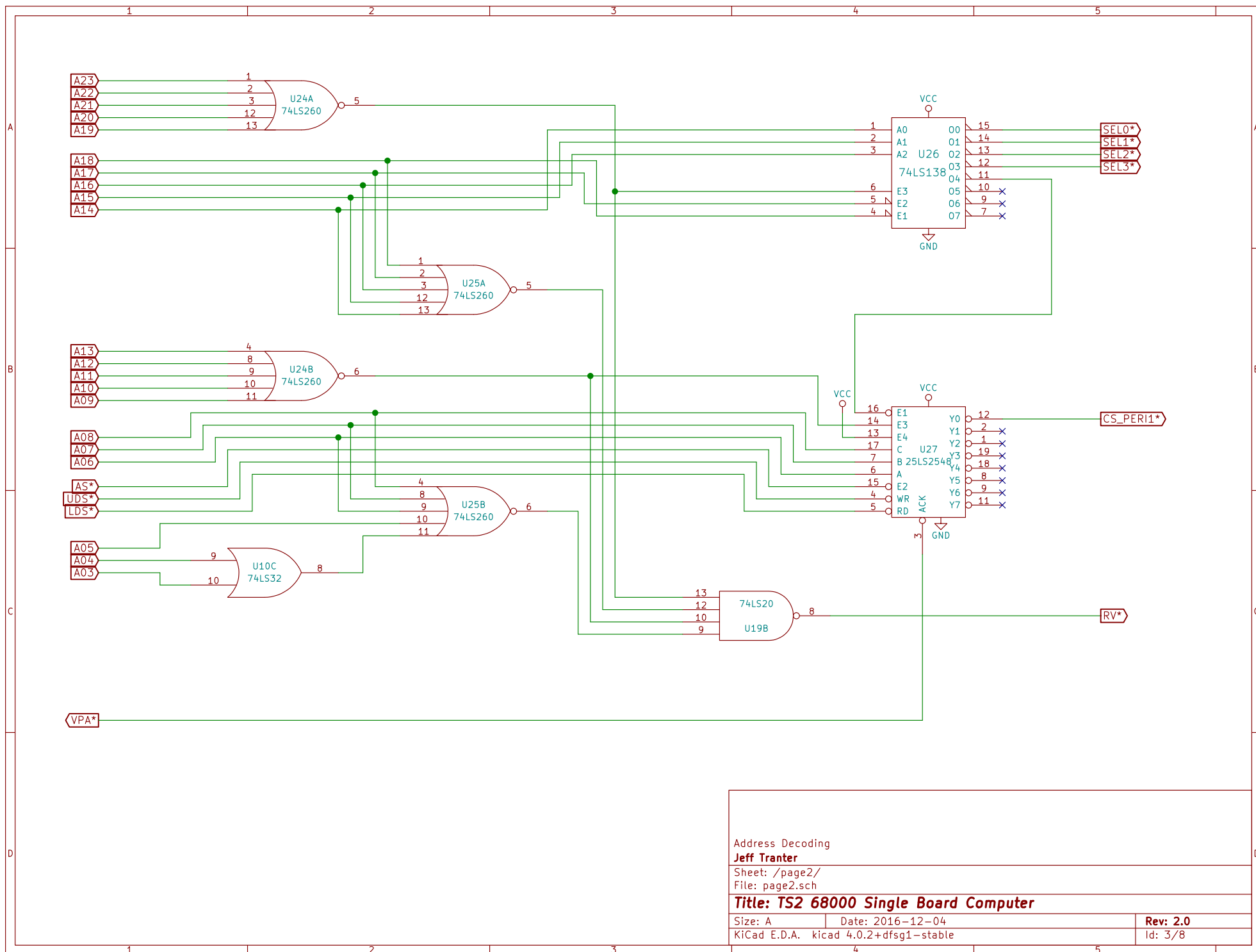
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Rev: 2.0

Id: 2/8



Address Decoding

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File: page2.sch

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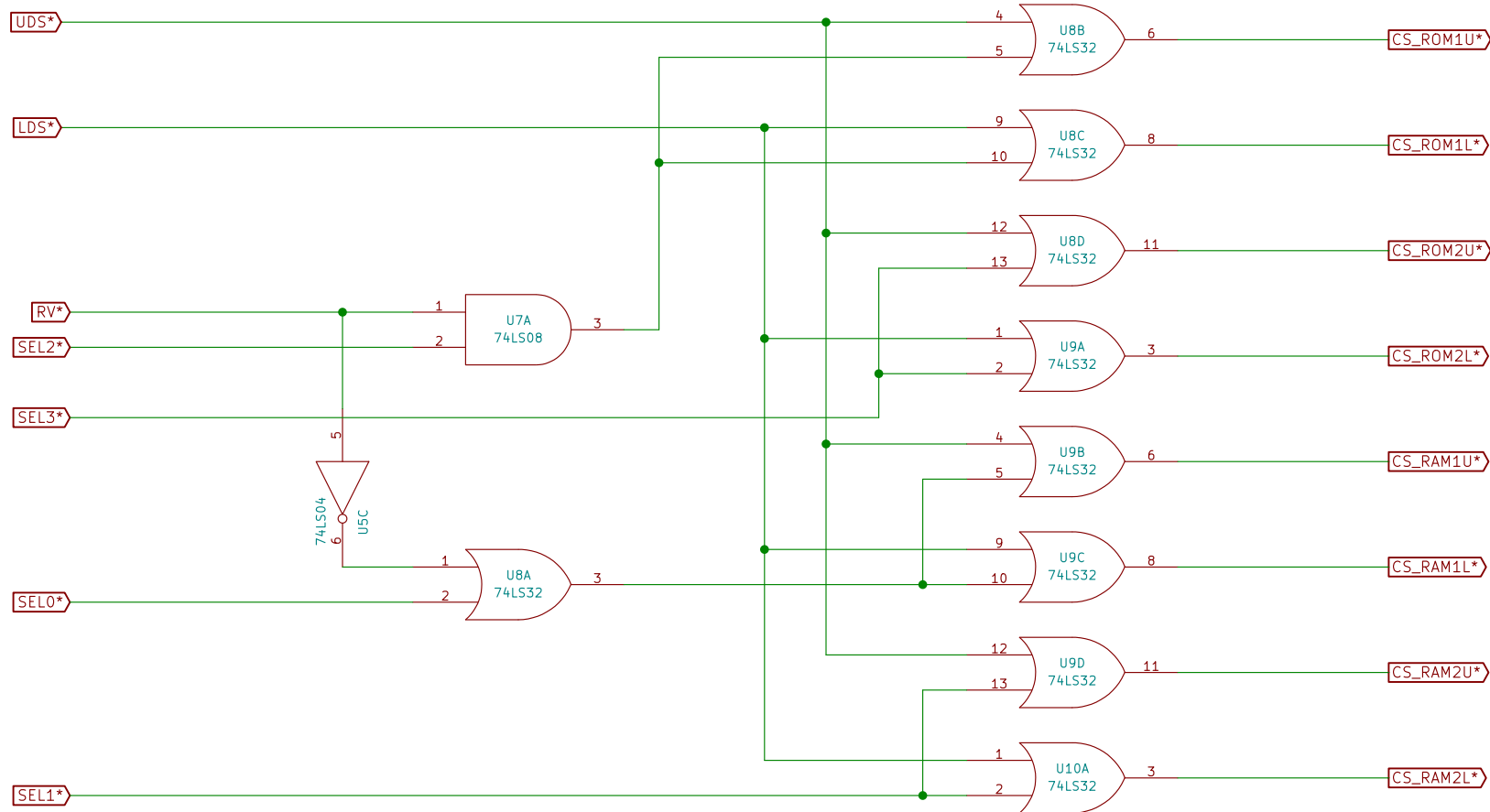
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Date: 2016-12-04

Rev: 2.0

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Id: 3/8



RAM and ROM Address Select

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File: page3.sch

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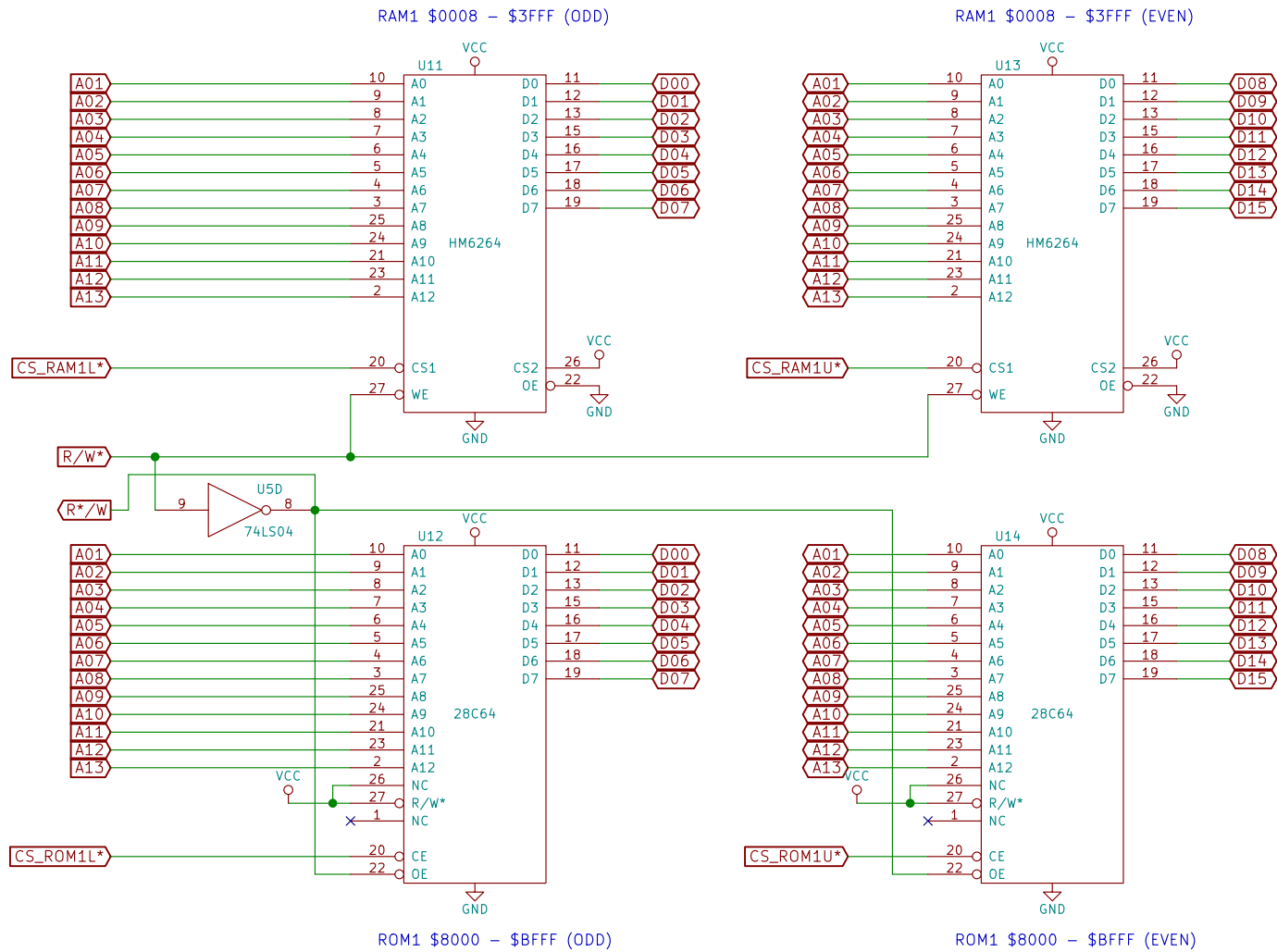
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Rev: 2.0

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Id: 4/8



RAM and ROM (1 of 2)

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File: page4.sch

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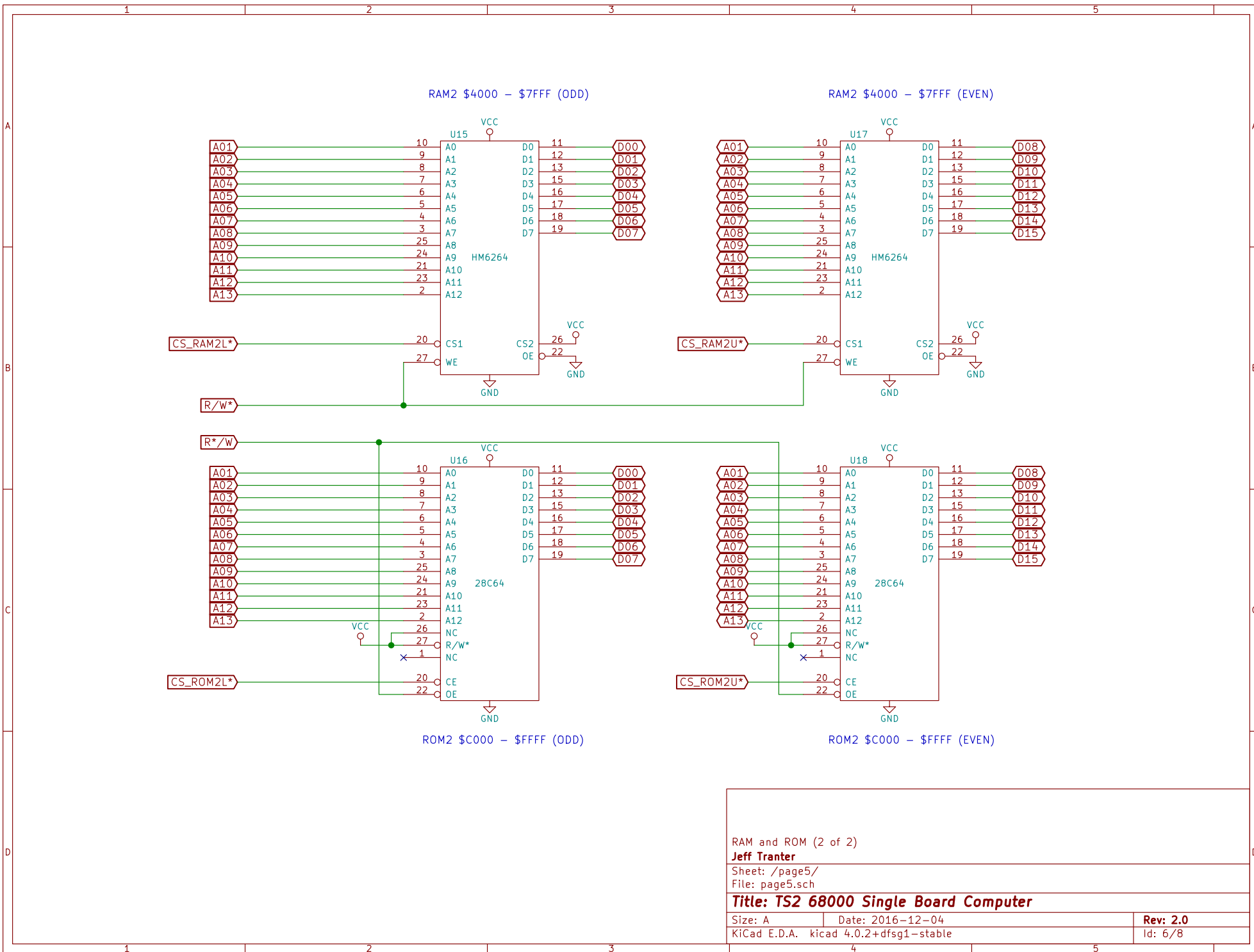
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Date: 2016-12-04

Rev: 2.0

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Id: 5/8



RAM and ROM (2 of 2)

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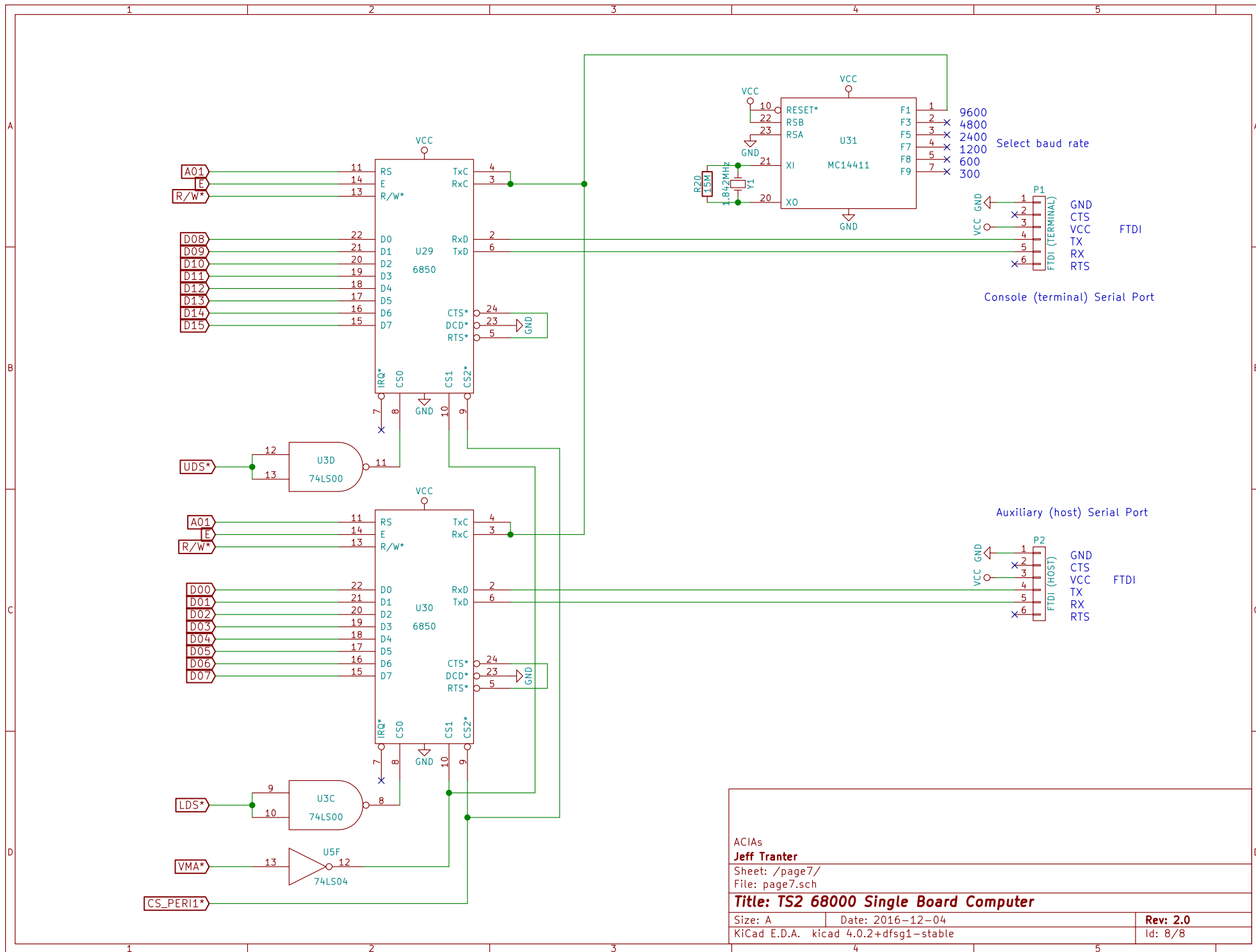
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Size: A Date: 2016-12-04
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Rev: 2.0
Id: 6/8



ACIAs

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File: page7.sch

Title: TS2 68000 Single Board Computer

Size: A

Date: 2016-12-04

Rev: 2.0

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Id: 8/8