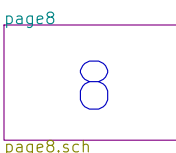
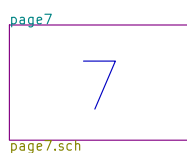
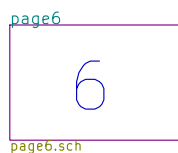
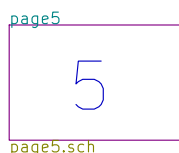
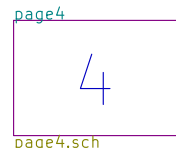
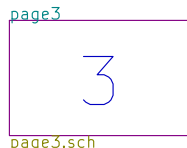
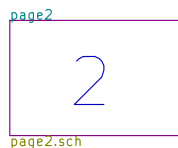
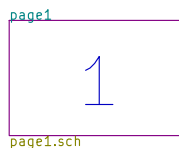
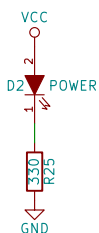
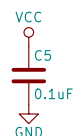
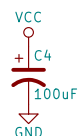


68000 Single Board Computer

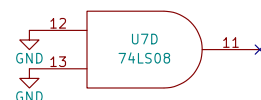
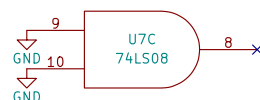
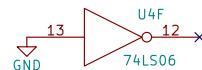
from "Microprocessor Systems Design" by Alan Clements Modified by Jeff Tranter



Bypass caps,
one per IC



Spare Gates



Top Level Schematic

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Sheet: /

File: ts2.sch

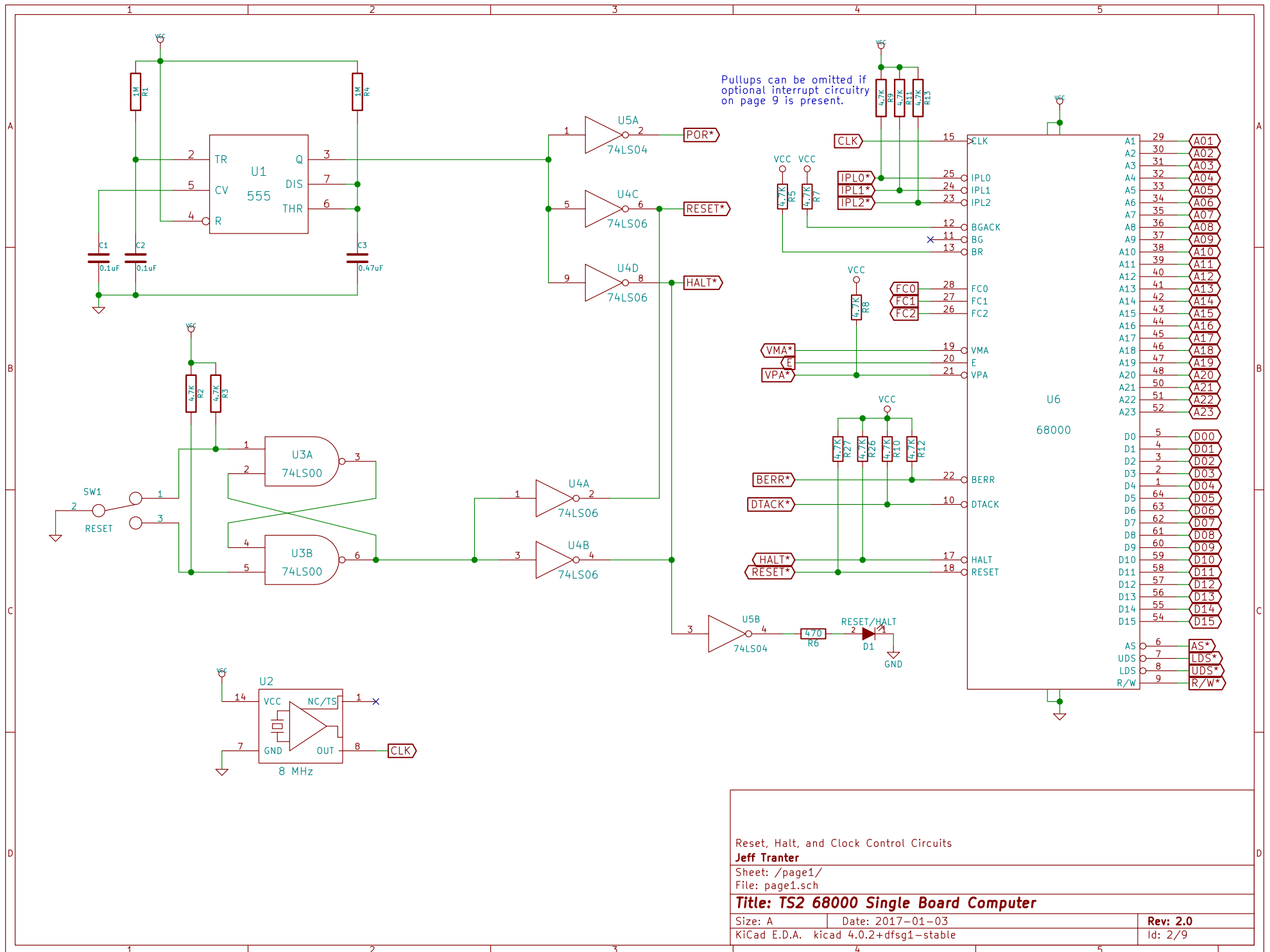
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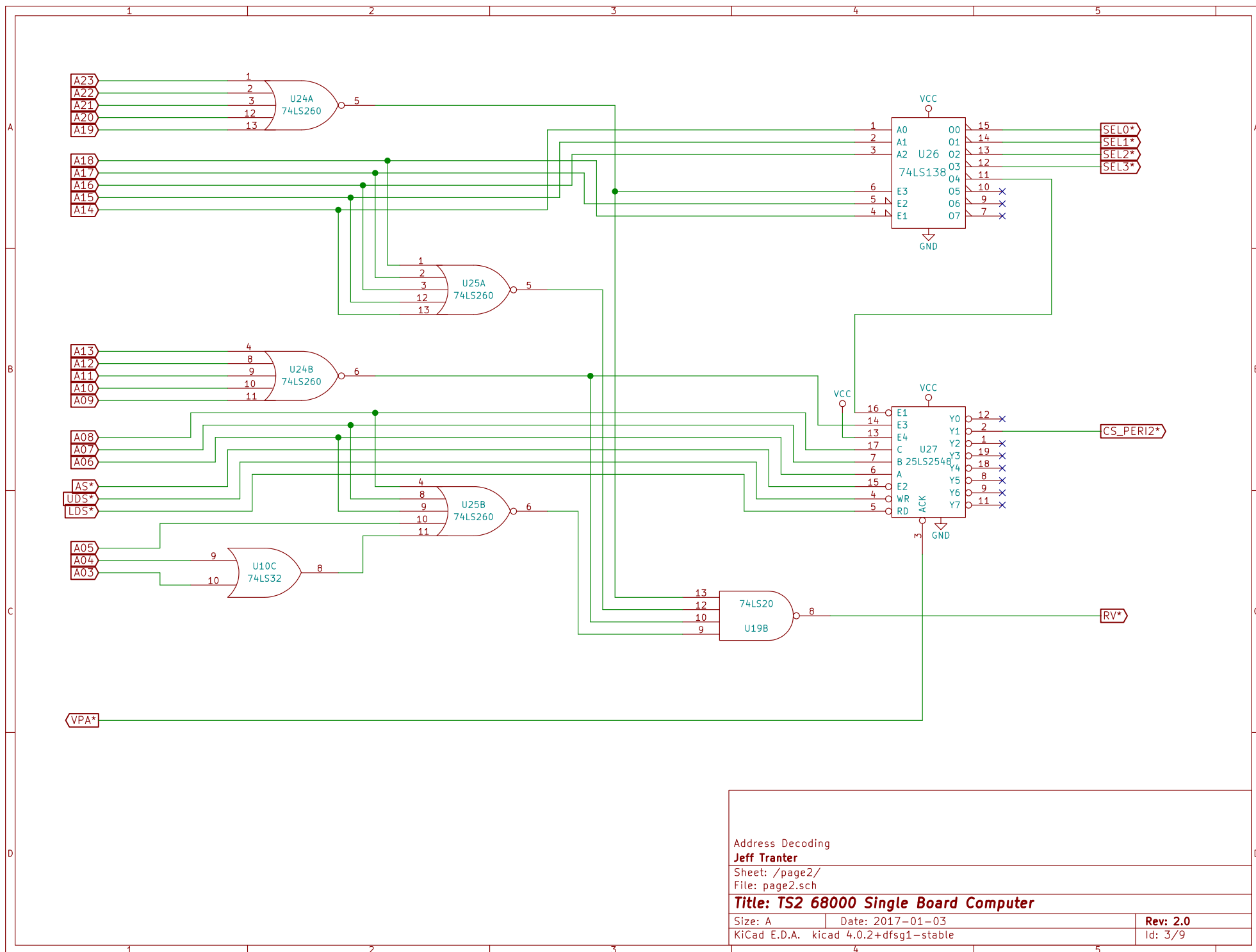
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KiCad E.D.A. kicad 4.0.2+dfsg1-stable

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Id: 1/9





Address Decoding

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File: page2.sch

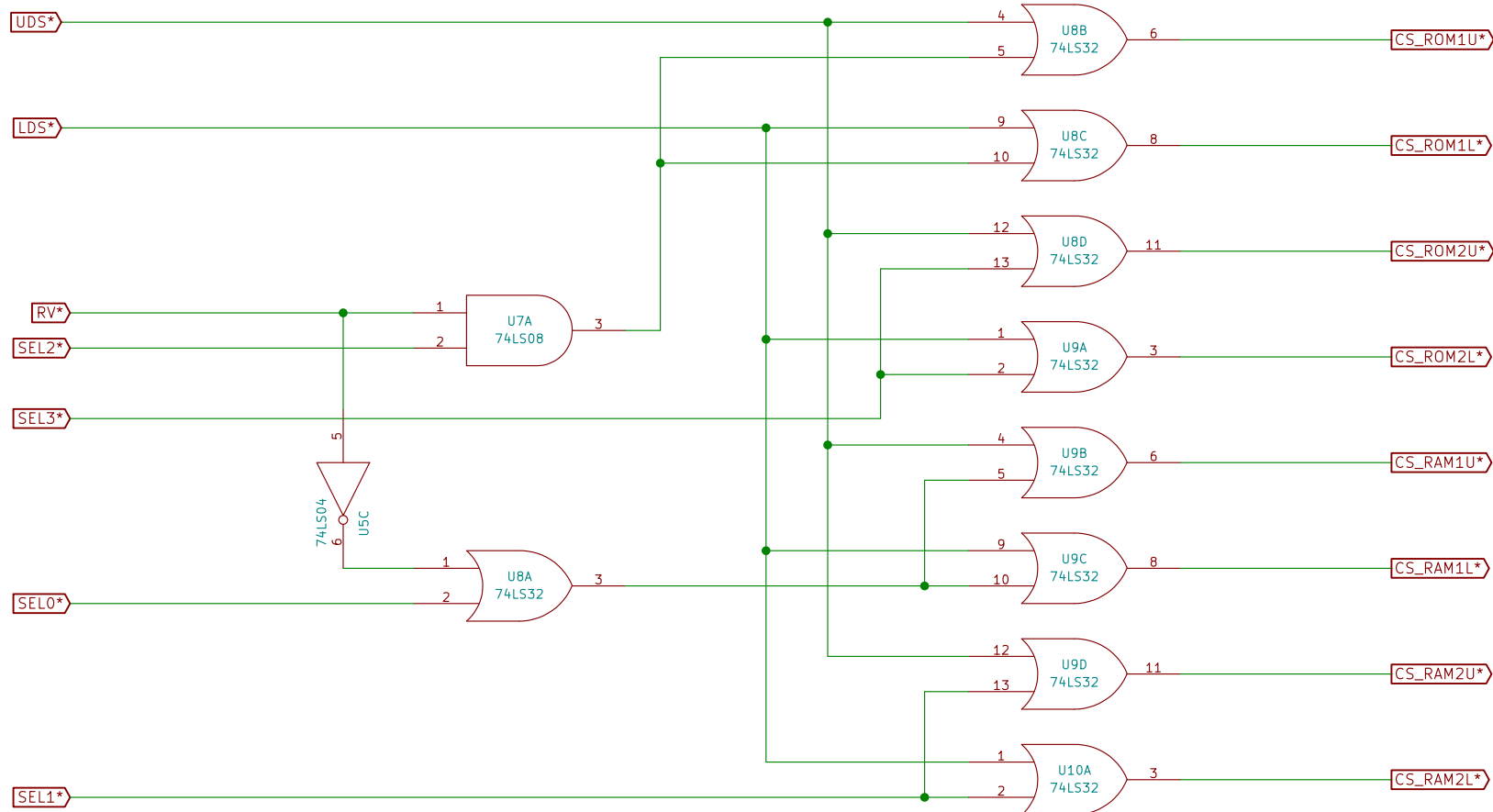
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Rev: 2.0

Id: 3/9



RAM and ROM Address Select

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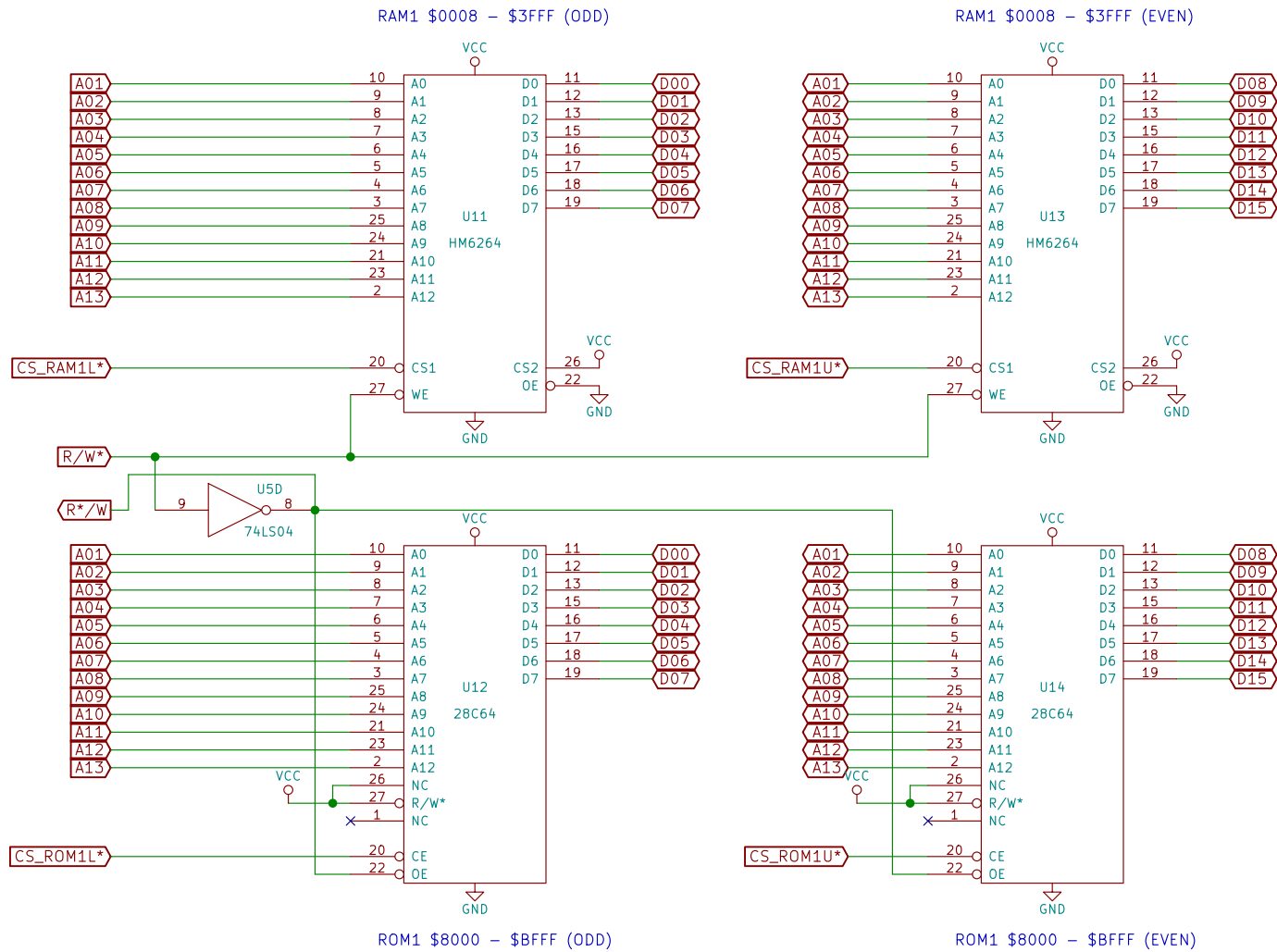
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Rev: 2.0

Id: 4/9



RAM and ROM (1 of 2)

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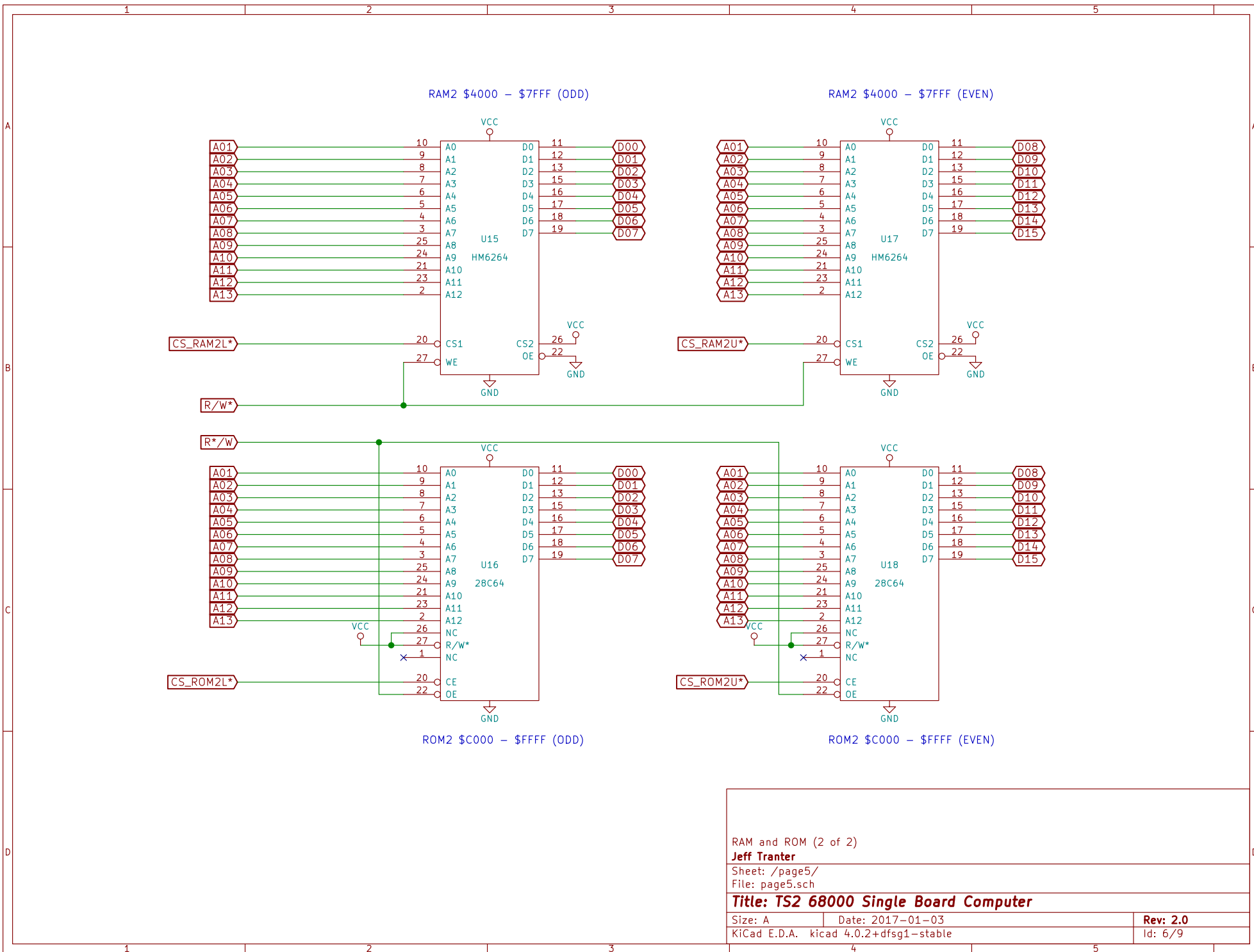
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Rev: 2.0
Id: 5/9



RAM and ROM (2 of 2)

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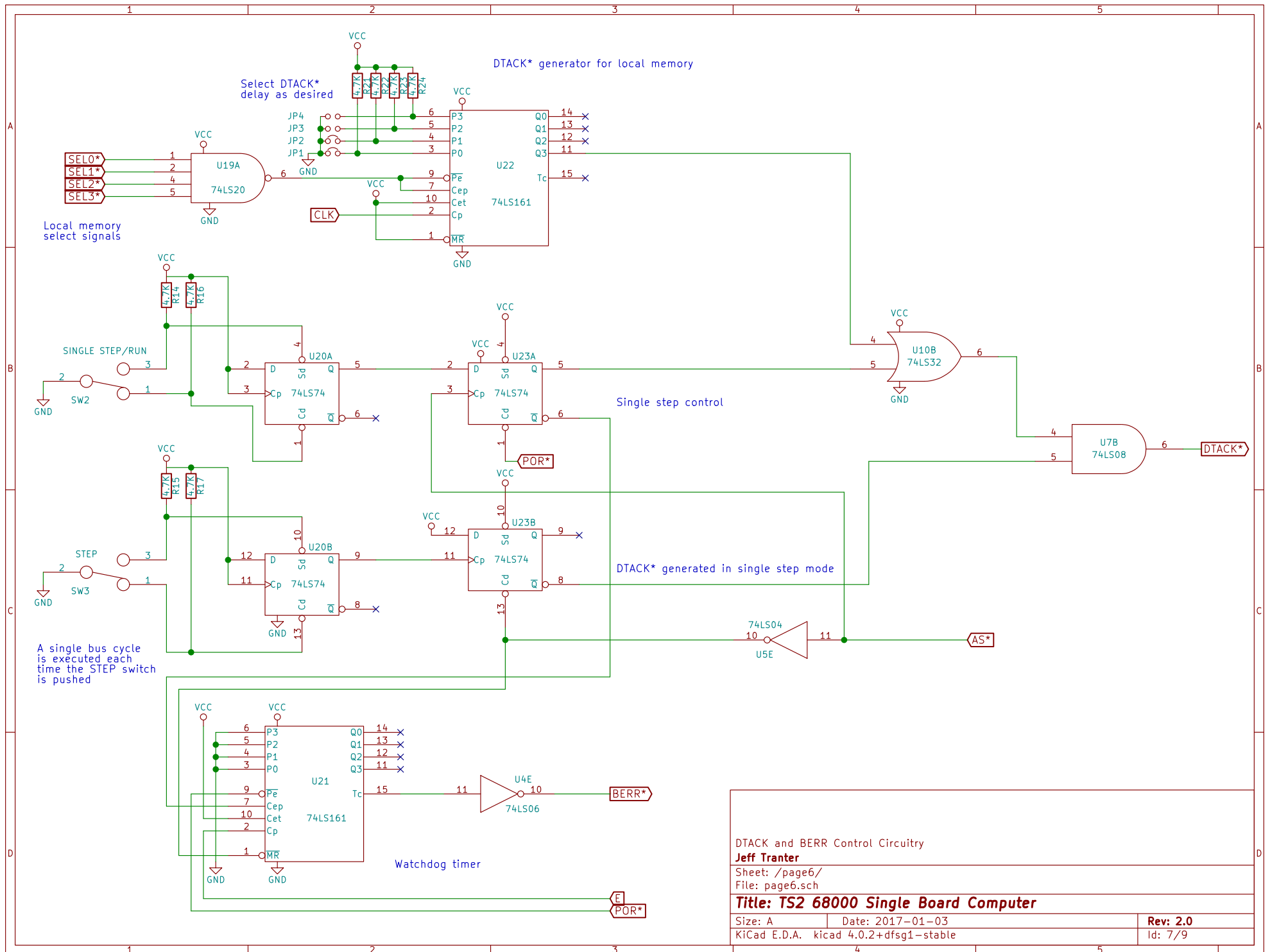
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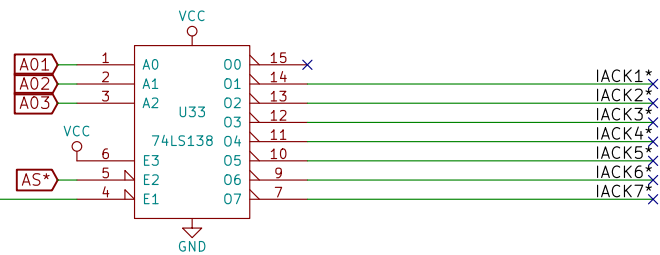
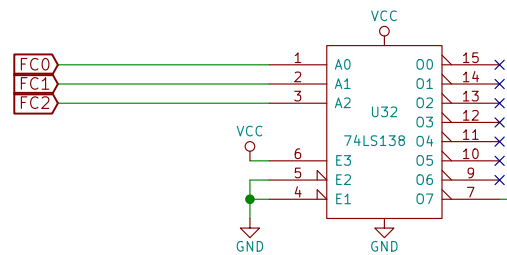
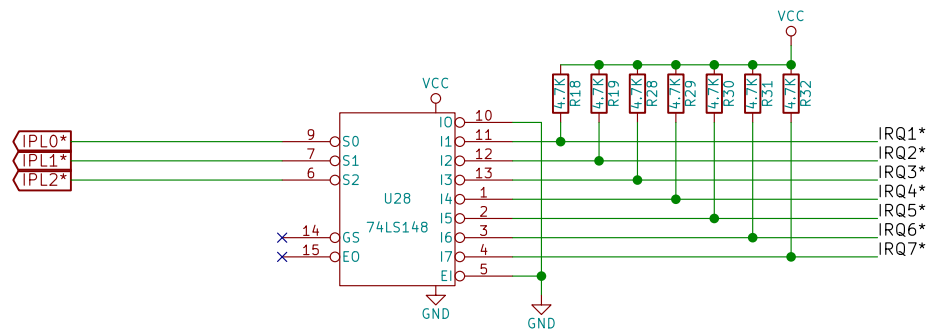
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Rev: 2.0

Id: 6/9





Interrupt Control Circuitry (optional)

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Sheet: /page8/

File: page8.sch

Title: TS2 68000 Single Board Computer

Size: A4

Date: 2017-01-03

Rev: 2.0

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

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