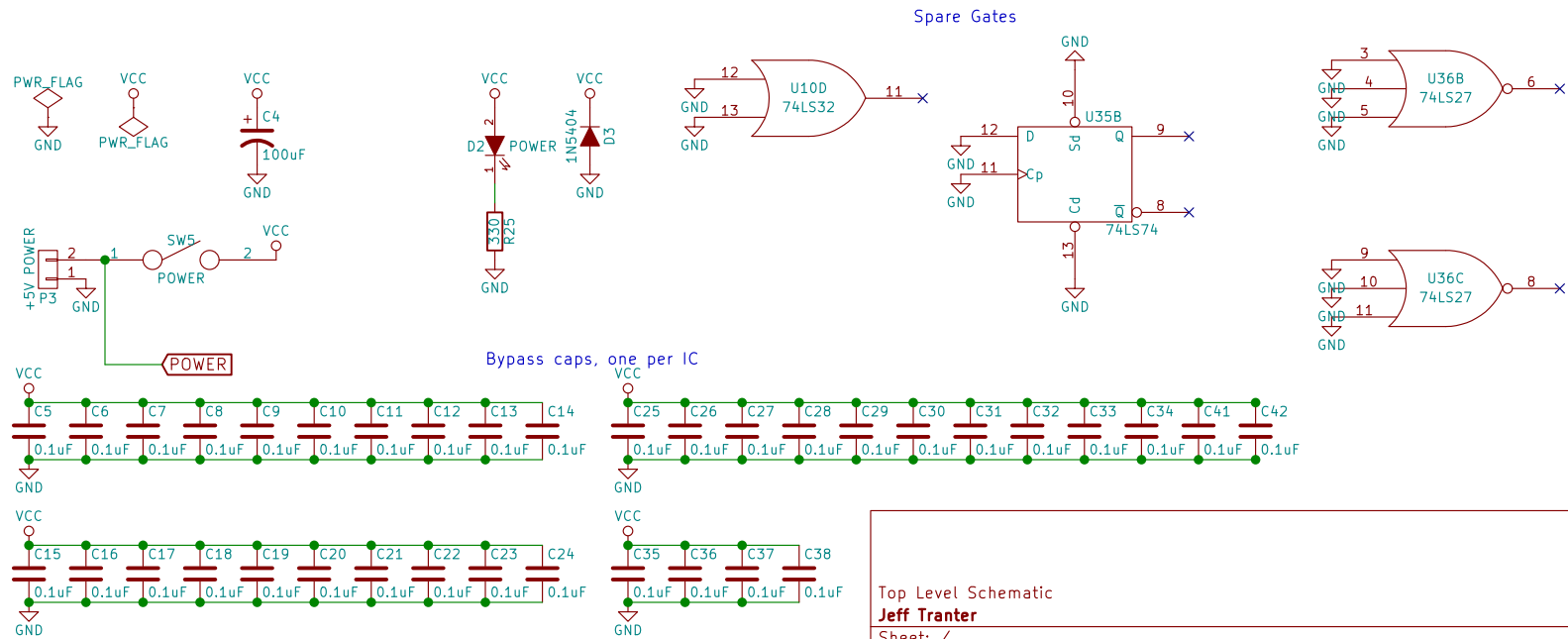
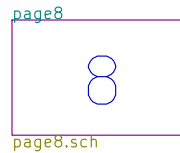
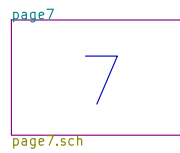
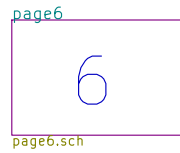
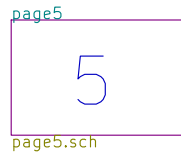
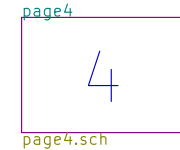
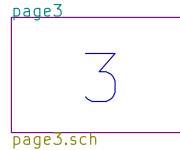
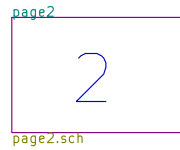
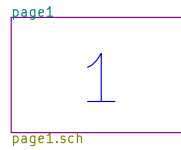


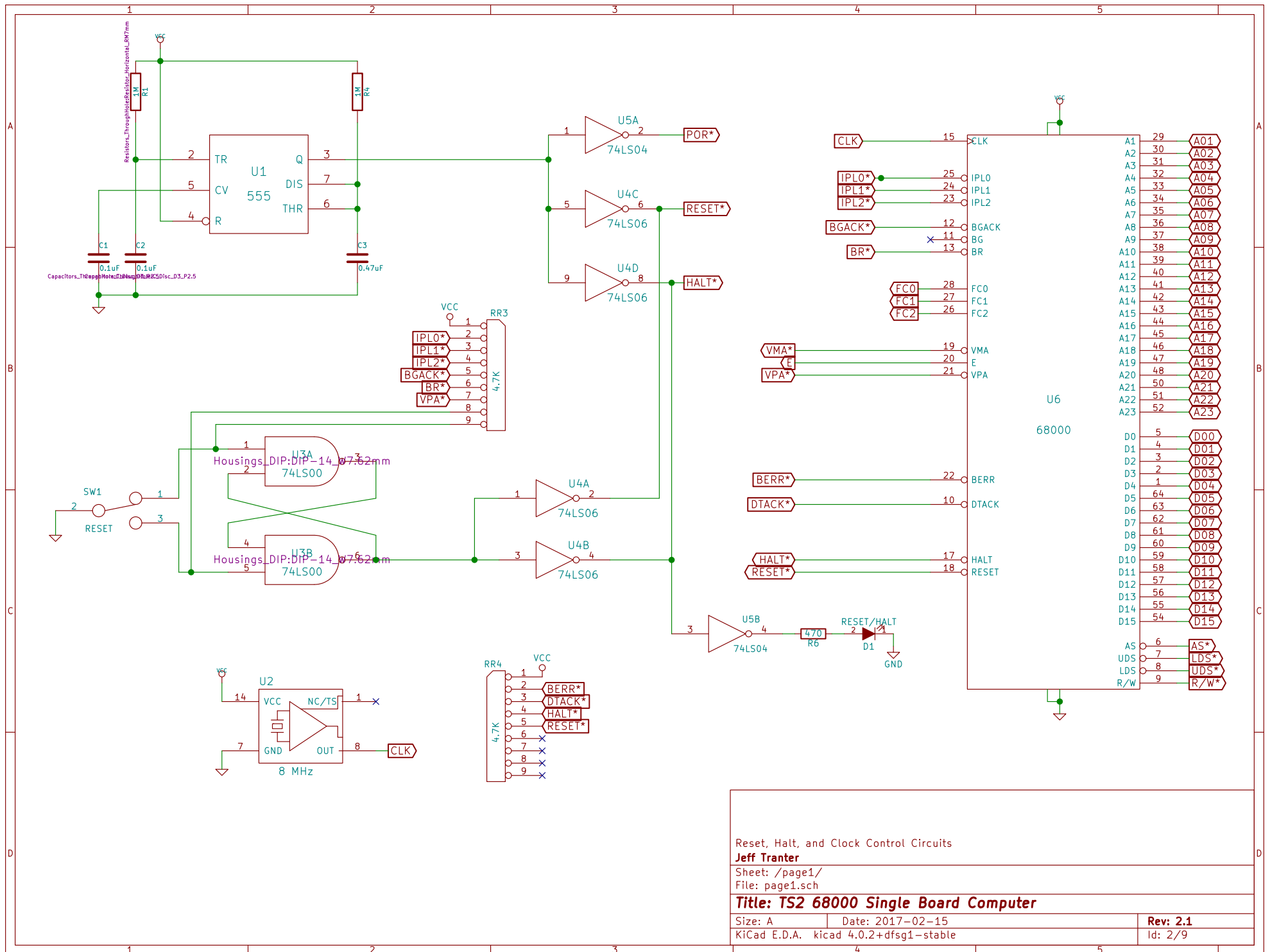
68000 Single Board Computer

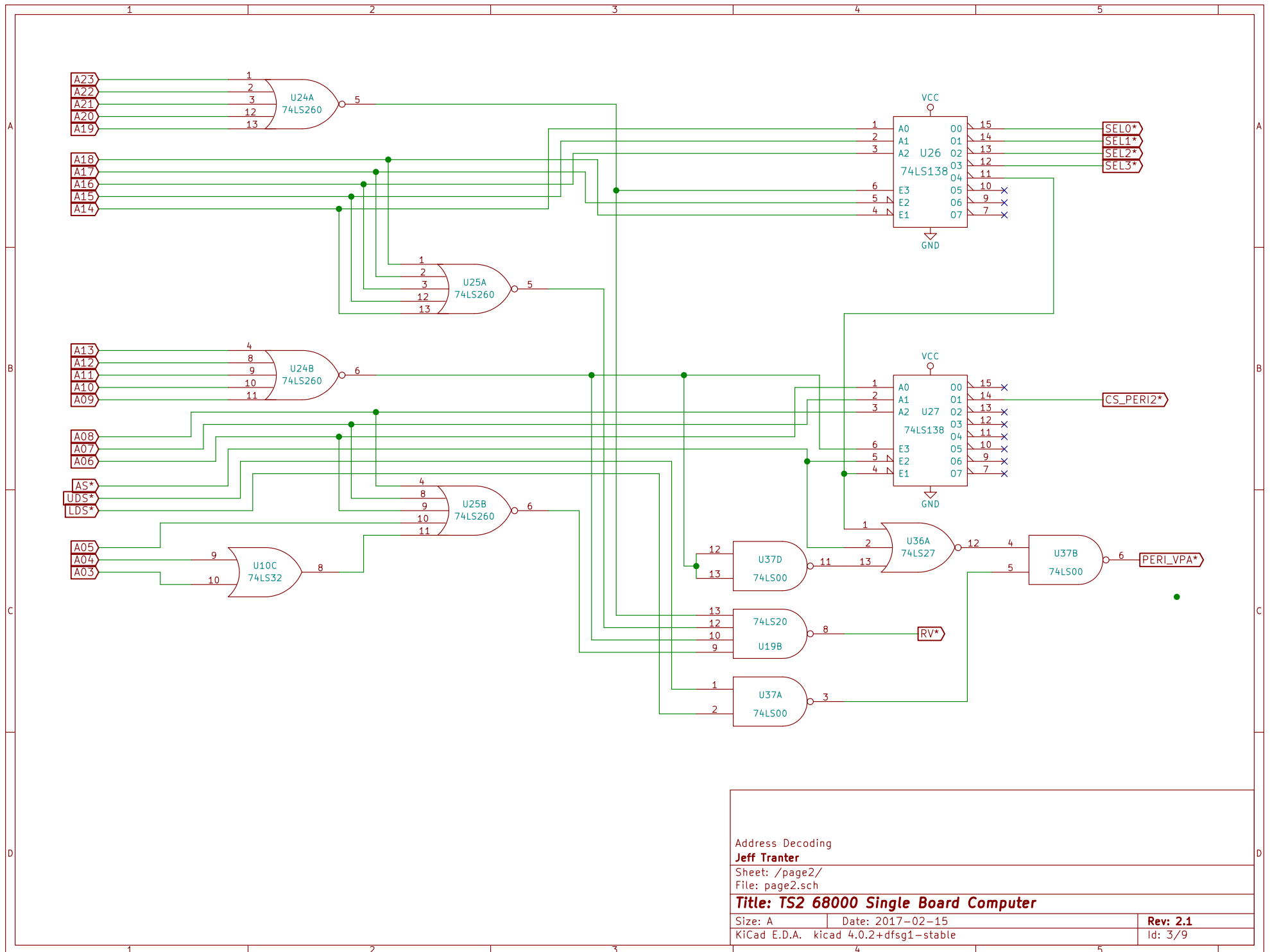
from

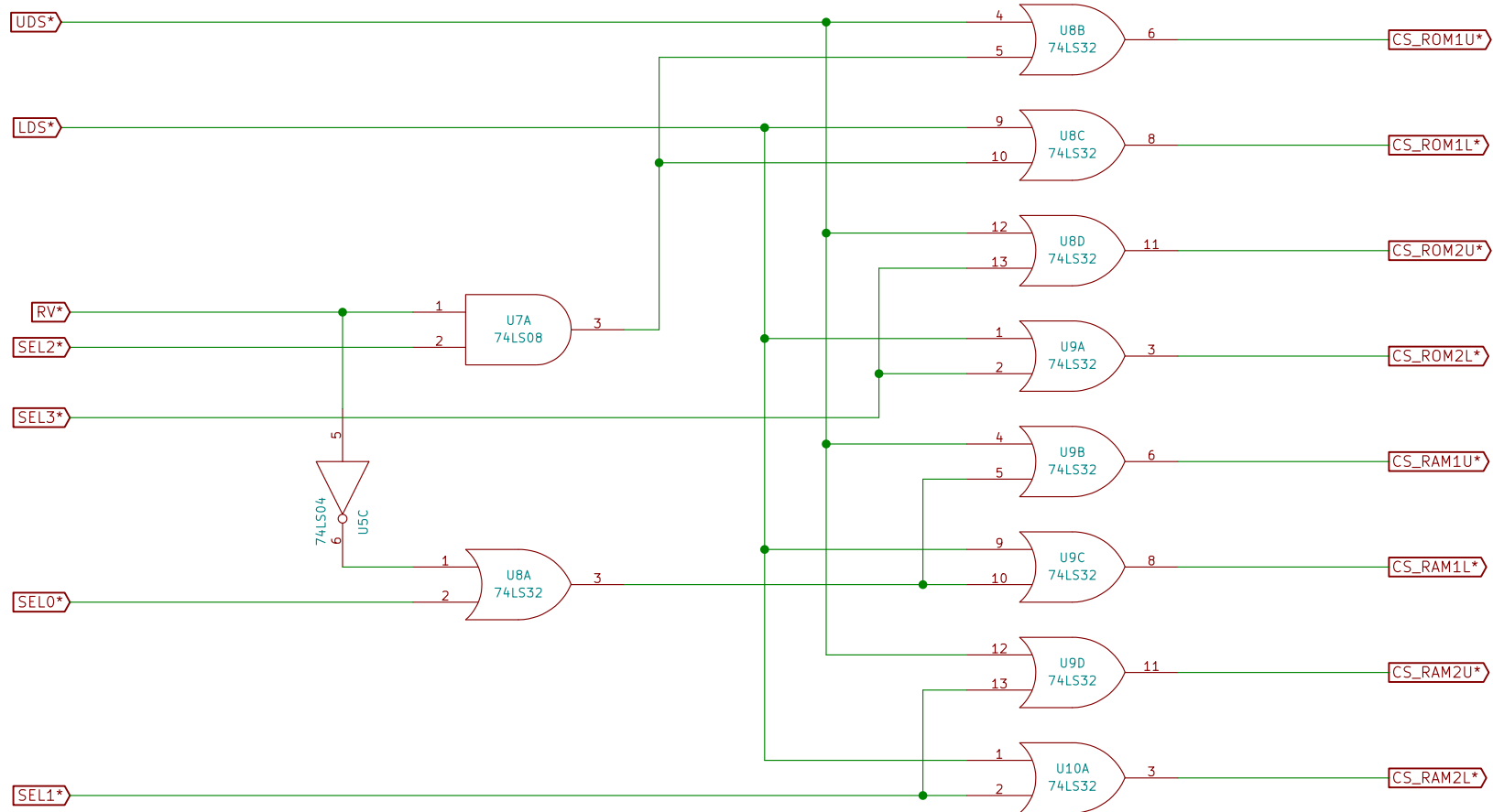
"Microprocessor Systems Design" by Alan Clements

Modified by Jeff Tranter









RAM and ROM Address Select

Jeff Tranter

Sheet: /page3/

File: page3.sch

Title: TS2 68000 Single Board Computer

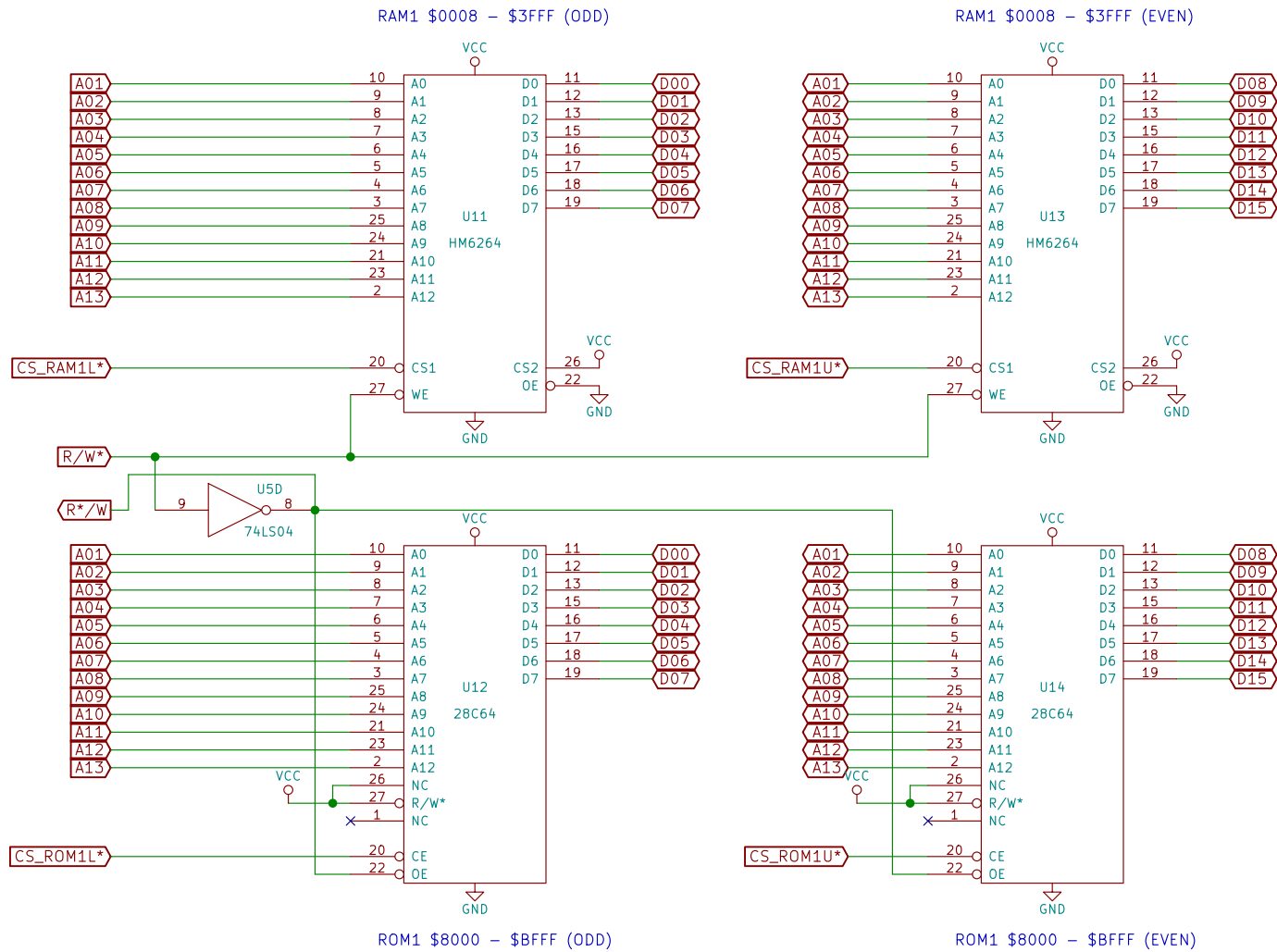
Size: A

Date: 2017-02-15

Rev: 2.1

KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Id: 4/9



RAM and ROM (1 of 2)

Jeff Tranter

Sheet: /page4/

File: page4.sch

Title: TS2 68000 Single Board Computer

Size: A Date: 2017-02-15
KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.1
Id: 5/9

