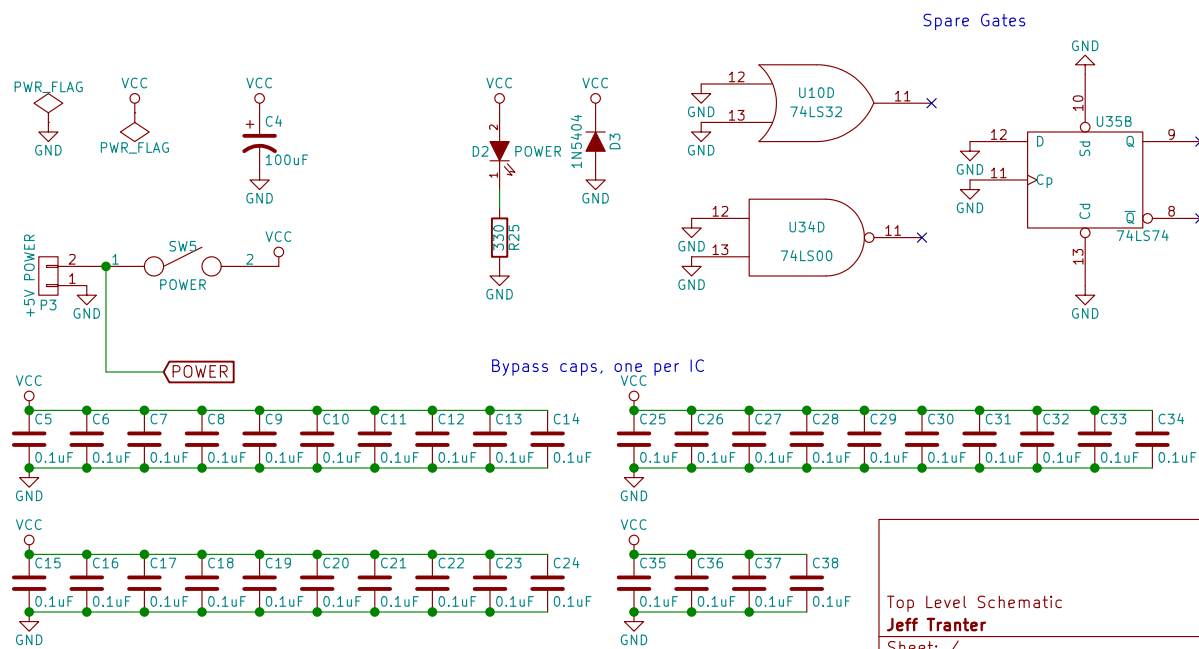
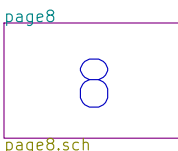
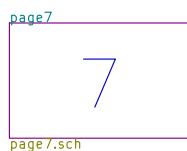
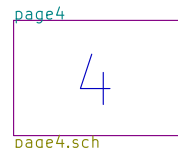
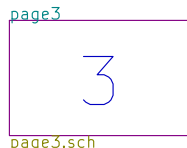
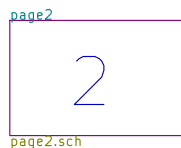
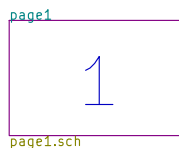


# 68000 Single Board Computer

## from

### "Microprocessor Systems Design" by Alan Clements

### Modified by Jeff Tranter



Top Level Schematic

Jeff Tranter

Sheet: /

File: ts2.sch

**Title: TS2 68000 Single Board Computer**

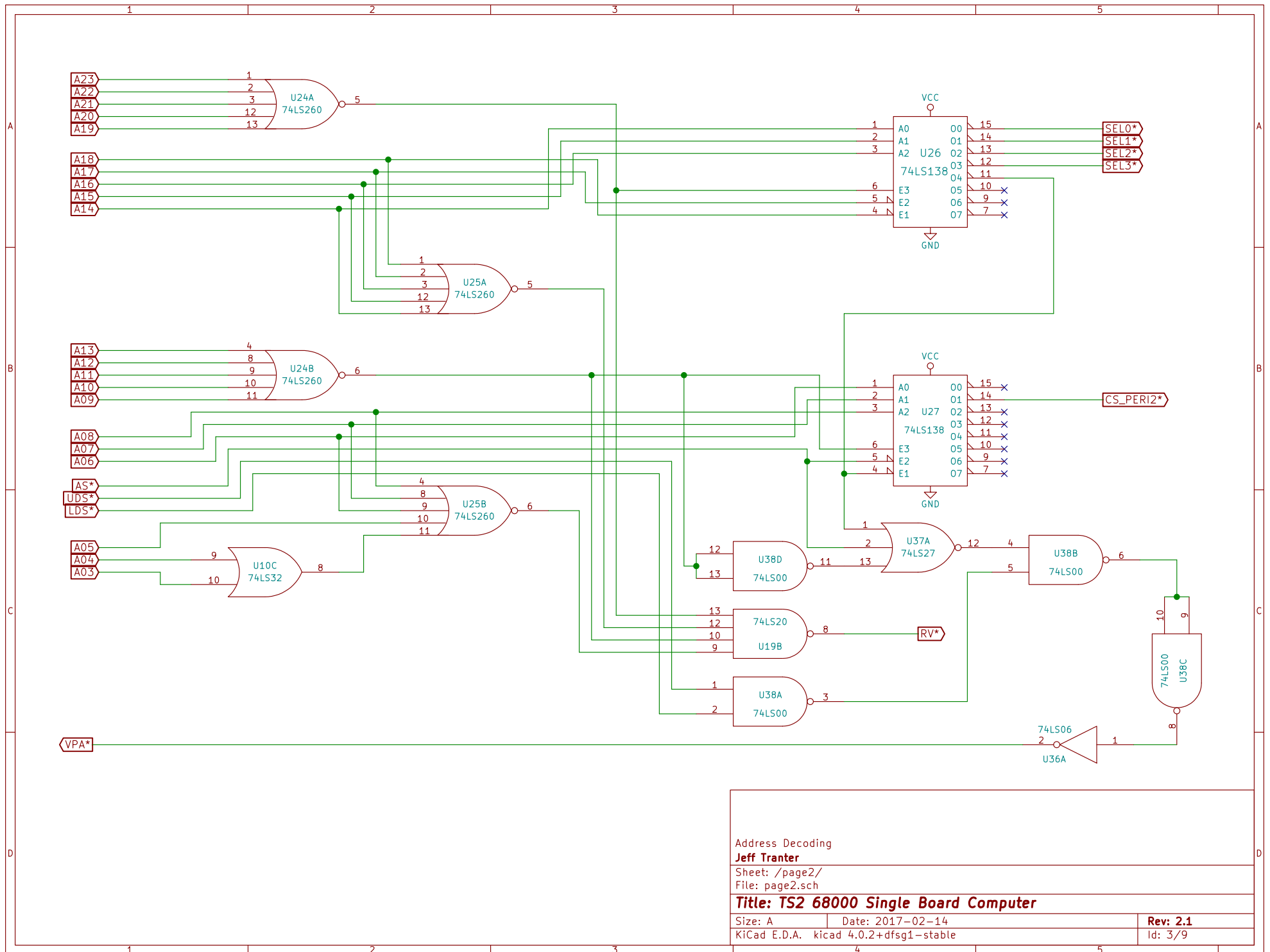
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KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 2.1

Id: 1/9





Address Decoding

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Sheet: /page2/

File: page2.sch

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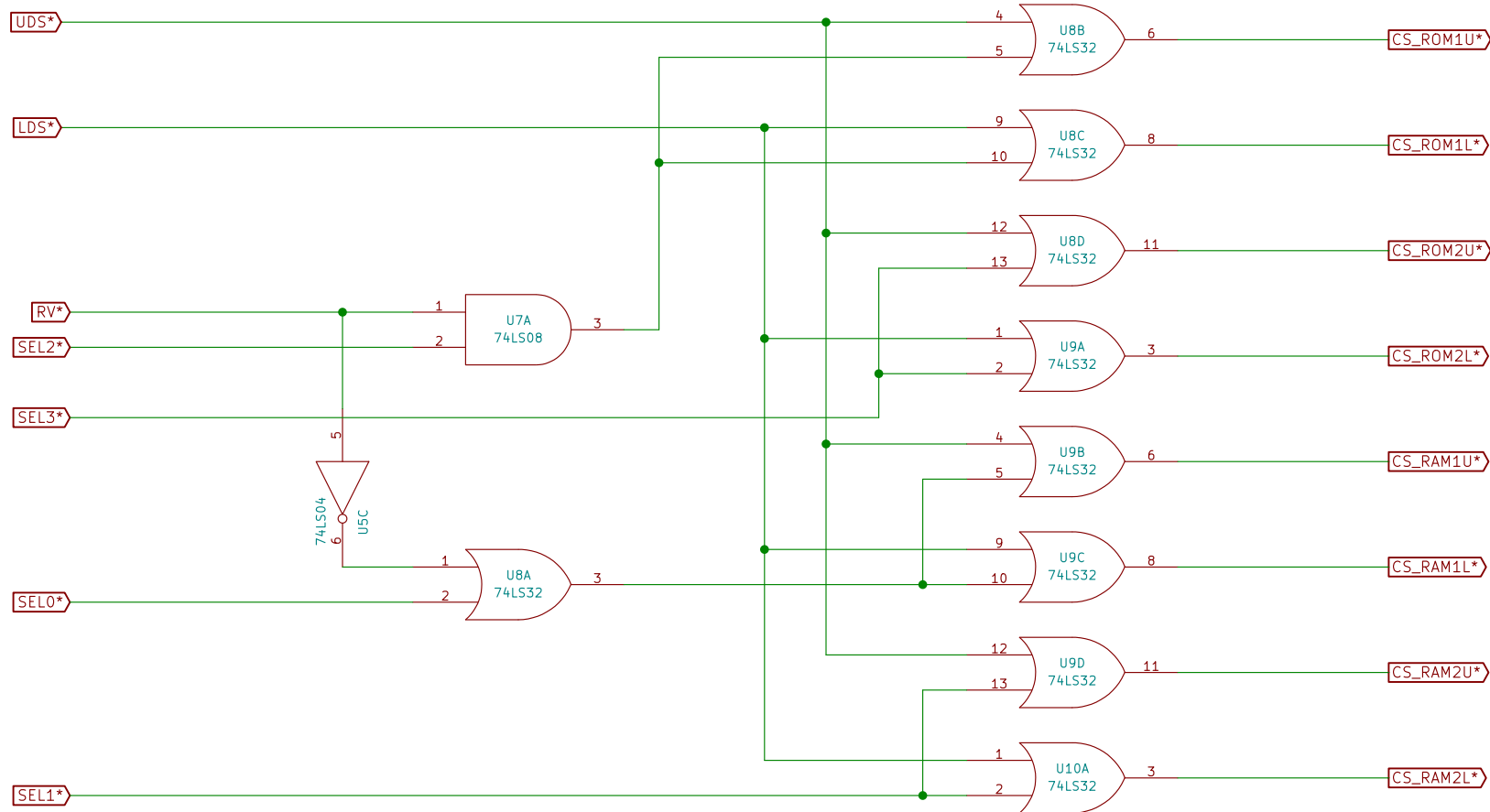
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Date: 2017-02-14

Rev: 2.1

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Id: 3/9



RAM and ROM Address Select

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Sheet: /page3/

File: page3.sch

**Title: TS2 68000 Single Board Computer**

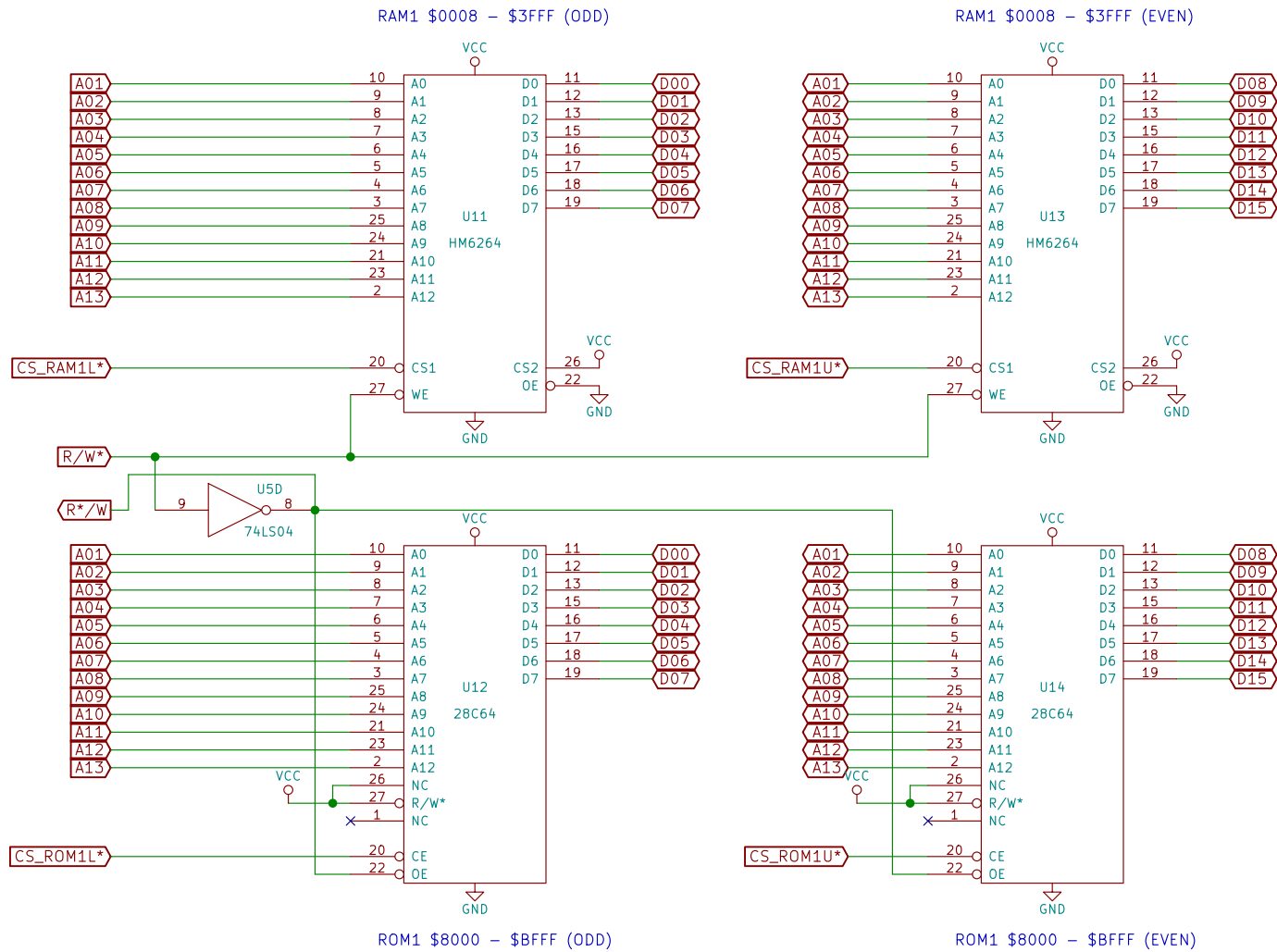
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Date: 2017-02-14

Rev: 2.1

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RAM and ROM (1 of 2)

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Sheet: /page4/

File: page4.sch

**Title: TS2 68000 Single Board Computer**

Size: A

Date: 2017-02-14

Rev: 2.1

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