68000 Single Board Computer
from
"Microprocessor Systems Design" by Alan Clements
Modified by Jeff Tranter

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Top Level Schematic
Jeff Tranter
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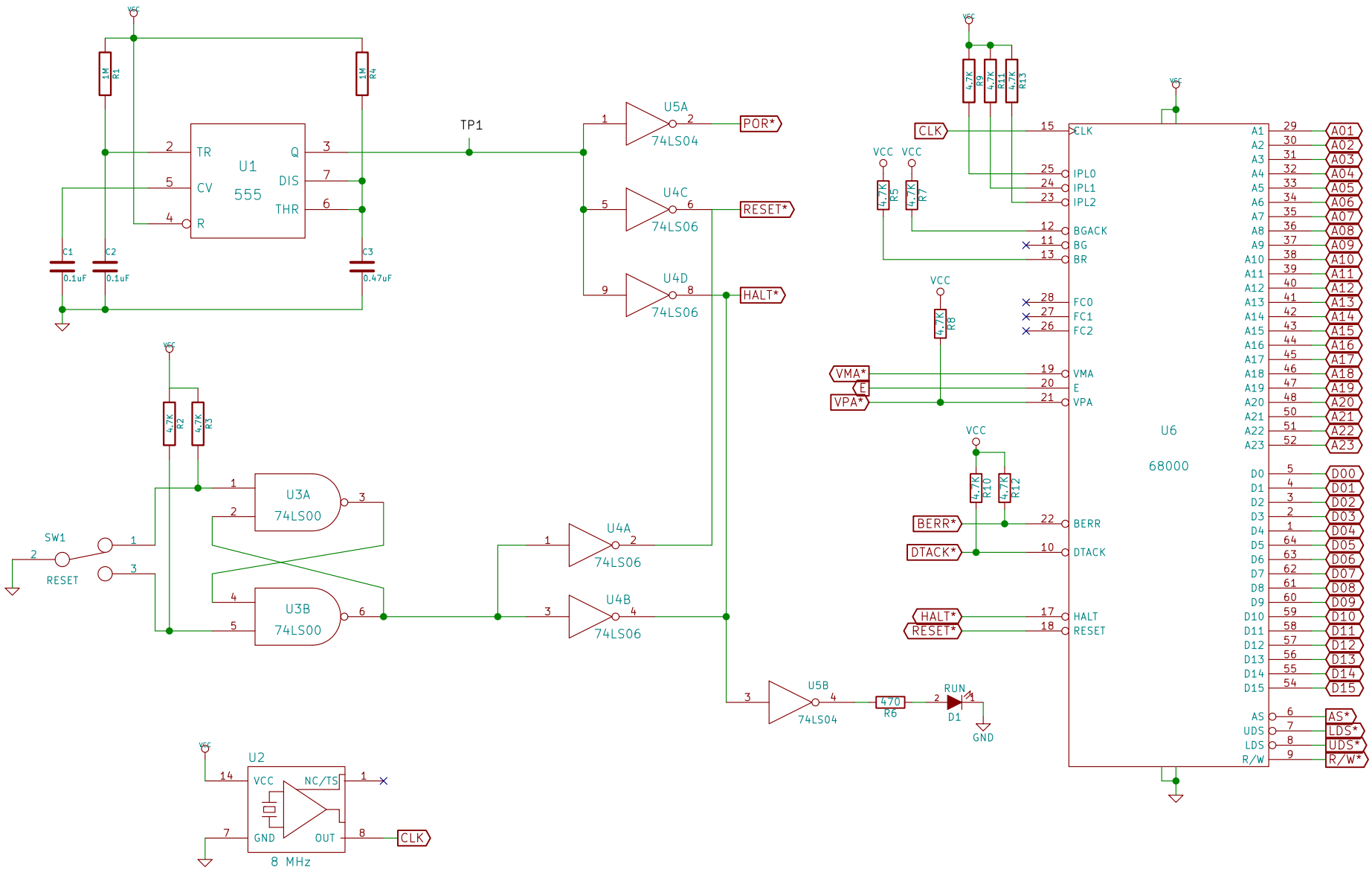
Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 1/8



Reset, Halt, and Clock Control Circuits

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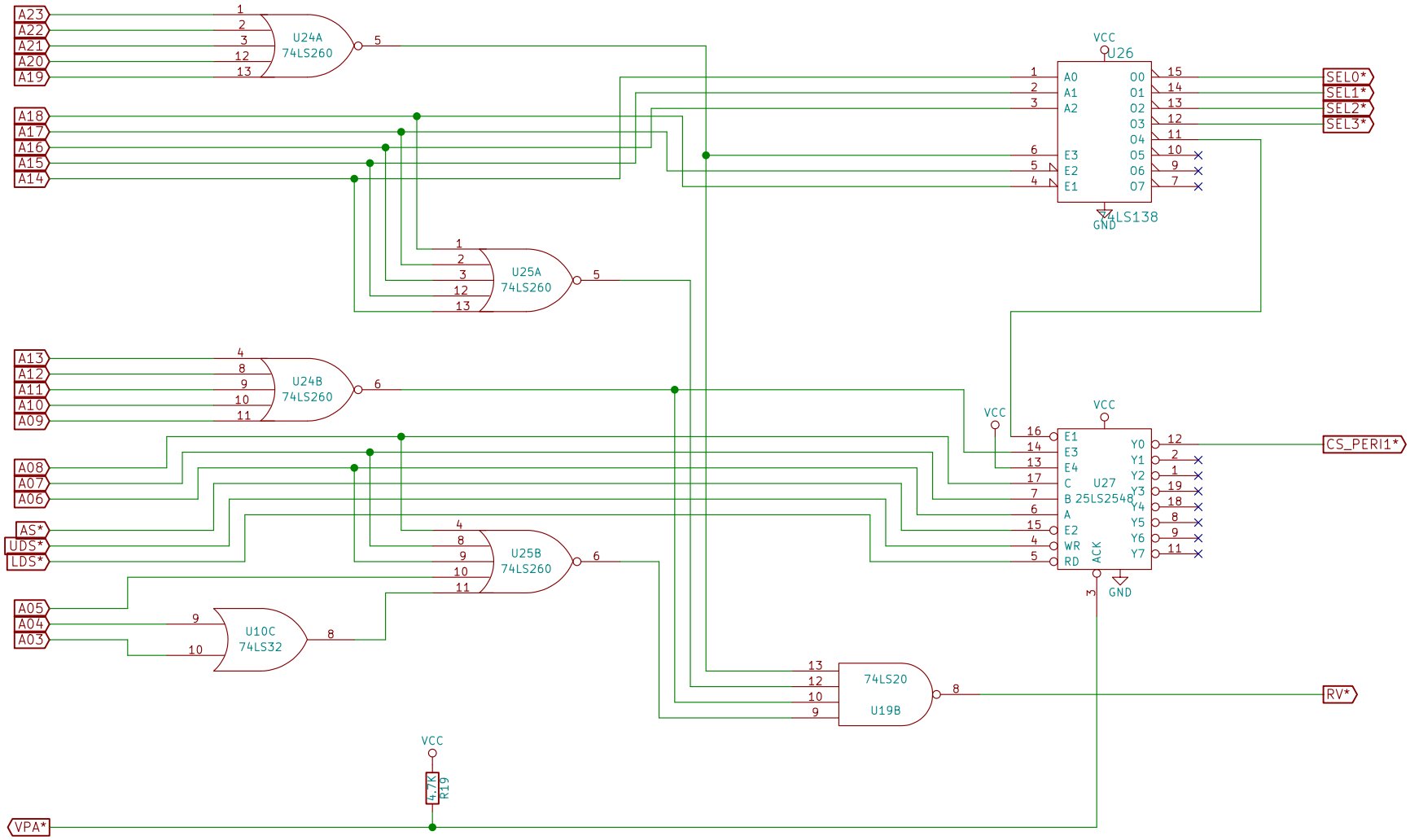
Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 2/8



Address Decoding

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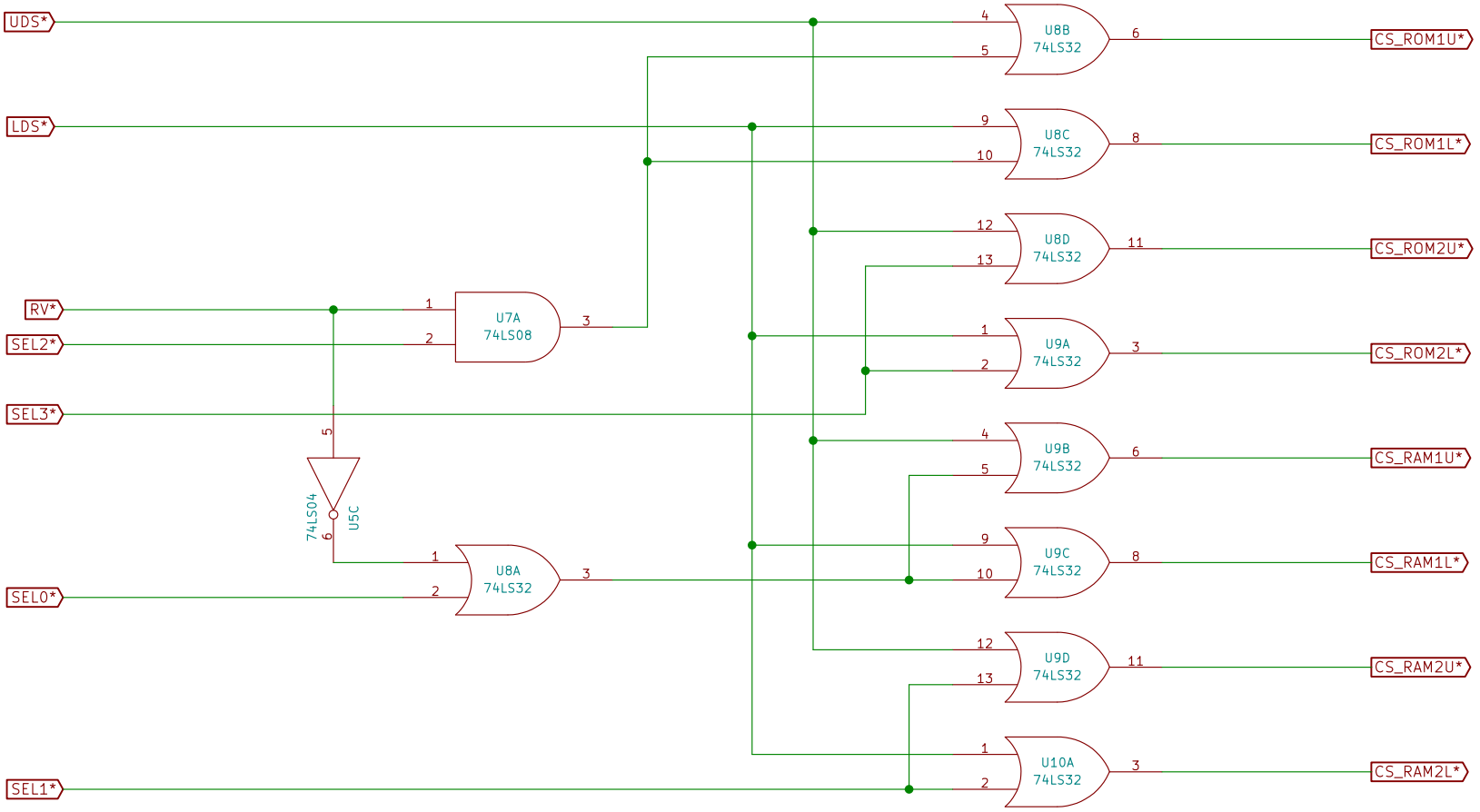
Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 3/8



RAM and ROM Address Select

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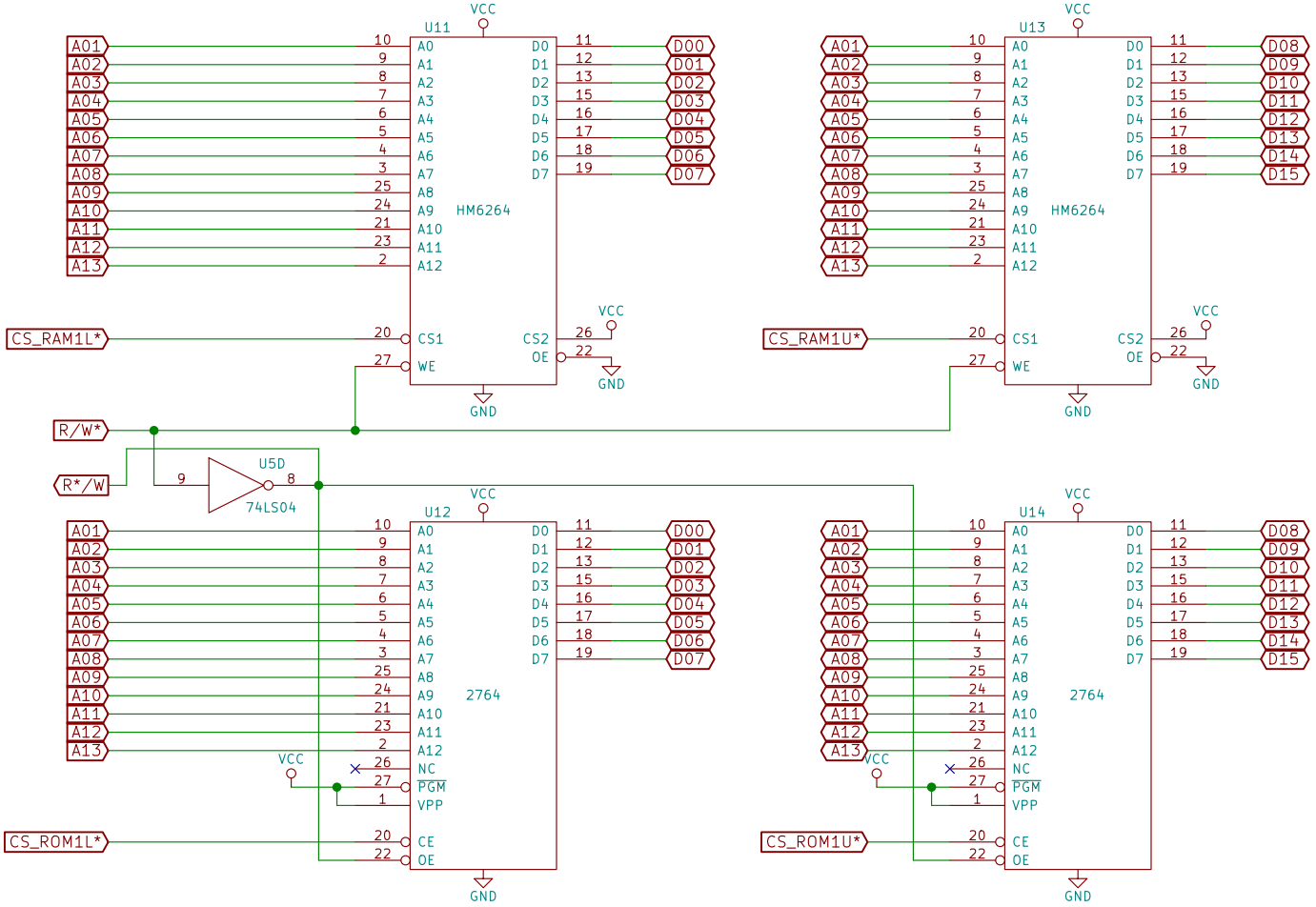
File: page3.sch

Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 4/8



RAM and ROM (1 of 2)

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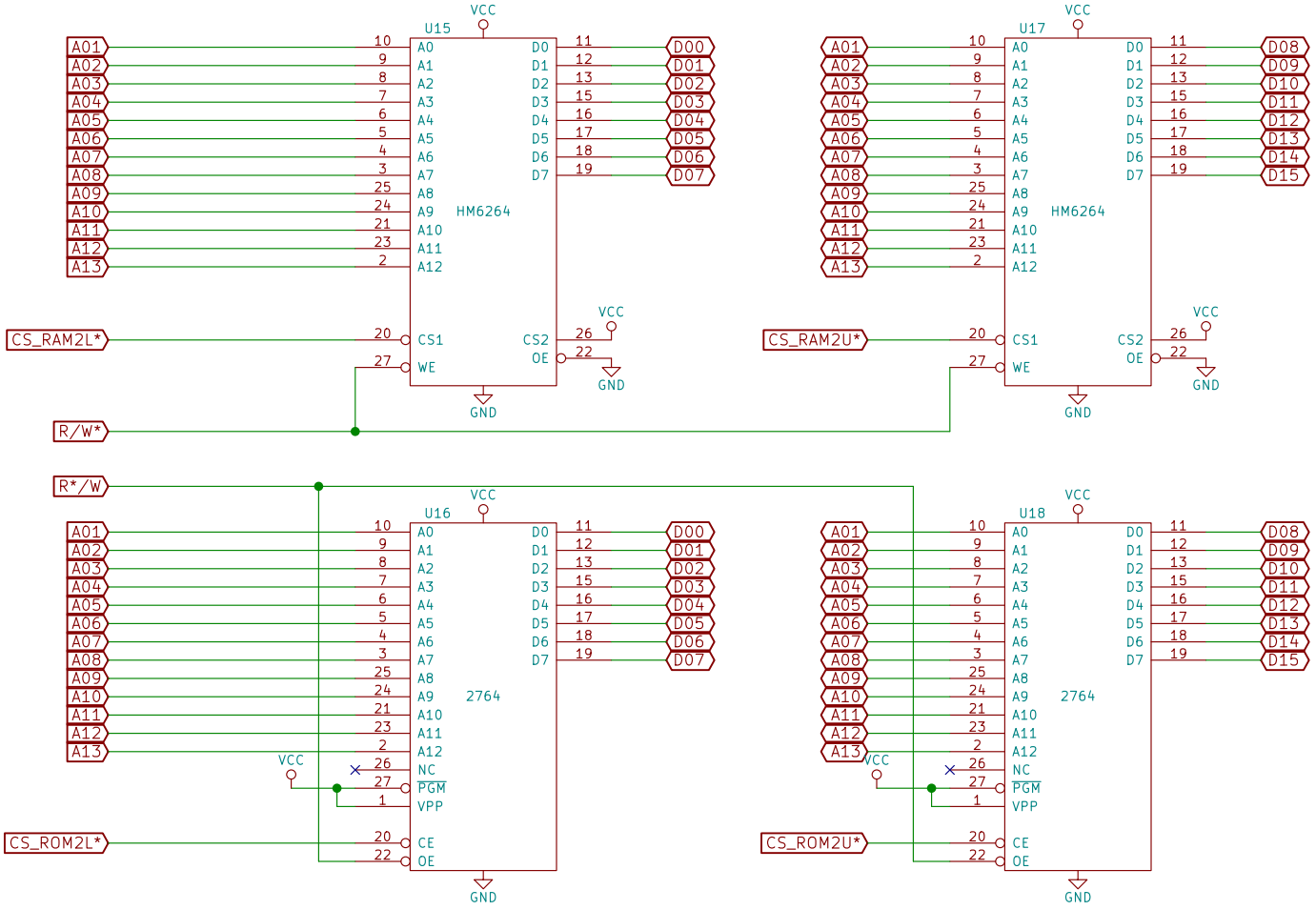
File: page4.sch

Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 5/8



RAM and ROM (2 of 2)

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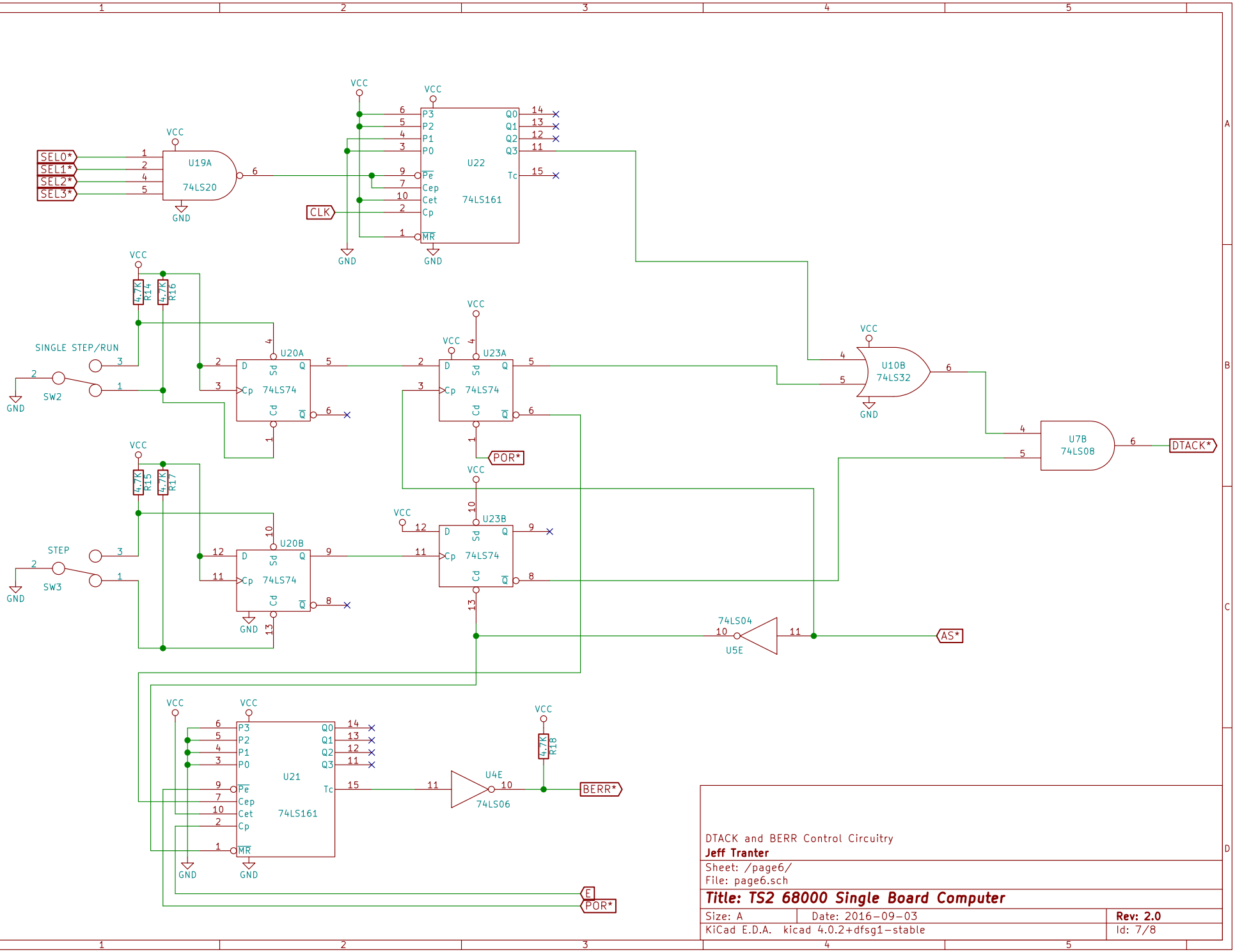
Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 6/8



DTACK and BERR Control Circuitry

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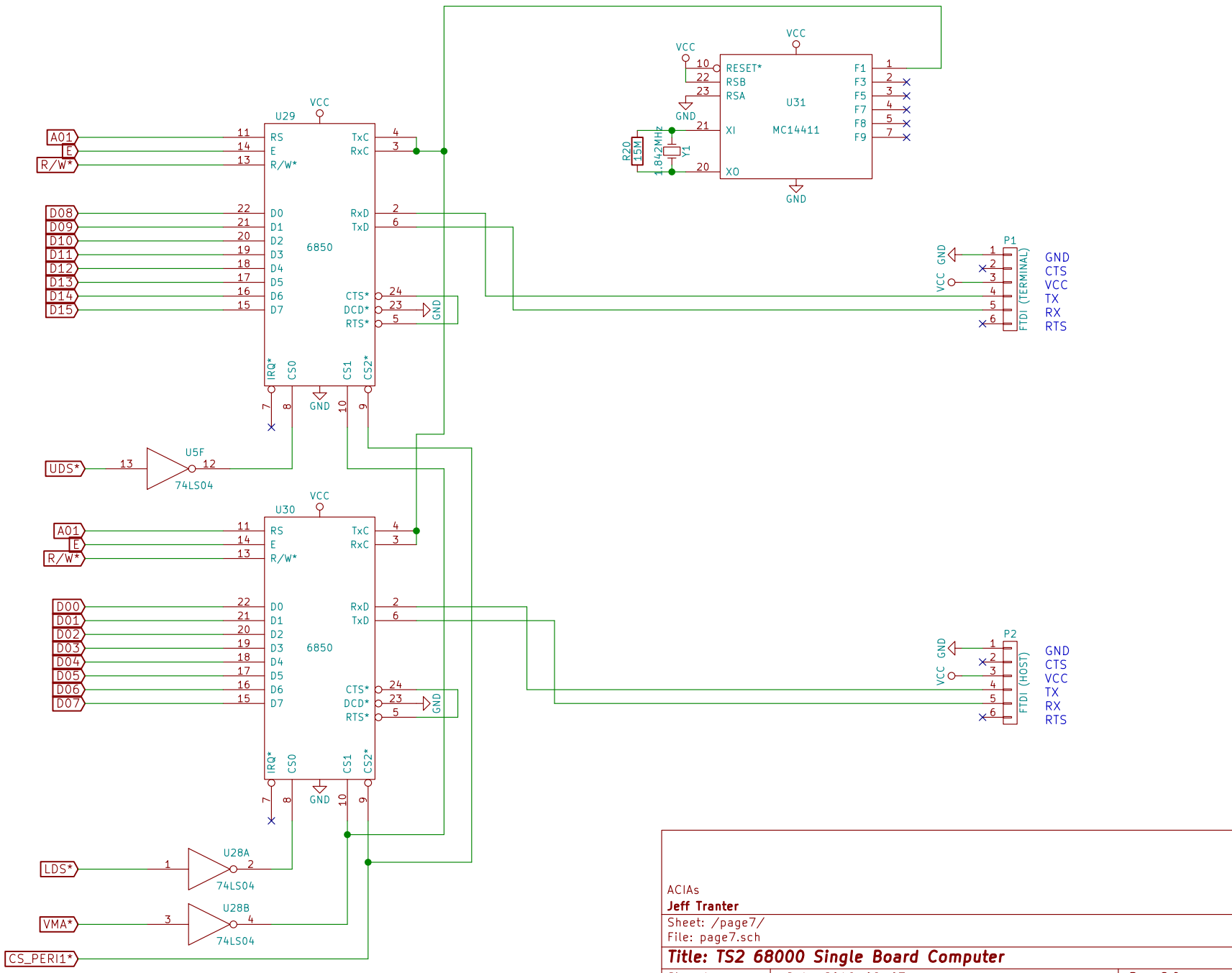
Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 7/8



ACIAs

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Title: TS2 68000 Single Board Computer

Size: A	Date: 2016-09-03	**Rev: 2.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 8/8