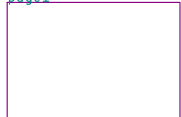


68000 Single Board Computer
from
"Microprocessor Systems Design" by Alan Clements

page1



page1.sch

page2



page2.sch

page3



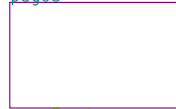
page3.sch

page4



page4.sch

page5



page5.sch

page6



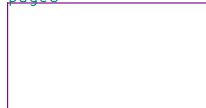
page6.sch

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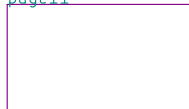
page9.sch

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page11.sch

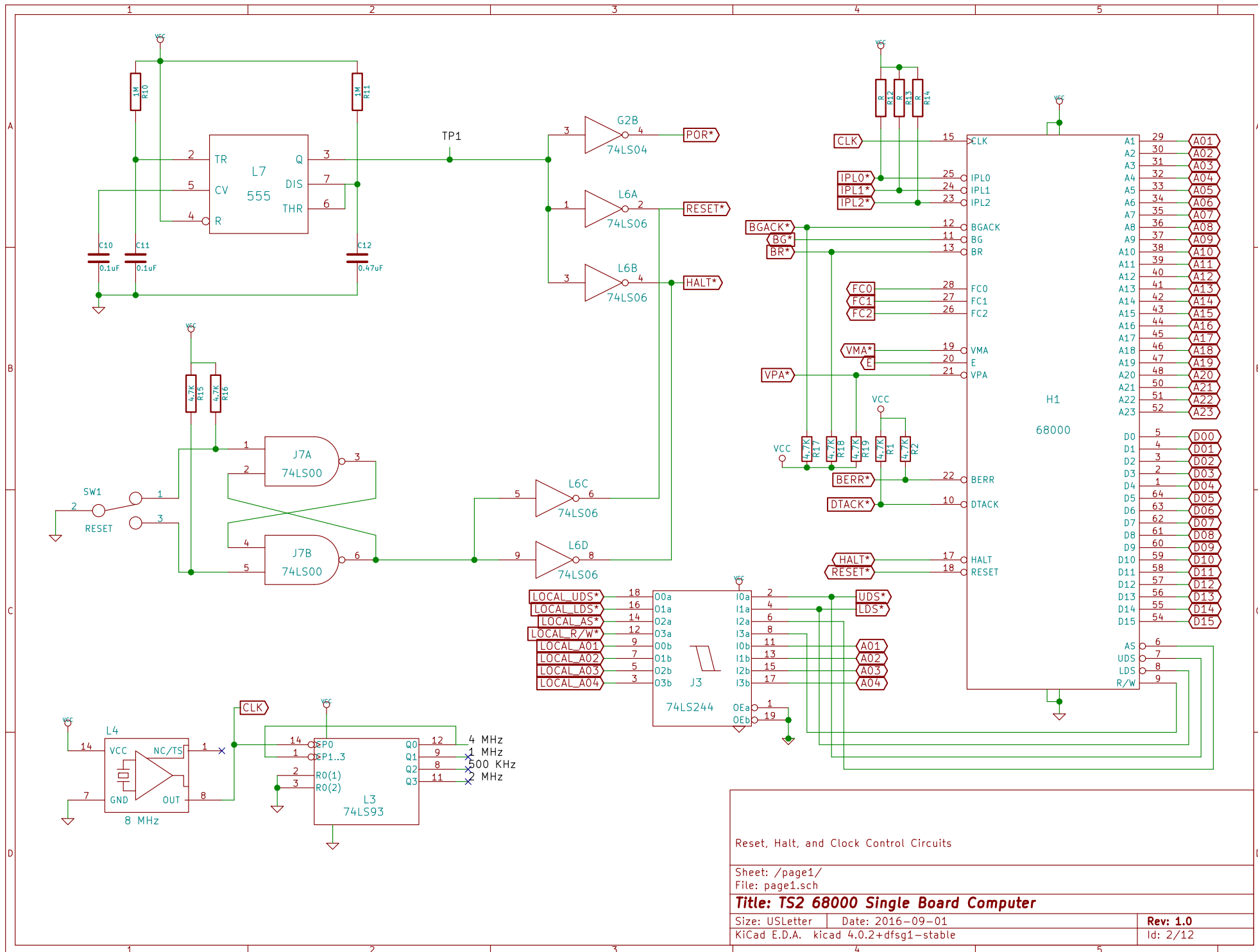
Top Level Schematic

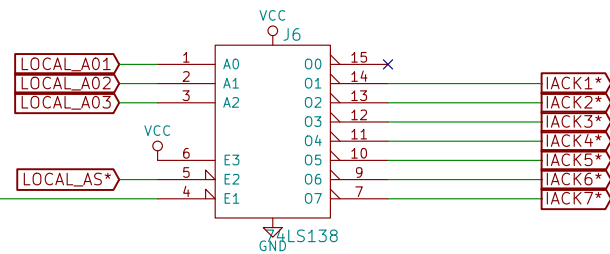
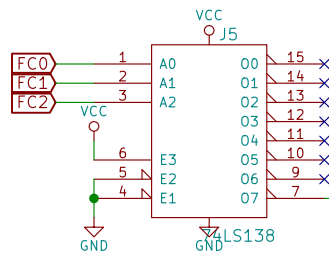
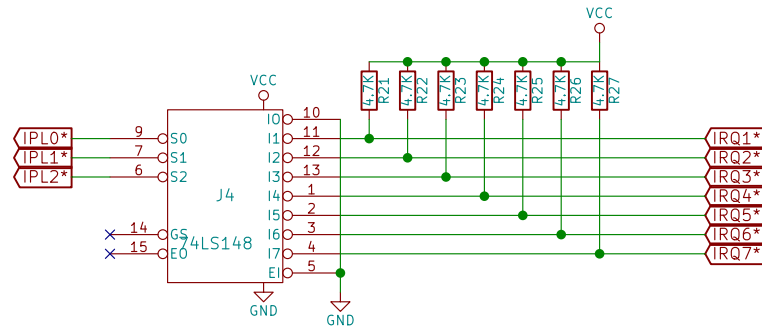
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Title: TS2 68000 Single Board Computer

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KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 1.0
Id: 1/12





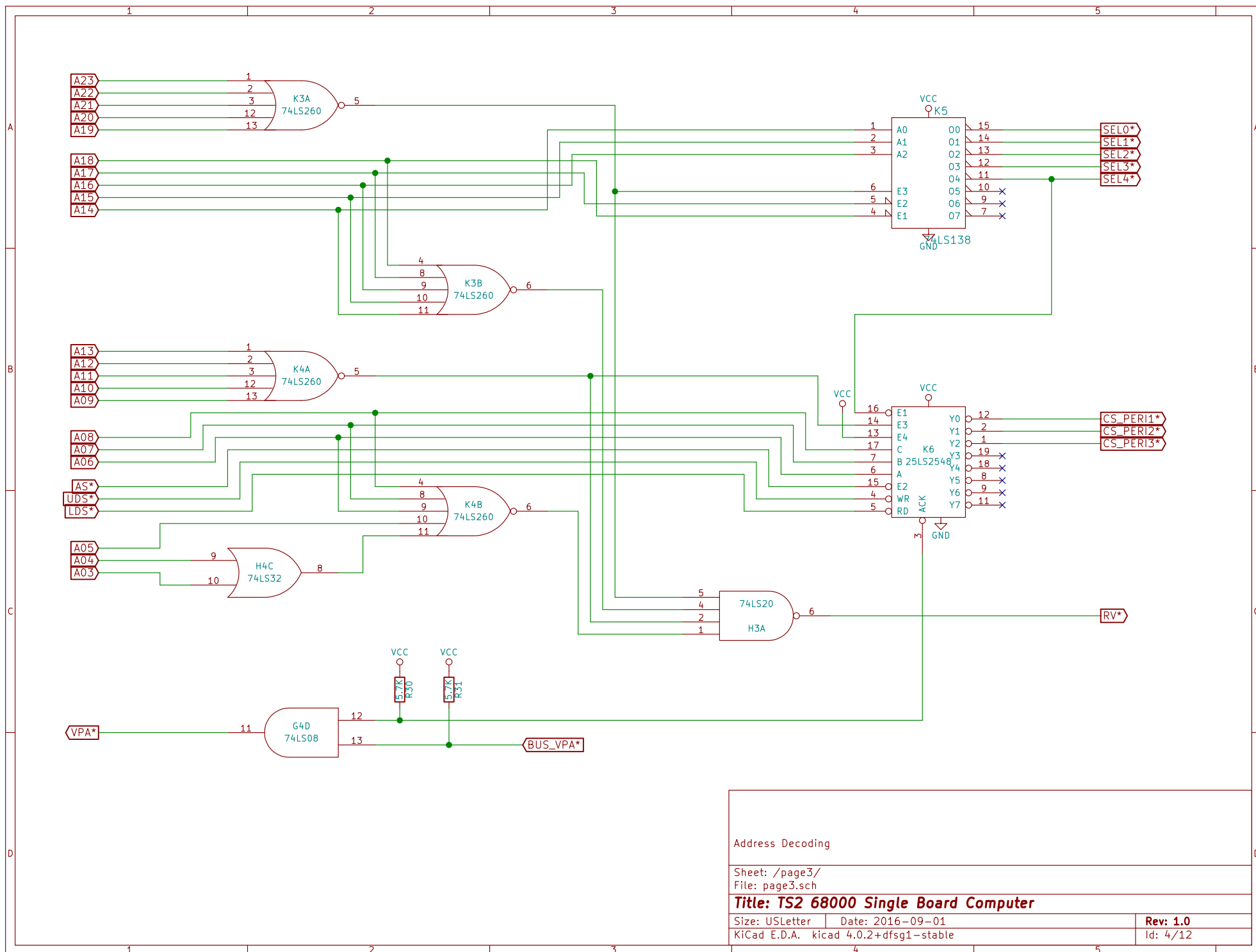
Interrupt Control Circuitry

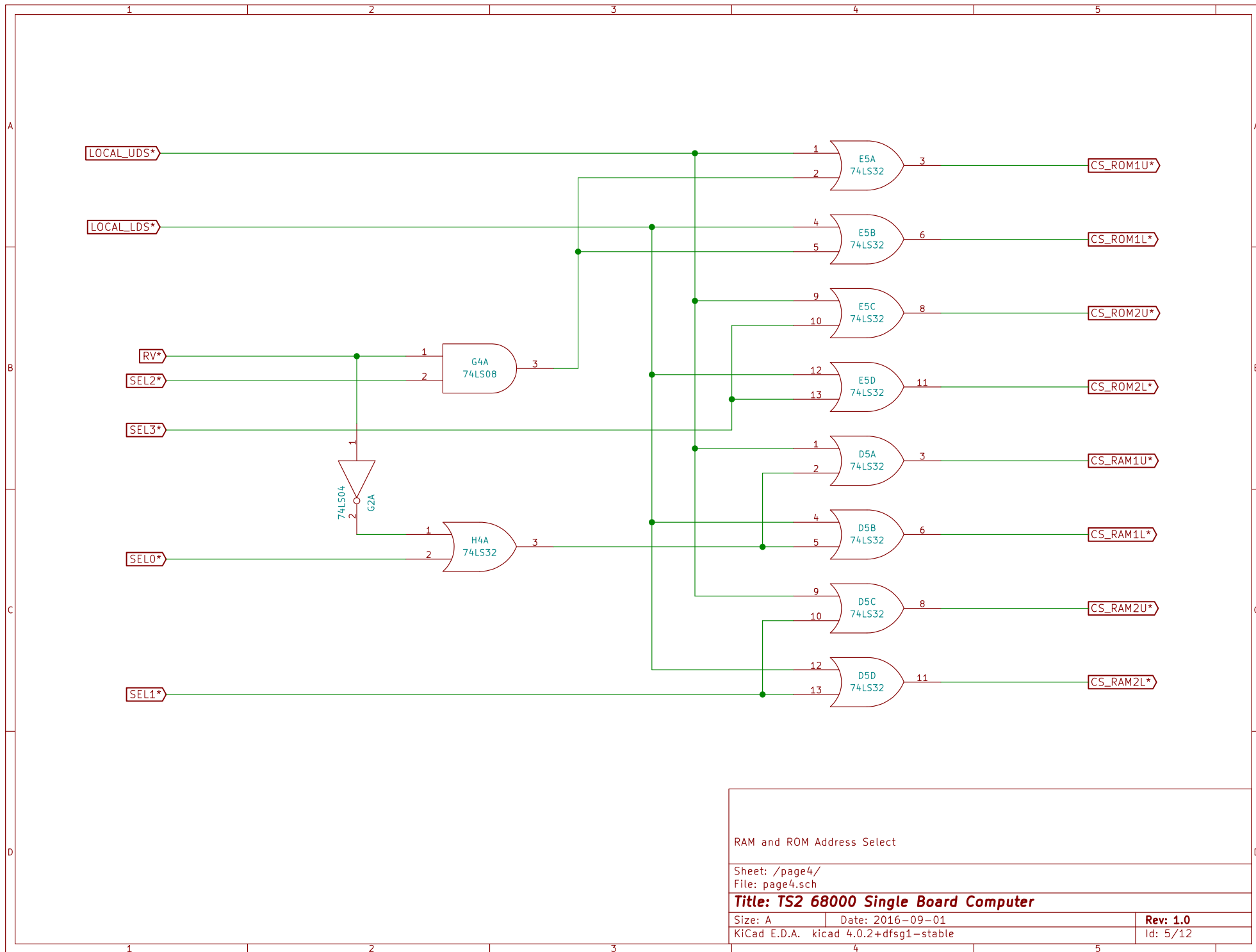
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Title: TS2 68000 Single Board Computer

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Rev: 1.0
Id: 3/12





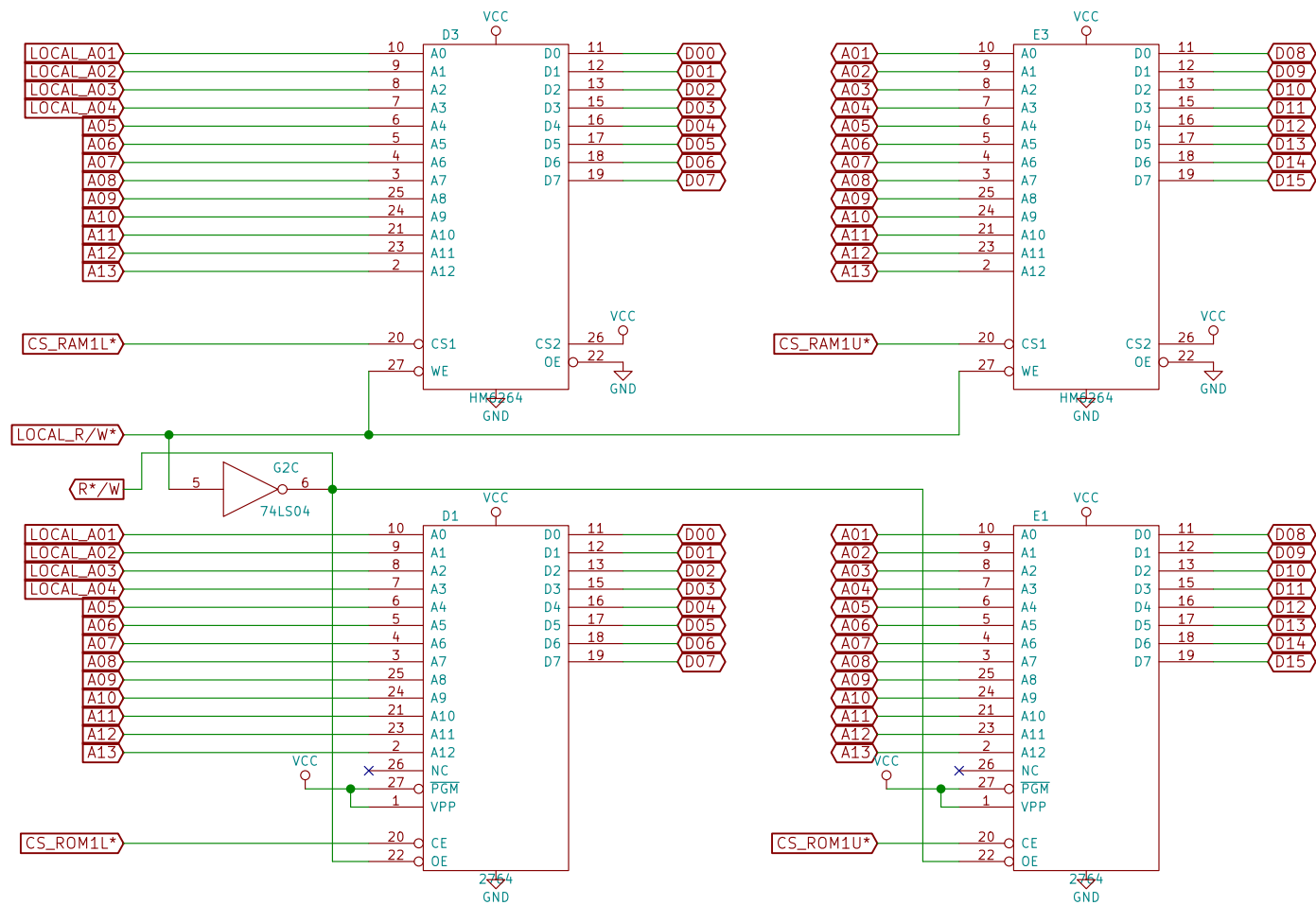
RAM and ROM Address Select

Sheet: /page4/
File: page4.sch

Title: TS2 68000 Single Board Computer

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Rev: 1.0
Id: 5/12



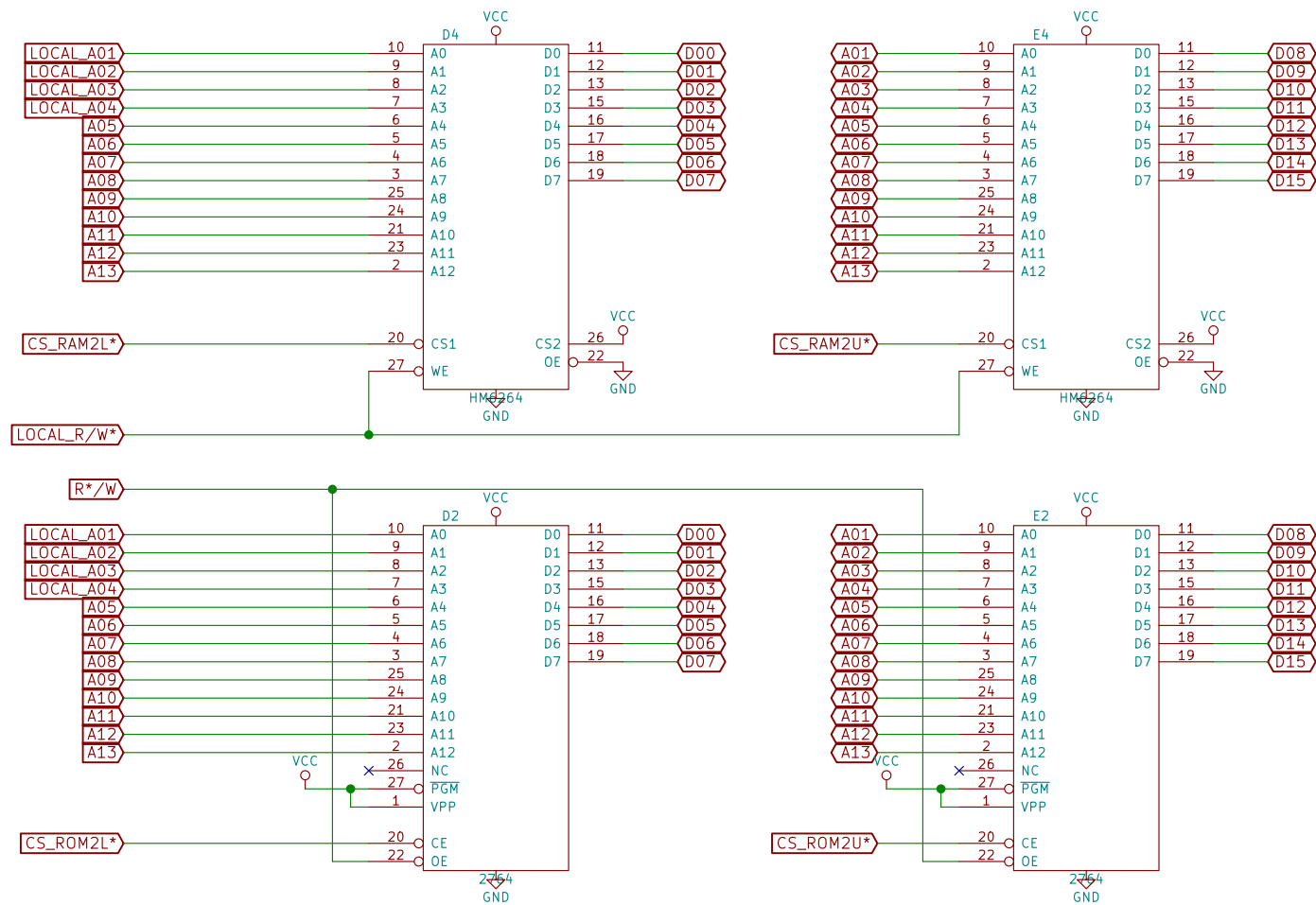
RAM and ROM (1 of 2)

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File: page5.sch

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Rev: 1.0
Id: 6/12



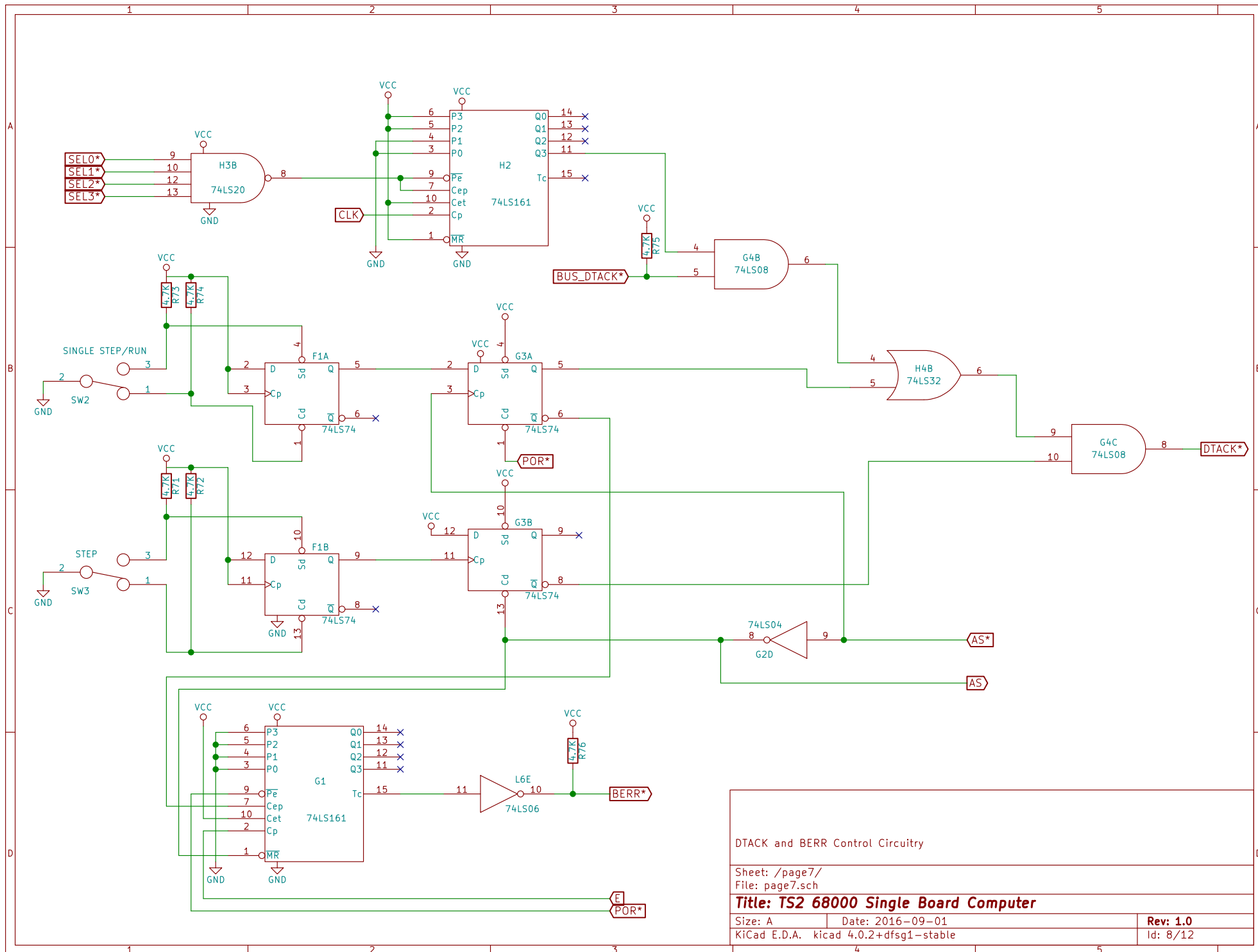
RAM and ROM (2 of 2)

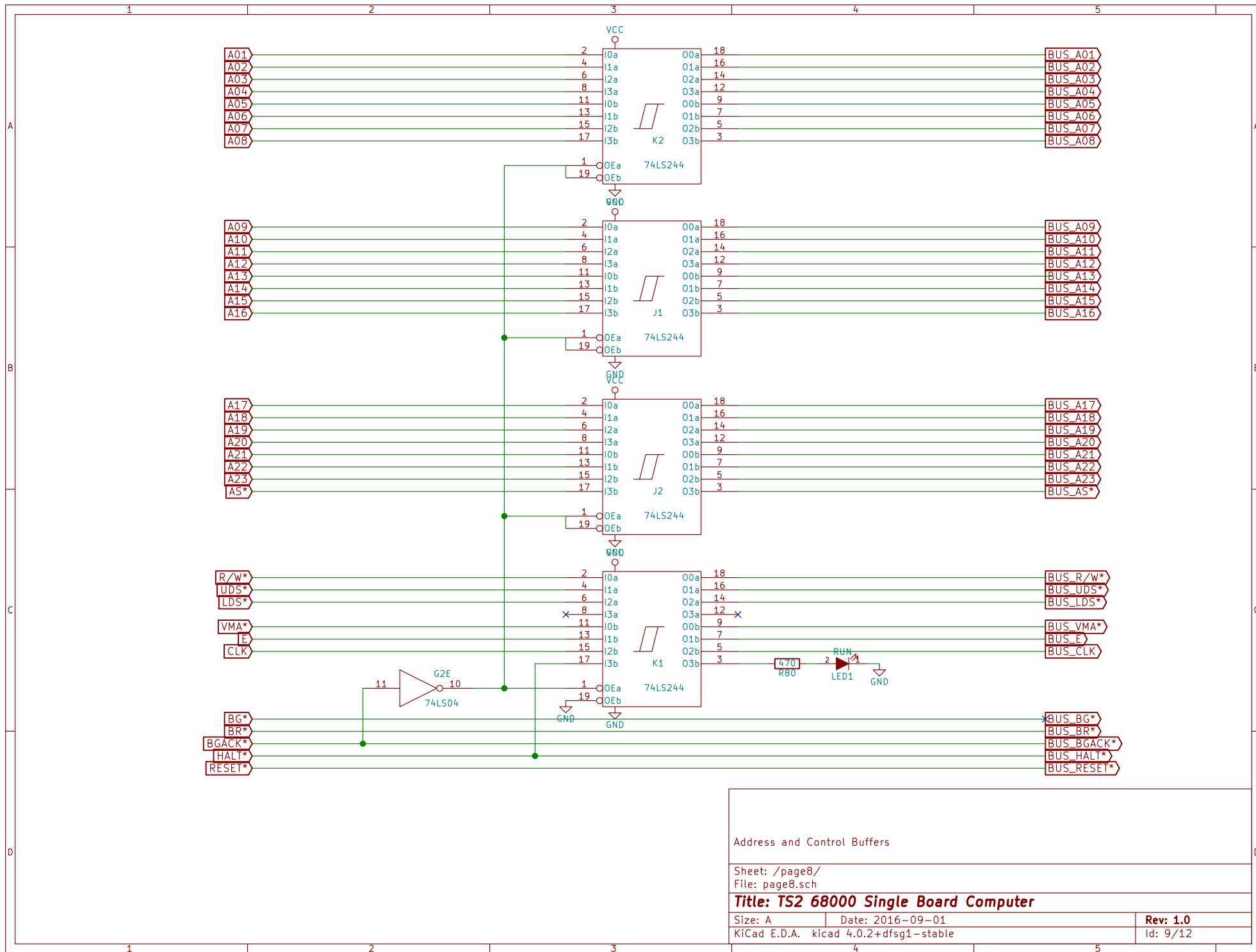
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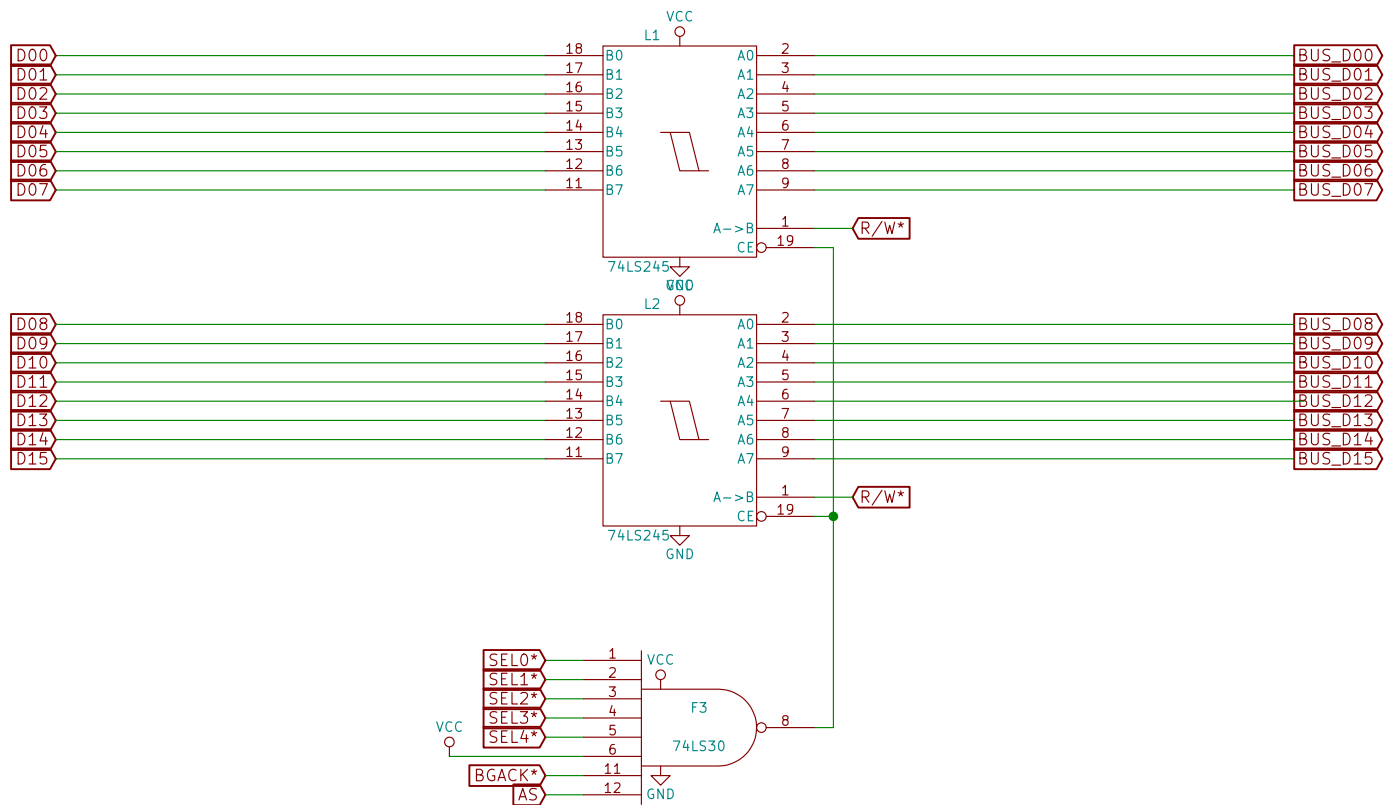
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Rev: 1.0
Id: 7/12







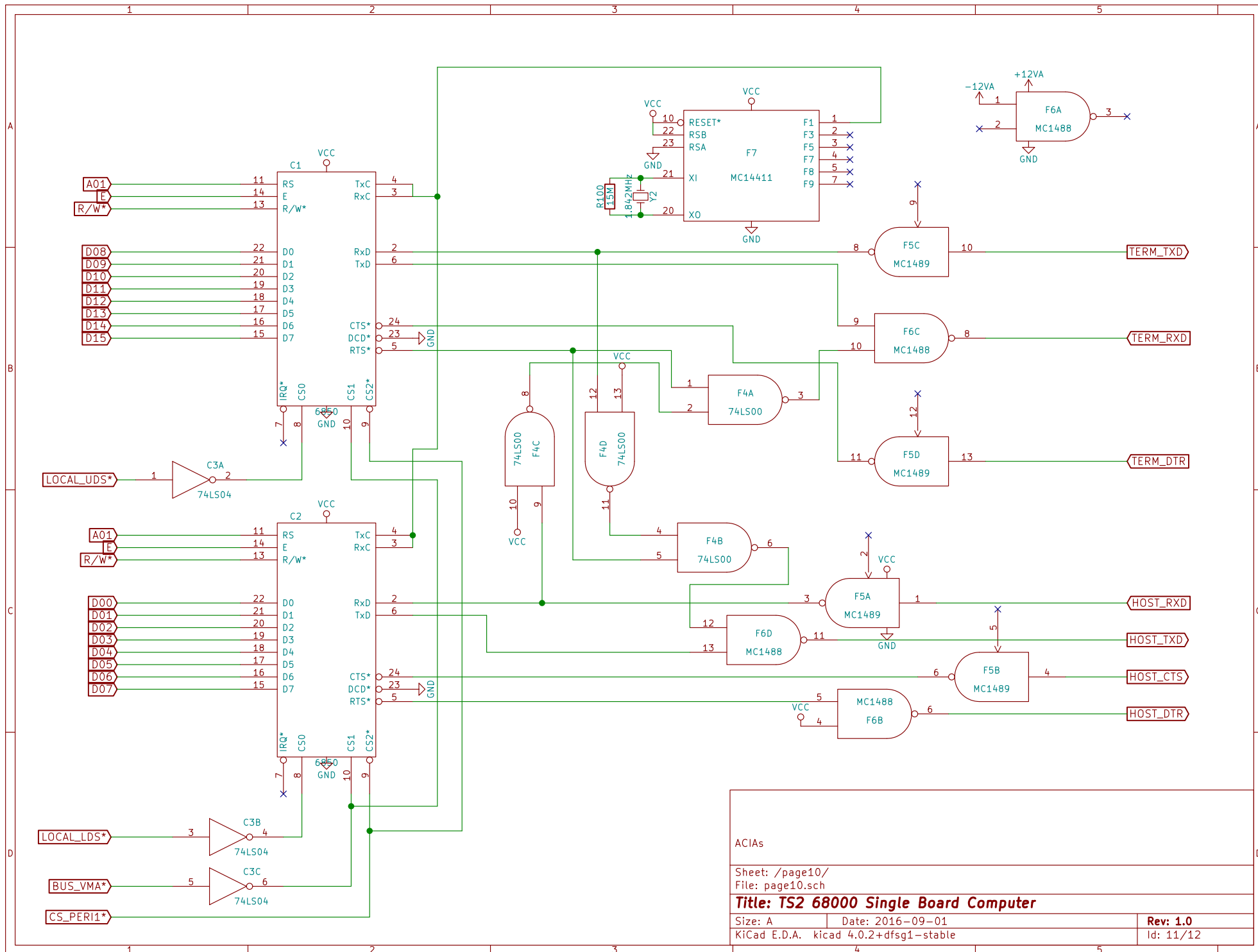
Data and Control Buffers

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File: page9.sch

Title: TS2 68000 Single Board Computer

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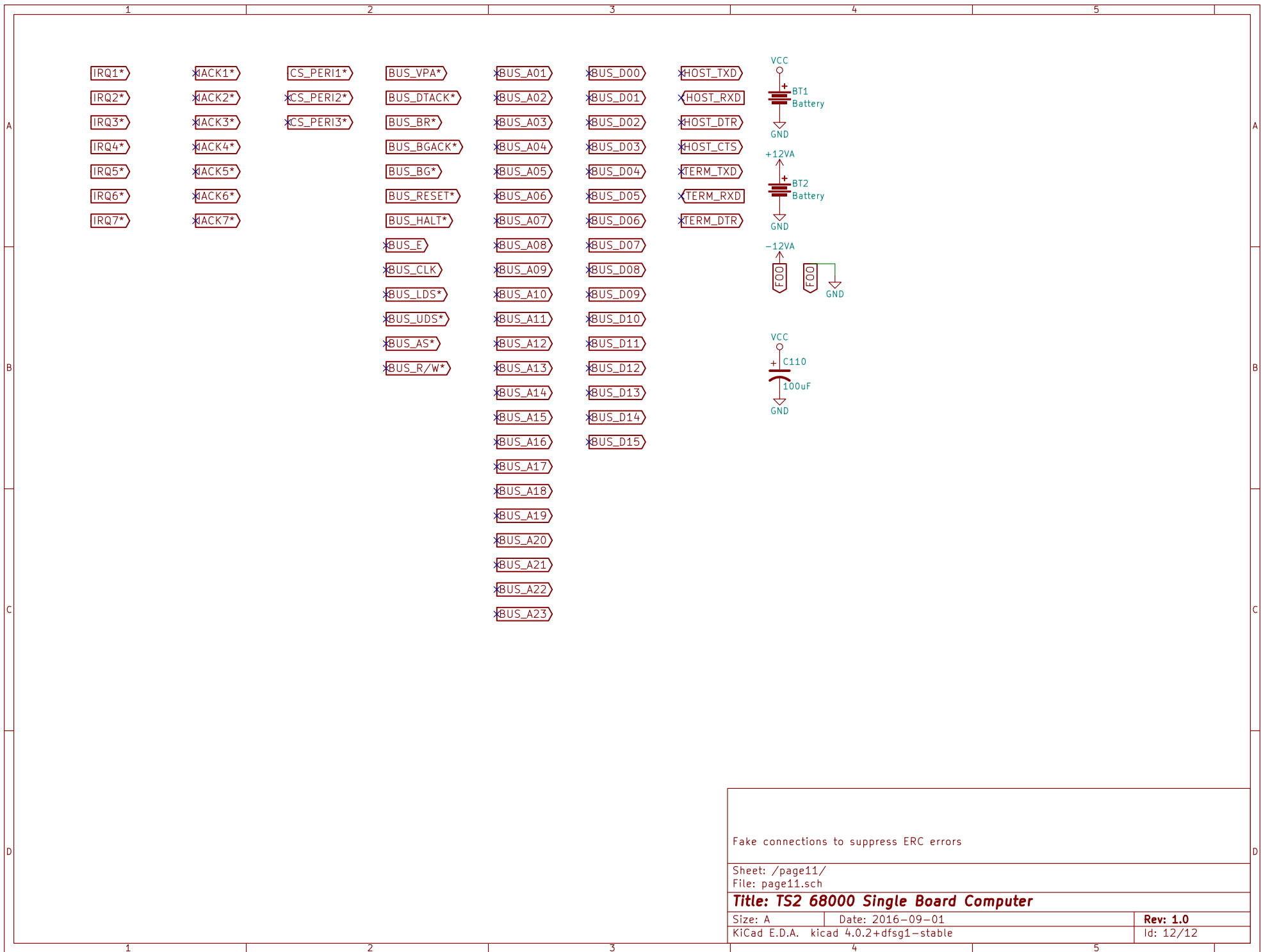
ACIAs

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KiCad E.D.A. kicad 4.0.2+dfsg1-stable

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Fake connections to suppress ERC errors

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File: page11.sch

Title: TS2 68000 Single Board Computer

Size: A Date: 2016-09-01
KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 1.0
Id: 12/12