

2. Configuration Summary

The table below compares the device series of feature and pin compatible devices, providing a seamless migration path.

Table 2-1. Configuration Summary and Device Comparison

Features	ATmega164A	ATmega324A	ATmega644A	ATmega1284
Pin Count	40/44/49	40/44/49	40/44	40/44
Flash (Bytes)	16K	32K	64K	128K
SRAM (Bytes)	1K	2K	4K	16K
EEPROM (Bytes)	512	1K	2K	4K
General Purpose I/O Lines	32	32	32	32
SPI	1	1	1	1
TWI (I ² C)	1	1	1	1
USART	2	2	2	2
ADC	10-bit 15ksps	10-bit 15ksps	10-bit 15ksps	10-bit 15ksps
ADC Channels	8	8	8	8
Analog Comparator	1	1	1	1
8-bit Timer/Counters	2	2	2	2
16-bit Timer/Counters	1	1	1	2
PWM channels	6	6	6	8
Packages	PDIP TQFP VQFN/QFN DRQFN VFBGA	PDIP TQFP VQFN/QFN DRQFN VFBGA	PDIP TQFP VQFN/QFN	PDIP TQFP VQFNQFN