

1. Describe the motives of the semiconductor industry to shift to a horizontal business model and how the horizontal business model introduces the risk of hardware Trojan insertion.
2. Who are the potential adversaries to implant a hardware Trojan? Provide a brief description on each of them. In your opinion, which one is most difficult to defend?
3. Provide a brief description of a generic Trojan structure.
4. Describe the difference between a combinational and a sequential Trojan.
5. Provide an example of hardware Trojan in a cryptomodule.
6. Give a comparison of hardware Trojans in ASIC and FPGA designs.
7. Illustrate the hardware trojan taxonomy and classify them based on activation mode and payload.
8. Illustrate the taxonomy of Trojan countermeasures.