LogicAnalyzer Chan	HydraNFC Pin	TRF7970A Pin
0	PC3	IO7_MOSI
1	PC2	IO6_MISO
2	PB10	DATA_CLK
3	PC1	IO4_CS
4	PA7	MOD
5	PA5	SYS_CLK (Out)
6	PA1	IRQ
7	PC4	IO5
8	PC5	IO3
9	PB1	ASK_OOK
10	PB11	EN (Chip En)
11	PC0	IO2
-	PA2	IO1
-	PA3	IO0

HydraBus Mode SPI With SS
SPI2_MOSI Master out
SPI2_MISO Master or RX DM1 in
SPI2_CLK Master out
SPI2_CS Master out
SPI1_MOSI Slave (sniffer) in
SPI1_CLK Slave (sniffer) in
IRQ Input in
TX SDM or RX DM1 CLK in
TX SDM out
TX SDM or Direct Mode 0 out
+3V3 out
+3V3 or TX SDM (Data En) out
+3V3 out
GND out