

## Lab 1 – writing a user space device driver

In this lab you will write a device driver to access a custom hardware (implemented on FPGA)

The device implement a cyclic counter from start value to end value

Device physical address : 0x101e8000

Device registers

Offset address(hex)	Name
0	Data
4	Match
8	Load
C	Control
10	Interrupt Mask
14	Int status
18	Masked status
1C	Int clear
20	Start value
24	Stop value

Your task

write a set of functions for the following operations

1. set start and stop value
2. read the current counter
3. main function to test all the above