

## Lab – Writing a simple Interrupt handler

The RTC is located in physical address 0x1c170000 with the following registers:

Address	Reg
0x1c170000	Data read register
0x1c170004	Match register
0x1c170008	Data load register
0x1c17000c	Control register
0x1c170010	Interrupt mask and set register
0x1c170014	Raw interrupt status register
0x1c170018	Masked interrupt status register
0x1c17001c	Interrupt clear register

- Configure the kernel to remove the RTC support (ARM AMBA PL031)
- Write a simple module with init/exit
- Register to the RTC interrupt – Interrupt number is 49
- On the interrupt handler write a message to the kernel log
- Set the RTC Match register to raise interrupt 5 seconds after module loading (read the RTC value, add 5 and write to Match register)
- Test your work