Instruction	Function (0-3 bits)	Opcode (0-4 bits)	Operation	Arithmetic Control	Instruction	Function (0-3 bits	Opcode (0-4 bits)	ALU unit	ALU Contro
ADD	0100	00000	Adder	000	ADD	0100	00000	Artithmetic	00
SUB	0101	00000	Subtractor	001	SUB	0101	00000	Artithmetic	00
SLT	0110	00000	SLT	010	SLT	0110	00000	Artithmetic	00
SLTU	0111	00000	SLTU	011	SLTU	0111	00000	Artithmetic	00
SEQ	1000	00000	SEQ	100	SEQ	1000	00000	Artithmetic	00
MUL	1101	00000	Multiplier	101	MUL	1101	00000	Artithmetic	00
ADDI	-	00101	Adder	000	ADDI	-	00101	Artithmetic	00
SLTI	-	00110	SLT	010	SLTI	-	00110	Artithmetic	00
SLTIU	-	00111	SLTU	011	SLTIU	-	00111	Artithmetic	00
SEQI	-	01000	SEQ	100	SEQI	-	01000	Artithmetic	00
JALR	-	01111	Adder	000	JALR	-	01111	Artithmetic	00
LW	-	10000	Adder	000	LW	-	10000	Artithmetic	00
SW	-	10001	Adder	000	SW	-	10001	Artithmetic	00
					XOR	1001	0000	Logic	01
Instruction	Function (0-3 bits)0	Opcode (0-4 bits)	Operation	Logic Control	OR	1010	0000	Logic	01
XOR	1001	00000	XOR	00	AND	1011	0000	Logic	01
OR	1010	00000	OR	01	NOR	1100	0000	Logic	01
AND	1011	00000	AND	10	XORI	-	1001	Logic	01
NOR	1100	00000	NOR	11	ORI	-	1010	Logic	01
XORI	-	01001	XOR	00	ANDI	-	1011	Logic	01
ORI	-	01010	OR	01	NORI	-	1100	Logic	01
ANDI	-	01011	AND	10	SLL	00	000	Shift	10
NORI	-	01100	NOR	11	SRL	01	000	Shift	10
					SRA	10	000	Shift	10
Instruction	Function (0-3 bits)0	Opcode (0-4 bits)	Operation	Shift Control	ROR	11	000	Shift	10
SLL	0000	00000	SLL	00	SLLI	-	001	Shift	10
SRL	0001	00000	SRL	01	SRLI	-	010	Shift	10
SRA	0010	00000	SRA	10	SRAI	-	011	Shift	10
ROR	0011	00000	ROR	11	RORI	-	100	Shift	10
SLLI	-	00001	SLL	00	SET	-	01101	SET	11
SRLI	-	00010	SRL	01	SSET	-	01110	SET	11
SRAI	-	00011	SRA	10					
RORI	-	00100	ROR	11					
Instruction	Opcode (0-4 bits)	Operation	SET Control						
SET	01101	SET	0						
SSET	01110	SSET	1						

Instruction	Opcode (0-4 bits)	Operation	Branch Control				
BEQ	10010	BEQ	001				
BNE	10011	BNE	010				
BLT	10100	BLT	011				
BGE	10101	BGE	100				
BLTU	10110	BLTU	101				
BGEU	10111	BGEU	110				