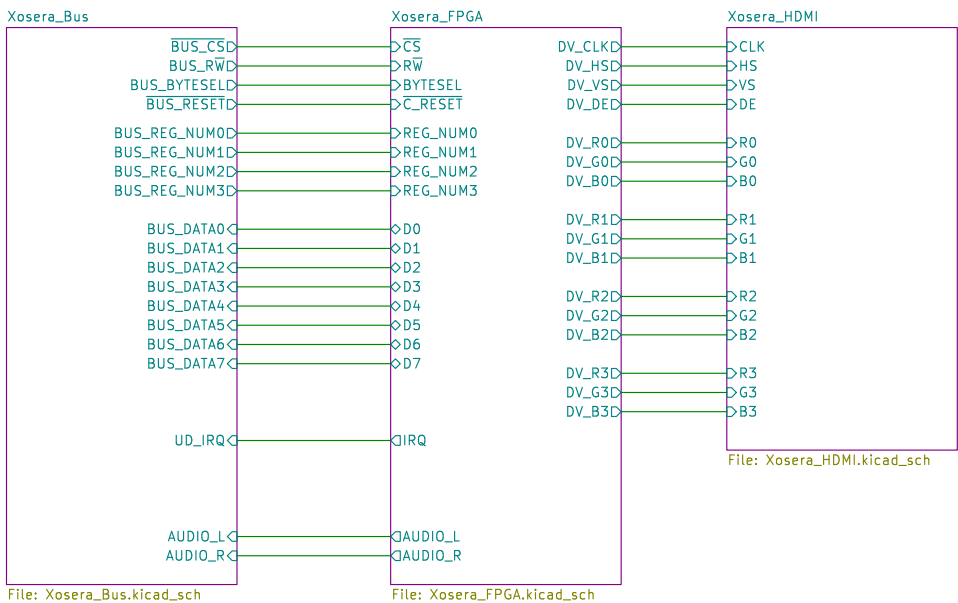


Xosera FPGA Retro Video Card

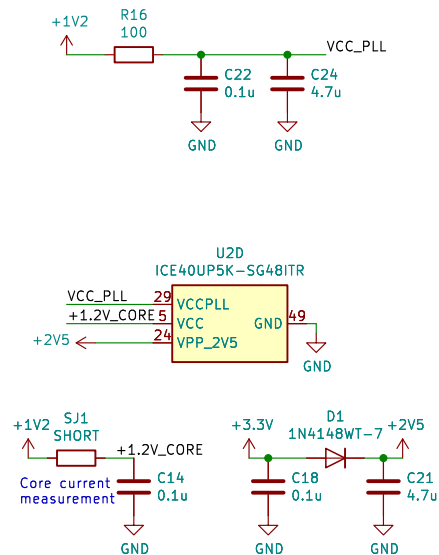
Architecture & Gateware by Xark
Electronics & m68k Interface by roscopeco
<https://github.com/XarkLabs/Xosera>

FPGA interface derived from the awesome UPduino 3.0
Portions Copyright (c) 2019 tinyvision-ai-inc
<https://github.com/tinyvision-ai-inc/UPduino-v3.0>

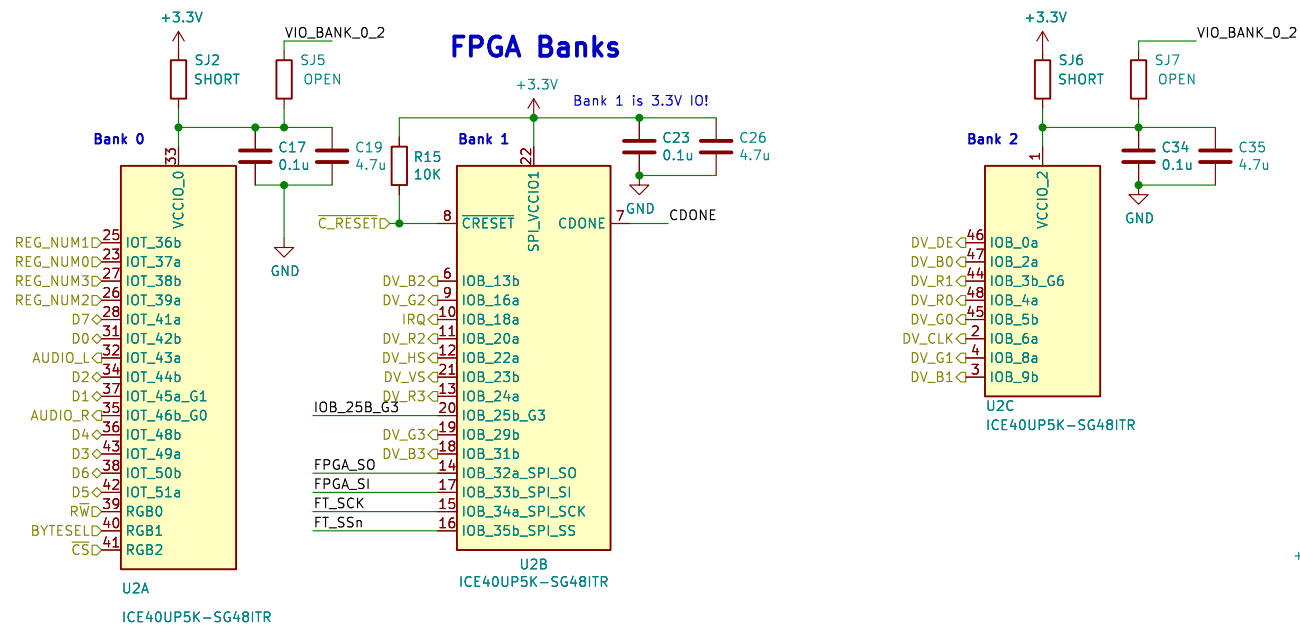
HDMI Output derived from iCEBreaker 12-bit DVI PMOD
Portions Copyright iCEBreaker FPGA Project
<https://github.com/icebreaker-fpga/icebreaker>



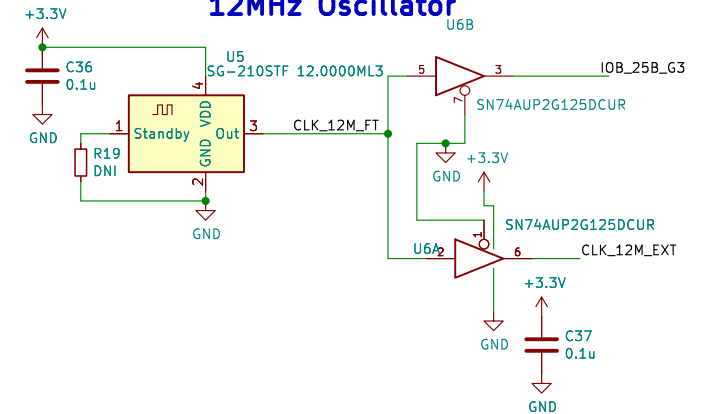
FPGA Power



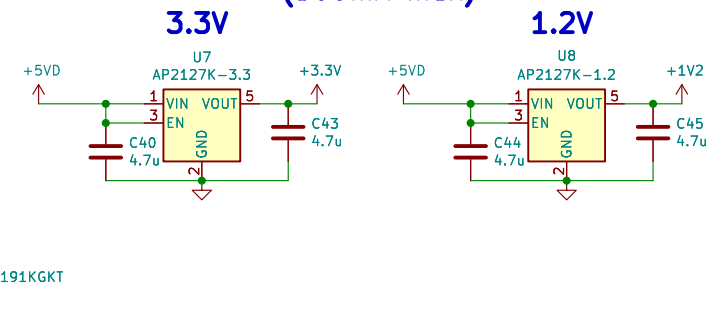
FPGA Banks



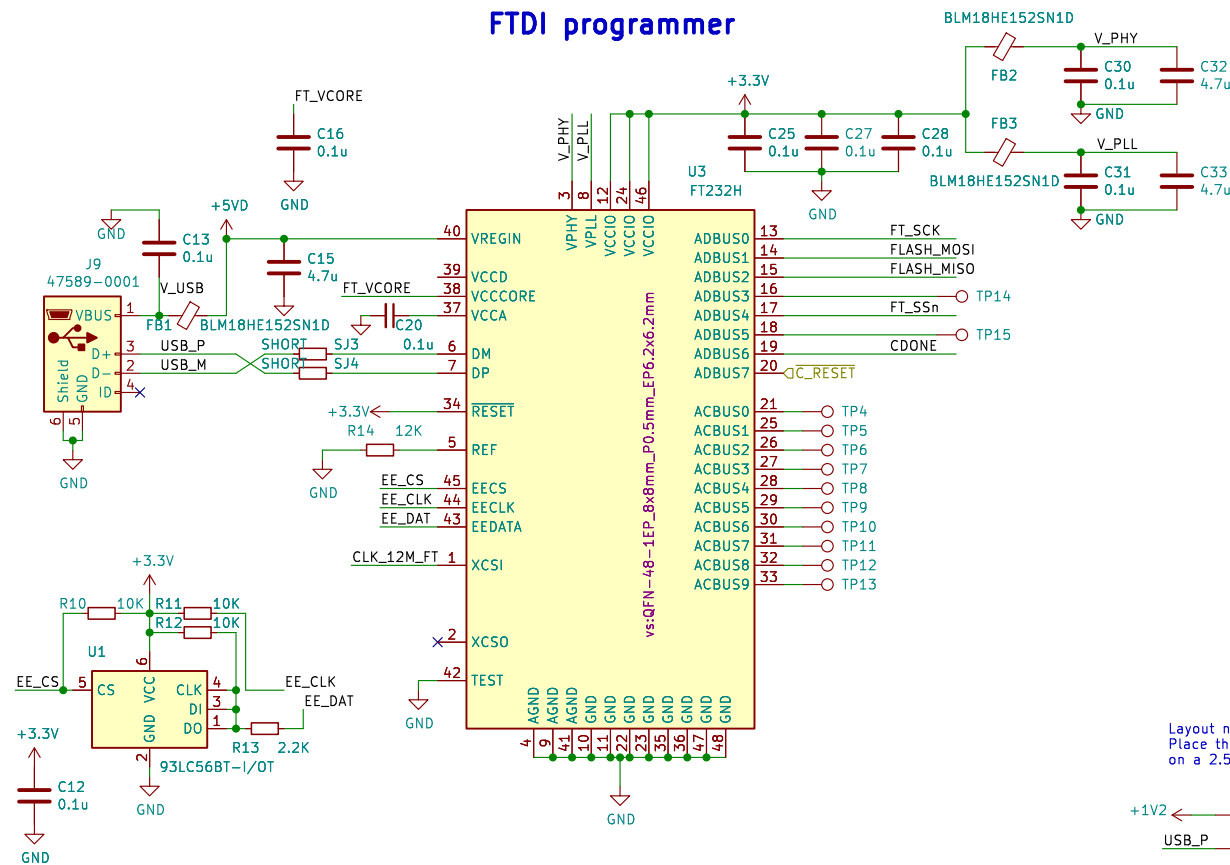
12MHz Oscillator



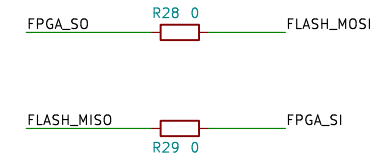
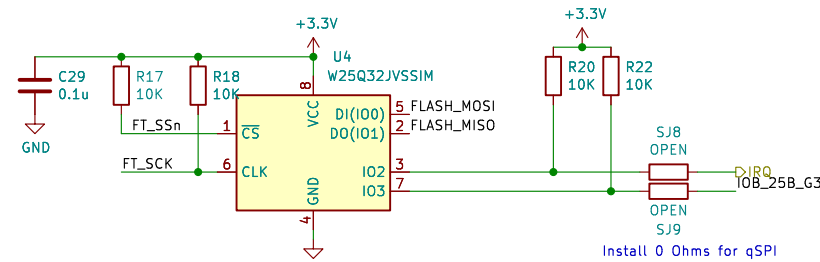
Regulators (300mA max)



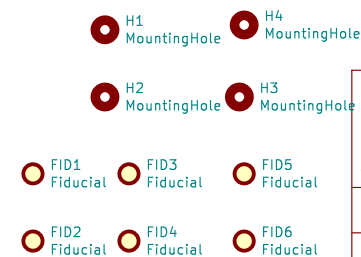
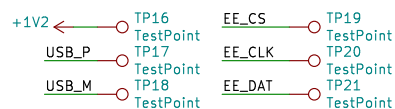
FTDI programmer



qSPI/DTR Flash



Layout note:
Place these testpoints on the bottom of the board on a 2.54mm grid.



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Sheet: /Xosera_FPGA/ File: Xosera_FPGA.kicad_sch		
Title: Xosera rosco_m68k daughterboard		
Size: A3	Date: 2023-12-21	Rev: 0
KiCad E.D.A. kicad 7.0.9		Id: 2/4

