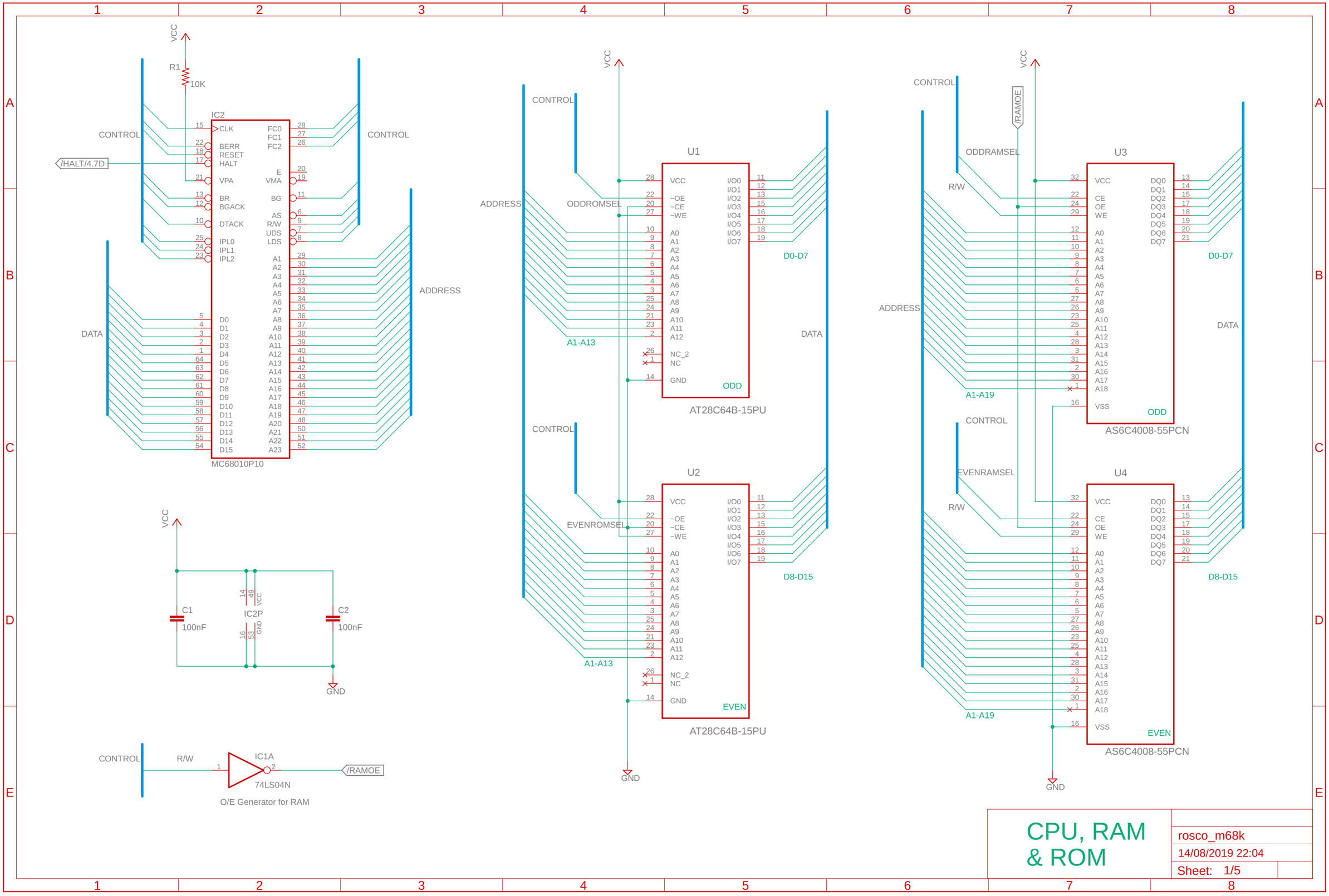
IC2
MC68010P10
CLK
BERR
RESET
HALT
VPA
BR
BGACK
DTACK
IPL0
IPL1
IPL2
FC0
FC1
FC2
E
VMA
BG
AS
R/W
UDS
LDS
CONTROL
/HALT/4.7D
DATA
ADDRESS
VCC
R1
10K
C1
100nF
C2
100nF
IC2P
GND
IC1A
74LS04N
/RAMOE
O/E Generator for RAM
U1
AT28C64B-15PU
ODDROMSEL
A1-A13
D0-D7
ODD
U2
EVENROMSEL
D8-D15
EVEN
U3
AS6C4008-55PCN
ODDRAMSEL
/RAMOE
A1-A19
U4
EVENRAMSEL
CPU, RAM
& ROM
rosco_m68k
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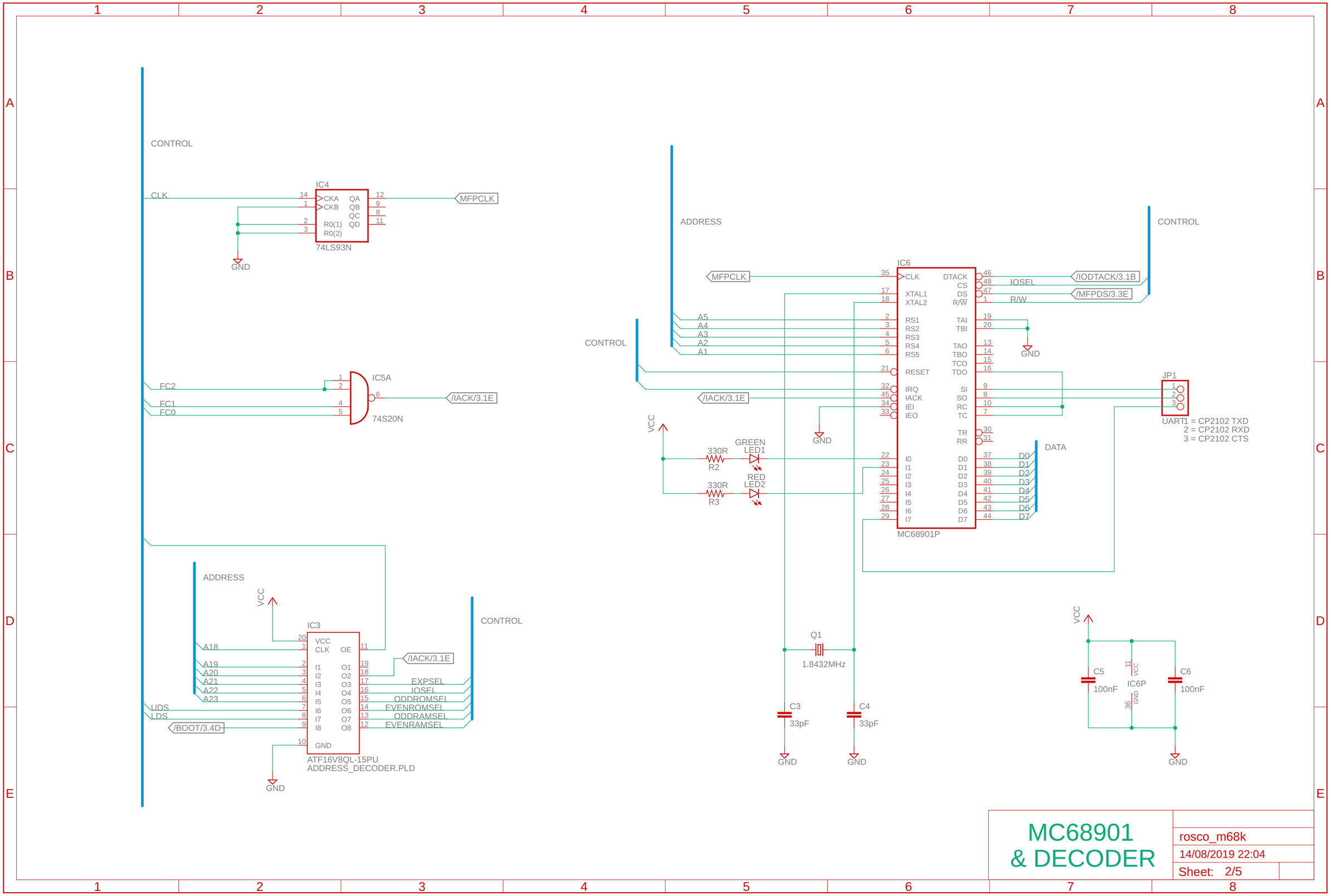
CONTROL
CLK
IC4
CKA
CKB
QA
QB
QC
QD
R0(1)
R0(2)
74LS93N
MFPCLK
GND
FC2
FC1
FC0
IC5A
74S20N
/IACK/3.1E
ADDRESS
VCC
IC3
VCC
CLK
OE
A18
A19
A20
A21
A22
A23
UDS
LDS
/BOOT/3.4D
I1
I2
I3
I4
I5
I6
I7
I8
O1
O2
O3
O4
O5
O6
O7
O8
GND
EXPSEL
IOSEL
ODDROMSEL
EVENROMSEL
ODDRAMSEL
EVENRAMSEL
ATF16V8QL-15PU
ADDRESS_DECODER.PLD
IC6
CLK
XTAL1
XTAL2
RS1
RS2
RS3
RS4
RS5
RESET
IRQ
IACK
IEI
IEO
DTACK
CS
DS
R/W
TAI
TBI
TAO
TBO
TCO
TDO
SI
SO
RC
TC
TR
RR
D0
D1
D2
D3
D4
D5
D6
D7
MC68901P
IOSEL
R/W
/IODTACK/3.1B
/MFPDS/3.3E
DATA
JP1
UART1 = CP2102 TXD
2 = CP2102 RXD
3 = CP2102 CTS
330R
R2
R3
GREEN
LED1
RED
LED2
Q1
1.8432MHz
C3
33pF
C4
33pF
C5
100nF
C6
100nF
IC6P
MC68901
& DECODER
rosco_m68k
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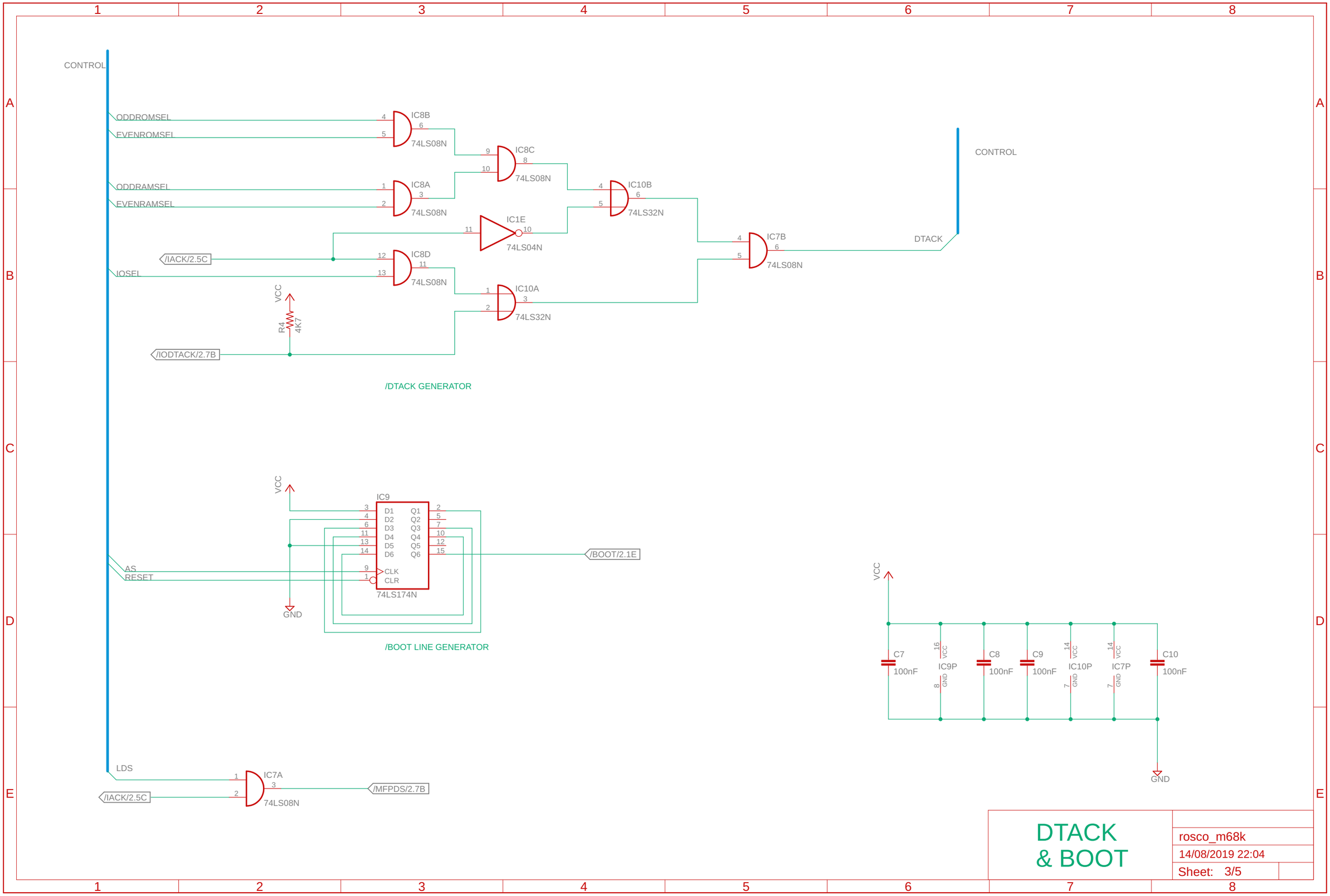
CONTROL
ODDROMSEL
EVENROMSEL
ODDRAMSEL
EVENRAMSEL
IOSEL
/IACK/2.5C
/IODTACK/2.7B
IC8B
74LS08N
IC8C
74LS08N
IC8A
74LS08N
IC1E
74LS04N
IC8D
74LS08N
IC10A
74LS32N
IC10B
74LS32N
IC7B
74LS08N
DTACK
CONTROL
VCC
R4
4K7
/DTACK GENERATOR
IC9
D1 D2 D3 D4 D5 D6
Q1 Q2 Q3 Q4 Q5 Q6
CLK
CLR
74LS174N
AS
RESET
GND
/BOOT/2.1E
/BOOT LINE GENERATOR
LDS
/IACK/2.5C
IC7A
74LS08N
/MFPDS/2.7B
C7 100nF
IC9P
C8 100nF
C9 100nF
IC10P
IC7P
C10 100nF
DTACK
& BOOT
rosco_m68k
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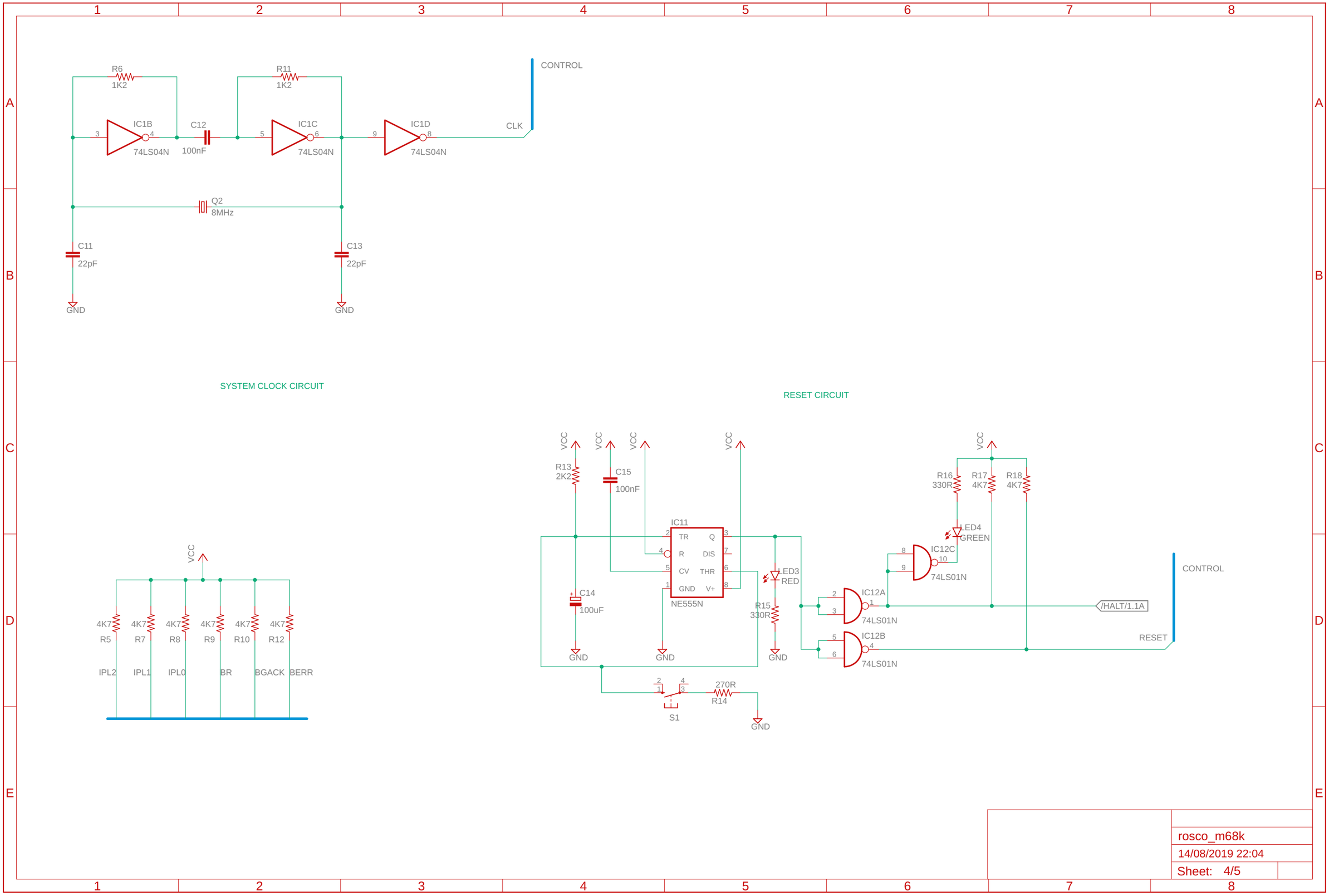
R6
1K2
R11
1K2
CONTROL
IC1B
C12
IC1C
IC1D
CLK
74LS04N
100nF
74LS04N
74LS04N
Q2
8MHz
C11
22pF
C13
22pF
GND
GND
SYSTEM CLOCK CIRCUIT
RESET CIRCUIT
VCC
R13
2K2
C15
100nF
IC11
TR
Q
R
DIS
CV
THR
GND
V+
NE555N
C14
100uF
LED3
RED
R15
330R
GND
GND
GND
R16
330R
R17
4K7
R18
4K7
LED4
GREEN
IC12C
74LS01N
IC12A
74LS01N
IC12B
74LS01N
/HALT/1.1A
CONTROL
RESET
S1
270R
R14
GND
4K7
R5
R7
R8
R9
R10
R12
IPL2
IPL1
IPL0
BR
BGACK
BERR
rosco_m68k
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