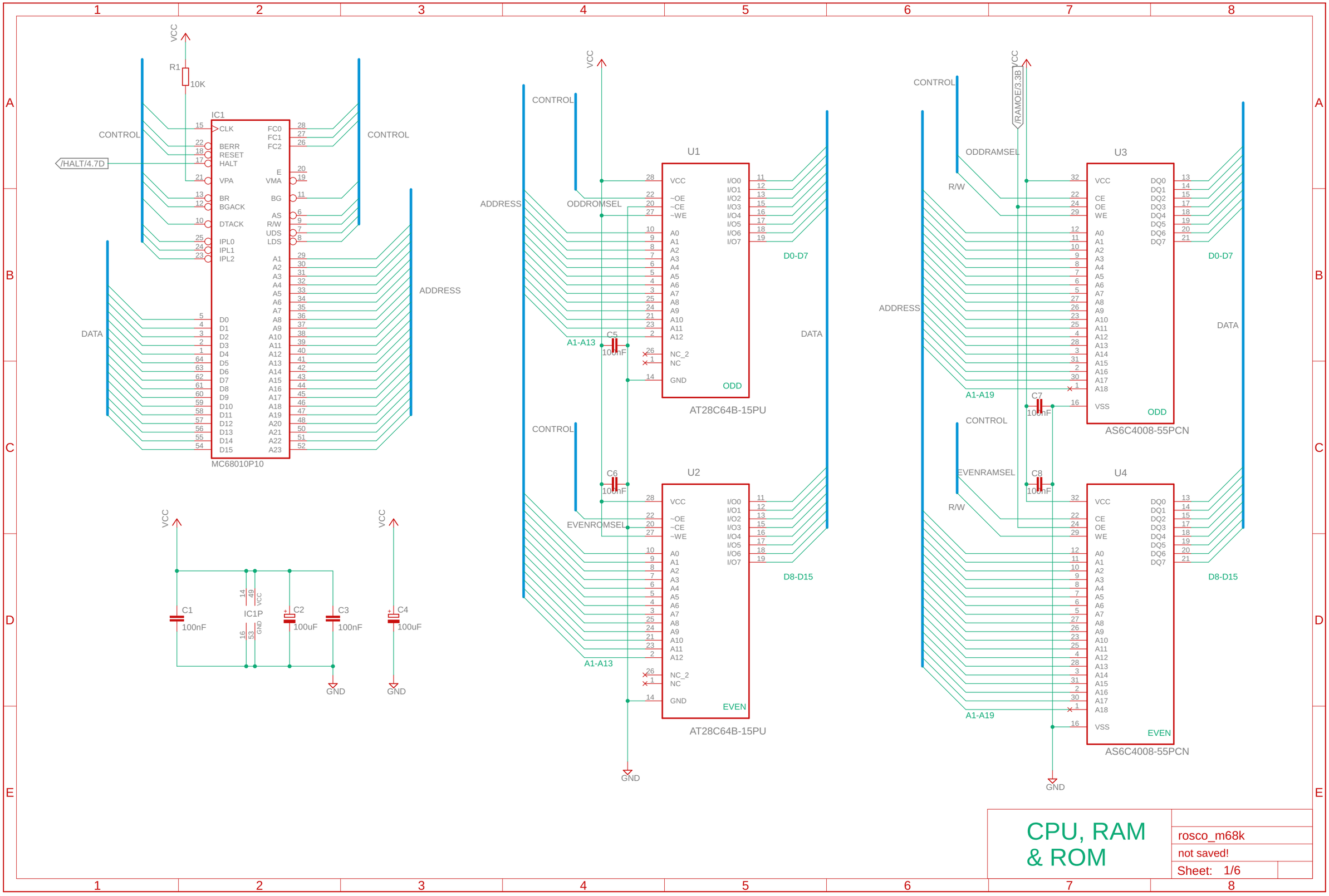
VCC
R1
10K
IC1
CONTROL
/HALT/4.7D
CLK
BERR
RESET
HALT
VPA
BR
BGACK
DTACK
IPL0
IPL1
IPL2
FC0
FC1
FC2
E
VMA
BG
AS
R/W
UDS
LDS
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
A17
A18
A19
A20
A21
A22
A23
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
DATA
ADDRESS
MC68010P10
C1
100nF
IC1P
C2
100uF
C3
100nF
C4
100uF
GND
U1
ODDROMSEL
VCC
~OE
~CE
~WE
NC_2
NC
GND
I/O0
I/O1
I/O2
I/O3
I/O4
I/O5
I/O6
I/O7
D0-D7
A1-A13
C5
100nF
ODD
AT28C64B-15PU
C6
100nF
U2
EVENROMSEL
D8-D15
EVEN
AT28C64B-15PU
/RAMOE/3.3B
ODDRAMSEL
R/W
U3
CE
OE
WE
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
A1-A19
C7
100nF
VSS
AS6C4008-55PCN
EVENRAMSEL
C8
100nF
U4
CPU, RAM
& ROM
rosco_m68k
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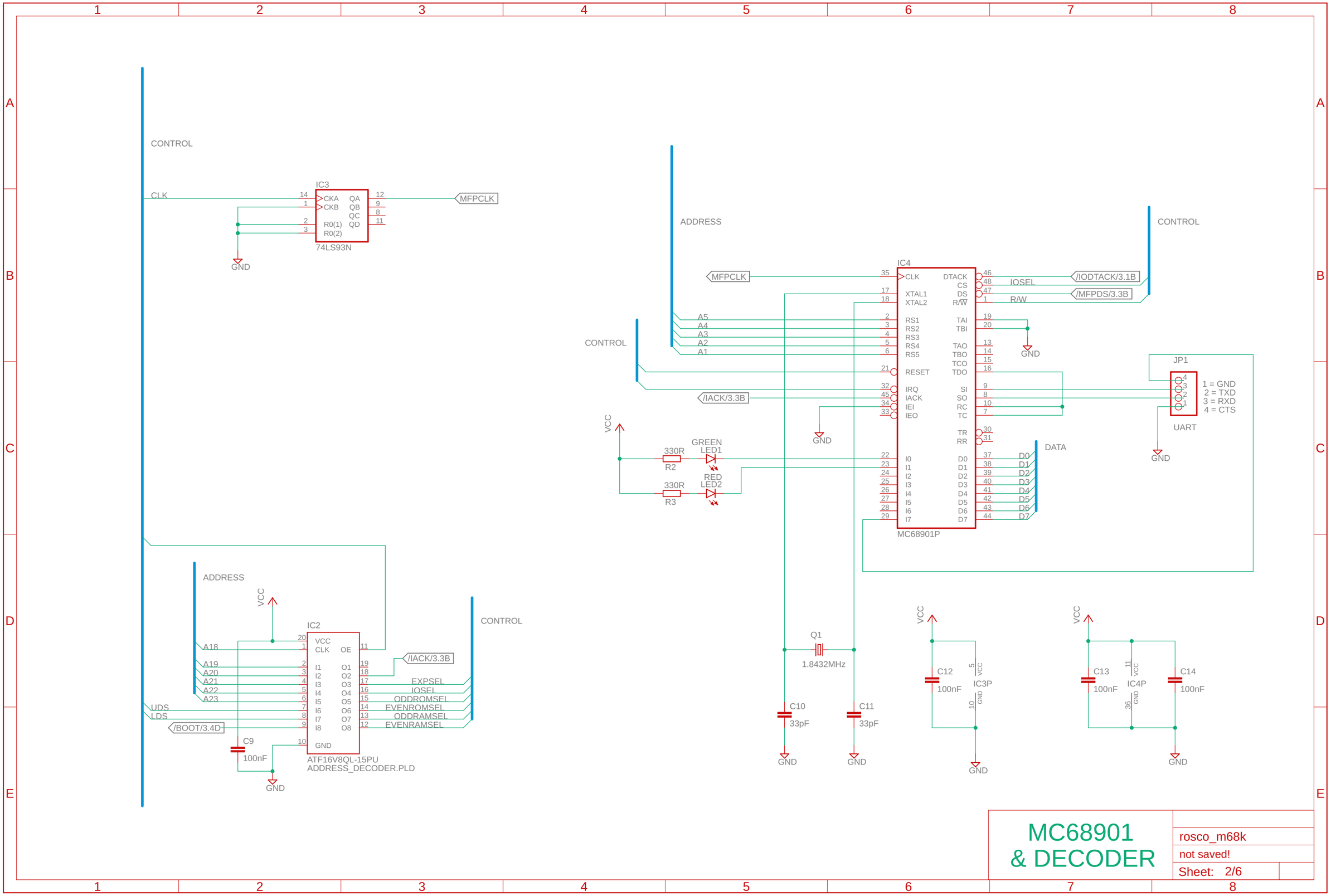
Sheet: 1/6



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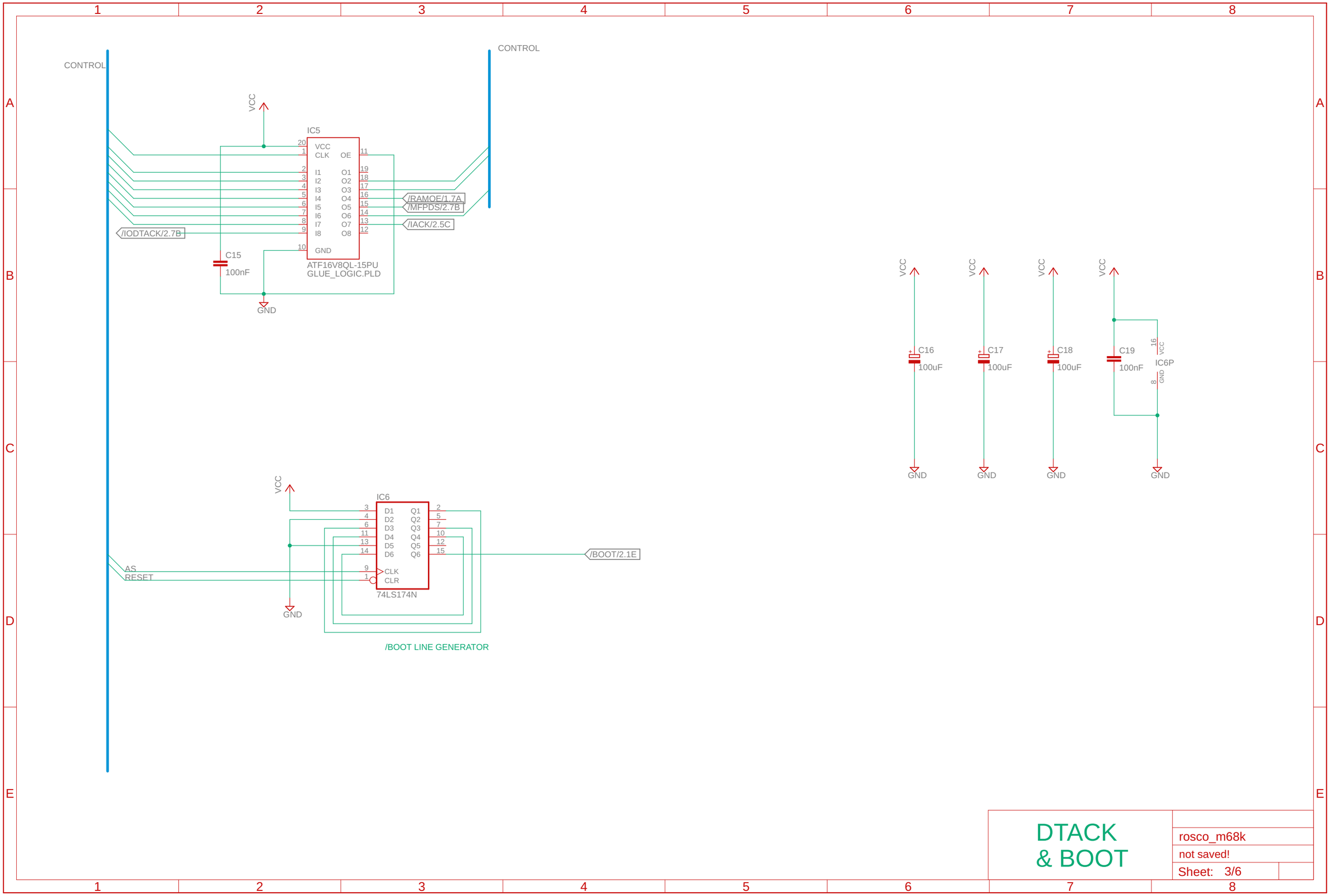
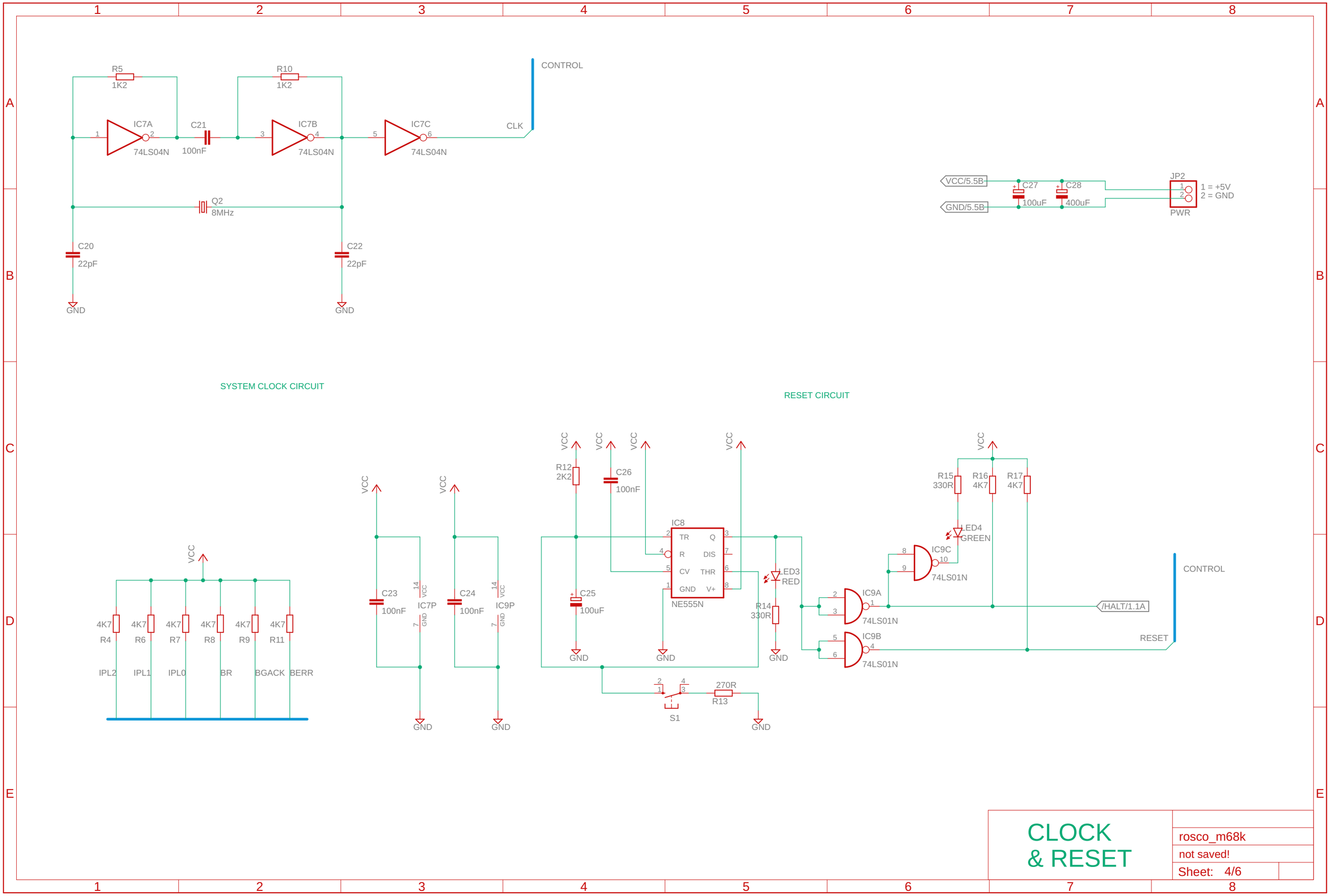
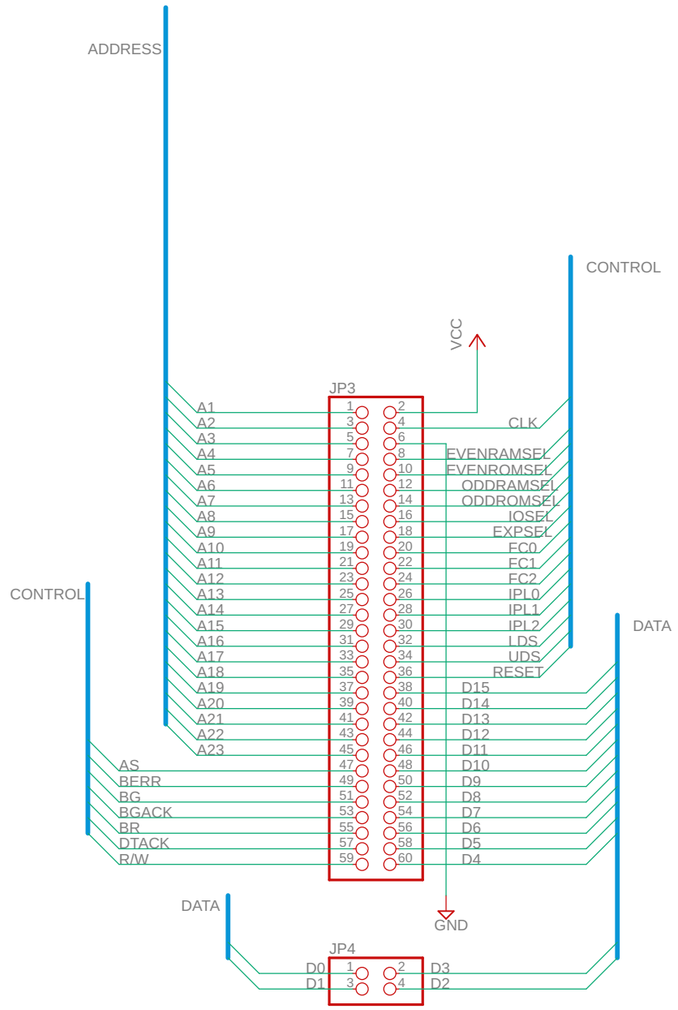

CONTROL
CONTROL
VCC
IC5
VCC
CLK
OE
I1
I2
I3
I4
I5
I6
I7
I8
O1
O2
O3
O4
O5
O6
O7
O8
GND
/RAMOE/1.7A
/MFPDS/2.7B
/IACK/2.5C
/IODTACK/2.7B
C15
100nF
ATF16V8QL-15PU
GLUE_LOGIC.PLD
GND
VCC
VCC
VCC
VCC
C16
100uF
C17
100uF
C18
100uF
C19
100nF
IC6P
GND
GND
GND
GND
VCC
IC6
D1
D2
D3
D4
D5
D6
Q1
Q2
Q3
Q4
Q5
Q6
/BOOT/2.1E
AS
RESET
CLK
CLR
74LS174N
GND
/BOOT LINE GENERATOR
DTACK
& BOOT
rosco_m68k
not saved!
Sheet: 3/6


R5
1K2
R10
1K2
CONTROL
IC7A
74LS04N
C21
100nF
IC7B
74LS04N
IC7C
74LS04N
CLK
Q2
8MHz
C20
22pF
C22
22pF
GND
GND
SYSTEM CLOCK CIRCUIT
VCC/5.5B
GND/5.5B
C27
100uF
C28
400uF
JP2
1 = +5V
2 = GND
PWR
RESET CIRCUIT
VCC
R12
2K2
C26
100nF
IC8
TR
R
CV
GND
Q
DIS
THR
V+
NE555N
C25
100uF
LED3
RED
R14
330R
R15
330R
R16
4K7
R17
4K7
LED4
GREEN
IC9C
74LS01N
IC9A
74LS01N
IC9B
74LS01N
/HALT/1.1A
CONTROL
RESET
S1
270R
R13
C23
100nF
IC7P
C24
100nF
IC9P
4K7
R4
R6
R7
R8
R9
R11
IPL2
IPL1
IPL0
BR
BGACK
BERR
CLOCK
& RESET
rosco_m68k
not saved!
Sheet: 4/6



EXPANSION

rosco_m68k

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