

TI-radar AWR1843 C674x DSP core

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Chapter 1

Data Structure Index

1.1 Data Structures

Here are the data structures with brief descriptions:

clusteringDBscanConfig	Structure element of the list of descriptors for clusteringDBscan configuration	??
clusteringDBscanInstance	Error code for clusteringDBscan	??
clusteringDBscanOutput	Structure of clustering output	??
clusteringDBscanReport	Structure for each cluster information report	??
clusteringDBscanReportForTx_t	Structure for each cluster information report	??
configPkg	??
cycleLog_t_	DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods	??
DSS2MSS_HSRAM	??
DSS_DataPathObj_t	Millimeter Wave Demo Data Path Information	??
Header	??
KFstate	Kalman filter state	??
KFtrackerInstance	Struct for the tracker configuration, and pointers to scratch buffers	??
maxVelEnhStruct_t_	Pre-computed parameters of the max-velocity-enhancement	??
MCB_t	DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design	??
mmWave_detObjMsg_t	The structure defines the message body for reporting detection information from data path to MSS	??
mmWave_dssAssertInfoMsg_t	The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information	??
mmWave_OUT_MSG_header_t	The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS	??

mmWave_OUT_MSG_stats_dataObjDescr_t	Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS	??
mmWave_OUT_MSG_tI_t	The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS	??
mmWaveMSG_body_u	The union defines the message body for various configuration messages. For passing configuration from MSS to DSS	??
mmWaveMSG_t	The structure defines the message structure used for communication between MSS and DSS	??
mmWaveMSG_TLV_t	The structure describes the TLV part of the message from DSS to MSS on data path detection information	??
MmwDemo_1D_DopplerLines	Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction	??
MmwDemo_ADCBufCfg_t	ADCBUF configuration	??
MmwDemo_AnaMonitorCfg_t	Millimeter Wave Demo analog monitor configuration	??
MmwDemo_CalibDcRangeSigCfg_t	Millimeter Wave Demo DC range signature compensation	??
MmwDemo_CfarCfg_t	Millimeter Wave Demo CFAR Configuration	??
MmwDemo_Cfg_t	Millimeter Wave Demo configuration	??
MmwDemo_CliCfg_t	Millimeter Wave Demo CLI related configuration	??
MmwDemo_CliCommonCfg_t	Millimeter Wave Demo CLI related configuration common across all subframes	??
MmwDemo_ClutterRemovalCfg_t	Clutter removal configuration	??
MmwDemo_compRxChannelBiasCfg_t	Range Bias and rx channel gain/phase compensation configuration	??
MmwDemo_detectedObj_t	Detected object estimated parameters	??
MmwDemo_detectedObjActual_t	Detected object estimated parameters	??
MmwDemo_detectedObjForTx_t	Detected object estimated parameters to be transmitted out	??
MmwDemo_DSS_STATS_t	??
MmwDemo_ExtendedMaxVelocityCfg_t	Millimeter Wave Demo Velocity Disambiguation	??
MmwDemo_GuiMonSel_t	Millimeter Wave Demo Gui Monitor Selection	??
MmwDemo_LvdsStreamCfg_t	LVDS streaming configuration	??
MmwDemo_measureRxChannelBiasCfg_t	Range Bias and rx channel gain/phase measurement configuration	??
MmwDemo_MultiObjBeamFormingCfg_t	Millimeter Wave Demo multi object beam formaing Configuration	??
MmwDemo_NearFieldCorrectionCfg_t	Millimeter Wave Demo near field correction	??
MmwDemo_objRaw1D	Parameters of CFAR detected object during the first round of CFAR detections	??
MmwDemo_objRaw2D	Parameters of CFAR detected object during the second round of CFAR detections	??

MmwDemo_PeakGroupingCfg_t	Millimeter Wave Demo Peak grouping Configuration	??
MmwDemo_timingInfo	Timing information	??
SNRThresholds	These parameters allow the SNR requirements to be varied as a function of range	??
ti_sysbios_BIOS_Module_State	??
ti_sysbios_BIOS_RtsGateProxy_Module	??
ti_sysbios_BIOS_RtsGateProxy_Object2	??
ti_sysbios_family_c64p_Cache_Module_State	??
ti_sysbios_family_c64p_EventCombiner_Module_State	??
ti_sysbios_family_c64p_Exception_Module_State	??
ti_sysbios_family_c64p_Hwi_S1	??
ti_sysbios_family_c64p_Hwi_Module	??
ti_sysbios_family_c64p_Hwi_Module_State	??
ti_sysbios_family_c64p_Hwi_Object2	??
ti_sysbios_family_c64p_Hwi_Object	??
ti_sysbios_gates_GateHwi_S1	??
ti_sysbios_gates_GateHwi_Module	??
ti_sysbios_gates_GateHwi_Object2	??
ti_sysbios_gates_GateHwi_Object	??
ti_sysbios_gates_GateMutex_S1	??
ti_sysbios_gates_GateMutex_Module	??
ti_sysbios_gates_GateMutex_Object2	??
ti_sysbios_gates_GateMutex_Object	??
ti_sysbios_hal_Hwi_S1	??
ti_sysbios_hal_Hwi_HwiProxy_Module	??
ti_sysbios_hal_Hwi_HwiProxy_Object2	??
ti_sysbios_hal_Hwi_Module	??
ti_sysbios_hal_Hwi_Object2	??
ti_sysbios_hal_Hwi_Object	??
ti_sysbios_heaps_HeapMem_S1	??
ti_sysbios_heaps_HeapMem_Module	??
ti_sysbios_heaps_HeapMem_Module_GateProxy_Module	??
ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2	??
ti_sysbios_heaps_HeapMem_Object2	??
ti_sysbios_heaps_HeapMem_Object	??
ti_sysbios_knl_Clock_S1	??
ti_sysbios_knl_Clock_Module	??
ti_sysbios_knl_Clock_Module_State	??
ti_sysbios_knl_Clock_Object2	??
ti_sysbios_knl_Clock_Object	??
ti_sysbios_knl_Clock_TimerProxy_Module	??
ti_sysbios_knl_Clock_TimerProxy_Object2	??
ti_sysbios_knl_Event_S1	??
ti_sysbios_knl_Event_Module	??
ti_sysbios_knl_Event_Object2	??
ti_sysbios_knl_Event_Object	??
ti_sysbios_knl_Queue_S1	??
ti_sysbios_knl_Queue_Module	??
ti_sysbios_knl_Queue_Object2	??
ti_sysbios_knl_Queue_Object	??
ti_sysbios_knl_Semaphore_S1	??
ti_sysbios_knl_Semaphore_Module	??
ti_sysbios_knl_Semaphore_Object2	??
ti_sysbios_knl_Semaphore_Object	??
ti_sysbios_knl_Task_S1	??
ti_sysbios_knl_Task_Module	??

ti_sysbios_knl_Task_Module_State	??
ti_sysbios_knl_Task_Object2	??
ti_sysbios_knl_Task_Object	??
ti_sysbios_rts_ti_ThreadLocalStorage_Module_State	??
ti_sysbios_timers_rti_Timer_S1	??
ti_sysbios_timers_rti_Timer_Module	??
ti_sysbios_timers_rti_Timer_Module_State	??
ti_sysbios_timers_rti_Timer_Object2	??
ti_sysbios_timers_rti_Timer_Object	??
ti_sysbios_utils_Load_Module_State	??
trackingInputReport	Input to tracking from the clustering output	??
trackingReportForTx_t	Structure for tracking report	??
xdc_runtime_Error_Module_State	??
xdc_runtime_Main_Module_GateProxy_Module	??
xdc_runtime_Main_Module_GateProxy_Object2	??
xdc_runtime_Memory_HeapProxy_Module	??
xdc_runtime_Memory_HeapProxy_Object2	??
xdc_runtime_Memory_Module_State	??
xdc_runtime_Registry_Module_State	??
xdc_runtime_Startup_Module_State	??
xdc_runtime_System_Module_GateProxy_Module	??
xdc_runtime_System_Module_GateProxy_Object2	??
xdc_runtime_System_Module_State	??
xdc_runtime_Text_Module_State	??

Chapter 2

File Index

2.1 File List

Here is a list of all files with brief descriptions:

clusteringDBscan.c	DBscan clustering module	??
clusteringDBscan.h	DBscan clustering module	??
dss_config_edma_util.c	EDMA Configuration Utility API implementation	??
dss_config_edma_util.h	EDMA Configuration Utility API definitions	??
dss_data_path.c	Implements Data path processing functionality	??
dss_data_path.h	This is the data path processing header	??
dss_main.c	??
EKF_XYZ_Consts.h	Constants for the Extended Kalman Filter	??
EKF_XYZ_Interface.h	Interface the Extended Kalman Filter	??
Extended_Kalman_Filter_xyz.c	Implements an 'Extended Kalman Filter' for radar tracking	??
gen_twiddle_fft16x16.c	??
gen_twiddle_fft32x32.c	??
common/ app_cfg.h	??
common/ detected_obj.h	??
common/ device_cfg.h	This file holds constants related to the system chirp configuration as well as the calling the profiles configs such as: config_chirp_design_USRR20.h (p. ??) config_chirp_design_USRR30.h (p. ??) To Add profiles, create the profile in the following path common/profiles/ under the following name convention: config_chirp_design_XRRxx.h X where S:Short, M: Medium, US: Ultrashort .. etc xx is the number of chips within the frame	??
common/ frame_cfg.c	??
common/ mmw_messages.h	??
common/ mmWave_XSS.h	??
common/ mrr_config.h	??
common/profiles/ config_chirp_design_MRR120.h	??
common/profiles/ config_chirp_design_MRR80.h	??

common/profiles/ config_chirp_design_USRR20.h	??
common/profiles/ config_chirp_design_USRR30.h	??
Debug/ clusteringDBscan.d	??
Debug/ dss_config_edma_util.d	??
Debug/ dss_data_path.d	??
Debug/ dss_main.d	??
Debug/ Extended_Kalman_Filter_xyz.d	??
Debug/ gen_twiddle_fft16x16.d	??
Debug/ gen_twiddle_fft32x32.d	??
Debug/common/ frame_cfg.d	??
Debug/configPkg/package/ configPkg.java	??
Debug/configPkg/package/ package.defs.h	??
Debug/configPkg/package/ package.xdc.inc	??
Debug/configPkg/package/ package_configPkg.c	??
Debug/configPkg/package/cfg/ dss_mrr_pe674.c	??
Debug/configPkg/package/cfg/ dss_mrr_pe674.h	??
Debug/configPkg/package/cfg/ dss_mrr_pe674.xdc.inc	??
Debug/configPkg/package/rel/ configPkg.xdc.inc	??

Chapter 3

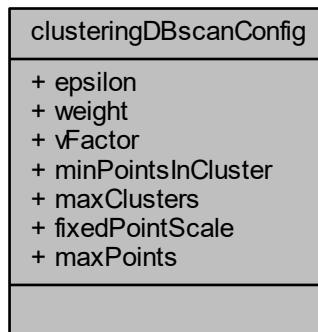
Data Structure Documentation

3.1 clusteringDBscanConfig Struct Reference

Structure element of the list of descriptors for clusteringDBscan configuration.

```
#include <dss_data_path.h>
```

Collaboration diagram for clusteringDBscanConfig:



Data Fields

- float **epsilon**
- float **weight**
- float **vFactor**
- uint16_t **minPointsInCluster**
- uint16_t **maxClusters**
- uint16_t **fixedPointSize**
- uint16_t **maxPoints**

3.1.1 Detailed Description

Structure element of the list of descriptors for clusteringDBscan configuration.

Definition at line 333 of file dss_data_path.h.

3.1.2 Field Documentation

3.1.2.1 epsilon

```
float clusteringDBscanConfig::epsilon
```

distance threshold for cluster check

Definition at line 335 of file dss_data_path.h.

3.1.2.2 fixedPointScale

```
uint16_t clusteringDBscanConfig::fixedPointScale
```

Block scale value to convert x-y from floating-point to fixed-point. Should be the same the one used in converting r-theta to x-y, not used for floating-point input

Definition at line 340 of file dss_data_path.h.

3.1.2.3 maxClusters

```
uint16_t clusteringDBscanConfig::maxClusters
```

maximum number of clusters

Definition at line 339 of file dss_data_path.h.

3.1.2.4 maxPoints

```
uint16_t clusteringDBscanConfig::maxPoints
```

Maximum number of points that can be services per dbSCANRun call

Definition at line 342 of file dss_data_path.h.

3.1.2.5 minPointsInCluster

```
uint16_t clusteringDBscanConfig::minPointsInCluster
```

minimum number of points in a cluster

Definition at line 338 of file dss_data_path.h.

3.1.2.6 vFactor

```
float clusteringDBscanConfig::vFactor
```

additional velocity factor for speed delta

Definition at line 337 of file dss_data_path.h.

3.1.2.7 weight

```
float clusteringDBscanConfig::weight
```

the weight between the distance and speed

Definition at line 336 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

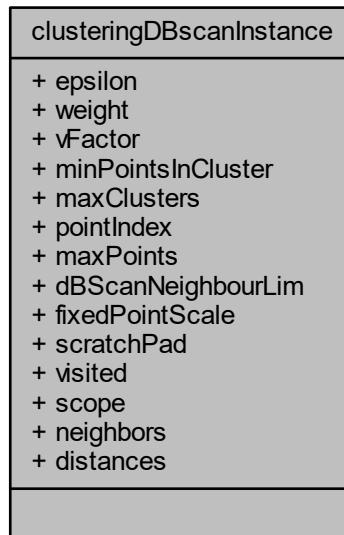
- [dss_data_path.h](#)

3.2 clusteringDBscanInstance Struct Reference

error code for clusteringDBscan.

```
#include <dss_data_path.h>
```

Collaboration diagram for clusteringDBscanInstance:



Data Fields

- float `epsilon`
- float `weight`
- float `vFactor`
- uint16_t `minPointsInCluster`
- uint16_t `maxClusters`
- uint16_t `pointIndex`
- uint16_t `maxPoints`
- uint16_t `dBScanNeighbourLim`
- uint16_t `fixedPointScale`
- char * `scratchPad`
- char * `visited`
- char * `scope`
- uint16_t * `neighbors`
- float * `distances`

3.2.1 Detailed Description

error code for clusteringDBscan.

Definition at line 308 of file `dss_data_path.h`.

3.2.2 Field Documentation

3.2.2.1 dBScanNeighbourLim

```
uint16_t clusteringDBscanInstance::dBScanNeighbourLim
```

Early terminate a search if the number of neighbours exceeds this number

Definition at line 318 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.2 distances

```
float* clusteringDBscanInstance::distances
```

Definition at line 326 of file dss_data_path.h.

3.2.2.3 epsilon

```
float clusteringDBscanInstance::epsilon
```

Definition at line 310 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.4 fixedPointScale

```
uint16_t clusteringDBscanInstance::fixedPointScale
```

Block scale value to convert x-y from floating-point to fixed-point. Should be the same the one used in converting r-theta to x-y, not used for floating-point input

Definition at line 319 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.5 maxClusters

```
uint16_t clusteringDBscanInstance::maxClusters
```

Definition at line 314 of file dss_data_path.h.

Referenced by clusteringDBscanRun(), and populateOutputs().

3.2.2.6 maxPoints

```
uint16_t clusteringDBscanInstance::maxPoints
```

Definition at line 317 of file dss_data_path.h.

3.2.2.7 minPointsInCluster

```
uint16_t clusteringDBscanInstance::minPointsInCluster
```

Definition at line 313 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.8 neighbors

```
uint16_t* clusteringDBscanInstance::neighbors
```

Definition at line 325 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.9 pointIndex

```
uint16_t clusteringDBscanInstance::pointIndex
```

Definition at line 315 of file dss_data_path.h.

3.2.2.10 scope

```
char* clusteringDBscanInstance::scope
```

Definition at line 324 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.11 scratchPad

```
char* clusteringDBscanInstance::scratchPad
```

Definition at line 322 of file dss_data_path.h.

3.2.2.12 vFactor

```
float clusteringDBscanInstance::vFactor
```

Definition at line 312 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.13 visited

```
char* clusteringDBscanInstance::visited
```

Definition at line 323 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.2.2.14 weight

```
float clusteringDBscanInstance::weight
```

Definition at line 311 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

The documentation for this struct was generated from the following file:

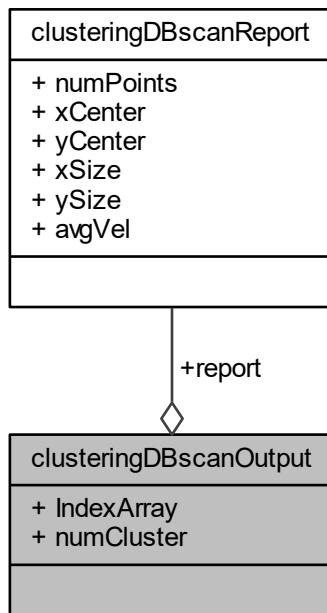
- [dss_data_path.h](#)

3.3 clusteringDBscanOutput Struct Reference

Structure of clustering output.

```
#include <dss_data_path.h>
```

Collaboration diagram for clusteringDBscanOutput:



Data Fields

- `uint16_t * IndexArray`
- `uint16_t numCluster`
- `clusteringDBscanReport_t * report`

3.3.1 Detailed Description

Structure of clustering output.

Definition at line 367 of file dss_data_path.h.

3.3.2 Field Documentation

3.3.2.1 IndexArray

`uint16_t* clusteringDBscanOutput::IndexArray`

Clustering result index array

Definition at line 369 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

3.3.2.2 numCluster

`uint16_t clusteringDBscanOutput::numCluster`

number of cluster detected

Definition at line 370 of file dss_data_path.h.

Referenced by clusteringDBscanRun(), and populateOutputs().

3.3.2.3 report

`clusteringDBscanReport_t* clusteringDBscanOutput::report`

information report for each cluster

Definition at line 371 of file dss_data_path.h.

Referenced by clusteringDBscanRun().

The documentation for this struct was generated from the following file:

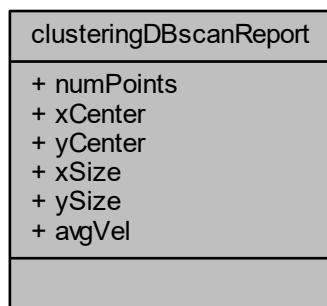
- `dss_data_path.h`

3.4 clusteringDBscanReport Struct Reference

Structure for each cluster information report .

`#include <dss_data_path.h>`

Collaboration diagram for clusteringDBscanReport:



Data Fields

- `uint16_t numPoints`
- `int16_t xCenter`
- `int16_t yCenter`
- `int16_t xSize`
- `int16_t ySize`
- `int16_t avgVel`

3.4.1 Detailed Description

Structure for each cluster information report .

Definition at line 347 of file dss_data_path.h.

3.4.2 Field Documentation

3.4.2.1 avgVel

```
int16_t clusteringDBscanReport::avgVel
```

the average velocity within this cluster

Definition at line 354 of file dss_data_path.h.

3.4.2.2 numPoints

```
uint16_t clusteringDBscanReport::numPoints
```

number of points in this cluster

Definition at line 349 of file dss_data_path.h.

3.4.2.3 xCenter

```
int16_t clusteringDBscanReport::xCenter
```

the clustering center on x direction

Definition at line 350 of file dss_data_path.h.

3.4.2.4 xSize

```
int16_t clusteringDBscanReport::xSize
```

the clustering size on x direction

Definition at line 352 of file dss_data_path.h.

3.4.2.5 yCenter

```
int16_t clusteringDBscanReport::yCenter
```

the clustering center on y direction

Definition at line 351 of file dss_data_path.h.

3.4.2.6 ySize

```
int16_t clusteringDBscanReport::ySize
```

the clustering size on y direction

Definition at line 353 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

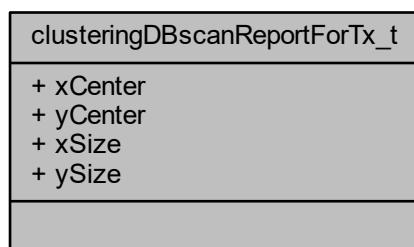
- **dss_data_path.h**

3.5 clusteringDBscanReportForTx_t Struct Reference

Structure for each cluster information report .

```
#include <dss_data_path.h>
```

Collaboration diagram for clusteringDBscanReportForTx_t:



Data Fields

- int16_t **xCenter**
- int16_t **yCenter**
- int16_t **xSize**
- int16_t **ySize**

3.5.1 Detailed Description

Structure for each cluster information report .

Definition at line 179 of file dss_data_path.h.

3.5.2 Field Documentation

3.5.2.1 xCenter

```
int16_t clusteringDBscanReportForTx_t::xCenter
```

the clustering center on x direction

Definition at line 181 of file dss_data_path.h.

3.5.2.2 xSize

```
int16_t clusteringDBscanReportForTx_t::xSize
```

the clustering size on x direction

Definition at line 183 of file dss_data_path.h.

3.5.2.3 yCenter

```
int16_t clusteringDBscanReportForTx_t::yCenter
```

the clustering center on y direction

Definition at line 182 of file dss_data_path.h.

3.5.2.4 ySize

```
int16_t clusteringDBscanReportForTx_t::ySize
```

the clustering size on y direction

Definition at line 184 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

- [dss_data_path.h](#)

3.6 configPkg Class Reference

Collaboration diagram for configPkg:



Public Member Functions

- void **exec** (Scriptable xdcO, Session ses)

3.6.1 Detailed Description

Definition at line 12 of file configPkg.java.

3.6.2 Member Function Documentation

3.6.2.1 exec()

```
void configPkg.exec (
    Scriptable xdcO,
    Session ses ) [inline]
```

Definition at line 125 of file configPkg.java.

The documentation for this class was generated from the following file:

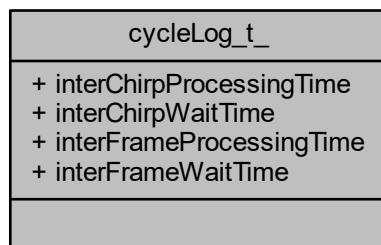
- Debug/configPkg/package/ **configPkg.java**

3.7 cycleLog_t_ Struct Reference

DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods.

```
#include <dss_data_path.h>
```

Collaboration diagram for cycleLog_t_:



Data Fields

- `uint32_t interChirpProcessingTime`
total processing time during all chirps in a frame excluding EDMA waiting time
- `uint32_t interChirpWaitTime`
total wait time for EDMA data transfer during all chirps in a frame
- `uint32_t interFrameProcessingTime`
total processing time for 2D and 3D excluding EDMA waiting time
- `uint32_t interFrameWaitTime`
total wait time for 2D and 3D EDMA data transfer

3.7.1 Detailed Description

DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods.

Definition at line 99 of file dss_data_path.h.

3.7.2 Field Documentation

3.7.2.1 interChirpProcessingTime

```
uint32_t cycleLog_t_::interChirpProcessingTime
```

total processing time during all chirps in a frame excluding EDMA waiting time

Definition at line 100 of file dss_data_path.h.

3.7.2.2 interChirpWaitTime

```
uint32_t cycleLog_t_::interChirpWaitTime
```

total wait time for EDMA data transfer during all chirps in a frame

Definition at line 101 of file dss_data_path.h.

Referenced by `MmwDemo_processChirp()`.

3.7.2.3 interFrameProcessingTime

```
uint32_t cycleLog_t_::interFrameProcessingTime
```

total processing time for 2D and 3D excluding EDMA waiting time

Definition at line 102 of file dss_data_path.h.

3.7.2.4 interFrameWaitTime

```
uint32_t cycleLog_t_::interFrameWaitTime
```

total wait time for 2D and 3D EDMA data transfer

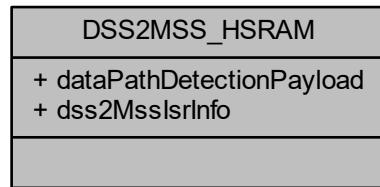
Definition at line 103 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

- [dss_data_path.h](#)

3.8 DSS2MSS_HSRAM Struct Reference

Collaboration diagram for DSS2MSS_HSRAM:



Data Fields

- `uint8_t dataPathDetectionPayload [DATAPATH_DET_PAYLOAD_SIZE]`
- `uint8_t dss2MssIsrInfo`
data path processing/detection related msg payloads, these msgs are signalled through DSS to MSS mailbox

3.8.1 Detailed Description

Definition at line 77 of file dss_main.c.

3.8.2 Field Documentation

3.8.2.1 dataPathDetectionPayload

```
uint8_t DSS2MSS_HSRAM::dataPathDetectionPayload[ DATAPATH_DET_PAYLOAD_SIZE ]
```

Definition at line 79 of file dss_main.c.

3.8.2.2 dss2MssIsrInfo

```
uint8_t DSS2MSS_HSRAM::dss2MssIsrInfo
```

data path processing/detection related msg payloads, these msgs are signalled through DSS to MSS mailbox

Definition at line 80 of file dss_main.c.

The documentation for this struct was generated from the following file:

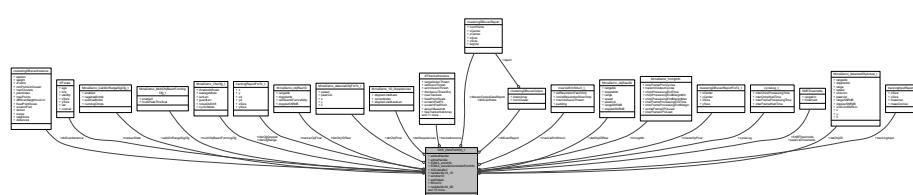
- [dss_main.c](#)

3.9 DSS_DataPathObj_t Struct Reference

Millimeter Wave Demo Data Path Information.

```
#include <dss_data_path.h>
```

Collaboration diagram for DSS_DataPathObj_t:



Data Fields

- **ADCBuf_Handle adcbufHandle**
ADCBUF handle.
- **EDMA_Handle edmaHandle [2]**
Handle of the EDMA driver.
- **EDMA_errorInfo_t EDMA_errorInfo**
EDMA error Information when there are errors like missing events.
- **EDMA_transferControllerErrorInfo_t EDMA_transferControllerErrorInfo**
EDMA transfer controller error information.
- **cmplx16ReIm_t * ADCdataBuf**
pointer to ADC buffer
- **cmplx16ReIm_t * twiddle16x16_1D**
twiddle table for 1D FFT
- **int16_t * window1D**
window coefficients for 1D FFT
- **cmplx16ReIm_t * adcDataIn**
ADCBUF input samples in L2 scratch memory.
- **cmplx16ReIm_t * fftOut1D**
1D FFT output
- **cmplx32ReIm_t * twiddle32x32_2D**
twiddle table for 2D FFT
- **int32_t * window2D**
window coefficients for 2D FFT
- **cmplx16ReIm_t * dstPingPong**
ping pong buffer for 2D from radar Cube
- **cmplx32ReIm_t * windowingBuf2D**
window output for 2D FFT
- **cmplx32ReIm_t * fftOut2D**
2D FFT output
- **uint16_t * log2Abs**
log2 absolute computation output buffer
- **uint16_t * sumAbs**
accumulated sum of log2 absolute over the antennae
- **uint16_t * sumAbsSlowChirp**
accumulated sum of log2 absolute over the antennae
- **uint16_t * sumAbsRange**
input buffer for CFAR processing from the detection matrix
- **uint16_t * cfarDetObjIndexBuf**
CFAR output objects index buffer.
- **uint16_t * cfarDetObjSNR**
CFAR output objects' SNR buffer.
- **cmplx32ReIm_t * azimuthIn**
input for Azimuth FFT
- **cmplx32ReIm_t * antInp**
input for Azimuth FFT
- **cmplx32ReIm_t * elevationIn**
input for Elevation FFT
- **cmplx32ReIm_t * azimuthOut**
output of Azimuth FFT
- **cmplx32ReIm_t * elevationOut**

- float * **azimuthMagSqr**
output of Azimuth FFT magnitude squared
- cmplx32ReIm_t * **azimuthTwiddle32x32**
twiddle factors table for Azimuth FFT
- cmplx16ImRe_t * **azimuthModCoefs**
Pointer to single point DFT coefficients used for Azimuth processing.
- cmplx16ImRe_t * **rxChPhaseComp**
Pointer to the Rx Gain phase compensation params.
- cmplx32ImRe_t * **dcRangeSigMean**
Pointer to DC range signature compensation buffer.
- cmplx16ReIm_t * **radarCube**
Pointer to Radar Cube memory in L3 RAM.
- uint16_t * **detMatrix**
Pointer to range/Doppler log2 magnitude detection matrix in L3 RAM.
- uint8_t * **dBScanScratchPad**
Pointer to dBScan scratch pad.
- uint16_t * **dbscanOutputDataIndexArray**
Pointer to dBScan index array.
- clusteringDBscanReport_t * **dbscanOutputDataReport**
Pointer to dBScan output Report array.
- trackingInputReport_t * **trackingInput**
Pointer to dBScan output used as input to the tracker.
- float * **trackerScratchPadFlt**
Pointer to tracker scratch pad for floats.
- int16_t * **trackerScratchPadShort**
Pointer to tracker scratch pad for int16_t.
- uint8_t **processingPath**
Processing path - either point-cloud or max-vel enhancement.
- uint8_t **chirpThreshold**
Chirp Threshold configuration used for ADCBUF driver.
- uint8_t **dcRangeSigCalibCntr**
DC range signature calibration counter.
- uint8_t **log2NumAvgChirps**
log2 of number of averaged chirps
- uint8_t **log2NumDopplerBins**
log 2 of number of doppler bins
- uint8_t **xyzOutputQFormat**
Q format of the output x/y/z coordinates.
- uint8_t **subframeIndx**
index of the subframe to which this object belongs
- uint8_t **log2numVirtAnt**
log2 of the number of virtual antennas.
- uint8_t **sinAzimQFormat**
Q format of the sin of the azimuth.
- uint8_t **parkingAssistNumBins**
Number of bins for the parkingAssist module.
- uint8_t **parkingAssistNumBinsLog2**
log2 of the number of bins for the parkingAssist module (used for scaling operations).
- uint8_t **padding**
padding. .

- **uint16_t cfarCfgRange_minIndxToIgnoreHPF**
The HPF can mess up the noise floor computation. So for a certain number of indices, we only use the non-hpf sides.
- **uint16_t parkingAssistMaxRange**
maximum range to look for obstacles. .
- **uint16_t parkingAssistMinRange**
minimum range to look for obstacles. .
- **uint16_t numChirpsPerFrame**
number of chirps per frame
- **uint16_t numDetObj**
Number of detected objects.
- **uint16_t numDetObjRaw**
Number of detected objects.
- **uint16_t minRange**
minimum range at which a target is detected (in xyzOutputQFormat precision).
- **uint16_t maxRange**
maximum range at which a target is detected (in xyzOutputQFormat precision).
- **uint16_t numActiveTrackers**
number of active trackers.
- **uint16_t maxNumObj2DRaw**
number of objects to be detected in 2D-CFAR.
- **uint16_t numRxAntennas**
Number of receive channels.
- **uint16_t numTxAntennas**
number of transmit antennas
- **uint16_t numVirtualAntennas**
number of virtual antennas
- **uint16_t numVirtualAntAzim**
number of virtual azimuth antennas
- **uint16_t numVirtualAntElev**
number of virtual elevation antennas
- **uint16_t numAngleBins**
number of angle bins
- **uint16_t numChirpsPerChirpType**
number of chirps per chirp type
- **uint16_t numDopplerBins**
number of doppler bins
- **uint16_t chirpCount**
chirp counter modulo number of chirps per frame
- **uint16_t txAntennaCount**
chirp counter modulo number of tx antennas
- **uint16_t dopplerBinCount**
chirp counter modulo number of Doppler bins
- **uint16_t chirpTypeCount**
chirp counter modulo number of subframe
- **uint16_t numAdcSamples**
number of ADC samples
- **uint16_t numRangeBins**
number of range bins
- **cmplx16ImRe_t azimuthModCoefsHalfBin**

- **cmplx16ImRe_t azimuthModCoefsThirdBin**

Half bin needed for doppler correction as part of Azimuth processing.
- **cmplx16ImRe_t azimuthModCoefsTwoThirdBin**

Half bin needed for doppler correction as part of Azimuth processing.
- float **rangeResolution**

range resolution in meters
- float **velResolution**

velocity resolution in meters/sec
- float **maxUnambiguousVel**

maximum unambiguous velocity (without algorithmic improvements) in meters/sec
- float **invOneQFormat**

inverse of the oneQformat
- float **invOneSinAzimFormat**

inverse of the oneQformat
- float **invNumAngleBins**

inverse of the numAngleBins
- **RangeDependantThresh_t SNRThresholds [MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS]**

SNR thresholds as a function of range.
- **RangeDependantThresh_t peakValThresholds [MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS]**

SNR thresholds as a function of range.
- **MmwDemo_1D_DopplerLines_t detDopplerLines**

Detected Doppler lines.
- **MmwDemo_detectedObjActual * detObj2D**

Detected objects after second pass in Range direction. These objects are send out as point clouds.
- **MmwDemo_objRaw1D_t * detObj1DRaw**

Detected objects after first pass in Doppler direction.
- **MmwDemo_objRaw2D_t * detObj2DRaw**

Detected objects before peak grouping.
- **MmwDemo_CfarCfg cfarCfgDoppler**

CFAR configuration in Doppler direction.
- **MmwDemo_CfarCfg cfarCfgRange**

CFAR configuration in Range direction.
- **MmwDemo_MultiObjBeamFormingCfg multiObjBeamFormingCfg**

Multi object beam forming configuration.
- **MmwDemo_CalibDcRangeSigCfg calibDcRangeSigCfg**

DC Range antenna signature calibration configuration.
- **MmwDemo_timingInfo_t timingInfo**

Timing information.
- **cycleLog_t cycleLog**

DSP cycles for chirp and interframe processing and pending on EDMA data transferes.
- **maxVelEnhStruct_t maxVelEnhStruct**

Max-velocity constants.
- **clusteringDBscanInstance_t dbScanInstance**

The dBscan clustering configuration structure.
- **clusteringDBscanOutput_t dbScanReport**

The dBscan clustering result structures (holds pointers to the result).
- **KFtrackerInstance_t trackerInstance**

Tracking configuration structure.
- **KFstate_t * trackerState**

- **float * trackerQvecList**
Process noise constants.
- **clusteringDBscanReport_t * dbScanState**
Pointer to dBScan output Report array.
- **MmwDemo_detectedObjForTx * detObjFinal**
Final list of detected object for transmission.
- **clusteringDBscanReportForTx * clusterOpFinal**
Final list of clusters for transmission.
- **trackingReportForTx * trackerOpFinal**
Final list of tracked objects for transmission.
- **uint16_t * parkingAssistBins**
Nearest object as a function of azimuth.
- **uint16_t * parkingAssistBinsState**
Filtered result of the nearest object as a function of azimuth.
- **uint16_t * parkingAssistBinsStateCnt**
The 'age' of the filtered result of the parking state. Added to make the 'smoother' visually.

3.9.1 Detailed Description

Millimeter Wave Demo Data Path Information.

The structure is used to hold all the relevant information for the data path.

Definition at line 444 of file dss_data_path.h.

3.9.2 Field Documentation

3.9.2.1 adcbufHandle

```
ADCBuf_Handle DSS_DataPathObj_t::adcbufHandle
```

ADCBUF handle.

Definition at line 447 of file dss_data_path.h.

3.9.2.2 ADCdataBuf

```
cmplx16ReIm_t* DSS_DataPathObj_t::ADCdataBuf
```

pointer to ADC buffer

Definition at line 459 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma().

3.9.2.3 adcDataIn

```
cmplx16ReIm_t* DSS_DataPathObj_t::adcDataIn
```

ADCBUF input samples in L2 scratch memory.

Definition at line 468 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma().

3.9.2.4 antInp

```
cmplx32ReIm_t* DSS_DataPathObj_t::antInp
```

input for Azimuth FFT

Definition at line 510 of file dss_data_path.h.

3.9.2.5 azimuthIn

```
cmplx32ReIm_t* DSS_DataPathObj_t::azimuthIn
```

input for Azimuth FFT

Definition at line 507 of file dss_data_path.h.

3.9.2.6 azimuthMagSqr

```
float* DSS_DataPathObj_t::azimuthMagSqr
```

output of Azimuth FFT magnitude squared

Definition at line 522 of file dss_data_path.h.

Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().

3.9.2.7 azimuthModCoefs

```
cmplx16ImRe_t* DSS_DataPathObj_t::azimuthModCoefs
```

Pointer to single point DFT coefficients used for Azimuth processing.

Definition at line 528 of file dss_data_path.h.

3.9.2.8 azimuthModCoefsHalfBin

```
cmplx16ImRe_t DSS_DataPathObj_t::azimuthModCoefsHalfBin
```

Half bin needed for doppler correction as part of Azimuth processing.

Definition at line 670 of file dss_data_path.h.

3.9.2.9 azimuthModCoefsThirdBin

```
cmplx16ImRe_t DSS_DataPathObj_t::azimuthModCoefsThirdBin
```

Half bin needed for doppler correction as part of Azimuth processing.

Definition at line 673 of file dss_data_path.h.

3.9.2.10 azimuthModCoefsTwoThirdBin

```
cmplx16ImRe_t DSS_DataPathObj_t::azimuthModCoefsTwoThirdBin
```

Half bin needed for doppler correction as part of Azimuth processing.

Definition at line 676 of file dss_data_path.h.

3.9.2.11 azimuthOut

```
cmplx32ReIm_t* DSS_DataPathObj_t::azimuthOut
```

output of Azimuth FFT

Definition at line 516 of file dss_data_path.h.

3.9.2.12 azimuthTwiddle32x32

```
cmplx32ReIm_t* DSS_DataPathObj_t::azimuthTwiddle32x32
```

twiddle factors table for Azimuth FFT

Definition at line 525 of file dss_data_path.h.

3.9.2.13 calibDcRangeSigCfg

```
MmwDemo_CalibDcRangeSigCfg DSS_DataPathObj_t::calibDcRangeSigCfg
```

DC Range antenna signature calibration configuration.

Definition at line 725 of file dss_data_path.h.

3.9.2.14 cfarCfgDoppler

```
MmwDemo_CfarCfg DSS_DataPathObj_t::cfarCfgDoppler
```

CFAR configuration in Doppler direction.

Definition at line 716 of file dss_data_path.h.

3.9.2.15 cfarCfgRange

```
MmwDemo_CfarCfg DSS_DataPathObj_t::cfarCfgRange
```

CFAR configuration in Range direction.

Definition at line 719 of file dss_data_path.h.

3.9.2.16 cfarCfgRange_minIdxToIgnoreHPF

```
uint16_t DSS_DataPathObj_t::cfarCfgRange_minIdxToIgnoreHPF
```

The HPF can mess up the noise floor computation. So for a certain number of indices, we only use the non-hpf sides.

Definition at line 598 of file dss_data_path.h.

3.9.2.17 cfarDetObjIndexBuf

```
uint16_t* DSS_DataPathObj_t::cfarDetObjIndexBuf
```

CFAR output objects index buffer.

Definition at line 501 of file dss_data_path.h.

3.9.2.18 cfarDetObjSNR

```
uint16_t* DSS_DataPathObj_t::cfarDetObjSNR
```

CFAR output objects' SNR buffer.

Definition at line 504 of file dss_data_path.h.

3.9.2.19 chirpCount

```
uint16_t DSS_DataPathObj_t::chirpCount
```

chirp counter modulo number of chirps per frame

Definition at line 652 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.20 chirpThreshold

```
uint8_t DSS_DataPathObj_t::chirpThreshold
```

Chirp Threshold configuration used for ADCBUF driver.

Definition at line 564 of file dss_data_path.h.

3.9.2.21 chirpTypeCount

```
uint16_t DSS_DataPathObj_t::chirpTypeCount
```

chirp counter modulo number of subframe

Definition at line 661 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.22 clusterOpFinal

```
clusteringDBscanReportForTx* DSS_DataPathObj_t::clusterOpFinal
```

Final list of clusters for transmission.

Definition at line 759 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.23 cycleLog

```
cycleLog_t DSS_DataPathObj_t::cycleLog
```

DSP cycles for chirp and interframe processing and pending on EDMA data transfers.

Definition at line 732 of file dss_data_path.h.

3.9.2.24 dbScanInstance

```
clusteringDBscanInstance_t DSS_DataPathObj_t::dbScanInstance
```

The dBscan clustering configuration structure.

Definition at line 738 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.25 dbscanOutputDataIndexArray

```
uint16_t* DSS_DataPathObj_t::dbscanOutputDataIndexArray
```

Pointer to dBScan index array.

Definition at line 546 of file dss_data_path.h.

3.9.2.26 dbscanOutputDataReport

```
clusteringDBscanReport_t* DSS_DataPathObj_t::dbscanOutputDataReport
```

Pointer to dBScan output Report array.

Definition at line 549 of file dss_data_path.h.

3.9.2.27 dbScanReport

```
clusteringDBscanOutput_t DSS_DataPathObj_t::dbScanReport
```

The dBscan clustering result structures (holds pointers to the result).

Definition at line 741 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.28 dBscanScratchPad

```
uint8_t* DSS_DataPathObj_t::dBscanScratchPad
```

Pointer to dBScan scratch pad.

Definition at line 543 of file dss_data_path.h.

3.9.2.29 dbScanState

```
clusteringDBscanReport_t* DSS_DataPathObj_t::dbScanState
```

Pointer to dBScan output Report array.

Definition at line 753 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.30 dcRangeSigCalibCntr

```
uint8_t DSS_DataPathObj_t::dcRangeSigCalibCntr
```

DC range signature calibration counter.

Definition at line 567 of file dss_data_path.h.

3.9.2.31 dcRangeSigMean

```
cplx32ImRe_t* DSS_DataPathObj_t::dcRangeSigMean
```

Pointer to DC range signature compensation buffer.

Definition at line 534 of file dss_data_path.h.

3.9.2.32 detDopplerLines

```
MmwDemo_1D_DopplerLines_t DSS_DataPathObj_t::detDopplerLines
```

Detected Doppler lines.

Definition at line 703 of file dss_data_path.h.

3.9.2.33 detMatrix

```
uint16_t* DSS_DataPathObj_t::detMatrix
```

Pointer to range/Doppler log2 magnitude detection matrix in L3 RAM.

Definition at line 540 of file dss_data_path.h.

3.9.2.34 detObj1DRaw

```
MmwDemo_objRaw1D_t* DSS_DataPathObj_t::detObj1DRaw
```

Detected objects after first pass in Doppler direction.

Definition at line 710 of file dss_data_path.h.

3.9.2.35 detObj2D

```
MmwDemo_detectedObjActual* DSS_DataPathObj_t::detObj2D
```

Detected objects after second pass in Range direction. These objects are send out as point clouds.

Definition at line 707 of file dss_data_path.h.

Referenced by clusteringDBscanRun(), MmwDemo_XYestimation(), MmwDemo_XYZestimation(), and populate←Outputs().

3.9.2.36 detObj2DRaw

```
MmwDemo_objRaw2D_t* DSS_DataPathObj_t::detObj2DRaw
```

Detected objects before peak grouping.

Definition at line 713 of file dss_data_path.h.

3.9.2.37 detObjFinal

```
MmwDemo_detectedObjForTx* DSS_DataPathObj_t::detObjFinal
```

Final list of detected object for transmission.

Definition at line 756 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.38 dopplerBinCount

```
uint16_t DSS_DataPathObj_t::dopplerBinCount
```

chirp counter modulo number of Doppler bins

Definition at line 658 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.39 dstPingPong

```
cplx16ReIm_t* DSS_DataPathObj_t::dstPingPong
```

ping pong buffer for 2D from radar Cube

Definition at line 480 of file dss_data_path.h.

3.9.2.40 EDMA_errorInfo

```
EDMA_errorInfo_t DSS_DataPathObj_t::EDMA_errorInfo
```

EDMA error Information when there are errors like missing events.

Definition at line 453 of file dss_data_path.h.

3.9.2.41 EDMA_transferControllerErrorInfo

```
EDMA_transferControllerErrorInfo_t DSS_DataPathObj_t::EDMA_transferControllerErrorInfo
```

EDMA transfer controller error information.

Definition at line 456 of file dss_data_path.h.

3.9.2.42 edmaHandle

```
EDMA_Handle DSS_DataPathObj_t::edmaHandle[2]
```

Handle of the EDMA driver.

Definition at line 450 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma(), MmwDemo_dataPathWait1DInputData(), MmwDemo_dataPathWait1DOOutputData(), MmwDemo_dataPathWait2DInputData(), MmwDemo_dataPathWait3DInputData(), MmwDemo_dataPathWaitTransDetMatrix(), MmwDemo_dataPathWaitTransDetMatrix2(), and MmwDemo_processChirp().

3.9.2.43 elevationIn

```
cmplx32ReIm_t* DSS_DataPathObj_t::elevationIn
```

input for Elevation FFT

Definition at line 513 of file dss_data_path.h.

3.9.2.44 elevationOut

```
cmplx32ReIm_t* DSS_DataPathObj_t::elevationOut
```

output of Elevation FFT

Definition at line 519 of file dss_data_path.h.

3.9.2.45 fftOut1D

```
cmplx16ReIm_t* DSS_DataPathObj_t::fftOut1D
```

1D FFT output

Definition at line 471 of file dss_data_path.h.

3.9.2.46 fftOut2D

```
cmplx32ReIm_t* DSS_DataPathObj_t::fftOut2D
```

2D FFT output

Definition at line 486 of file dss_data_path.h.

3.9.2.47 invNumAngleBins

```
float DSS_DataPathObj_t::invNumAngleBins
```

inverse of the numAngleBins

Definition at line 694 of file dss_data_path.h.

3.9.2.48 invOneQFormat

```
float DSS_DataPathObj_t::invOneQFormat
```

inverse of the oneQformat

Definition at line 688 of file dss_data_path.h.

3.9.2.49 invOneSinAzimFormat

```
float DSS_DataPathObj_t::invOneSinAzimFormat
```

inverse of the oneQformat

Definition at line 691 of file dss_data_path.h.

3.9.2.50 log2Abs

```
uint16_t* DSS_DataPathObj_t::log2Abs
```

log2 absolute computation output buffer

Definition at line 489 of file dss_data_path.h.

3.9.2.51 log2NumAvgChirps

```
uint8_t DSS_DataPathObj_t::log2NumAvgChirps
```

log2 of number of averaged chirps

Definition at line 570 of file dss_data_path.h.

3.9.2.52 log2NumDopplerBins

```
uint8_t DSS_DataPathObj_t::log2NumDopplerBins
```

log 2 of number of doppler bins

Definition at line 573 of file dss_data_path.h.

3.9.2.53 log2numVirtAnt

```
uint8_t DSS_DataPathObj_t::log2numVirtAnt
```

log2 of the number of virtual antennas.

Definition at line 582 of file dss_data_path.h.

3.9.2.54 maxNumObj2DRaw

```
uint16_t DSS_DataPathObj_t::maxNumObj2DRaw
```

number of objects to be detected in 2D-CFAR.

Definition at line 625 of file dss_data_path.h.

3.9.2.55 maxRange

```
uint16_t DSS_DataPathObj_t::maxRange
```

maximum range at which a target is detected (in xyzOutputQFormat precision).

Definition at line 619 of file dss_data_path.h.

3.9.2.56 maxUnambiguousVel

```
float DSS_DataPathObj_t::maxUnambiguousVel
```

maximum unambiguous velocity (without algorithmic improvements) in meters/sec

Definition at line 685 of file dss_data_path.h.

Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().

3.9.2.57 maxVelEnhStruct

```
maxVelEnhStruct_t DSS_DataPathObj_t::maxVelEnhStruct
```

Max-velocity constants.

Definition at line 735 of file dss_data_path.h.

3.9.2.58 minRange

```
uint16_t DSS_DataPathObj_t::minRange
```

minimum range at which a target is detected (in xyzOutputQFormat precision).

Definition at line 616 of file dss_data_path.h.

3.9.2.59 multiObjBeamFormingCfg

```
MmwDemo_MultiObjBeamFormingCfg DSS_DataPathObj_t::multiObjBeamFormingCfg
```

Multi object beam forming configuration.

Definition at line 722 of file dss_data_path.h.

Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().

3.9.2.60 numActiveTrackers

```
uint16_t DSS_DataPathObj_t::numActiveTrackers
```

number of active trackers.

Definition at line 622 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.61 numAdcSamples

```
uint16_t DSS_DataPathObj_t::numAdcSamples
```

number of ADC samples

Definition at line 664 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma().

3.9.2.62 numAngleBins

`uint16_t DSS_DataPathObj_t::numAngleBins`

number of angle bins

Definition at line 643 of file `dss_data_path.h`.

Referenced by `MmwDemo_XYestimation()`, and `MmwDemo_XYZestimation()`.

3.9.2.63 numChirpsPerChirpType

`uint16_t DSS_DataPathObj_t::numChirpsPerChirpType`

number of chirps per chirp type

Definition at line 646 of file `dss_data_path.h`.

3.9.2.64 numChirpsPerFrame

`uint16_t DSS_DataPathObj_t::numChirpsPerFrame`

number of chirps per frame

Definition at line 607 of file `dss_data_path.h`.

3.9.2.65 numDetObj

`uint16_t DSS_DataPathObj_t::numDetObj`

Number of detected objects.

Definition at line 610 of file `dss_data_path.h`.

Referenced by `MmwDemo_XYestimation()`, `MmwDemo_XYZestimation()`, and `populateOutputs()`.

3.9.2.66 numDetObjRaw

`uint16_t DSS_DataPathObj_t::numDetObjRaw`

Number of detected objects.

Definition at line 613 of file `dss_data_path.h`.

3.9.2.67 numDopplerBins

```
uint16_t DSS_DataPathObj_t::numDopplerBins
```

number of doppler bins

Definition at line 649 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.68 numRangeBins

```
uint16_t DSS_DataPathObj_t::numRangeBins
```

number of range bins

Definition at line 667 of file dss_data_path.h.

3.9.2.69 numRxAntennas

```
uint16_t DSS_DataPathObj_t::numRxAntennas
```

Number of receive channels.

Definition at line 628 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma(), and MmwDemo_processChirp().

3.9.2.70 numTxAntennas

```
uint16_t DSS_DataPathObj_t::numTxAntennas
```

number of transmit antennas

Definition at line 631 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.71 numVirtualAntAzim

```
uint16_t DSS_DataPathObj_t::numVirtualAntAzim
```

number of virtual azimuth antennas

Definition at line 637 of file dss_data_path.h.

3.9.2.72 numVirtualAntElev

```
uint16_t DSS_DataPathObj_t::numVirtualAntElev
```

number of virtual elevation antennas

Definition at line 640 of file dss_data_path.h.

3.9.2.73 numVirtualAntennas

```
uint16_t DSS_DataPathObj_t::numVirtualAntennas
```

number of virtual antennas

Definition at line 634 of file dss_data_path.h.

3.9.2.74 padding

```
uint8_t DSS_DataPathObj_t::padding
```

padding. .

Definition at line 594 of file dss_data_path.h.

3.9.2.75 parkingAssistBins

```
uint16_t* DSS_DataPathObj_t::parkingAssistBins
```

Nearest object as a function of azimuth.

Definition at line 765 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.76 parkingAssistBinsState

```
uint16_t* DSS_DataPathObj_t::parkingAssistBinsState
```

Filtered result of the nearest object as a function of azimuth.

Definition at line 768 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.77 parkingAssistBinsStateCnt

```
uint16_t* DSS_DataPathObj_t::parkingAssistBinsStateCnt
```

The 'age' of the filtered result of the parking state. Added to make the 'smoother' visually.

Definition at line 772 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.78 parkingAssistMaxRange

```
uint16_t DSS_DataPathObj_t::parkingAssistMaxRange
```

maximum range to look for obstacles. .

Definition at line 601 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.79 parkingAssistMinRange

```
uint16_t DSS_DataPathObj_t::parkingAssistMinRange
```

minimum range to look for obstacles. .

Definition at line 604 of file dss_data_path.h.

3.9.2.80 parkingAssistNumBins

```
uint8_t DSS_DataPathObj_t::parkingAssistNumBins
```

Number of bins for the parkingAssist module.

Definition at line 588 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.81 parkingAssistNumBinsLog2

```
uint8_t DSS_DataPathObj_t::parkingAssistNumBinsLog2
```

log2 of the number of bins for the parkingAssist module (used for scaling operations).

Definition at line 591 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.82 peakValThresholds

```
RangeDependantThresh_t DSS_DataPathObj_t::peakValThresholds[ MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS ]
```

SNR thresholds as a function of range.

Definition at line 700 of file dss_data_path.h.

3.9.2.83 processingPath

```
uint8_t DSS_DataPathObj_t::processingPath
```

Processing path - either point-cloud or max-vel enhancement.

Definition at line 561 of file dss_data_path.h.

Referenced by MmwDemo_dataPathConfigEdma(), MmwDemo_processChirp(), MmwDemo_XYestimation(), MmwDemo_XYZestimation(), populateOutputs(), and rxGainPhaseParam_Init().

3.9.2.84 radarCube

```
cplx16ReIm_t* DSS_DataPathObj_t::radarCube
```

Pointer to Radar Cube memory in L3 RAM.

Definition at line 537 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.85 rangeResolution

float DSS_DataPathObj_t::rangeResolution

range resolution in meters

Definition at line 679 of file dss_data_path.h.

3.9.2.86 rxChPhaseComp

cplx16ImRe_t* DSS_DataPathObj_t::rxChPhaseComp

Pointer to the Rx Gain phase compensation params.

Definition at line 531 of file dss_data_path.h.

Referenced by rxGainPhaseParam_Init().

3.9.2.87 sinAzimQFormat

uint8_t DSS_DataPathObj_t::sinAzimQFormat

Q format of the sin of the azimuth.

Definition at line 585 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.88 SNRThresholds

RangeDependantThresh_t DSS_DataPathObj_t::SNRThresholds[MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS]

SNR thresholds as a function of range.

Definition at line 697 of file dss_data_path.h.

3.9.2.89 subframeIdx

uint8_t DSS_DataPathObj_t::subframeIdx

index of the subframe to which this object belongs

Definition at line 579 of file dss_data_path.h.

3.9.2.90 sumAbs

```
uint16_t* DSS_DataPathObj_t::sumAbs  
  
accumulated sum of log2 absolute over the antennae  
  
Definition at line 492 of file dss_data_path.h.
```

3.9.2.91 sumAbsRange

```
uint16_t* DSS_DataPathObj_t::sumAbsRange  
  
input buffer for CFAR processing from the detection matrix  
  
Definition at line 498 of file dss_data_path.h.
```

3.9.2.92 sumAbsSlowChirp

```
uint16_t* DSS_DataPathObj_t::sumAbsSlowChirp  
  
accumulated sum of log2 absolute over the antennae  
  
Definition at line 495 of file dss_data_path.h.
```

3.9.2.93 timingInfo

```
MmwDemo_timingInfo_t DSS_DataPathObj_t::timingInfo  
  
Timing information.  
  
Definition at line 728 of file dss_data_path.h.
```

3.9.2.94 trackerInstance

```
KFtrackerInstance_t DSS_DataPathObj_t::trackerInstance  
  
Tracking configuration structure.  
  
Definition at line 744 of file dss_data_path.h.  
  
Referenced by populateOutputs().
```

3.9.2.95 trackerOpFinal

```
trackingReportForTx* DSS_DataPathObj_t::trackerOpFinal
```

Final list of tracked objects for transmission.

Definition at line 762 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.96 trackerQvecList

```
float* DSS_DataPathObj_t::trackerQvecList
```

Process noise constants.

Definition at line 750 of file dss_data_path.h.

3.9.2.97 trackerScratchPadFlt

```
float* DSS_DataPathObj_t::trackerScratchPadFlt
```

Pointer to tracker scratch pad for floats.

Definition at line 555 of file dss_data_path.h.

3.9.2.98 trackerScratchPadShort

```
int16_t* DSS_DataPathObj_t::trackerScratchPadShort
```

Pointer to tracker scratch pad for int16_t.

Definition at line 558 of file dss_data_path.h.

3.9.2.99 trackerState

```
KFstate_t* DSS_DataPathObj_t::trackerState
```

tracking state.

Definition at line 747 of file dss_data_path.h.

Referenced by populateOutputs().

3.9.2.100 trackingInput

```
trackingInputReport_t* DSS_DataPathObj_t::trackingInput
```

Pointer to dBScan output used as input to the tracker.

Definition at line 552 of file dss_data_path.h.

3.9.2.101 twiddle16x16_1D

```
cplx16ReIm_t* DSS_DataPathObj_t::twiddle16x16_1D
```

twiddle table for 1D FFT

Definition at line 462 of file dss_data_path.h.

3.9.2.102 twiddle32x32_2D

```
cplx32ReIm_t* DSS_DataPathObj_t::twiddle32x32_2D
```

twiddle table for 2D FFT

Definition at line 474 of file dss_data_path.h.

3.9.2.103 txAntennaCount

```
uint16_t DSS_DataPathObj_t::txAntennaCount
```

chirp counter modulo number of tx antennas

Definition at line 655 of file dss_data_path.h.

Referenced by MmwDemo_processChirp().

3.9.2.104 velResolution

```
float DSS_DataPathObj_t::velResolution
```

velocity resolution in meters/sec

Definition at line 682 of file dss_data_path.h.

3.9.2.105 window1D

```
int16_t* DSS_DataPathObj_t::window1D
```

window coefficients for 1D FFT

Definition at line 465 of file dss_data_path.h.

3.9.2.106 window2D

```
int32_t* DSS_DataPathObj_t::window2D
```

window coefficients for 2D FFT

Definition at line 477 of file dss_data_path.h.

3.9.2.107 windowingBuf2D

```
cmplx32ReIm_t* DSS_DataPathObj_t::windowingBuf2D
```

window output for 2D FFT

Definition at line 483 of file dss_data_path.h.

3.9.2.108 xyzOutputQFormat

```
uint8_t DSS_DataPathObj_t::xyzOutputQFormat
```

Q format of the output x/y/z coordinates.

Definition at line 576 of file dss_data_path.h.

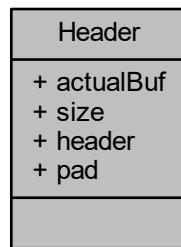
Referenced by MmwDemo_XYestimation(), MmwDemo_XYZestimation(), and populateOutputs().

The documentation for this struct was generated from the following file:

- [dss_data_path.h](#)

3.10 Header Union Reference

Collaboration diagram for Header:



Data Fields

- struct {
 Ptr **actualBuf**
 SizeT **size**
} **header**
- UArg **pad** [2]

3.10.1 Detailed Description

Definition at line 2789 of file dss_mrr_pe674.c.

3.10.2 Field Documentation

3.10.2.1 **actualBuf**

Ptr Header::actualBuf

Definition at line 2791 of file dss_mrr_pe674.c.

Referenced by free(), memalign(), and ti_sysbios_rts_MemAlloc_alloc().

3.10.2.2 header

```
struct { ... } Header::header
```

Referenced by free(), memalign(), realloc(), and ti_sysbios_rts_MemAlloc_alloc().

3.10.2.3 pad

```
UArg Header::pad[2]
```

Definition at line 2794 of file dss_mrr_pe674.c.

3.10.2.4 size

```
SizeT Header::size
```

Definition at line 2792 of file dss_mrr_pe674.c.

Referenced by free(), memalign(), realloc(), and ti_sysbios_rts_MemAlloc_alloc().

The documentation for this union was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.11 KFstate Struct Reference

Kalman filter state.

```
#include <dss_data_path.h>
```

Collaboration diagram for KFstate:



Data Fields

- `uint8_t age`
- `uint8_t tick`
- `uint16_t validity`
- `int16_t xSize`
- `int16_t ySize`
- `float vec [N_STATES]`
- `float covmat [N_UNIQ_ELEM_IN_SYM_COVMAT]`

3.11.1 Detailed Description

Kalman filter state.

Definition at line 423 of file dss_data_path.h.

3.11.2 Field Documentation

3.11.2.1 age

```
uint8_t KFstate::age
```

Definition at line 425 of file dss_data_path.h.

3.11.2.2 covmat

```
float KFstate::covmat[ N_UNIQ_ELEM_IN_SYM_COVMAT]
```

Definition at line 431 of file dss_data_path.h.

3.11.2.3 tick

```
uint8_t KFstate::tick
```

Definition at line 426 of file dss_data_path.h.

Referenced by `arrangeTracksByAge()`.

3.11.2.4 validity

```
uint16_t KFstate::validity
```

Definition at line 427 of file dss_data_path.h.

Referenced by arrangeTracksByAge().

3.11.2.5 vec

```
float KFstate::vec[ N_STATES ]
```

Definition at line 430 of file dss_data_path.h.

Referenced by populateOutputs().

3.11.2.6 xSize

```
int16_t KFstate::xSize
```

Definition at line 428 of file dss_data_path.h.

Referenced by populateOutputs().

3.11.2.7 ySize

```
int16_t KFstate::ySize
```

Definition at line 429 of file dss_data_path.h.

Referenced by populateOutputs().

The documentation for this struct was generated from the following file:

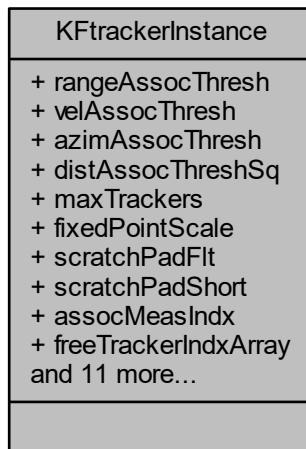
- [dss_data_path.h](#)

3.12 KFtrackerInstance Struct Reference

Struct for the tracker configuration, and pointers to scratch buffers.

```
#include <dss_data_path.h>
```

Collaboration diagram for KFtrackerInstance:



Data Fields

- float **rangeAssocThresh**
- float **velAssocThresh**
- float **azimAssocThresh**
- float **distAssocThreshSq**
- uint16_t **maxTrackers**
- uint16_t **fixedPointScale**
- float * **scratchPadFlt**
- int16_t * **scratchPadShort**
- int16_t * **assocMeasIdx**
- int16_t * **freeTrackerIdxArray**
- int16_t * **selectedIdxArr**
- float * **stateVecRrd**
- float * **residCovmat**
- float * **invResidCovmat**
- float * **hMat**
- float * **kalmanGain**
- float * **covmattmp**
- float * **kalmanGainTemp**
- float * **stateCovMattemp**
- float * **stateCovMattempP**
- float * **measResidual**

3.12.1 Detailed Description

Struct for the tracker configuration, and pointers to scratch buffers.

Definition at line 392 of file dss_data_path.h.

3.12.2 Field Documentation

3.12.2.1 assocMeasIdx

```
int16_t* KFtrackerInstance::assocMeasIdx
```

Definition at line 404 of file dss_data_path.h.

Referenced by ekfRun().

3.12.2.2 azimAssocThresh

```
float KFtrackerInstance::azimAssocThresh
```

Definition at line 396 of file dss_data_path.h.

3.12.2.3 covmattmp

```
float* KFtrackerInstance::covmattmp
```

Definition at line 412 of file dss_data_path.h.

3.12.2.4 distAssocThreshSq

```
float KFtrackerInstance::distAssocThreshSq
```

Definition at line 397 of file dss_data_path.h.

3.12.2.5 **fixedPointScale**

```
uint16_t KFtrackerInstance::fixedPointScale
```

Definition at line 399 of file dss_data_path.h.

3.12.2.6 **freeTrackerIdxArray**

```
int16_t* KFtrackerInstance::freeTrackerIdxArray
```

Definition at line 405 of file dss_data_path.h.

3.12.2.7 **hMat**

```
float* KFtrackerInstance::hMat
```

Definition at line 410 of file dss_data_path.h.

3.12.2.8 **invResidCovmat**

```
float* KFtrackerInstance::invResidCovmat
```

Definition at line 409 of file dss_data_path.h.

3.12.2.9 **kalmanGain**

```
float* KFtrackerInstance::kalmanGain
```

Definition at line 411 of file dss_data_path.h.

3.12.2.10 **kalmanGainTemp**

```
float* KFtrackerInstance::kalmanGainTemp
```

Definition at line 413 of file dss_data_path.h.

Referenced by kalmanGainComputation().

3.12.2.11 maxTrackers

```
uint16_t KFtrackerInstance::maxTrackers
```

Definition at line 398 of file dss_data_path.h.

Referenced by populateOutputs().

3.12.2.12 measResidual

```
float* KFtrackerInstance::measResidual
```

Definition at line 416 of file dss_data_path.h.

3.12.2.13 rangeAssocThresh

```
float KFtrackerInstance::rangeAssocThresh
```

Definition at line 394 of file dss_data_path.h.

3.12.2.14 residCovmat

```
float* KFtrackerInstance::residCovmat
```

Definition at line 408 of file dss_data_path.h.

3.12.2.15 scratchPadFlt

```
float* KFtrackerInstance::scratchPadFlt
```

Definition at line 402 of file dss_data_path.h.

Referenced by ekfInit().

3.12.2.16 scratchPadShort

```
int16_t* KFtrackerInstance::scratchPadShort
```

Definition at line 403 of file dss_data_path.h.

3.12.2.17 selectedIdxArr

```
int16_t* KFtrackerInstance::selectedIdxArr
```

Definition at line 406 of file dss_data_path.h.

3.12.2.18 stateCovMattemp

```
float* KFtrackerInstance::stateCovMattemp
```

Definition at line 414 of file dss_data_path.h.

Referenced by stateCovmatMeasurementUpdate().

3.12.2.19 stateCovMattempP

```
float* KFtrackerInstance::stateCovMattempP
```

Definition at line 415 of file dss_data_path.h.

Referenced by stateCovmatMeasurementUpdate().

3.12.2.20 stateVecRrd

```
float* KFtrackerInstance::stateVecRrd
```

Definition at line 407 of file dss_data_path.h.

3.12.2.21 velAssocThresh

```
float KFtrackerInstance::velAssocThresh
```

Definition at line 395 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

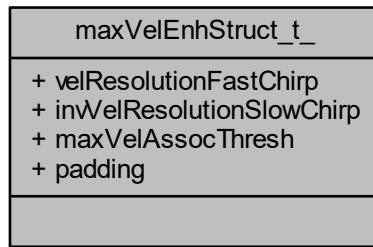
- [dss_data_path.h](#)

3.13 maxVelEnhStruct_t_ Struct Reference

pre-computed parameters of the max-velocity-enhancement.

```
#include <dss_data_path.h>
```

Collaboration diagram for maxVelEnhStruct_t_ :



Data Fields

- float **velResolutionFastChirp**
- float **invVelResolutionSlowChirp**
- uint16_t **maxVelAssocThresh**
- uint16_t **padding**

3.13.1 Detailed Description

pre-computed parameters of the max-velocity-enhancement.

Definition at line 109 of file dss_data_path.h.

3.13.2 Field Documentation

3.13.2.1 invVelResolutionSlowChirp

```
float maxVelEnhStruct_t_::invVelResolutionSlowChirp
```

Definition at line 112 of file dss_data_path.h.

3.13.2.2 maxVelAssocThresh

```
uint16_t maxVelEnhStruct_t_::maxVelAssocThresh
```

Definition at line 113 of file dss_data_path.h.

3.13.2.3 padding

```
uint16_t maxVelEnhStruct_t_::padding
```

Definition at line 114 of file dss_data_path.h.

3.13.2.4 velResolutionFastChirp

```
float maxVelEnhStruct_t_::velResolutionFastChirp
```

Definition at line 111 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

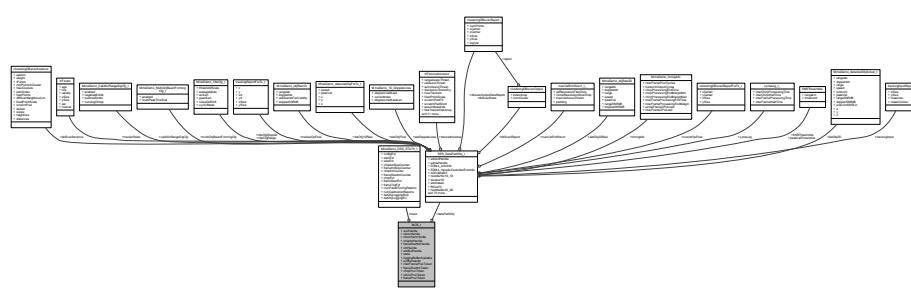
- [dss_data_path.h](#)

3.14 MCB_t Struct Reference

DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design.

```
#include <mmWave_XSS.h>
```

Collaboration diagram for MCB_t:



Data Fields

- SOC_Handle **socHandle**
! Handle to the SOC Module
- Mbox_Handle **mboxHandle**
! Handle to the peer Mailbox used to exchange messages between the MSS and DSS
- Semaphore_Handle **mboxSemHandle**
! Handle to the peer Mailbox used to exchange messages between the MSS and DSS
- SOC_SysIntListenerHandle **chirpIntHandle**
! Semaphore handle for the mailbox communication
- SOC_SysIntListenerHandle **frameStartIntHandle**
! Handle to the SOC chirp interrupt listener Handle
- MMWave_Handle **ctrlHandle**
! Handle to the SOC frame interrupt listener Handle
- ADCBuf_Handle **adcBufHandle**
mmWave control handle use to initialize the link infrastructure, which allows communication between the MSS and BSS
- **MmwDemo_DSS_STATE state**
! Handle to the ADCBUF Driver
- **MmwDemo_DSS_STATS stats**
! mmw Demo state
- **DSS_DataPathObj dataPathObj [NUM_SUBFRAMES]**
! mmw Demo statistics
- uint8_t **loggingBufferAvailable**
! Data Path object
- uint8_t **subframeIdx**
! Logging buffer flag
- uint8_t **interFrameProcToken**
! Subframe index
- uint8_t **frameStartIntToken**
! inter frameProc token
- uint8_t **chirpProcToken**
! frame start token
- uint8_t **mboxProcToken**
! chirpProc token
- uint8_t **frameProcToken**
! 'mailbox has a message' token

3.14.1 Detailed Description

DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design.

Definition at line 210 of file mmWave_XSS.h.

3.14.2 Field Documentation

3.14.2.1 adcBufHandle

ADCBuf_Handle MCB_t::adcBufHandle
mmWave control handle use to initialize the link infrastructure, which allows communication between the MSS and BSS

Definition at line 224 of file mmWave_XSS.h.

3.14.2.2 chirpIntHandle

```
SOC_SysIntListenerHandle MCB_t::chirpIntHandle  
! Semaphore handle for the mailbox communication  
Definition at line 216 of file mmWave_XSS.h.
```

3.14.2.3 chirpProcToken

```
uint8_t MCB_t::chirpProcToken  
! frame start token  
Definition at line 232 of file mmWave_XSS.h.
```

3.14.2.4 ctrlHandle

```
MMWave_Handle MCB_t::ctrlHandle  
! Handle to the SOC frame interrupt listener Handle  
Definition at line 218 of file mmWave_XSS.h.
```

3.14.2.5 dataPathObj

```
DSS_DataPathObj MCB_t::dataPathObj[ NUM_SUBFRAMES ]  
! mmmw Demo statistics  
Definition at line 227 of file mmWave_XSS.h.
```

3.14.2.6 frameProcToken

```
uint8_t MCB_t::frameProcToken  
! 'mailbox has a message' token  
Definition at line 234 of file mmWave_XSS.h.
```

3.14.2.7 frameStartIntHandle

```
SOC_SysIntListenerHandle MCB_t::frameStartIntHandle  
! Handle to the SOC chirp interrupt listener Handle  
Definition at line 217 of file mmWave_XSS.h.
```

3.14.2.8 frameStartIntToken

```
uint8_t MCB_t::frameStartIntToken  
! inter frameProc token  
Definition at line 231 of file mmWave_XSS.h.
```

3.14.2.9 interFrameProcToken

```
uint8_t MCB_t::interFrameProcToken  
! Subframe index  
Definition at line 230 of file mmWave_XSS.h.
```

3.14.2.10 loggingBufferAvailable

```
uint8_t MCB_t::loggingBufferAvailable  
! Data Path object
```

Definition at line 228 of file mmWave_XSS.h.

3.14.2.11 mboxHandle

Mbox_Handle MCB_t::mboxHandle

! Handle to the SOC Module

Definition at line 214 of file mmWave_XSS.h.

3.14.2.12 mboxProcToken

uint8_t MCB_t::mboxProcToken

! chirpProc token

Definition at line 233 of file mmWave_XSS.h.

3.14.2.13 mboxSemHandle

Semaphore_Handle MCB_t::mboxSemHandle

! Handle to the peer Mailbox used to exchange messages between the MSS and DSS

Definition at line 215 of file mmWave_XSS.h.

3.14.2.14 socHandle

SOC_Handle MCB_t::socHandle

Definition at line 213 of file mmWave_XSS.h.

3.14.2.15 state

MmwDemo_DSS_STATE MCB_t::state

! Handle to the ADCBUF Driver

Definition at line 225 of file mmWave_XSS.h.

3.14.2.16 stats

MmwDemo_DSS_STATS MCB_t::stats

! mmw Demo state

Definition at line 226 of file mmWave_XSS.h.

3.14.2.17 subframeIdx

uint8_t MCB_t::subframeIdx

! Logging buffer flag

Definition at line 229 of file mmWave_XSS.h.

The documentation for this struct was generated from the following file:

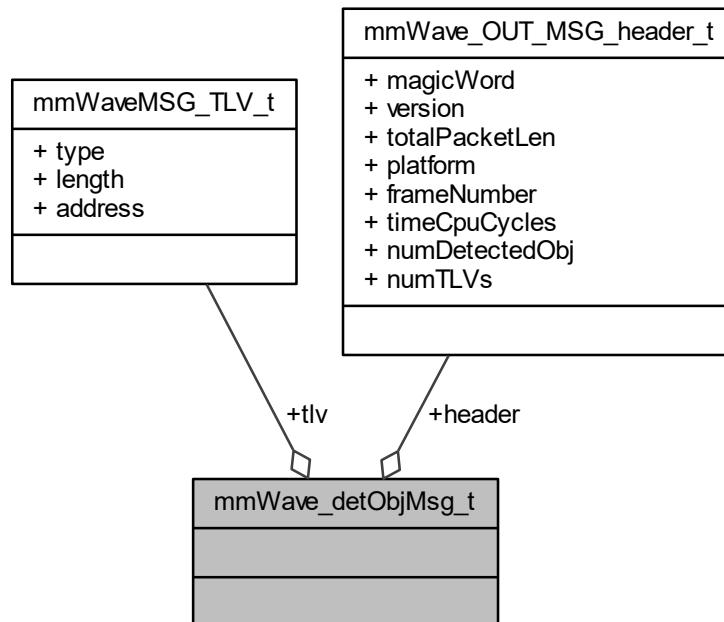
- common/ **mmWave_XSS.h**

3.15 mmWave_detObjMsg_t Struct Reference

The structure defines the message body for reporting detection information from data path to MSS.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWave_detObjMsg_t:



Data Fields

- **mmWave_OUT_MSG_header header**
Header (p. ??) of the detection information message.
- **mmWaveMSG_TLV tlv [OUTPUT_MSG_MAX]**
TLVs of the detection information.

3.15.1 Detailed Description

The structure defines the message body for reporting detection information from data path to MSS.
 Definition at line 194 of file mmw_messages.h.

3.15.2 Field Documentation

3.15.2.1 header

mmWave_OUT_MSG_header mmWave_detObjMsg_t::header
Header (p. ??) of the detection information message.
 Definition at line 195 of file mmw_messages.h.

3.15.2.2 tlv

mmWaveMSG_TLV mmWave_detObjMsg_t::tlv [OUTPUT_MSG_MAX]
 TLVs of the detection information.
 Definition at line 196 of file mmw_messages.h.

The documentation for this struct was generated from the following file:

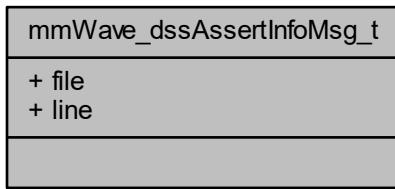
- common/ **mmw_messages.h**

3.16 mmWave_dssAssertInfoMsg_t Struct Reference

The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWave_dssAssertInfoMsg_t:



Data Fields

- char **file** [MMWAVE_MAX_FILE_NAME_SIZE]
file name
- uint32_t **line**
line number

3.16.1 Detailed Description

The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information.

Definition at line 207 of file mmw_messages.h.

3.16.2 Field Documentation

3.16.2.1 file

```
char mmWave_dssAssertInfoMsg_t::file[ MMWAVE_MAX_FILE_NAME_SIZE]
file name
```

Definition at line 208 of file mmw_messages.h.

3.16.2.2 line

```
uint32_t mmWave_dssAssertInfoMsg_t::line
line number
```

Definition at line 209 of file mmw_messages.h.

The documentation for this struct was generated from the following file:

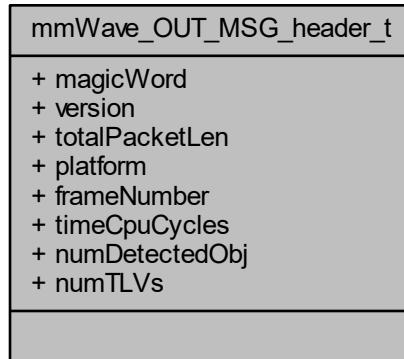
- common/ **mmw_messages.h**

3.17 mmWave_OUT_MSG_header_t Struct Reference

The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWave_OUT_MSG_header_t:



Data Fields

- **uint16_t magicWord [4]**
Output buffer magic word (sync word). It is initialized to {0x0102,0x0304,0x0506,0x0708}.
- **uint32_t version**
*Version: : MajorNum * 2^24 + MinorNum * 2^16 + BugfixNum * 2^8 + BuildNum*
- **uint32_t totalPacketLen**
Total packet length including header in Bytes.
- **uint32_t platform**
platform type
- **uint32_t frameNumber**
Frame number.
- **uint32_t timeCpuCycles**
Time in CPU cycles when the message was created. For XWR16xx/XWR18xx: DSP CPU cycles, for XWR14xx: R4F CPU cycles.
- **uint32_t numDetectedObj**
Number of detected objects.
- **uint32_t numTLVs**
Number of TLVs.

3.17.1 Detailed Description

The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS.

Definition at line 53 of file mmw_messages.h.

3.17.2 Field Documentation

3.17.2.1 frameNumber

```
uint32_t mmWave_OUT_MSG_header_t::frameNumber
```

Frame number.

Definition at line 59 of file mmw_messages.h.

3.17.2.2 magicWord

```
uint16_t mmWave_OUT_MSG_header_t::magicWord[4]
```

Output buffer magic word (sync word). It is initialized to {0x0102,0x0304,0x0506,0x0708}.

Definition at line 55 of file mmw_messages.h.

3.17.2.3 numDetectedObj

```
uint32_t mmWave_OUT_MSG_header_t::numDetectedObj
```

Number of detected objects.

Definition at line 61 of file mmw_messages.h.

3.17.2.4 numTLVs

```
uint32_t mmWave_OUT_MSG_header_t::numTLVs
```

Number of TLVs.

Definition at line 62 of file mmw_messages.h.

3.17.2.5 platform

```
uint32_t mmWave_OUT_MSG_header_t::platform
```

platform type

Definition at line 58 of file mmw_messages.h.

3.17.2.6 timeCpuCycles

```
uint32_t mmWave_OUT_MSG_header_t::timeCpuCycles
```

Time in CPU cycles when the message was created. For XWR16xx/XWR18xx: DSP CPU cycles, for XWR14xx: R4F CPU cycles.

Definition at line 60 of file mmw_messages.h.

3.17.2.7 totalPacketLen

```
uint32_t mmWave_OUT_MSG_header_t::totalPacketLen
```

Total packet length including header in Bytes.

Definition at line 57 of file mmw_messages.h.

3.17.2.8 version

```
uint32_t mmWave_OUT_MSG_header_t::version
```

Version: : MajorNum * 2^24 + MinorNum * 2^16 + BugfixNum * 2^8 + BuildNum

Definition at line 56 of file mmw_messages.h.

The documentation for this struct was generated from the following file:

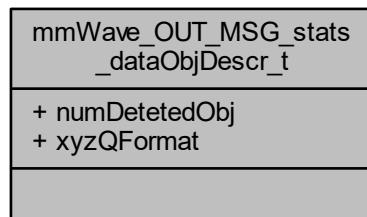
- common/ **mmw_messages.h**

3.18 mmWave_OUT_MSG_stats_dataObjDescr_t Struct Reference

Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWave_OUT_MSG_stats_dataObjDescr_t:



Data Fields

- `uint16_t numDetetedObj`
Number of detected objects.
- `uint16_t xyzQFormat`
Q format of detected objects x/y/z coordinates.

3.18.1 Detailed Description

Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS.

Definition at line 76 of file mmw_messages.h.

3.18.2 Field Documentation

3.18.2.1 numDetetedObj

```
uint16_t mmWave_OUT_MSG_stats_dataObjDescr_t::numDetetedObj
Number of detected objects.
```

Definition at line 78 of file mmw_messages.h.

3.18.2.2 xyzQFormat

```
uint16_t mmWave_OUT_MSG_stats_dataObjDescr_t::xyzQFormat
Q format of detected objects x/y/z coordinates.
```

Definition at line 79 of file mmw_messages.h.

The documentation for this struct was generated from the following file:

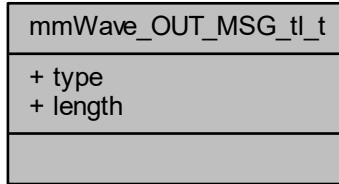
- common/ `mmw_messages.h`

3.19 mmWave_OUT_MSG_tl_t Struct Reference

The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWave_OUT_MSG_tl_t:



Data Fields

- `uint32_t type`

TLV type.

- `uint32_t length`

Length in bytes.

3.19.1 Detailed Description

The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS.

Definition at line 90 of file mmw_messages.h.

3.19.2 Field Documentation

3.19.2.1 length

```
uint32_t mmWave_OUT_MSG_tl_t::length
```

Length in bytes.

Definition at line 93 of file mmw_messages.h.

3.19.2.2 type

```
uint32_t mmWave_OUT_MSG_tl_t::type
```

TLV type.

Definition at line 92 of file mmw_messages.h.

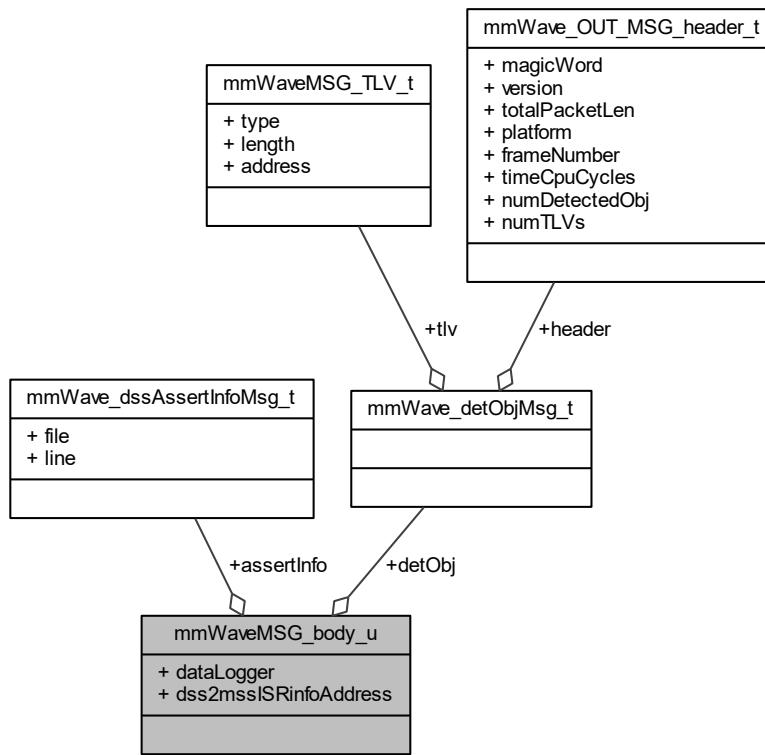
The documentation for this struct was generated from the following file:

- common/ **mmw_messages.h**

3.20 mmWaveMSG_body_u Union Reference

The union defines the message body for various configuration messages. For passing configuration from MSS to DSS.

```
#include <mmw_messages.h>
Collaboration diagram for mmWaveMSG_body_u:
```



Data Fields

- **mmWave_detInfoMsg detObj**
Detection Information message.
- **uint8_t dataLogger**
Datapath output logger setting.
- **mmWave_dssAssertInfoMsg assertInfo**
DSS assertion information.
- **uint32_t dss2mssiSRinfoAddress**
Address of DSS to MSS ISR information storage, typically in HSRAM.

3.20.1 Detailed Description

The union defines the message body for various configuration messages. For passing configuration from MSS to DSS.

Definition at line 217 of file `mmw_messages.h`.

3.20.2 Field Documentation

3.20.2.1 assertInfo

mmWave_dssAssertInfoMsg mmWaveMSG_body_u::assertInfo
DSS assertion information.
Definition at line 221 of file mmw_messages.h.

3.20.2.2 dataLogger

uint8_t mmWaveMSG_body_u::dataLogger
Datapath output logger setting.
Definition at line 220 of file mmw_messages.h.

3.20.2.3 detObj

mmWave_detInfoMsg mmWaveMSG_body_u::detObj
Detection Information message.
Definition at line 219 of file mmw_messages.h.

3.20.2.4 dss2mssiSRinfoAddress

uint32_t mmWaveMSG_body_u::dss2mssiSRinfoAddress
Address of DSS to MSS ISR information storage, typically in HSRAM.
Definition at line 222 of file mmw_messages.h.
The documentation for this union was generated from the following file:

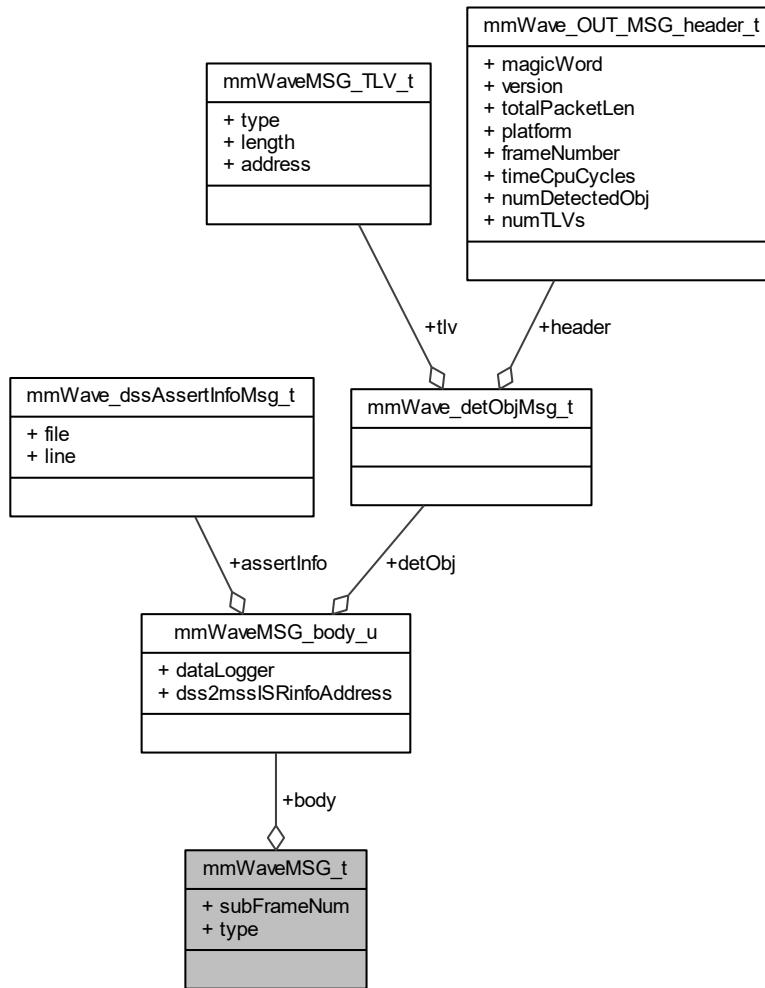
- common/ **mmw_messages.h**

3.21 mmWaveMSG_t Struct Reference

The structure defines the message structure used for communication between MSS and DSS.

```
#include <mmw_messages.h>
```

Collaboration diagram for mmWaveMSG_t:



Data Fields

- `int8_t subFrameNum`
Subframe number for which this message is applicable.
- `mbox_message_type type`
message type
- `mmWaveMSG_body body`
message body

3.21.1 Detailed Description

The structure defines the message structure used for communication between MSS and DSS.
Definition at line 234 of file `mmw_messages.h`.

3.21.2 Field Documentation

3.21.2.1 body

mmWaveMSG_body mmWaveMSG_t::body
 message body
 Definition at line 238 of file mmw_messages.h.

3.21.2.2 subFrameNum

int8_t mmWaveMSG_t::subFrameNum
 Subframe number for which this message is applicable.
 Definition at line 236 of file mmw_messages.h.

3.21.2.3 type

mbox_message_type mmWaveMSG_t::type
 message type
 Definition at line 237 of file mmw_messages.h.
 The documentation for this struct was generated from the following file:

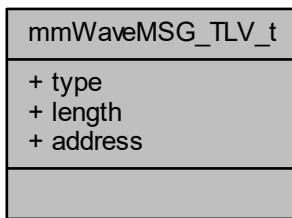
- common/ **mmw_messages.h**

3.22 mmWaveMSG_TLV_t Struct Reference

The structure describes the TLV part of the message from DSS to MSS on data path detection information.

#include <mmw_messages.h>

Collaboration diagram for mmWaveMSG_TLV_t:



Data Fields

- uint32_t **type**
Payload type.
- uint32_t **length**
Length in bytes.
- uint32_t **address**
Address of the payload.

3.22.1 Detailed Description

The structure describes the TLV part of the message from DSS to MSS on data path detection information.
 Definition at line 183 of file mmw_messages.h.

3.22.2 Field Documentation

3.22.2.1 address

`uint32_t mmWaveMSG_TLV_t::address`

Address of the payload.

Definition at line 186 of file `mmw_messages.h`.

3.22.2.2 length

`uint32_t mmWaveMSG_TLV_t::length`

Length in bytes.

Definition at line 185 of file `mmw_messages.h`.

3.22.2.3 type

`uint32_t mmWaveMSG_TLV_t::type`

Payload type.

Definition at line 184 of file `mmw_messages.h`.

The documentation for this struct was generated from the following file:

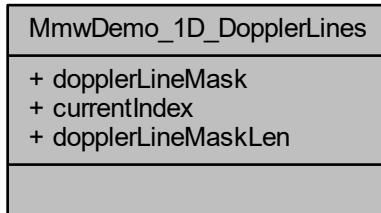
- common/ `mmw_messages.h`

3.23 MmwDemo_1D_DopplerLines Struct Reference

Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction.

```
#include <dss_data_path.h>
```

Collaboration diagram for `MmwDemo_1D_DopplerLines`:



Data Fields

- `uint32_t * dopplerLineMask`

*Doppler line bit mask of active (CFAR detected) Doppler bins in the first round of CFAR detections in Doppler direction.
The LSB bit of the first word corresponds to Doppler bin zero of the range/Doppler detection matrix.*

- `uint16_t currentIndex`

starting index for the search for next active Doppler line

- `uint16_t dopplerLineMaskLen`

size of dopplerLineMask array, (number of 32_bit words, for example for Doppler FFT size of 64 this length is equal to 2)

3.23.1 Detailed Description

Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction.

Definition at line 238 of file dss_data_path.h.

3.23.2 Field Documentation

3.23.2.1 currentIndex

```
uint16_t MmwDemo_1D_DopplerLines::currentIndex  
starting index for the search for next active Doppler line
```

Definition at line 246 of file dss_data_path.h.

Referenced by MmwDemo_getDopplerLine(), and MmwDemo_resetDopplerLines().

3.23.2.2 dopplerLineMask

```
uint32_t* MmwDemo_1D_DopplerLines::dopplerLineMask  
Doppler line bit mask of active (CFAR detected) Doppler bins in the first round of CFAR detections in Doppler direction. The LSB bit of the first word corresponds to Doppler bin zero of the range/Doppler detection matrix.
```

Definition at line 240 of file dss_data_path.h.

Referenced by MmwDemo_getDopplerLine(), MmwDemo_isSetDopplerLine(), MmwDemo_resetDopplerLines(), and MmwDemo_setDopplerLine().

3.23.2.3 dopplerLineMaskLen

```
uint16_t MmwDemo_1D_DopplerLines::dopplerLineMaskLen  
size of dopplerLineMask array, (number of 32_bit words, for example for Doppler FFT size of 64 this length is equal to 2)
```

Definition at line 248 of file dss_data_path.h.

Referenced by MmwDemo_resetDopplerLines().

The documentation for this struct was generated from the following file:

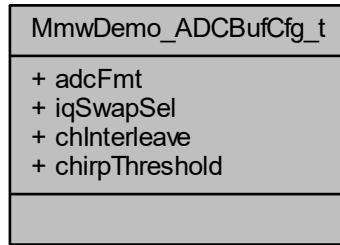
- **dss_data_path.h**

3.24 MmwDemo_ADCBufCfg_t Struct Reference

ADCBUF configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_ADCBufCfg_t:



Data Fields

- uint8_t **adcFmt**
- uint8_t **iqSwapSel**
- uint8_t **chInterleave**
- uint8_t **chirpThreshold**

Chirp Threshold configuration used for ADCBUF buffer.

3.24.1 Detailed Description

ADCBUF configuration.

The structure is used to hold all the relevant configuration which is used to configure ADCBUF.

Definition at line 243 of file mrr_config.h.

3.24.2 Field Documentation

3.24.2.1 adcFmt

uint8_t MmwDemo_ADCBufCfg_t::adcFmt

ADCBUF out format: 0-Complex, 1-Real

Definition at line 248 of file mrr_config.h.

3.24.2.2 chInterleave

uint8_t MmwDemo_ADCBufCfg_t::chInterleave

ADCBUF channel interleave configuration: 0-interleaved(not supported on XWR16xx), 1- non-interleaved

Definition at line 258 of file mrr_config.h.

3.24.2.3 chirpThreshold

uint8_t MmwDemo_ADCBufCfg_t::chirpThreshold

Chirp Threshold configuration used for ADCBUF buffer.

Definition at line 263 of file mrr_config.h.

3.24.2.4 iqSwapSel

`uint8_t MmwDemo_ADCBufCfg_t::iqSwapSel`
 ADCBUF IQ swap selection: 0-I in LSB, Q in MSB, 1-Q in LSB, I in MSB
 Definition at line 253 of file mrr_config.h.

The documentation for this struct was generated from the following file:

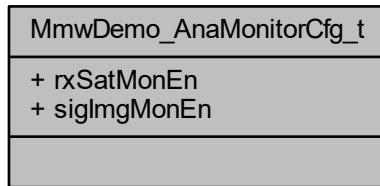
- common/ **mrr_config.h**

3.25 MmwDemo_AnaMonitorCfg_t Struct Reference

Millimeter Wave Demo analog monitor configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_AnaMonitorCfg_t:



Data Fields

- `uint8_t rxSatMonEn`
Setting for Rx Saturation monitor.
- `uint8_t sigImgMonEn`
Setting for signal & image band monitor

3.25.1 Detailed Description

Millimeter Wave Demo analog monitor configuration.
 The structure contains the flags that select analog monitors to be enabled.
 Definition at line 306 of file mrr_config.h.

3.25.2 Field Documentation

3.25.2.1 rxSatMonEn

`uint8_t MmwDemo_AnaMonitorCfg_t::rxSatMonEn`
 Setting for Rx Saturation monitor.
 Definition at line 309 of file mrr_config.h.

3.25.2.2 sigImgMonEn

`uint8_t MmwDemo_AnaMonitorCfg_t::sigImgMonEn`

Setting for signal & image band monitor

Definition at line 312 of file mrr_config.h.

The documentation for this struct was generated from the following file:

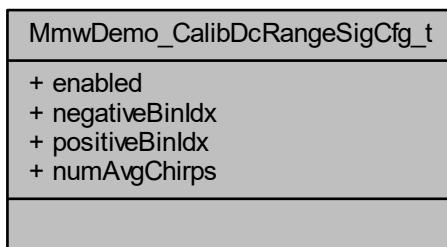
- common/ **mrr_config.h**

3.26 MmwDemo_CalibDcRangeSigCfg_t Struct Reference

Millimeter Wave Demo DC range signature compensation.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_CalibDcRangeSigCfg_t:



Data Fields

- **uint16_t enabled**
enabled flag: 1-enabled 0-disabled
- **int16_t negativeBinIdx**
maximum negative range bin (1D FFT index) to be compensated
- **int16_t positiveBinIdx**
maximum positive range bin (1D FFT index) to be compensated
- **uint16_t numAvgChirps**
number of chirps in the averaging phase

3.26.1 Detailed Description

Millimeter Wave Demo DC range signature compensation.

The structure contains the DC range antenna signature removal configuration used in data path

Definition at line 220 of file mrr_config.h.

3.26.2 Field Documentation

3.26.2.1 enabled

```
uint16_t MmwDemo_CalibDcRangeSigCfg_t::enabled
```

enabled flag: 1-enabled 0-disabled

Definition at line 223 of file mrr_config.h.

3.26.2.2 negativeBinIdx

`int16_t MmwDemo_CalibDcRangeSigCfg_t::negativeBinIdx`
maximum negative range bin (1D FFT index) to be compensated
Definition at line 226 of file mrr_config.h.

3.26.2.3 numAvgChirps

`uint16_t MmwDemo_CalibDcRangeSigCfg_t::numAvgChirps`
number of chirps in the averaging phase
Definition at line 232 of file mrr_config.h.

3.26.2.4 positiveBinIdx

`int16_t MmwDemo_CalibDcRangeSigCfg_t::positiveBinIdx`
maximum positive range bin (1D FFT index) to be compensated
Definition at line 229 of file mrr_config.h.

The documentation for this struct was generated from the following file:

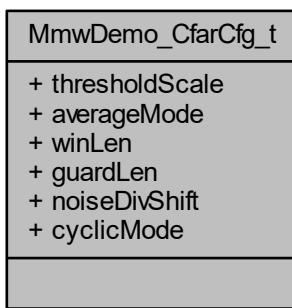
- common/ **mrr_config.h**

3.27 MmwDemo_CfarCfg_t Struct Reference

Millimeter Wave Demo CFAR Configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_CfarCfg_t:



Data Fields

- `uint16_t thresholdScale`
CFAR threshold scale.
- `uint8_t averageMode`
CFAR averaging mode 0-CFAR_CA, 1-CFAR_CAGO, 2-CFAR_CASO.
- `uint8_t winLen`
CFAR noise averaging window length.
- `uint8_t guardLen`
CFAR guard length.

- `uint8_t noiseDivShift`
CFAR cumulative noise sum divisor.
- `uint8_t cyclicMode`
CFAR 0-cyclic mode disabled, 1-cyclic mode enabled.

3.27.1 Detailed Description

Millimeter Wave Demo CFAR Configuration.
The structure contains the cfar configuration used in data path
Definition at line 61 of file mrr_config.h.

3.27.2 Field Documentation

3.27.2.1 averageMode

```
uint8_t MmwDemo_CfarCfg_t::averageMode
```

CFAR averaging mode 0-CFAR_CA, 1-CFAR_CAGO, 2-CFAR_CASO.
Definition at line 67 of file mrr_config.h.

3.27.2.2 cyclicMode

```
uint8_t MmwDemo_CfarCfg_t::cyclicMode
```

CFAR 0-cyclic mode disabled, 1-cyclic mode enabled.
Definition at line 79 of file mrr_config.h.

3.27.2.3 guardLen

```
uint8_t MmwDemo_CfarCfg_t::guardLen
```

CFAR guard length.
Definition at line 73 of file mrr_config.h.

3.27.2.4 noiseDivShift

```
uint8_t MmwDemo_CfarCfg_t::noiseDivShift
```

CFAR cumulative noise sum divisor.
Definition at line 76 of file mrr_config.h.

3.27.2.5 thresholdScale

```
uint16_t MmwDemo_CfarCfg_t::thresholdScale
```

CFAR threshold scale.
Definition at line 64 of file mrr_config.h.

3.27.2.6 winLen

```
uint8_t MmwDemo_CfarCfg_t::winLen
```

CFAR noise avraging window length.
Definition at line 70 of file mrr_config.h.
The documentation for this struct was generated from the following file:

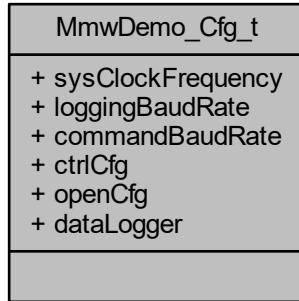
- common/ **mrr_config.h**

3.28 MmwDemo_Cfg_t Struct Reference

Millimeter Wave Demo configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_Cfg_t:



Data Fields

- `uint32_t sysClockFrequency`
CPU Clock Frequency.
- `uint32_t loggingBaudRate`
UART Logging Baud Rate.
- `uint32_t commandBaudRate`
UART Command Baud Rate.
- `MMWave_CtrlCfg ctrlCfg`
mmWave Control Configuration.
- `MMWave_OpenCfg openCfg`
mmWave Open Configuration.
- `uint8_t dataLogger`
Datapath output loggerSetting 0 (default): MSS UART logger 1: DSS UART logger.

3.28.1 Detailed Description

Millimeter Wave Demo configuration.

The structure is used to hold all the relevant configuration which is used to execute the Millimeter Wave Demo.
Definition at line 462 of file mrr_config.h.

3.28.2 Field Documentation

3.28.2.1 commandBaudRate

```
uint32_t MmwDemo_Cfg_t::commandBaudRate
```

UART Command Baud Rate.

Definition at line 471 of file mrr_config.h.

3.28.2.2 ctrlCfg

`MMWave_CtrlCfg MmwDemo_Cfg_t::ctrlCfg`
mmWave Control Configuration.

Definition at line 474 of file mrr_config.h.

3.28.2.3 dataLogger

`uint8_t MmwDemo_Cfg_t::dataLogger`
Datapath output loggerSetting 0 (default): MSS UART logger 1: DSS UART logger.
Definition at line 483 of file mrr_config.h.

3.28.2.4 loggingBaudRate

`uint32_t MmwDemo_Cfg_t::loggingBaudRate`
UART Logging Baud Rate.
Definition at line 468 of file mrr_config.h.

3.28.2.5 openCfg

`MMWave_OpenCfg MmwDemo_Cfg_t::openCfg`
mmWave Open Configuration.
Definition at line 477 of file mrr_config.h.

3.28.2.6 sysClockFrequency

`uint32_t MmwDemo_Cfg_t::sysClockFrequency`
CPU Clock Frequency.
Definition at line 465 of file mrr_config.h.

The documentation for this struct was generated from the following file:

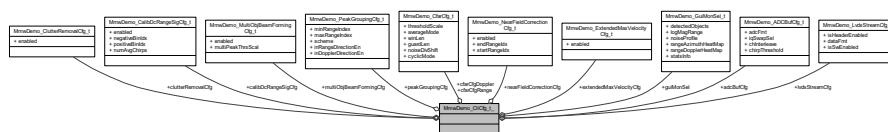
- common/ **mrr_config.h**

3.29 MmwDemo_CliCfg_t Struct Reference

Millimeter Wave Demo CLI related configuration.

#include <mrr_config.h>

Collaboration diagram for `MmwDemo_CliCfg_t`:



Data Fields

- MmwDemo_ADCBufCfg adcBufCfg**
ADCBUF Configuration.
- MmwDemo_GuiMonSel guiMonSel**
Gui Monitor Selection.
- MmwDemo_CfarCfg cfarCfgRange**
CFAR configuration for range.

- **MmwDemo_CfarCfg cfarCfgDoppler**
CFAR configuration for doppler.
- **MmwDemo_ExtendedMaxVelocityCfg extendedMaxVelocityCfg**
Velocity disambiguation configuration.
- **MmwDemo_NearFieldCorrectionCfg nearFieldCorrectionCfg**
Near Field Correction configuration.
- **MmwDemo_PeakGroupingCfg peakGroupingCfg**
Peak grouping configuration.
- **MmwDemo_MultiObjBeamFormingCfg multiObjBeamFormingCfg**
Multi object beam forming configuration.
- **MmwDemo_CalibDcRangeSigCfg calibDcRangeSigCfg**
Calibrate DC (zero) range signature.
- **MmwDemo_ClutterRemovalCfg clutterRemovalCfg**
Clutter removal configuration.
- **MmwDemo_LvdsStreamCfg lvdsStreamCfg**
LVDS stream configuration.

3.29.1 Detailed Description

Millimeter Wave Demo CLI related configuration.

Definition at line 351 of file mrr_config.h.

3.29.2 Field Documentation

3.29.2.1 adcBufCfg

MmwDemo_ADCBufCfg `MmwDemo_CliCfg_t::adcBufCfg`
 ADCBUF Configuration.

Definition at line 354 of file mrr_config.h.

3.29.2.2 calibDcRangeSigCfg

MmwDemo_CalibDcRangeSigCfg `MmwDemo_CliCfg_t::calibDcRangeSigCfg`
 Calibrate DC (zero) range signature.

Definition at line 382 of file mrr_config.h.

3.29.2.3 cfarCfgDoppler

MmwDemo_CfarCfg `MmwDemo_CliCfg_t::cfarCfgDoppler`
 CFAR configuration for doppler.

Definition at line 366 of file mrr_config.h.

3.29.2.4 cfarCfgRange

MmwDemo_CfarCfg `MmwDemo_CliCfg_t::cfarCfgRange`
 CFAR configuration for range.

Definition at line 363 of file mrr_config.h.

3.29.2.5 clutterRemovalCfg

MmwDemo_ClutterRemovalCfg MmwDemo_CliCfg_t_::clutterRemovalCfg
 Clutter removal configuration.
 Definition at line 385 of file mrr_config.h.

3.29.2.6 extendedMaxVelocityCfg

MmwDemo_ExtendedMaxVelocityCfg MmwDemo_CliCfg_t_::extendedMaxVelocityCfg
 Velocity disambiguation configuration.
 Definition at line 369 of file mrr_config.h.

3.29.2.7 guiMonSel

MmwDemo_GuiMonSel MmwDemo_CliCfg_t_::guiMonSel
 Gui Monitor Selection.
 Definition at line 357 of file mrr_config.h.

3.29.2.8 lvdsStreamCfg

MmwDemo_LvdsStreamCfg MmwDemo_CliCfg_t_::lvdsStreamCfg
 LVDS stream configuration.
 Definition at line 392 of file mrr_config.h.

3.29.2.9 multiObjBeamFormingCfg

MmwDemo_MultiObjBeamFormingCfg MmwDemo_CliCfg_t_::multiObjBeamFormingCfg
 Multi object beam forming configuration.
 Definition at line 379 of file mrr_config.h.

3.29.2.10 nearFieldCorrectionCfg

MmwDemo_NearFieldCorrectionCfg MmwDemo_CliCfg_t_::nearFieldCorrectionCfg
 Near Field Correction configuration.
 Definition at line 372 of file mrr_config.h.

3.29.2.11 peakGroupingCfg

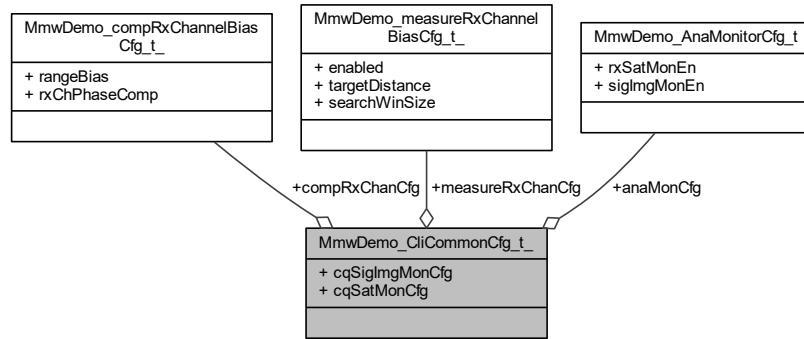
MmwDemo_PeakGroupingCfg MmwDemo_CliCfg_t_::peakGroupingCfg
 Peak grouping configuration.
 Definition at line 376 of file mrr_config.h.
 The documentation for this struct was generated from the following file:

- common/ **mrr_config.h**

3.30 MmwDemo_CliCommonCfg_t_ Struct Reference

Millimeter Wave Demo CLI related configuration common across all subframes.
`#include <mrr_config.h>`

Collaboration diagram for MmwDemo_CliCommonCfg_t:



Data Fields

- **MmwDemo_compRxChannelBiasCfg_t compRxChanCfg**
Configuration for compensation for range bias and Rx channel phase offset.
- **MmwDemo_measureRxChannelBiasCfg_t measureRxChanCfg**
Configuration for measurement of range bias and Rx channel phase offset.
- **rlSigImgMonConf_t cqSigImgMonCfg [RL_MAX_PROFILES_CNT]**
CQ monitor configuration - Signal Image band data.
- **rlRxSatMonConf_t cqSatMonCfg [RL_MAX_PROFILES_CNT]**
CQ monitor configuration - Signal Image band data.
- **MmwDemo_AnaMonitorCfg anaMonCfg**
Analog monitor bit mask.

3.30.1 Detailed Description

Millimeter Wave Demo CLI related configuration common across all subframes.
Definition at line 434 of file mrr_config.h.

3.30.2 Field Documentation

3.30.2.1 anaMonCfg

MmwDemo_AnaMonitorCfg MmwDemo_CliCommonCfg_t::anaMonCfg
Analog monitor bit mask.
Definition at line 451 of file mrr_config.h.

3.30.2.2 compRxChanCfg

MmwDemo_compRxChannelBiasCfg_t MmwDemo_CliCommonCfg_t::compRxChanCfg
Configuration for compensation for range bias and Rx channel phase offset.
Definition at line 438 of file mrr_config.h.

3.30.2.3 cqSatMonCfg

`rlRxSatMonConf_t MmwDemo_CliCommonCfg_t::cqSatMonCfg[RL_MAX_PROFILES_CNT]`
 CQ monitor configuration - Signal Image band data.

Definition at line 448 of file mrr_config.h.

3.30.2.4 cqSigImgMonCfg

`rlSigImgMonConf_t MmwDemo_CliCommonCfg_t::cqSigImgMonCfg[RL_MAX_PROFILES_CNT]`
 CQ monitor configuration - Signal Image band data.

Definition at line 445 of file mrr_config.h.

3.30.2.5 measureRxChanCfg

`MmwDemo_measureRxChannelBiasCfg_t MmwDemo_CliCommonCfg_t::measureRxChanCfg`
 Configuration for measurement of range bias and Rx channel phase offset.

Definition at line 442 of file mrr_config.h.

The documentation for this struct was generated from the following file:

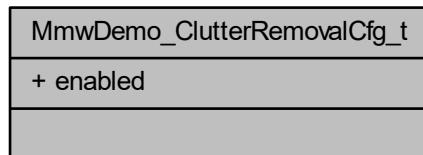
- common/ **mrr_config.h**

3.31 MmwDemo_ClutterRemovalCfg_t Struct Reference

Clutter removal configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_ClutterRemovalCfg_t:



Data Fields

- `uint8_t enabled`

enabled flag: 1-enabled 0-disabled

3.31.1 Detailed Description

Clutter removal configuration.

The structure contains clutter removal configuration

Definition at line 206 of file mrr_config.h.

3.31.2 Field Documentation

3.31.2.1 enabled

```
uint8_t MmwDemo_ClutterRemovalCfg_t::enabled
enabled flag: 1-enabled 0-disabled
```

Definition at line 209 of file mrr_config.h.

The documentation for this struct was generated from the following file:

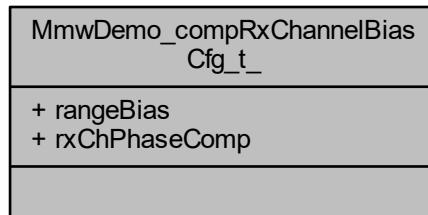
- common/ **mrr_config.h**

3.32 MmwDemo_compRxChannelBiasCfg_t Struct Reference

Range Bias and rx channel gain/phase compensation configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_compRxChannelBiasCfg_t:



Data Fields

- float **rangeBias**
Compensation for range estimation bias.
- cmplx16ImRe_t **rxChPhaseComp** [SYS_COMMON_NUM_TX_ANTENNAS *SYS_COMMON_NUM_RX CHANNEL]
Compensation for Rx channel phase bias in Q15 format.

3.32.1 Detailed Description

Range Bias and rx channel gain/phase compensation configuration.

Definition at line 399 of file mrr_config.h.

3.32.2 Field Documentation

3.32.2.1 rangeBias

```
float MmwDemo_compRxChannelBiasCfg_t::rangeBias
```

Compensation for range estimation bias.

Definition at line 403 of file mrr_config.h.

3.32.2.2 rxChPhaseComp

```
cmplx16ImRe_t MmwDemo_compRxChannelBiasCfg_t::rxChPhaseComp [SYS_COMMON_NUM_TX_ANTENNAS *SYS_COMMON_NUM_RX_CHANNEL]
```

Compensation for Rx channel phase bias in Q15 format.

Definition at line 406 of file mrr_config.h.

The documentation for this struct was generated from the following file:

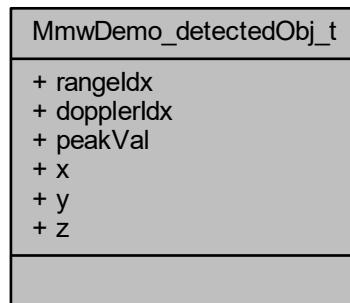
- common/ **mrr_config.h**

3.33 MmwDemo_detectedObj_t Struct Reference

Detected object estimated parameters.

```
#include <detected_obj.h>
```

Collaboration diagram for MmwDemo_detectedObj_t:



Data Fields

- **uint16_t rangeldx**
Range index.
- **int16_t dopplerIdx**
Doppler index. Note that it is changed to signed integer in order to handle extended maximum velocity. Negative values correspond to the object moving toward sensor, and positive values correspond to the object moving away from the sensor.
- **uint16_t peakVal**
Peak value.
- **int16_t x**
x - coordinate in meters. Q format depends on the range resolution
- **int16_t y**
y - coordinate in meters. Q format depends on the range resolution
- **int16_t z**
z - coordinate in meters. Q format depends on the range resolution

3.33.1 Detailed Description

Detected object estimated parameters.

Definition at line 27 of file detected_obj.h.

3.33.2 Field Documentation

3.33.2.1 dopplerIdx

```
int16_t MmwDemo_detectedObj_t::dopplerIdx
```

Doppler index. Note that it is changed to signed integer in order to handle extended maximum velocity. Negative values correspond to the object moving toward sensor, and positive values correspond to the object moving away from the sensor.

Definition at line 30 of file detected_obj.h.

3.33.2.2 peakVal

```
uint16_t MmwDemo_detectedObj_t::peakVal
```

Peak value.

Definition at line 35 of file detected_obj.h.

3.33.2.3 rangeIdx

```
uint16_t MmwDemo_detectedObj_t::rangeIdx
```

Range index.

Definition at line 29 of file detected_obj.h.

3.33.2.4 x

```
int16_t MmwDemo_detectedObj_t::x
```

x - coordinate in meters. Q format depends on the range resolution

Definition at line 36 of file detected_obj.h.

3.33.2.5 y

```
int16_t MmwDemo_detectedObj_t::y
```

y - coordinate in meters. Q format depends on the range resolution

Definition at line 37 of file detected_obj.h.

3.33.2.6 z

```
int16_t MmwDemo_detectedObj_t::z
```

z - coordinate in meters. Q format depends on the range resolution

Definition at line 38 of file detected_obj.h.

The documentation for this struct was generated from the following file:

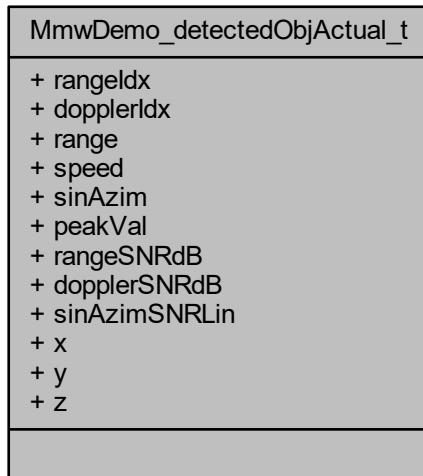
- common/ **detected_obj.h**

3.34 MmwDemo_detectedObjActual_t Struct Reference

Detected object estimated parameters.

```
#include <dss_data_path.h>
```

Collaboration diagram for MmwDemo_detectedObjActual_t:



Data Fields

- uint16_t **rangelidx**
Range index.
- uint16_t **dopplerIdx**
Doppler index.
- uint16_t **range**
Range (meters in oneQformat)
- int16_t **speed**
Doppler (m/s in oneQformat)
- int16_t **sinAzim**
wx sin(Azim). Q format provides the bitwidth.
- uint16_t **peakVal**
Peak value.
- uint16_t **rangeSNRdB**
Range SNR (dB)
- uint16_t **dopplerSNRdB**
Doppler SNR (dB)
- uint16_t **sinAzimSNRLin**
omega SNR (linear scale)
- int16_t **x**
x - coordinate in meters. Q format provides the bitwidth.
- int16_t **y**
y - coordinate in meters. Q format provides the bitwidth.
- int16_t **z**
z - coordinate in meters. Q format provides the bitwidth.

3.34.1 Detailed Description

Detected object estimated parameters.

Definition at line 153 of file dss_data_path.h.

3.34.2 Field Documentation

3.34.2.1 dopplerIdx

```
uint16_t MmwDemo_detectedObjActual_t::dopplerIdx
```

Doppler index.

Definition at line 156 of file dss_data_path.h.

3.34.2.2 dopplerSNRdB

```
uint16_t MmwDemo_detectedObjActual_t::dopplerSNRdB
```

Doppler SNR (dB)

Definition at line 165 of file dss_data_path.h.

3.34.2.3 peakVal

```
uint16_t MmwDemo_detectedObjActual_t::peakVal
```

Peak value.

Definition at line 162 of file dss_data_path.h.

3.34.2.4 range

```
uint16_t MmwDemo_detectedObjActual_t::range
```

Range (meters in oneQformat)

Definition at line 158 of file dss_data_path.h.

3.34.2.5 rangefIdx

```
uint16_t MmwDemo_detectedObjActual_t::rangeIdx
```

Range index.

Definition at line 155 of file dss_data_path.h.

3.34.2.6 rangeSNRdB

```
uint16_t MmwDemo_detectedObjActual_t::rangeSNRdB
```

Range SNR (dB)

Definition at line 164 of file dss_data_path.h.

3.34.2.7 sinAzim

```
int16_t MmwDemo_detectedObjActual_t::sinAzim
```

wx sin(Azim). Q format provides the bitwidth.

Definition at line 160 of file dss_data_path.h.

3.34.2.8 sinAzimSNRLin

```
uint16_t MmwDemo_detectedObjActual_t::sinAzimSNRLin  
omega SNR (linear scale)  
Definition at line 166 of file dss_data_path.h.
```

3.34.2.9 speed

```
int16_t MmwDemo_detectedObjActual_t::speed  
Doppler (m/s in oneQformat)  
Definition at line 159 of file dss_data_path.h.  
Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().
```

3.34.2.10 x

```
int16_t MmwDemo_detectedObjActual_t::x  
x - coordinate in meters. Q format provides the bitwidth.  
Definition at line 168 of file dss_data_path.h.
```

3.34.2.11 y

```
int16_t MmwDemo_detectedObjActual_t::y  
y - coordinate in meters. Q format provides the bitwidth.  
Definition at line 169 of file dss_data_path.h.
```

3.34.2.12 z

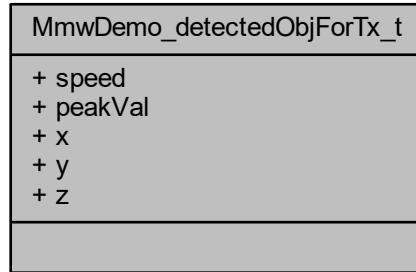
```
int16_t MmwDemo_detectedObjActual_t::z  
z - coordinate in meters. Q format provides the bitwidth.  
Definition at line 170 of file dss_data_path.h.  
The documentation for this struct was generated from the following file:
```

- **dss_data_path.h**

3.35 MmwDemo_detectedObjForTx_t Struct Reference

Detected object estimated parameters to be transmitted out.
`#include <dss_data_path.h>`

Collaboration diagram for MmwDemo_detectedObjForTx_t:



Data Fields

- **int16_t speed**
Doppler index.
- **uint16_t peakVal**
Peak value.
- **int16_t x**
x - coordinate in meters. Q format provides the bitwidth.
- **int16_t y**
y - coordinate in meters. Q format provides the bitwidth.
- **int16_t z**
z - coordinate in meters. Q format provides the bitwidth.

3.35.1 Detailed Description

Detected object estimated parameters to be transmitted out.
Definition at line 205 of file dss_data_path.h.

3.35.2 Field Documentation

3.35.2.1 peakVal

`uint16_t MmwDemo_detectedObjForTx_t::peakVal`
Peak value.
Definition at line 208 of file dss_data_path.h.

3.35.2.2 speed

`int16_t MmwDemo_detectedObjForTx_t::speed`
Doppler index.
Definition at line 207 of file dss_data_path.h.

3.35.2.3 x

```
int16_t MmwDemo_detectedObjForTx_t::x
x - coordinate in meters. Q format provides the bitwidth.
Definition at line 209 of file dss_data_path.h.
```

3.35.2.4 y

```
int16_t MmwDemo_detectedObjForTx_t::y
y - coordinate in meters. Q format provides the bitwidth.
Definition at line 210 of file dss_data_path.h.
```

3.35.2.5 z

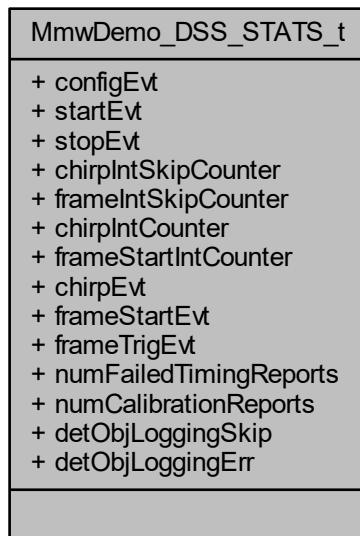
```
int16_t MmwDemo_detectedObjForTx_t::z
z - coordinate in meters. Q format provides the bitwidth.
Definition at line 211 of file dss_data_path.h.
The documentation for this struct was generated from the following file:
```

- [dss_data_path.h](#)

3.36 MmwDemo_DSS_STATS_t Struct Reference

```
#include <mmWave_XSS.h>
```

Collaboration diagram for MmwDemo_DSS_STATS_t:



Data Fields

- `uint8_t configEvt`

Counter which tracks the number of config events. The config event is triggered in mmwave config callback function when remote sends configuration.

- **uint8_t startEvt**
Counter which tracks the number of start events The start event is triggered in mmwave start callback function when remote calls mmwave_start()
- **uint8_t stopEvt**
Counter which tracks the number of stop events The start event is triggered in mmwave stop callback function when remote calls mmwave_stop()
- **uint32_t chirpIntSkipCounter**
Counter which tracks the number of chirp interrupt skipped due to stopped state of sensor.
- **uint32_t frameIntSkipCounter**
Counter which tracks the number of chirp interrupt skipped due to stopped state of sensor.
- **uint32_t chirpIntCounter**
Counter which tracks the number of chirp interrupt detected.
- **uint32_t frameStartIntCounter**
Counter which tracks the number of frame start interrupt detected.
- **uint32_t chirpEvt**
Counter which tracks the number of chirp event detected The chirp event is triggered in the ISR for chirp interrupt.
- **uint32_t frameStartEvt**
Counter which tracks the number of frame start event detected The frame start event is triggered in the ISR for frame start interrupt.
- **uint32_t frameTrigEvt**
Counter which tracks the number of frames triggered in BSS detected The frame trigger event is triggered in the mmwave async event callback function.
- **uint32_t numFailedTimingReports**
Counter which tracks the number of Failed Timing Reports received from BSS
- **uint32_t numCalibrationReports**
Counter which tracks the number of Calibration Report received from BSS
- **uint32_t detObjLoggingSkip**
Counter which tracks the number of times saving detected objects in logging buffer is skipped.
- **uint32_t detObjLoggingErr**
Counter which tracks the number of times saving detected objects in logging buffer has an error.

3.36.1 Detailed Description

Definition at line 122 of file mmWave_XSS.h.

3.36.2 Field Documentation

3.36.2.1 chirpEvt

```
uint32_t MmwDemo_DSS_STATS_t::chirpEvt
```

Counter which tracks the number of chirp event detected The chirp event is triggered in the ISR for chirp interrupt.
 Definition at line 153 of file mmWave_XSS.h.

3.36.2.2 chirpIntCounter

```
uint32_t MmwDemo_DSS_STATS_t::chirpIntCounter
```

Counter which tracks the number of chirp interrupt detected.
 Definition at line 146 of file mmWave_XSS.h.

3.36.2.3 chirpIntSkipCounter

```
uint32_t MmwDemo_DSS_STATS_t::chirpIntSkipCounter
```

Counter which tracks the number of chirp interrupt skipped due to stopped state of sensor.
Definition at line 140 of file mmWave_XSS.h.

3.36.2.4 configEvt

```
uint8_t MmwDemo_DSS_STATS_t::configEvt
```

Counter which tracks the number of config events The config event is triggered in mmwave config callback function when remote sends configuration.
Definition at line 127 of file mmWave_XSS.h.

3.36.2.5 detObjLoggingErr

```
uint32_t MmwDemo_DSS_STATS_t::detObjLoggingErr
```

Counter which tracks the number of times saving detected objects in logging buffer has an error.
Definition at line 175 of file mmWave_XSS.h.

3.36.2.6 detObjLoggingSkip

```
uint32_t MmwDemo_DSS_STATS_t::detObjLoggingSkip
```

Counter which tracks the number of times saving detected objects in logging buffer is skipped.
Definition at line 171 of file mmWave_XSS.h.

3.36.2.7 frameIntSkipCounter

```
uint32_t MmwDemo_DSS_STATS_t::frameIntSkipCounter
```

Counter which tracks the number of chirp interrupt skipped due to stopped state of sensor.
Definition at line 143 of file mmWave_XSS.h.

3.36.2.8 frameStartEvt

```
uint32_t MmwDemo_DSS_STATS_t::frameStartEvt
```

Counter which tracks the number of frame start event detected The frame start event is triggered in the ISR for frame start interrupt.
Definition at line 157 of file mmWave_XSS.h.

3.36.2.9 frameStartIntCounter

```
uint32_t MmwDemo_DSS_STATS_t::frameStartIntCounter
```

Counter which tracks the number of frame start interrupt detected.
Definition at line 149 of file mmWave_XSS.h.

3.36.2.10 frameTrigEvt

```
uint32_t MmwDemo_DSS_STATS_t::frameTrigEvt
```

Counter which tracks the number of frames triggered in BSS detected The frame trigger event is triggered in the mmwave async event callback function.
Definition at line 161 of file mmWave_XSS.h.

3.36.2.11 numCalibrationReports

`uint32_t MmwDemo_DSS_STATS_t::numCalibrationReports`
 Counter which tracks the number of Calibration Report received from BSS

Definition at line 167 of file mmWave_XSS.h.

3.36.2.12 numFailedTimingReports

`uint32_t MmwDemo_DSS_STATS_t::numFailedTimingReports`
 Counter which tracks the number of Failed Timing Reports received from BSS

Definition at line 164 of file mmWave_XSS.h.

3.36.2.13 startEvt

`uint8_t MmwDemo_DSS_STATS_t::startEvt`
 Counter which tracks the number of start events The start event is triggered in mmwave start callback function when remote calls mmwave_start()
 Definition at line 132 of file mmWave_XSS.h.

3.36.2.14 stopEvt

`uint8_t MmwDemo_DSS_STATS_t::stopEvt`
 Counter which tracks the number of stop events The start event is triggered in mmwave stop callback function when remote calls mmwave_stop()
 Definition at line 137 of file mmWave_XSS.h.
 The documentation for this struct was generated from the following file:

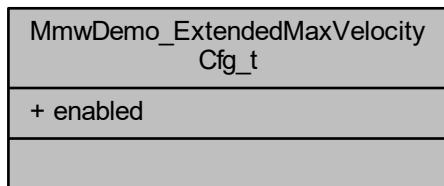
- common/ **mmWave_XSS.h**

3.37 MmwDemo_ExtendedMaxVelocityCfg_t Struct Reference

Millimeter Wave Demo Velocity Disambiguation.

#include <mrr_config.h>

Collaboration diagram for MmwDemo_ExtendedMaxVelocityCfg_t:



Data Fields

- `uint8_t enabled`

enabled flag: 1-enabled 0-disabled

3.37.1 Detailed Description

Millimeter Wave Demo Velocity Disambiguation.
The structure contains Velocity Disambiguation configuration
Definition at line 134 of file mrr_config.h.

3.37.2 Field Documentation

3.37.2.1 enabled

`uint8_t MmwDemo_ExtendedMaxVelocityCfg_t::enabled`

enabled flag: 1-enabled 0-disabled

Definition at line 137 of file mrr_config.h.

The documentation for this struct was generated from the following file:

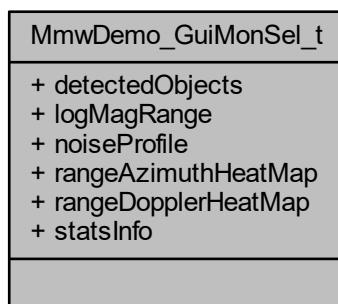
- common/ **mrr_config.h**

3.38 MmwDemo_GuiMonSel_t Struct Reference

Millimeter Wave Demo Gui Monitor Selection.

#include <mrr_config.h>

Collaboration diagram for MmwDemo_GuiMonSel_t:



Data Fields

- `uint8_t detectedObjects`
*Send list of detected objects (see **MmwDemo_detectedObj_t** (p. ??))*
- `uint8_t logMagRange`
Send log magnitude range array
- `uint8_t noiseProfile`
Send noise floor profile.
- `uint8_t rangeAzimuthHeatMap`
Send complex range bins at zero doppler, all antenna symbols for range-azimuth heat map.
- `uint8_t rangeDopplerHeatMap`
Send complex range bins at zero doppler, (all antenna symbols), for range-azimuth heat map.
- `uint8_t statsInfo`
Send stats.

3.38.1 Detailed Description

Millimeter Wave Demo Gui Monitor Selection.

The structure contains the flags which select what information is placed to the output packet, and sent out to GUI. If the flag is set to 1, information is sent out. If the flag is set to 0, information is not sent out.

Definition at line 325 of file mrr_config.h.

3.38.2 Field Documentation

3.38.2.1 detectedObjects

```
uint8_t MmwDemo_GuiMonSel_t::detectedObjects
```

Send list of detected objects (see **MmwDemo_detectedObj_t** (p. ??))

Definition at line 328 of file mrr_config.h.

3.38.2.2 logMagRange

```
uint8_t MmwDemo_GuiMonSel_t::logMagRange
```

Send log magnitude range array

Definition at line 331 of file mrr_config.h.

3.38.2.3 noiseProfile

```
uint8_t MmwDemo_GuiMonSel_t::noiseProfile
```

Send noise floor profile.

Definition at line 334 of file mrr_config.h.

3.38.2.4 rangeAzimuthHeatMap

```
uint8_t MmwDemo_GuiMonSel_t::rangeAzimuthHeatMap
```

Send complex range bins at zero doppler, all antenna symbols for range-azimuth heat map.

Definition at line 337 of file mrr_config.h.

3.38.2.5 rangeDopplerHeatMap

```
uint8_t MmwDemo_GuiMonSel_t::rangeDopplerHeatMap
```

Send complex range bins at zero doppler, (all antenna symbols), for range-azimuth heat map.

Definition at line 340 of file mrr_config.h.

3.38.2.6 statsInfo

```
uint8_t MmwDemo_GuiMonSel_t::statsInfo
```

Send stats.

Definition at line 343 of file mrr_config.h.

The documentation for this struct was generated from the following file:

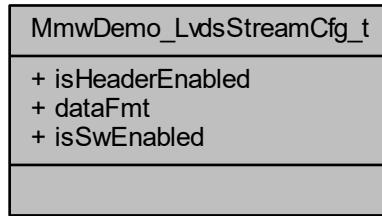
- common/ **mrr_config.h**

3.39 MmwDemo_LvdsStreamCfg_t Struct Reference

LVDS streaming configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_LvdsStreamCfg_t:



Data Fields

- bool **isHeaderEnabled**
HSI Header (p. ??) enabled/disabled flag.
- uint8_t **dataFmt**
- bool **isSwEnabled**
SW enabled/disabled flag.

3.39.1 Detailed Description

LVDS streaming configuration.

The structure is used to hold all the relevant configuration for the LVDS streaming.

Definition at line 275 of file mrr_config.h.

3.39.2 Field Documentation

3.39.2.1 dataFmt

```
uint8_t MmwDemo_LvdsStreamCfg_t::dataFmt
```

HW streaming data format: 0-HW STREAMING DISABLED 1-ADC 2-CP_ADC 3-ADC_CP 4-CP_ADC_CQ

Definition at line 290 of file mrr_config.h.

3.39.2.2 isHeaderEnabled

```
bool MmwDemo_LvdsStreamCfg_t::isHeaderEnabled
```

HSI Header (p. ??) enabled/disabled flag.

Definition at line 280 of file mrr_config.h.

3.39.2.3 isSwEnabled

```
bool MmwDemo_LvdsStreamCfg_t::isSwEnabled
```

SW enabled/disabled flag.

Definition at line 295 of file mrr_config.h.

The documentation for this struct was generated from the following file:

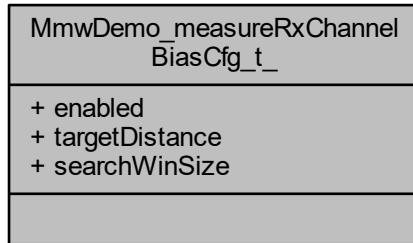
- common/ **mrr_config.h**

3.40 MmwDemo_measureRxChannelBiasCfg_t_Struct Reference

Range Bias and rx channel gain/phase measurement configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_measureRxChannelBiasCfg_t_:



Data Fields

- **uint8_t enabled**
1-enabled 0-disabled
- **float targetDistance**
Target distance during measurement (in meters)
- **float searchWinSize**
Search window size (in meters), the search is done in range [-searchWinSize/2 + targetDistance, targetDistance + searchWinSize/2].

3.40.1 Detailed Description

Range Bias and rx channel gain/phase measurement configuration.

Definition at line 414 of file mrr_config.h.

3.40.2 Field Documentation

3.40.2.1 enabled

```
uint8_t MmwDemo_measureRxChannelBiasCfg_t_::enabled
```

1-enabled 0-disabled

Definition at line 418 of file mrr_config.h.

3.40.2.2 searchWinSize

```
float MmwDemo_measureRxChannelBiasCfg_t_::searchWinSize
```

Search window size (in meters), the search is done in range [-searchWinSize/2 + targetDistance, targetDistance + searchWinSize/2].

Definition at line 425 of file mrr_config.h.

3.40.2.3 targetDistance

```
float MmwDemo_measureRxChannelBiasCfg_t::targetDistance
```

Target distance during measurement (in meters)

Definition at line 421 of file mrr_config.h.

The documentation for this struct was generated from the following file:

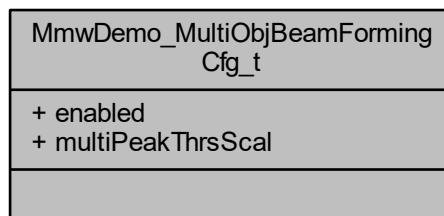
- common/ **mrr_config.h**

3.41 MmwDemo_MultiObjBeamFormingCfg_t Struct Reference

Millimeter Wave Demo multi object beam formaing Configuration.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_MultiObjBeamFormingCfg_t:



Data Fields

- **uint8_t enabled**
enabled flag: 1-enabled 0-disabled
- **float multiPeakThrsScal**
second peak detection threshold

3.41.1 Detailed Description

Millimeter Wave Demo multi object beam formaing Configuration.

The structure contains the Peak grouping configuration used in data path

Definition at line 117 of file mrr_config.h.

3.41.2 Field Documentation

3.41.2.1 enabled

```
uint8_t MmwDemo_MultiObjBeamFormingCfg_t::enabled
```

enabled flag: 1-enabled 0-disabled

Definition at line 120 of file mrr_config.h.

Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().

3.41.2.2 multiPeakThrsScal

```
float MmwDemo_MultiObjBeamFormingCfg_t::multiPeakThrsScal
second peak detection threshold
Definition at line 123 of file mrr_config.h.
Referenced by MmwDemo_XYestimation(), and MmwDemo_XYZestimation().
The documentation for this struct was generated from the following file:
```

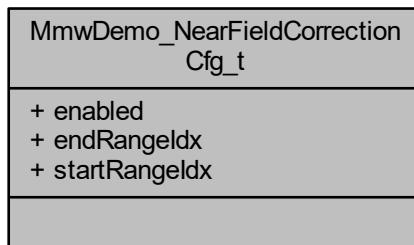
- common/ **mrr_config.h**

3.42 MmwDemo_NearFieldCorrectionCfg_t Struct Reference

Millimeter Wave Demo near field correction.

```
#include <mrr_config.h>
```

Collaboration diagram for MmwDemo_NearFieldCorrectionCfg_t:



Data Fields

- **uint8_t enabled**
enabled flag: 1-enabled 0-disabled
- **uint16_t endRangeldx**
Range index beyond which the algorithm is disabled.
- **uint16_t startRangeldx**
Range index below which the algorithm is disabled.

3.42.1 Detailed Description

Millimeter Wave Demo near field correction.

The structure contains Near Field Correction configuration

Definition at line 148 of file mrr_config.h.

3.42.2 Field Documentation

3.42.2.1 enabled

```
uint8_t MmwDemo_NearFieldCorrectionCfg_t::enabled
enabled flag: 1-enabled 0-disabled
Definition at line 151 of file mrr_config.h.
```

3.42.2.2 endRangeldx

`uint16_t MmwDemo_NearFieldCorrectionCfg_t::endRangeIdx`

Range index beyond which the algorithm is disabled.

Definition at line 154 of file mrr_config.h.

3.42.2.3 startRangeldx

`uint16_t MmwDemo_NearFieldCorrectionCfg_t::startRangeIdx`

Range index below which the algorithm is disabled.

Definition at line 157 of file mrr_config.h.

The documentation for this struct was generated from the following file:

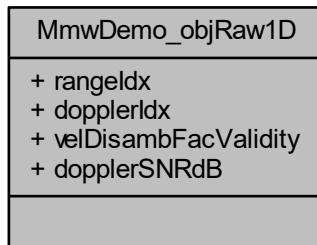
- common/ **mrr_config.h**

3.43 MmwDemo_objRaw1D Struct Reference

Parameters of CFAR detected object during the first round of CFAR detections.

#include <dss_data_path.h>

Collaboration diagram for MmwDemo_objRaw1D:



Data Fields

- `uint16_t rangeldx`
Range index.
- `uint16_t dopplerldx`
Doppler index.
- `int16_t velDisambFacValidity`
velocity disambiguation factor
- `uint16_t dopplerSNRdB`
Peak value.

3.43.1 Detailed Description

Parameters of CFAR detected object during the first round of CFAR detections.

Definition at line 123 of file dss_data_path.h.

3.43.2 Field Documentation

3.43.2.1 dopplerIdx

`uint16_t MmwDemo_objRaw1D::dopplerIdx`
 Doppler index.
 Definition at line 126 of file dss_data_path.h.

3.43.2.2 dopplerSNRdB

`uint16_t MmwDemo_objRaw1D::dopplerSNRdB`
 Peak value.
 Definition at line 128 of file dss_data_path.h.

3.43.2.3 rangelIdx

`uint16_t MmwDemo_objRaw1D::rangeIdx`
 Range index.
 Definition at line 125 of file dss_data_path.h.

3.43.2.4 velDisambFacValidity

`int16_t MmwDemo_objRaw1D::velDisambFacValidity`
 velocity disambiguation factor
 Definition at line 127 of file dss_data_path.h.
 The documentation for this struct was generated from the following file:

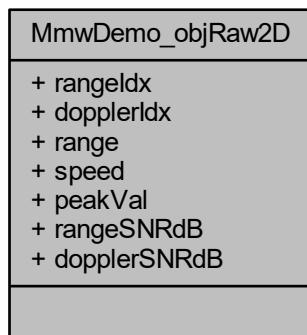
- `dss_data_path.h`

3.44 MmwDemo_objRaw2D Struct Reference

Parameters of CFAR detected object during the second round of CFAR detections.

```
#include <dss_data_path.h>
```

Collaboration diagram for MmwDemo_objRaw2D:



Data Fields

- `uint16_t rangelIdx`
Range index.

- **uint16_t dopplerIdx**
Doppler index.
- **uint16_t range**
*Range (in meters * (1 << xyzOutputQFormat))*
- **int16_t speed**
*relative velocity (in meters/sec * (1 << xyzOutputQFormat))*
- **uint16_t peakVal**
Peak value.
- **uint16_t rangeSNRdB**
SNR of the peak in the range dimension.
- **uint16_t dopplerSNRdB**
SNR of the peak in the doppler dimension.

3.44.1 Detailed Description

Parameters of CFAR detected object during the second round of CFAR detections.
 Definition at line 138 of file dss_data_path.h.

3.44.2 Field Documentation

3.44.2.1 dopplerIdx

```
uint16_t MmwDemo_objRaw2D::dopplerIdx
Doppler index.
Definition at line 141 of file dss_data_path.h.
```

3.44.2.2 dopplerSNRdB

```
uint16_t MmwDemo_objRaw2D::dopplerSNRdB
SNR of the peak in the doppler dimension.
Definition at line 146 of file dss_data_path.h.
```

3.44.2.3 peakVal

```
uint16_t MmwDemo_objRaw2D::peakVal
Peak value.
Definition at line 144 of file dss_data_path.h.
```

3.44.2.4 range

```
uint16_t MmwDemo_objRaw2D::range
Range (in meters * (1 << xyzOutputQFormat))
Definition at line 142 of file dss_data_path.h.
```

3.44.2.5 rangefIdx

```
uint16_t MmwDemo_objRaw2D::rangefIdx
Range index.
Definition at line 140 of file dss_data_path.h.
```

3.44.2.6 rangeSNRdB

`uint16_t MmwDemo_objRaw2D::rangeSNRdB`

SNR of the peak in the range dimension.

Definition at line 145 of file dss_data_path.h.

3.44.2.7 speed

`int16_t MmwDemo_objRaw2D::speed`

relative velocity (in meters/sec * (1 << xyzOutputQFormat))

Definition at line 143 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

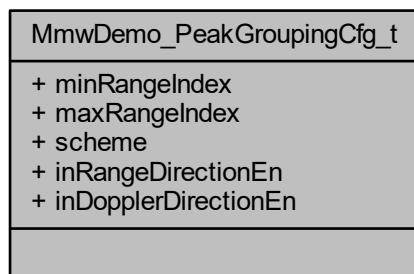
- `dss_data_path.h`

3.45 MmwDemo_PeakGroupingCfg_t Struct Reference

Millimeter Wave Demo Peak grouping Configuration.

`#include <mrr_config.h>`

Collaboration diagram for MmwDemo_PeakGroupingCfg_t:



Data Fields

- `uint16_t minRangeIndex`
minimum range index exported
- `uint16_t maxRangeIndex`
maximum range index exported
- `uint8_t scheme`
Peak grouping scheme 1-based on neighboring peaks from detection matrix 2-based on on neighboring CFAR detected peaks.
- `uint8_t inRangeDirectionEn`
Grouping in range direction, 0- disabled, 1-enabled.
- `uint8_t inDopplerDirectionEn`
Grouping in Doppler direction, 0- disabled, 1-enabled.

3.45.1 Detailed Description

Millimeter Wave Demo Peak grouping Configuration.

The structure contains the Peak grouping configuration used in data path

Definition at line 90 of file mrr_config.h.

3.45.2 Field Documentation

3.45.2.1 inDopplerDirectionEn

```
uint8_t MmwDemo_PeakGroupingCfg_t::inDopplerDirectionEn
```

Grouping in Doppler direction, 0- disabled, 1-enabled.

Definition at line 106 of file mrr_config.h.

3.45.2.2 inRangeDirectionEn

```
uint8_t MmwDemo_PeakGroupingCfg_t::inRangeDirectionEn
```

Grouping in range direction, 0- disabled, 1-enabled.

Definition at line 103 of file mrr_config.h.

3.45.2.3 maxRangeIndex

```
uint16_t MmwDemo_PeakGroupingCfg_t::maxRangeIndex
```

maximum range index exported

Definition at line 96 of file mrr_config.h.

3.45.2.4 minRangeIndex

```
uint16_t MmwDemo_PeakGroupingCfg_t::minRangeIndex
```

minimum range index exported

Definition at line 93 of file mrr_config.h.

3.45.2.5 scheme

```
uint8_t MmwDemo_PeakGroupingCfg_t::scheme
```

Peak grouping scheme 1-based on neighboring peaks from detection matrix 2-based on on neighboring CFAR detected peaks.

Definition at line 100 of file mrr_config.h.

The documentation for this struct was generated from the following file:

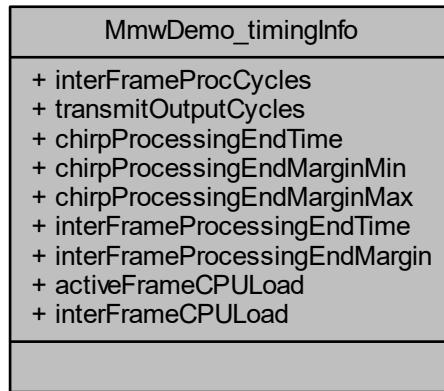
- common/ [mrr_config.h](#)

3.46 MmwDemo_timingInfo Struct Reference

Timing information.

```
#include <dss_data_path.h>
```

Collaboration diagram for MmwDemo_timingInfo:



Data Fields

- **uint32_t interFrameProcCycles**
number of processor cycles between frames excluding processing time to transmit output on UART
- **uint32_t transmitOutputCycles**
time to transmit out detection information (in DSP cycles)
- **uint32_t chirpProcessingEndTime**
Chirp processing end time.
- **uint32_t chirpProcessingEndMarginMin**
Chirp processing end margin in number of cycles before due time to start processing next chirp, minimum value.
- **uint32_t chirpProcessingEndMarginMax**
Chirp processing end margin in number of cycles before due time to start processing next chirp, maximum value.
- **uint32_t interFrameProcessingEndTime**
Inter frame processing end time.
- **uint32_t interFrameProcessingEndMargin**
Inter frame processing end margin in number of cycles before due time to start processing first chirp of the next frame.
- **uint32_t activeFrameCPULoad**
CPU Load during active frame period - i.e. chirping.
- **uint32_t interFrameCPULoad**
CPU Load during inter frame period - i.e. after chirps are done and before next frame starts.

3.46.1 Detailed Description

Timing information.

Definition at line 256 of file dss_data_path.h.

3.46.2 Field Documentation

3.46.2.1 activeFrameCPULoad

```
uint32_t MmwDemo_timingInfo::activeFrameCPULoad
```

CPU Load during active frame period - i.e. chirping.
Definition at line 285 of file dss_data_path.h.

3.46.2.2 chirpProcessingEndMarginMax

```
uint32_t MmwDemo_timingInfo::chirpProcessingEndMarginMax
```

Chirp processing end margin in number of cycles before due time to start processing next chirp, maximum value.
Definition at line 275 of file dss_data_path.h.

3.46.2.3 chirpProcessingEndMarginMin

```
uint32_t MmwDemo_timingInfo::chirpProcessingEndMarginMin
```

Chirp processing end margin in number of cycles before due time to start processing next chirp, minimum value.
Definition at line 271 of file dss_data_path.h.

3.46.2.4 chirpProcessingEndTime

```
uint32_t MmwDemo_timingInfo::chirpProcessingEndTime
```

Chirp processing end time.
Definition at line 267 of file dss_data_path.h.

3.46.2.5 interFrameCPULoad

```
uint32_t MmwDemo_timingInfo::interFrameCPULoad
```

CPU Load during inter frame period - i.e. after chirps are done and before next frame starts.
Definition at line 289 of file dss_data_path.h.

3.46.2.6 interFrameProcCycles

```
uint32_t MmwDemo_timingInfo::interFrameProcCycles
```

number of processor cycles between frames excluding processing time to transmit output on UART
Definition at line 260 of file dss_data_path.h.

3.46.2.7 interFrameProcessingEndMargin

```
uint32_t MmwDemo_timingInfo::interFrameProcessingEndMargin
```

Inter frame processing end margin in number of cycles before due time to start processing first chirp of the next frame.
Definition at line 282 of file dss_data_path.h.

3.46.2.8 interFrameProcessingEndTime

```
uint32_t MmwDemo_timingInfo::interFrameProcessingEndTime
```

Inter frame processing end time.
Definition at line 278 of file dss_data_path.h.

3.46.2.9 transmitOutputCycles

```
uint32_t MmwDemo_timingInfo::transmitOutputCycles
time to transmit out detection information (in DSP cycles)
```

Definition at line 264 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

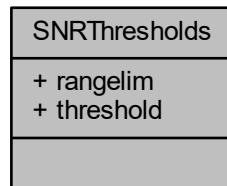
- [dss_data_path.h](#)

3.47 SNRThresholds Struct Reference

These parameters allow the SNR requirements to be varied as a function of range.

```
#include <dss_data_path.h>
```

Collaboration diagram for SNRThresholds:



Data Fields

- `uint16_t rangelim`
*Range (in meters * (1 << xyzOutputQFormat)) upto which the SNR requirement is valid.*
- `uint16_t threshold`
SNR threshold (dB) for the range.

3.47.1 Detailed Description

These parameters allow the SNR requirements to be varied as a function of range.

Definition at line 224 of file dss_data_path.h.

3.47.2 Field Documentation

3.47.2.1 rangelim

```
uint16_t SNRThresholds::rangelim
```

Range (in meters * (1 << xyzOutputQFormat)) upto which the SNR requirement is valid.

Definition at line 226 of file dss_data_path.h.

3.47.2.2 threshold

```
uint16_t SNRThresholds::threshold
```

SNR threshold (dB) for the range.

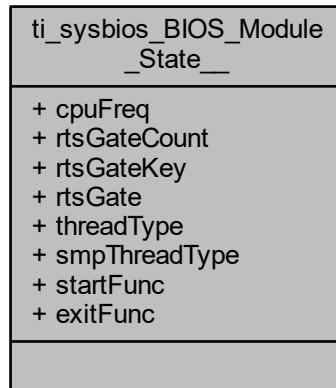
Definition at line 228 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

- `dss_data_path.h`

3.48 `ti_sysbios_BIOS_Module_State__` Struct Reference

Collaboration diagram for `ti_sysbios_BIOS_Module_State__`:



Data Fields

- `xdc_runtime_Types_FreqHz` **`cpuFreq`**
- `xdc_UInt` **`rtsGateCount`**
- `xdc_IArg` **`rtsGateKey`**
- `ti_sysbios_BIOS_RtsGateProxy_Handle` **`rtsGate`**
- `ti_sysbios_BIOS_ThreadType` **`threadType`**
- `__TA_ti_sysbios_BIOS_Module_State__smpThreadType` **`smpThreadType`**
- volatile `ti_sysbios_BIOS_StartFuncPtr` **`startFunc`**
- volatile `ti_sysbios_BIOS_ExitFuncPtr` **`exitFunc`**

3.48.1 Detailed Description

Definition at line 902 of file `dss_mrr_pe674.c`.

3.48.2 Field Documentation

3.48.2.1 `cpuFreq`

`xdc_runtime_Types_FreqHz` `ti_sysbios_BIOS_Module_State__::cpuFreq`

Definition at line 903 of file `dss_mrr_pe674.c`.

3.48.2.2 `exitFunc`

volatile `ti_sysbios_BIOS_ExitFuncPtr` `ti_sysbios_BIOS_Module_State__::exitFunc`

Definition at line 910 of file `dss_mrr_pe674.c`.

3.48.2.3 rtsGate

`ti_sysbios_BIOS_RtsGateProxy_Handle ti_sysbios_BIOS_Module_State__::rtsGate`
 Definition at line 906 of file `dss_mrr_pe674.c`.

3.48.2.4 rtsGateCount

`xdc_UInt ti_sysbios_BIOS_Module_State__::rtsGateCount`
 Definition at line 904 of file `dss_mrr_pe674.c`.

3.48.2.5 rtsGateKey

`xdc_IArg ti_sysbios_BIOS_Module_State__::rtsGateKey`
 Definition at line 905 of file `dss_mrr_pe674.c`.

3.48.2.6 smpThreadType

`__TA ti_sysbios_BIOS_Module_State__smpThreadType ti_sysbios_BIOS_Module_State__::smpThreadType`
 Definition at line 908 of file `dss_mrr_pe674.c`.

3.48.2.7 startFunc

`volatile ti_sysbios_BIOS_StartFuncPtr ti_sysbios_BIOS_Module_State__::startFunc`
 Definition at line 909 of file `dss_mrr_pe674.c`.

3.48.2.8 threadType

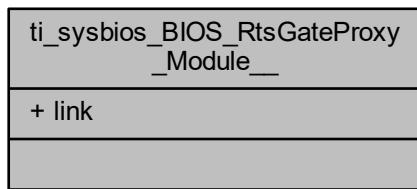
`ti_sysbios_BIOS_ThreadType ti_sysbios_BIOS_Module_State__::threadType`
 Definition at line 907 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.49 ti_sysbios_BIOS_RtsGateProxy_Module__ Struct Reference

Collaboration diagram for `ti_sysbios_BIOS_RtsGateProxy_Module__`:



Data Fields

- `xdc_runtime_Types_Link link`

3.49.1 Detailed Description

Definition at line 100 of file `dss_mrr_pe674.c`.

3.49.2 Field Documentation

3.49.2.1 link

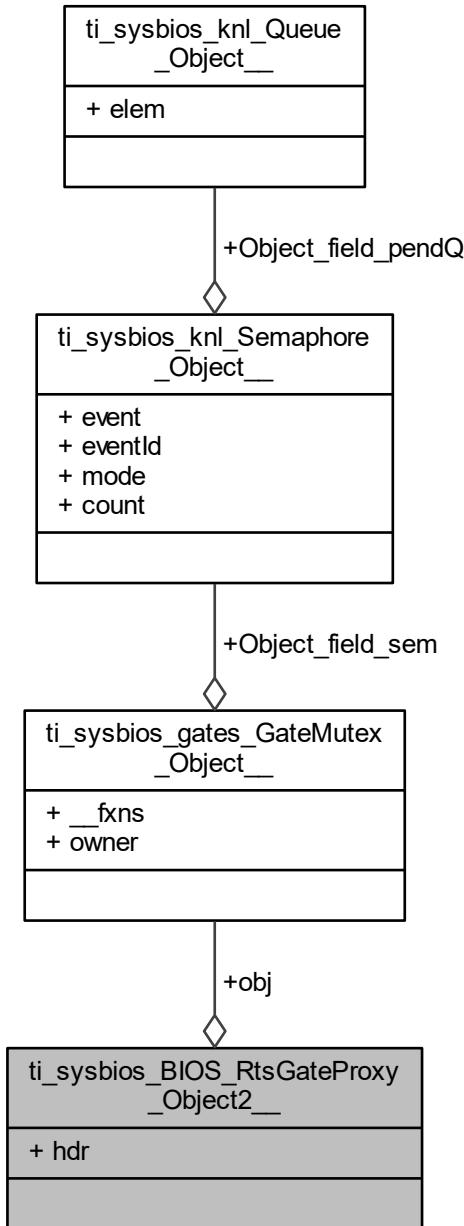
`xdc_runtime_Types_Link ti_sysbios_BIOS_RtsGateProxy_Module__::link`
Definition at line 101 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **`dss_mrr_pe674.c`**

3.50 ti_sysbios_BIOS_RtsGateProxy_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_BIOS_RtsGateProxy_Object2__:



Data Fields

- `xdc_runtime_Types_InstHdr` `hdr`
- `ti_sysbios_BIOS_RtsGateProxy_Object__` `obj`

3.50.1 Detailed Description

Definition at line 150 of file dss_mrr_pe674.c.

3.50.2 Field Documentation

3.50.2.1 `hdr`

```
xdc_runtime_Types_InstHdr ti_sysbios_BIOS_RtsGateProxy_Object2__::hdr
```

Definition at line 151 of file dss_mrr_pe674.c.

3.50.2.2 `obj`

```
ti_sysbios_BIOS_RtsGateProxy_Object__ ti_sysbios_BIOS_RtsGateProxy_Object2__::obj
```

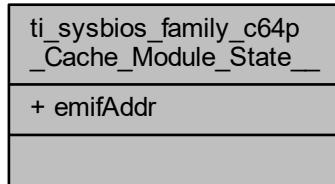
Definition at line 152 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.51 `ti_sysbios_family_c64p_Cache_Module_State__` Struct Reference

Collaboration diagram for `ti_sysbios_family_c64p_Cache_Module_State__`:



Data Fields

- volatile `xdc_UInt32 * emifAddr`

3.51.1 Detailed Description

Definition at line 943 of file dss_mrr_pe674.c.

3.51.2 Field Documentation

3.51.2.1 `emifAddr`

```
volatile xdc_UInt32* ti_sysbios_family_c64p_Cache_Module_State__::emifAddr
```

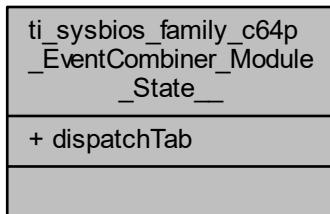
Definition at line 944 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.52 `ti_sysbios_family_c64p_EventCombiner_Module_State__` Struct Reference

Collaboration diagram for `ti_sysbios_family_c64p_EventCombiner_Module_State__`:



Data Fields

- `__TA_ti_sysbios_family_c64p_EventCombiner_Module_State__dispatchTab` **dispatchTab**

3.52.1 Detailed Description

Definition at line 956 of file `dss_mrr_pe674.c`.

3.52.2 Field Documentation

3.52.2.1 **dispatchTab**

`__TA_ti_sysbios_family_c64p_EventCombiner_Module_State__dispatchTab` `ti_sysbios_family_c64p_EventCombiner_Module_State__`::`dispatchTab`

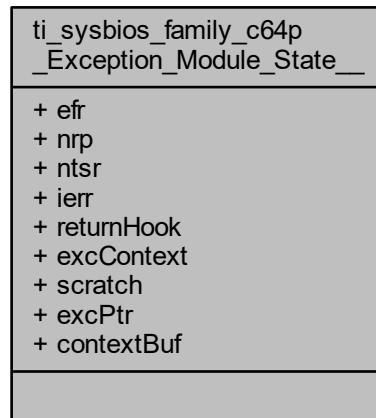
Definition at line 957 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.53 ti_sysbios_family_c64p_Exception_Module_State__ Struct Reference

Collaboration diagram for ti_sysbios_family_c64p_Exception_Module_State__:



Data Fields

- xdc_Bits32 **efr**
- xdc_Bits32 **nrp**
- xdc_Bits32 **ntsr**
- xdc_Bits32 **ierr**
- ti_sysbios_family_c64p_Exception_FuncPtr **returnHook**
- ti_sysbios_family_c64p_Exception_Context * **excContext**
- __TA_ti_sysbios_family_c64p_Exception_Module_State_s **scratch**
- xdc_Char * **excPtr**
- __TA_ti_sysbios_family_c64p_Exception_Module_State_s **contextBuf**

3.53.1 Detailed Description

Definition at line 1353 of file dss_mrr_pe674.c.

3.53.2 Field Documentation

3.53.2.1 contextBuf

```

__TA_ti_sysbios_family_c64p_Exception_Module_State_s contextBuf ti_sysbios_family_c64p_Exception_Module_State__::contextBuf

```

Definition at line 1362 of file dss_mrr_pe674.c.

3.53.2.2 efr

```

xdc_Bits32 ti_sysbios_family_c64p_Exception_Module_State__::efr

```

Definition at line 1354 of file dss_mrr_pe674.c.

3.53.2.3 excContext

```
ti_sysbios_family_c64p_Exception_Context* ti_sysbios_family_c64p_Exception_Module_State__←  
::excContext  
Definition at line 1359 of file dss_mrr_pe674.c.
```

3.53.2.4 excPtr

```
xdc_Char* ti_sysbios_family_c64p_Exception_Module_State__::excPtr  
Definition at line 1361 of file dss_mrr_pe674.c.
```

3.53.2.5 ierr

```
xdc_Bits32 ti_sysbios_family_c64p_Exception_Module_State__::ierr  
Definition at line 1357 of file dss_mrr_pe674.c.
```

3.53.2.6 nrp

```
xdc_Bits32 ti_sysbios_family_c64p_Exception_Module_State__::nrp  
Definition at line 1355 of file dss_mrr_pe674.c.
```

3.53.2.7 ntsr

```
xdc_Bits32 ti_sysbios_family_c64p_Exception_Module_State__::ntsr  
Definition at line 1356 of file dss_mrr_pe674.c.
```

3.53.2.8 returnHook

```
ti_sysbios_family_c64p_Exception_FuncPtr ti_sysbios_family_c64p_Exception_Module_State__←  
::returnHook  
Definition at line 1358 of file dss_mrr_pe674.c.
```

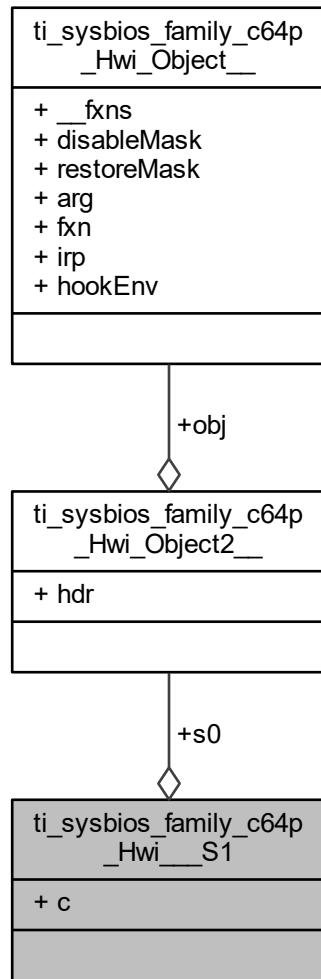
3.53.2.9 scratch

```
__TA_ti_sysbios_family_c64p_Exception_Module_State__scratch ti_sysbios_family_c64p_Exception←  
_Module_State__::scratch  
Definition at line 1360 of file dss_mrr_pe674.c.  
The documentation for this struct was generated from the following file:
```

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.54 ti_sysbios_family_c64p_Hwi____S1 Struct Reference

Collaboration diagram for ti_sysbios_family_c64p_Hwi____S1:



Data Fields

- `ti_sysbios_family_c64p_Hwi_Object2 s0`
- `char c`

3.54.1 Detailed Description

Definition at line 9185 of file dss_mrr_pe674.c.

3.54.2 Field Documentation

3.54.2.1 c

`char ti_sysbios_family_c64p_Hwi____S1::c`

Definition at line 9185 of file dss_mrr_pe674.c.

3.54.2.2 s0

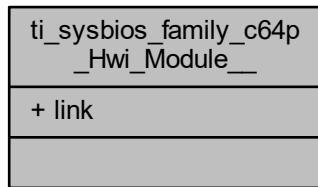
`ti_sysbios_family_c64p_Hwi_Object2__ ti_sysbios_family_c64p_Hwi__S1::s0`
Definition at line 9185 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.55 ti_sysbios_family_c64p_Hwi_Module__ Struct Reference

Collaboration diagram for ti_sysbios_family_c64p_Hwi_Module__:



Data Fields

- `xdc_runtime_Types_Link link`

3.55.1 Detailed Description

Definition at line 186 of file dss_mrr_pe674.c.

3.55.2 Field Documentation

3.55.2.1 link

`xdc_runtime_Types_Link ti_sysbios_family_c64p_Hwi_Module__::link`
Definition at line 187 of file dss_mrr_pe674.c.

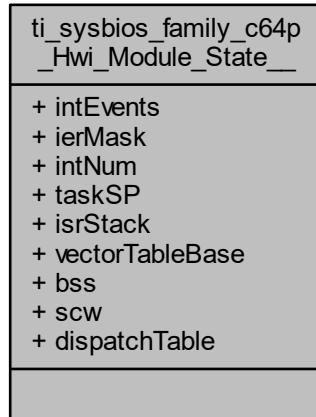
Referenced by `ti_sysbios_family_c64p_Hwi_Object__first__S()`, and `ti_sysbios_family_c64p_Hwi_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.56 ti_sysbios_family_c64p_Hwi_Module_State__ Struct Reference

Collaboration diagram for ti_sysbios_family_c64p_Hwi_Module_State__:



Data Fields

- __TA_ti_sysbios_family_c64p_Hwi_Module_State__intEvents **intEvents**
- xdc_Bits16 **ierMask**
- volatile xdc_Int **intNum**
- xdc_Char * **taskSP**
- xdc_Char * **isrStack**
- xdc_Ptr **vectorTableBase**
- xdc_Ptr **bss**
- xdc_Int **scw**
- __TA_ti_sysbios_family_c64p_Hwi_Module_State__dispatchTable **dispatchTable**

3.56.1 Detailed Description

Definition at line 1389 of file dss_mrr_pe674.c.

3.56.2 Field Documentation

3.56.2.1 **bss**

xdc_Ptr ti_sysbios_family_c64p_Hwi_Module_State__::bss

Definition at line 1396 of file dss_mrr_pe674.c.

3.56.2.2 **dispatchTable**

__TA_ti_sysbios_family_c64p_Hwi_Module_State__dispatchTable ti_sysbios_family_c64p_Hwi__<→
Module_State__::dispatchTable

Definition at line 1398 of file dss_mrr_pe674.c.

3.56.2.3 ierMask

`xdc_Bits16 ti_sysbios_family_c64p_Hwi_Module_State__::ierMask`
Definition at line 1391 of file `dss_mrr_pe674.c`.

3.56.2.4 intEvents

`__TA ti_sysbios_family_c64p_Hwi_Module_State__intEvents ti_sysbios_family_c64p_Hwi_Module__←
State__::intEvents`
Definition at line 1390 of file `dss_mrr_pe674.c`.

3.56.2.5 intNum

`volatile xdc_Int ti_sysbios_family_c64p_Hwi_Module_State__::intNum`
Definition at line 1392 of file `dss_mrr_pe674.c`.

3.56.2.6 isrStack

`xdc_Char* ti_sysbios_family_c64p_Hwi_Module_State__::isrStack`
Definition at line 1394 of file `dss_mrr_pe674.c`.

3.56.2.7 scw

`xdc_Int ti_sysbios_family_c64p_Hwi_Module_State__::scw`
Definition at line 1397 of file `dss_mrr_pe674.c`.

3.56.2.8 taskSP

`xdc_Char* ti_sysbios_family_c64p_Hwi_Module_State__::taskSP`
Definition at line 1393 of file `dss_mrr_pe674.c`.

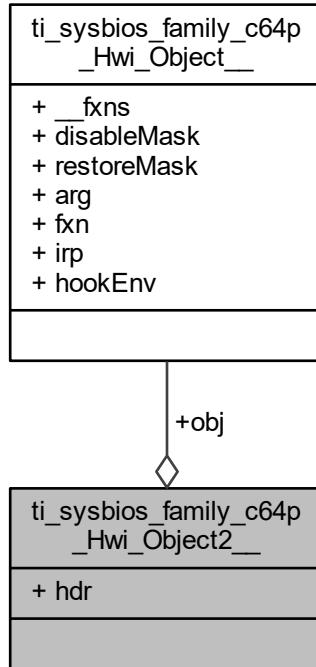
3.56.2.9 vectorTableBase

`xdc_Ptr ti_sysbios_family_c64p_Hwi_Module_State__::vectorTableBase`
Definition at line 1395 of file `dss_mrr_pe674.c`.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.57 ti_sysbios_family_c64p_Hwi_Object2__ Struct Reference

Collaboration diagram for `ti_sysbios_family_c64p_Hwi_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`
- `ti_sysbios_family_c64p_Hwi_Object__ obj`

3.57.1 Detailed Description

Definition at line 205 of file `dss_mrr_pe674.c`.

3.57.2 Field Documentation

3.57.2.1 **hdr**

`xdc_runtime_Types_InstHdr ti_sysbios_family_c64p_Hwi_Object2__::hdr`

Definition at line 206 of file `dss_mrr_pe674.c`.

3.57.2.2 **obj**

`ti_sysbios_family_c64p_Hwi_Object__ ti_sysbios_family_c64p_Hwi_Object2__::obj`

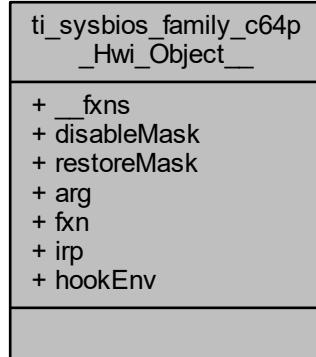
Definition at line 207 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.58 ti_sysbios_family_c64p_Hwi_Object__ Struct Reference

Collaboration diagram for ti_sysbios_family_c64p_Hwi_Object__:



Data Fields

- const ti_sysbios_family_c64p_Hwi_Fxns__ * __fxns
- xdc_Bits16 disableMask
- xdc_Bits16 restoreMask
- xdc_UArg arg
- ti_sysbios_family_c64p_Hwi_FuncPtr fxn
- ti_sysbios_family_c64p_Hwi_Irp irp
- __TA_ti_sysbios_family_c64p_Hwi_Instance_State_hookEnv hookEnv

3.58.1 Detailed Description

Definition at line 194 of file dss_mrr_pe674.c.

3.58.2 Field Documentation

3.58.2.1 __fxns

```
const ti_sysbios_family_c64p_Hwi_Fxns__* ti_sysbios_family_c64p_Hwi_Object__::__fxns
```

Definition at line 195 of file dss_mrr_pe674.c.

3.58.2.2 arg

```
xdc_UArg ti_sysbios_family_c64p_Hwi_Object__::arg
```

Definition at line 198 of file dss_mrr_pe674.c.

3.58.2.3 disableMask

```
xdc_Bits16 ti_sysbios_family_c64p_Hwi_Object__::disableMask
```

Definition at line 196 of file dss_mrr_pe674.c.

3.58.2.4 fxn

```
ti_sysbios_family_c64p_Hwi_FuncPtr ti_sysbios_family_c64p_Hwi_Object__::fxn
Definition at line 199 of file dss_mrr_pe674.c.
```

3.58.2.5 hookEnv

```
__TA_ti_sysbios_family_c64p_Hwi_Instance_State__hookEnv ti_sysbios_family_c64p_Hwi_Object__←
::hookEnv
Definition at line 201 of file dss_mrr_pe674.c.
```

3.58.2.6 irp

```
ti_sysbios_family_c64p_Hwi_Irp ti_sysbios_family_c64p_Hwi_Object__::irp
Definition at line 200 of file dss_mrr_pe674.c.
```

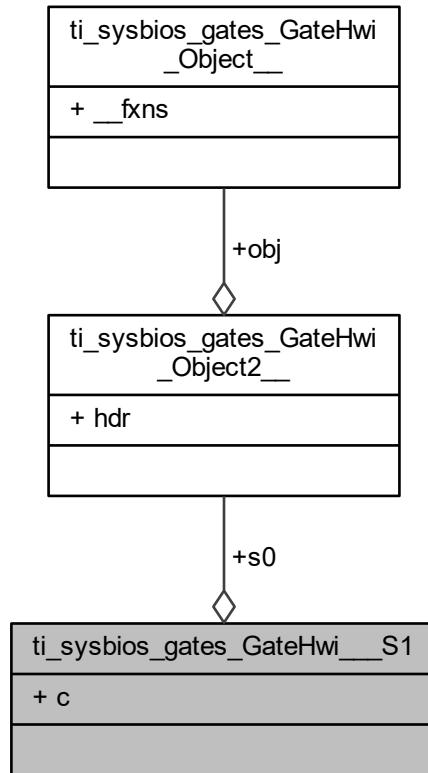
3.58.2.7 restoreMask

```
xdc_Bits16 ti_sysbios_family_c64p_Hwi_Object__::restoreMask
Definition at line 197 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:
```

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.59 ti_sysbios_gates_GateHwi__S1 Struct Reference

Collaboration diagram for ti_sysbios_gates_GateHwi__S1:



Data Fields

- `ti_sysbios_gates_GateHwi__Object2 s0`
- `char c`

3.59.1 Detailed Description

Definition at line 9204 of file dss_mrr_pe674.c.

3.59.2 Field Documentation

3.59.2.1 c

```
char ti_sysbios_gates_GateHwi__S1::c
```

Definition at line 9204 of file dss_mrr_pe674.c.

3.59.2.2 s0

```
ti_sysbios_gates_GateHwi__Object2 ti_sysbios_gates_GateHwi__S1::s0
```

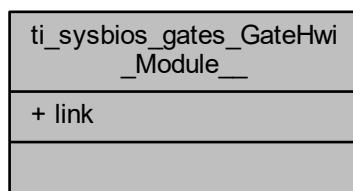
Definition at line 9204 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.60 ti_sysbios_gates_GateHwi_Module__ Struct Reference

Collaboration diagram for ti_sysbios_gates_GateHwi_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.60.1 Detailed Description

Definition at line 221 of file dss_mrr_pe674.c.

3.60.2 Field Documentation

3.60.2.1 link

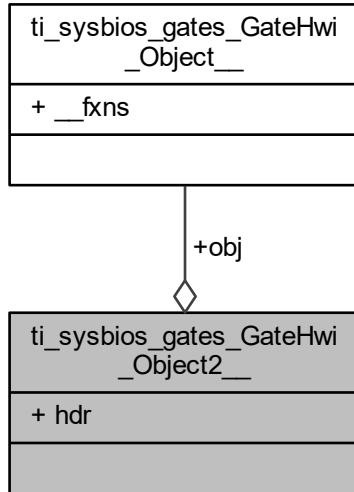
`xdc_runtime_Types_Link ti_sysbios_gates_GateHwi_Module__::link`
Definition at line 222 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateHwi_Object__first__S()`, and `ti_sysbios_gates_GateHwi_Object__next__S()`.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.61 ti_sysbios_gates_GateHwi_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_gates_GateHwi_Object2__:



Data Fields

- `xdc_runtime_Types_InstHdr` `hdr`
- `ti_sysbios_gates_GateHwi_Object` `obj`

3.61.1 Detailed Description

Definition at line 234 of file `dss_mrr_pe674.c`.

3.61.2 Field Documentation

3.61.2.1 `hdr`

`xdc_runtime_Types_InstHdr` `ti_sysbios_gates_GateHwi_Object2::hdr`
Definition at line 235 of file `dss_mrr_pe674.c`.

3.61.2.2 `obj`

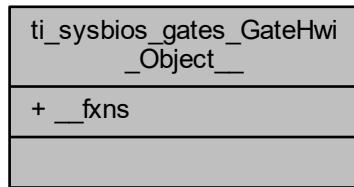
`ti_sysbios_gates_GateHwi_Object` `ti_sysbios_gates_GateHwi_Object2::obj`
Definition at line 236 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.62 ti_sysbios_gates_GateHwi_Object__ Struct Reference

Collaboration diagram for ti_sysbios_gates_GateHwi_Object__:



Data Fields

- const ti_sysbios_gates_GateHwi_Fxns__ * __fxns

3.62.1 Detailed Description

Definition at line 229 of file dss_mrr_pe674.c.

3.62.2 Field Documentation

3.62.2.1 __fxns

```
const ti_sysbios_gates_GateHwi_Fxns__* ti_sysbios_gates_GateHwi_Object__::__fxns
```

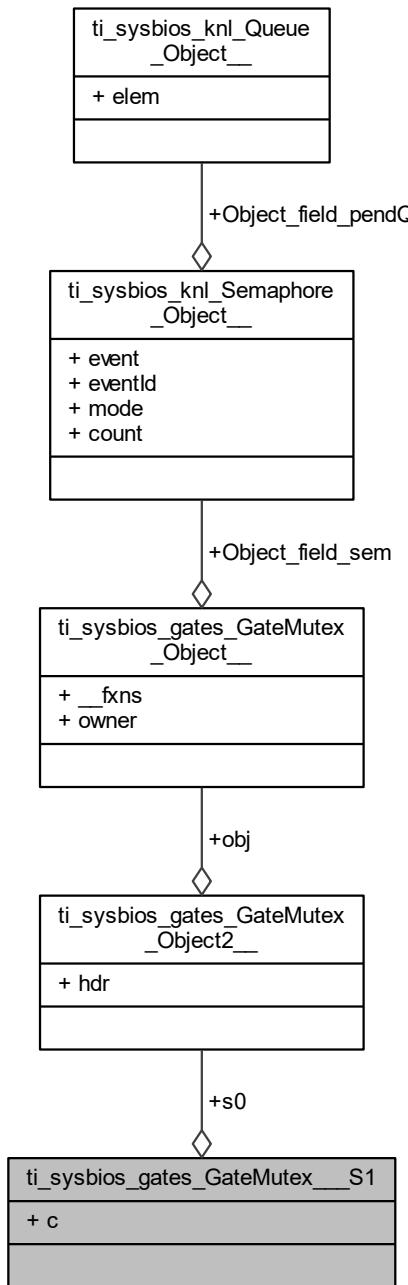
Definition at line 230 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.63 ti_sysbios_gates_GateMutex__S1 Struct Reference

Collaboration diagram for ti_sysbios_gates_GateMutex__S1:



Data Fields

- `ti_sysbios_gates_GateMutex_Object2__ s0`
- `char c`

3.63.1 Detailed Description

Definition at line 9223 of file dss_mrr_pe674.c.

3.63.2 Field Documentation

3.63.2.1 c

`char ti_sysbios_gates_GateMutex__S1::c`

Definition at line 9223 of file dss_mrr_pe674.c.

3.63.2.2 s0

`ti_sysbios_gates_GateMutex_Object2__ ti_sysbios_gates_GateMutex__S1::s0`

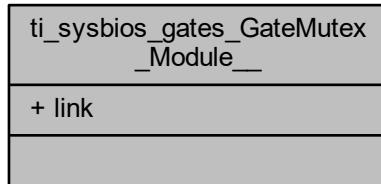
Definition at line 9223 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.64 `ti_sysbios_gates_GateMutex_Module` Struct Reference

Collaboration diagram for `ti_sysbios_gates_GateMutex_Module`:



Data Fields

- `xdc_runtime_Types_Link link`

3.64.1 Detailed Description

Definition at line 245 of file dss_mrr_pe674.c.

3.64.2 Field Documentation

3.64.2.1 link

`xdc_runtime_Types_Link ti_sysbios_gates_GateMutex_Module__::link`

Definition at line 246 of file dss_mrr_pe674.c.

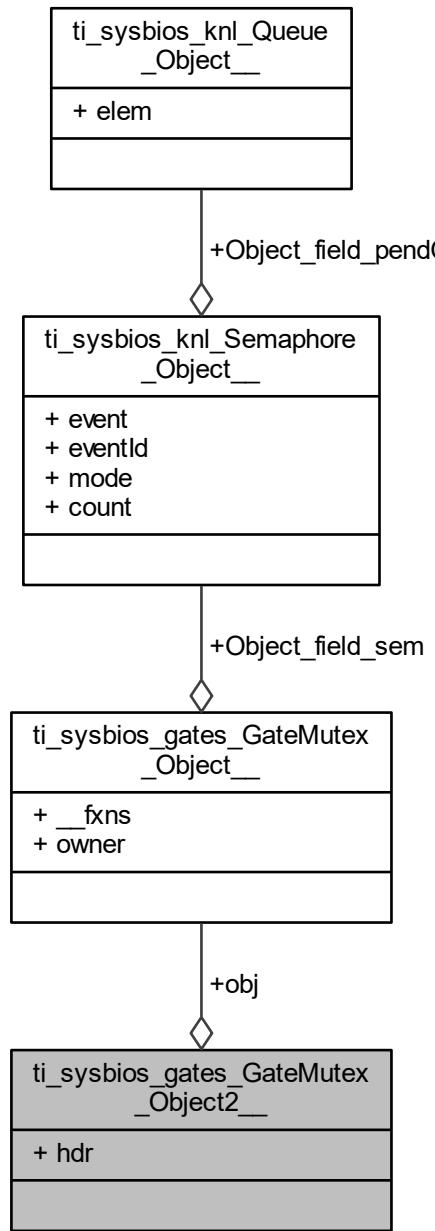
Referenced by `ti_sysbios_gates_GateMutex_Object__first__S()`, and `ti_sysbios_gates_GateMutex_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.65 ti_sysbios_gates_GateMutex_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_gates_GateMutex_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_gates_GateMutex_Object__ obj**

3.65.1 Detailed Description

Definition at line 141 of file `dss_mrr_pe674.c`.

3.65.2 Field Documentation

3.65.2.1 `hdr`

`xdc_runtime_Types_InstHdr ti_sysbios_gates_GateMutex_Object2__::hdr`
Definition at line 142 of file `dss_mrr_pe674.c`.

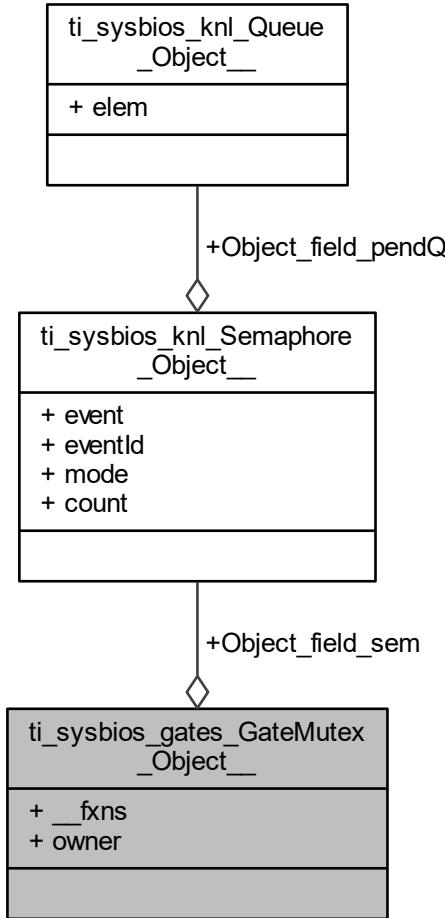
3.65.2.2 `obj`

`ti_sysbios_gates_GateMutex_Object__ ti_sysbios_gates_GateMutex_Object2__::obj`
Definition at line 143 of file `dss_mrr_pe674.c`.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.66 ti_sysbios_gates_GateMutex_Object__ Struct Reference

Collaboration diagram for ti_sysbios_gates_GateMutex_Object__:



Data Fields

- const ti_sysbios_gates_GateMutex_Fxns__ * __fxns
- ti_sysbios_knl_Task_Handle owner
- ti_sysbios_knl_Semaphore_Object__ Object_field_sem

3.66.1 Detailed Description

Definition at line 134 of file dss_mrr_pe674.c.

3.66.2 Field Documentation

3.66.2.1 __fxns

```
const ti_sysbios_gates_GateMutex_Fxns__* ti_sysbios_gates_GateMutex_Object__::__fxns
```

Definition at line 135 of file dss_mrr_pe674.c.

3.66.2.2 Object_field_sem

`ti_sysbios_knl_Semaphore_Object__ ti_sysbios_gates_GateMutex_Object__::Object_field_sem`
 Definition at line 137 of file dss_mrr_pe674.c.

3.66.2.3 owner

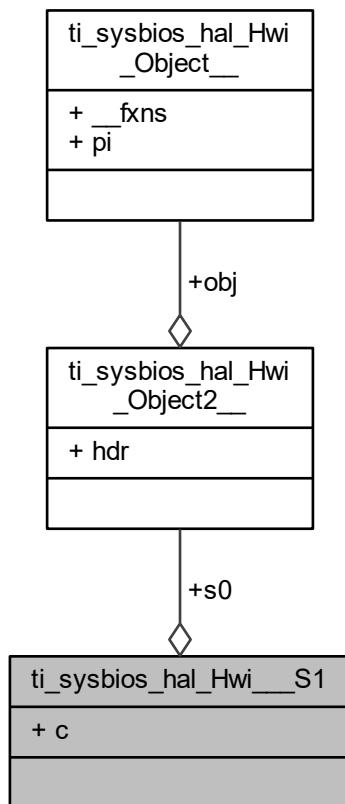
`ti_sysbios_knl_Task_Handle ti_sysbios_gates_GateMutex_Object__::owner`
 Definition at line 136 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.67 ti_sysbios_hal_Hwi__S1 Struct Reference

Collaboration diagram for `ti_sysbios_hal_Hwi__S1`:



Data Fields

- `ti_sysbios_hal_Hwi__Object2__ s0`
- `char c`

3.67.1 Detailed Description

Definition at line 9242 of file dss_mrr_pe674.c.

3.67.2 Field Documentation

3.67.2.1 c

```
char ti_sysbios_hal_Hwi__S1::c
```

Definition at line 9242 of file dss_mrr_pe674.c.

3.67.2.2 s0

```
ti_sysbios_hal_Hwi_Object2__ ti_sysbios_hal_Hwi__S1::s0
```

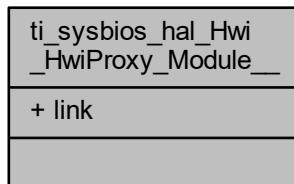
Definition at line 9242 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.68 ti_sysbios_hal_Hwi_HwiProxy_Module__ Struct Reference

Collaboration diagram for ti_sysbios_hal_Hwi_HwiProxy_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.68.1 Detailed Description

Definition at line 285 of file dss_mrr_pe674.c.

3.68.2 Field Documentation

3.68.2.1 link

```
xdc_runtime_Types_Link ti_sysbios_hal_Hwi_HwiProxy_Module__::link
```

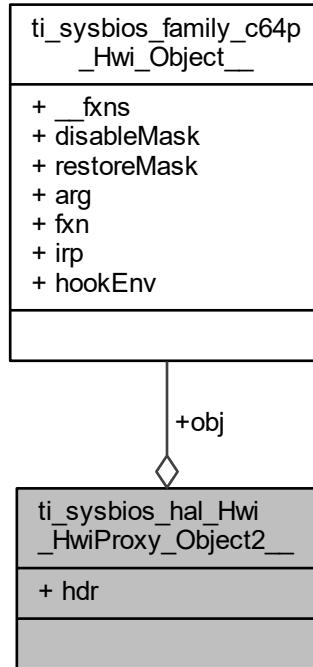
Definition at line 286 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.69 `ti_sysbios_hal_Hwi_HwiProxy_Object2__` Struct Reference

Collaboration diagram for `ti_sysbios_hal_Hwi_HwiProxy_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr` `hdr`
- `ti_sysbios_hal_Hwi_HwiProxy_Object__` `obj`

3.69.1 Detailed Description

Definition at line 298 of file `dss_mrr_pe674.c`.

3.69.2 Field Documentation

3.69.2.1 `hdr`

`xdc_runtime_Types_InstHdr` `ti_sysbios_hal_Hwi_HwiProxy_Object2__::hdr`

Definition at line 299 of file `dss_mrr_pe674.c`.

3.69.2.2 `obj`

`ti_sysbios_hal_Hwi_HwiProxy_Object__` `ti_sysbios_hal_Hwi_HwiProxy_Object2__::obj`

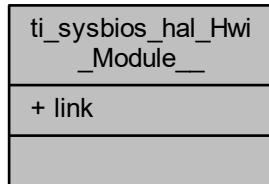
Definition at line 300 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.70 ti_sysbios_hal_Hwi_Module__ Struct Reference

Collaboration diagram for ti_sysbios_hal_Hwi_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.70.1 Detailed Description

Definition at line 260 of file dss_mrr_pe674.c.

3.70.2 Field Documentation

3.70.2.1 link

xdc_runtime_Types_Link ti_sysbios_hal_Hwi_Module__::link

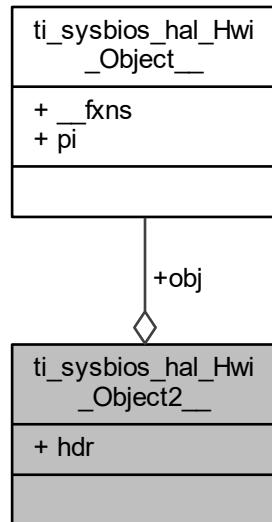
Definition at line 261 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_hal_Hwi_Object__first__S(), and ti_sysbios_hal_Hwi_Object__next__S().
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.71 ti_sysbios_hal_Hwi_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_hal_Hwi_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_hal_Hwi_Object__ obj**

3.71.1 Detailed Description

Definition at line 274 of file dss_mrr_pe674.c.

3.71.2 Field Documentation

3.71.2.1 **hdr**

`xdc_runtime_Types_InstHdr ti_sysbios_hal_Hwi_Object2__::hdr`
Definition at line 275 of file dss_mrr_pe674.c.

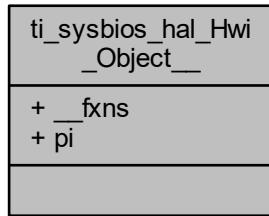
3.71.2.2 **obj**

`ti_sysbios_hal_Hwi_Object__ ti_sysbios_hal_Hwi_Object2__::obj`
Definition at line 276 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.72 ti_sysbios_hal_Hwi_Object__ Struct Reference

Collaboration diagram for ti_sysbios_hal_Hwi_Object__:



Data Fields

- const ti_sysbios_hal_Hwi_Fxns__ * __fxns
- ti_sysbios_hal_Hwi_HwiProxy_Handle pi

3.72.1 Detailed Description

Definition at line 268 of file dss_mrr_pe674.c.

3.72.2 Field Documentation

3.72.2.1 __fxns

const ti_sysbios_hal_Hwi_Fxns__* ti_sysbios_hal_Hwi_Object__::__fxns
Definition at line 269 of file dss_mrr_pe674.c.

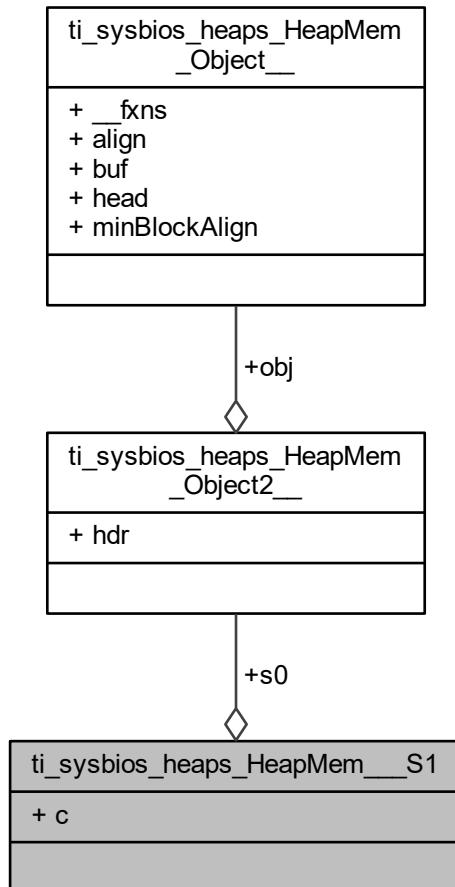
3.72.2.2 pi

ti_sysbios_hal_Hwi_HwiProxy_Handle ti_sysbios_hal_Hwi_Object__::pi
Definition at line 270 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.73 ti_sysbios_heaps_HeapMem__S1 Struct Reference

Collaboration diagram for ti_sysbios_heaps_HeapMem__S1:



Data Fields

- `ti_sysbios_heaps_HeapMem_Object2__ s0`
- `char c`

3.73.1 Detailed Description

Definition at line 9261 of file dss_mrr_pe674.c.

3.73.2 Field Documentation

3.73.2.1 c

`char ti_sysbios_heaps_HeapMem__S1::c`
Definition at line 9261 of file dss_mrr_pe674.c.

3.73.2.2 s0

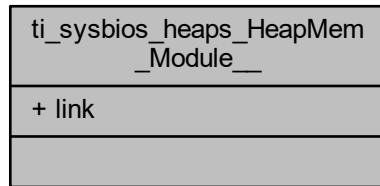
`ti_sysbios_heaps_HeapMem_Object2__ ti_sysbios_heaps_HeapMem__S1::s0`
Definition at line 9261 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.74 ti_sysbios_heaps_HeapMem_Module__ Struct Reference

Collaboration diagram for ti_sysbios_heaps_HeapMem_Module__:



Data Fields

- `xdc_runtime_Types_Link link`

3.74.1 Detailed Description

Definition at line 309 of file dss_mrr_pe674.c.

3.74.2 Field Documentation

3.74.2.1 link

`xdc_runtime_Types_Link ti_sysbios_heaps_HeapMem_Module__::link`
Definition at line 310 of file dss_mrr_pe674.c.

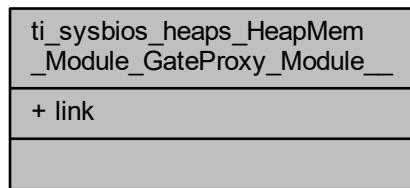
Referenced by `ti_sysbios_heaps_HeapMem_Object__first__S()`, and `ti_sysbios_heaps_HeapMem_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.75 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__ Struct Reference

Collaboration diagram for ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.75.1 Detailed Description

Definition at line 337 of file dss_mrr_pe674.c.

3.75.2 Field Documentation

3.75.2.1 link

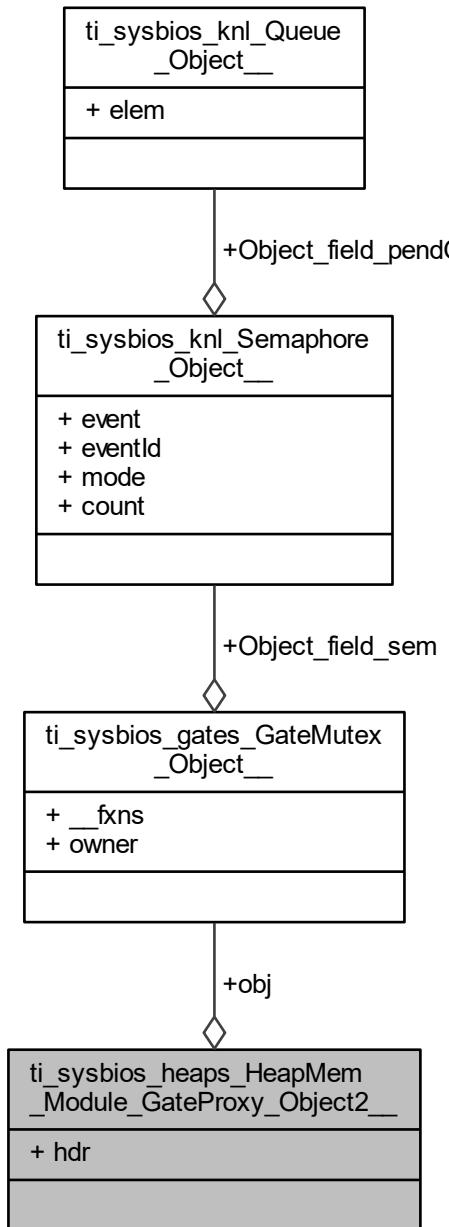
xdc_runtime_Types_Link **ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__::link**
Definition at line 338 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.76 ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_heaps_HeapMem_Module_GateProxy_Object__ obj**

3.76.1 Detailed Description

Definition at line 350 of file dss_mrr_pe674.c.

3.76.2 Field Documentation

3.76.2.1 `hdr`

```
xdc_runtime_Types_InstHdr ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2__::hdr
```

Definition at line 351 of file dss_mrr_pe674.c.

3.76.2.2 `obj`

```
ti_sysbios_heaps_HeapMem_Module_GateProxy_Object__ ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2__::obj
```

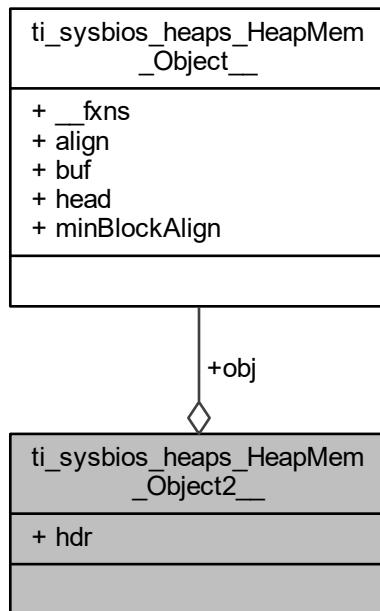
Definition at line 352 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.77 `ti_sysbios_heaps_HeapMem_Object2__` Struct Reference

Collaboration diagram for `ti_sysbios_heaps_HeapMem_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`
- `ti_sysbios_heaps_HeapMem_Object__ obj`

3.77.1 Detailed Description

Definition at line 326 of file dss_mrr_pe674.c.

3.77.2 Field Documentation

3.77.2.1 hdr

`xdc_runtime_Types_InstHdr ti_sysbios_heaps_HeapMem_Object2__::hdr`
Definition at line 327 of file dss_mrr_pe674.c.

3.77.2.2 obj

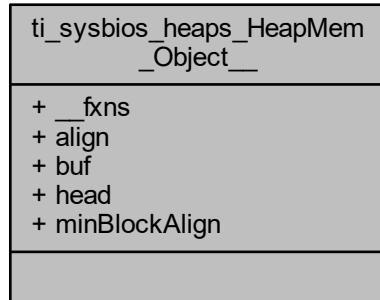
`ti_sysbios_heaps_HeapMem_Object__ ti_sysbios_heaps_HeapMem_Object2__::obj`
Definition at line 328 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.78 ti_sysbios_heaps_HeapMem_Object__ Struct Reference

Collaboration diagram for `ti_sysbios_heaps_HeapMem_Object__`:



Data Fields

- `const ti_sysbios_heaps_HeapMem_Fxns__ * __fxns`
- `xdc_runtime_Memory_Size align`
- `__TA_ti_sysbios_heaps_HeapMem_Instance_State__buf buf`
- `ti_sysbios_heaps_HeapMem_Header head`
- `xdc_SizeT minBlockAlign`

3.78.1 Detailed Description

Definition at line 317 of file dss_mrr_pe674.c.

3.78.2 Field Documentation

3.78.2.1 __fxns

```
const ti_sysbios_heaps_HeapMem_Fxns__* ti_sysbios_heaps_HeapMem_Object__::__fxns
Definition at line 318 of file dss_mrr_pe674.c.
```

3.78.2.2 align

```
xdc_runtime_Memory_Size ti_sysbios_heaps_HeapMem_Object__::align
Definition at line 319 of file dss_mrr_pe674.c.
```

3.78.2.3 buf

```
__TA_ti_sysbios_heaps_HeapMem_Instance_State__buf ti_sysbios_heaps_HeapMem_Object__::buf
Definition at line 320 of file dss_mrr_pe674.c.
```

3.78.2.4 head

```
ti_sysbios_heaps_HeapMem_Header ti_sysbios_heaps_HeapMem_Object__::head
Definition at line 321 of file dss_mrr_pe674.c.
```

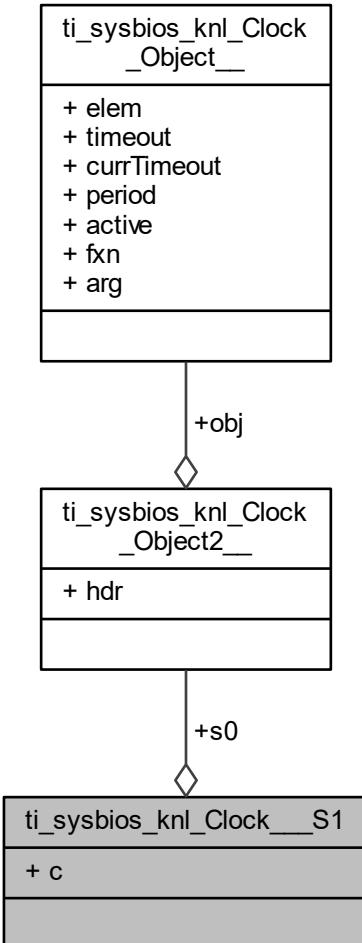
3.78.2.5 minBlockAlign

```
xdc_SizeT ti_sysbios_heaps_HeapMem_Object__::minBlockAlign
Definition at line 322 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:
```

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.79 ti_sysbios_knl_Clock__S1 Struct Reference

Collaboration diagram for ti_sysbios_knl_Clock__S1:



Data Fields

- `ti_sysbios_knl_Clock__Object2__ s0`
- `char c`

3.79.1 Detailed Description

Definition at line 9280 of file `dss_mrr_pe674.c`.

3.79.2 Field Documentation

3.79.2.1 c

```
char ti_sysbios_knl_Clock__S1::c
```

Definition at line 9280 of file dss_mrr_pe674.c.

3.79.2.2 s0

`ti_sysbios_knl_Clock_Object2 ti_sysbios_knl_Clock__S1::s0`

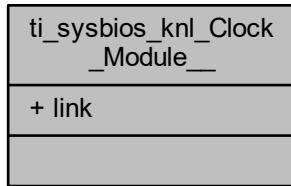
Definition at line 9280 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.80 `ti_sysbios_knl_Clock_Module` Struct Reference

Collaboration diagram for `ti_sysbios_knl_Clock_Module`:



Data Fields

- `xdc_runtime_Types_Link link`

3.80.1 Detailed Description

Definition at line 361 of file dss_mrr_pe674.c.

3.80.2 Field Documentation

3.80.2.1 link

`xdc_runtime_Types_Link ti_sysbios_knl_Clock_Module__::link`

Definition at line 362 of file dss_mrr_pe674.c.

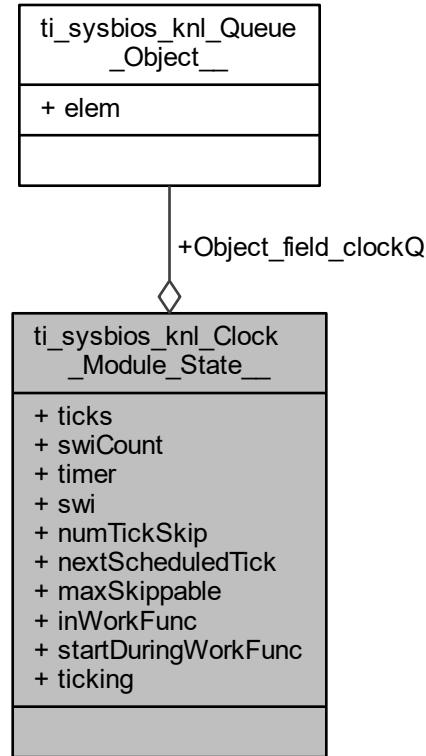
Referenced by `ti_sysbios_knl_Clock_Object__first__S()`, and `ti_sysbios_knl_Clock_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.81 ti_sysbios_knl_Clock_Module_State__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Clock_Module_State__:



Data Fields

- volatile xdc_UInt32 **ticks**
- xdc_UInt **swiCount**
- ti_sysbios_knl_Clock_TimerProxy_Handle **timer**
- ti_sysbios_knl_Swi_Handle **swi**
- volatile xdc_UInt **numTickSkip**
- xdc_UInt32 **nextScheduledTick**
- xdc_UInt32 **maxSkippable**
- xdc_Bool **inWorkFunc**
- volatile xdc_Bool **startDuringWorkFunc**
- xdc_Bool **ticking**
- **ti_sysbios_knl_Queue_Object__ Object_field_clockQ**

3.81.1 Detailed Description

Definition at line 1491 of file dss_mrr_pe674.c.

3.81.2 Field Documentation

3.81.2.1 inWorkFunc

```
xdc_Bool ti_sysbios_knl_Clock_Module_State__::inWorkFunc  
Definition at line 1499 of file dss_mrr_pe674.c.
```

3.81.2.2 maxSkippable

```
xdc_UInt32 ti_sysbios_knl_Clock_Module_State__::maxSkippable  
Definition at line 1498 of file dss_mrr_pe674.c.
```

3.81.2.3 nextScheduledTick

```
xdc_UInt32 ti_sysbios_knl_Clock_Module_State__::nextScheduledTick  
Definition at line 1497 of file dss_mrr_pe674.c.
```

3.81.2.4 numTickSkip

```
volatile xdc_UInt ti_sysbios_knl_Clock_Module_State__::numTickSkip  
Definition at line 1496 of file dss_mrr_pe674.c.
```

3.81.2.5 Object_field_clockQ

```
ti_sysbios_knl_Queue_Object__ ti_sysbios_knl_Clock_Module_State__::Object_field_clockQ  
Definition at line 1502 of file dss_mrr_pe674.c.
```

3.81.2.6 startDuringWorkFunc

```
volatile xdc_Bool ti_sysbios_knl_Clock_Module_State__::startDuringWorkFunc  
Definition at line 1500 of file dss_mrr_pe674.c.
```

3.81.2.7 swi

```
ti_sysbios_knl_Swi_Handle ti_sysbios_knl_Clock_Module_State__::swi  
Definition at line 1495 of file dss_mrr_pe674.c.
```

3.81.2.8 swiCount

```
xdc_UInt ti_sysbios_knl_Clock_Module_State__::swiCount  
Definition at line 1493 of file dss_mrr_pe674.c.
```

3.81.2.9 ticking

```
xdc_Bool ti_sysbios_knl_Clock_Module_State__::ticking  
Definition at line 1501 of file dss_mrr_pe674.c.
```

3.81.2.10 ticks

```
volatile xdc_UInt32 ti_sysbios_knl_Clock_Module_State__::ticks  
Definition at line 1492 of file dss_mrr_pe674.c.
```

3.81.2.11 timer

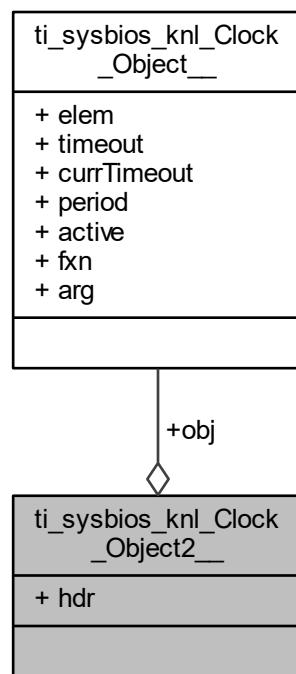
`ti_sysbios_knl_Clock_TimerProxy_Handle ti_sysbios_knl_Clock_Module_State__::timer`
 Definition at line 1494 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.82 **ti_sysbios_knl_Clock_Object2__ Struct Reference**

Collaboration diagram for `ti_sysbios_knl_Clock_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`
- `ti_sysbios_knl_Clock_Object__ obj`

3.82.1 Detailed Description

Definition at line 382 of file `dss_mrr_pe674.c`.

3.82.2 Field Documentation

3.82.2.1 hdr

`xdc_runtime_Types_InstHdr ti_sysbios_knl_Clock_Object2__::hdr`
 Definition at line 383 of file `dss_mrr_pe674.c`.

3.82.2.2 obj

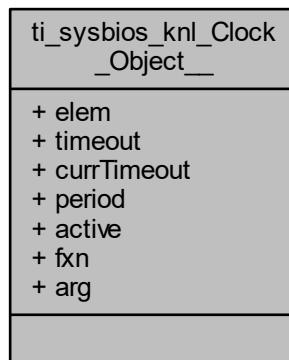
`ti_sysbios_knl_Clock_Object__ ti_sysbios_knl_Clock_Object2__::obj`
 Definition at line 384 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.83 `ti_sysbios_knl_Clock_Object__` Struct Reference

Collaboration diagram for `ti_sysbios_knl_Clock_Object__`:



Data Fields

- `ti_sysbios_knl_Queue_Elem elem`
- `xdc_UInt32 timeout`
- `xdc_UInt32 currTimeout`
- `xdc_UInt32 period`
- volatile `xdc_Bool active`
- `ti_sysbios_knl_Clock_FuncPtr ffn`
- `xdc_UArg arg`

3.83.1 Detailed Description

Definition at line 371 of file dss_mrr_pe674.c.

3.83.2 Field Documentation

3.83.2.1 active

`volatile xdc_Bool ti_sysbios_knl_Clock_Object__::active`
 Definition at line 376 of file dss_mrr_pe674.c.

3.83.2.2 arg

```
xdc_UArg ti_sysbios_knl_Clock_Object__::arg
Definition at line 378 of file dss_mrr_pe674.c.
```

3.83.2.3 currTimeout

```
xdc_UInt32 ti_sysbios_knl_Clock_Object__::currTimeout
Definition at line 374 of file dss_mrr_pe674.c.
```

3.83.2.4 elem

```
ti_sysbios_knl_Queue_Elem ti_sysbios_knl_Clock_Object__::elem
Definition at line 372 of file dss_mrr_pe674.c.
```

3.83.2.5 fxn

```
ti_sysbios_knl_Clock_FuncPtr ti_sysbios_knl_Clock_Object__::fxn
Definition at line 377 of file dss_mrr_pe674.c.
```

3.83.2.6 period

```
xdc_UInt32 ti_sysbios_knl_Clock_Object__::period
Definition at line 375 of file dss_mrr_pe674.c.
```

3.83.2.7 timeout

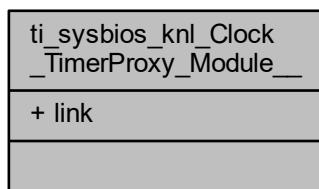
```
xdc_UInt32 ti_sysbios_knl_Clock_Object__::timeout
Definition at line 373 of file dss_mrr_pe674.c.
```

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.84 ti_sysbios_knl_Clock_TimerProxy_Module__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Clock_TimerProxy_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.84.1 Detailed Description

Definition at line 393 of file dss_mrr_pe674.c.

3.84.2 Field Documentation

3.84.2.1 link

`xdc_runtime_Types_Link ti_sysbios_knl_Clock_TimerProxy_Module__::link`

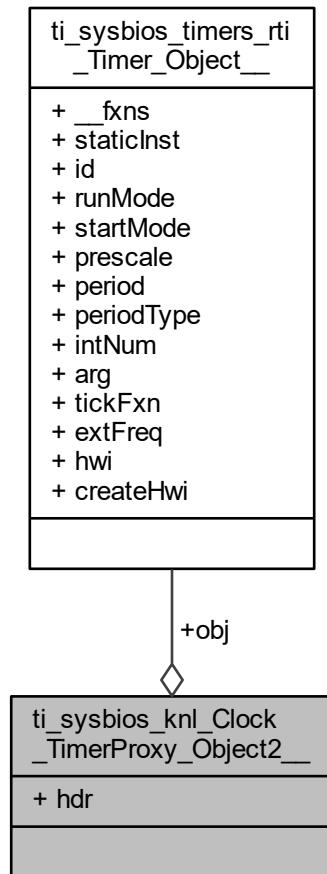
Definition at line 394 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.85 `ti_sysbios_knl_Clock_TimerProxy_Object2__` Struct Reference

Collaboration diagram for `ti_sysbios_knl_Clock_TimerProxy_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`

- **ti_sysbios_knl_Clock_TimerProxy_Object__ obj**

3.85.1 Detailed Description

Definition at line 428 of file **dss_mrr_pe674.c**.

3.85.2 Field Documentation

3.85.2.1 **hdr**

`xdc_runtime_Types_InstHdr ti_sysbios_knl_Clock_TimerProxy_Object2__::hdr`
Definition at line 429 of file **dss_mrr_pe674.c**.

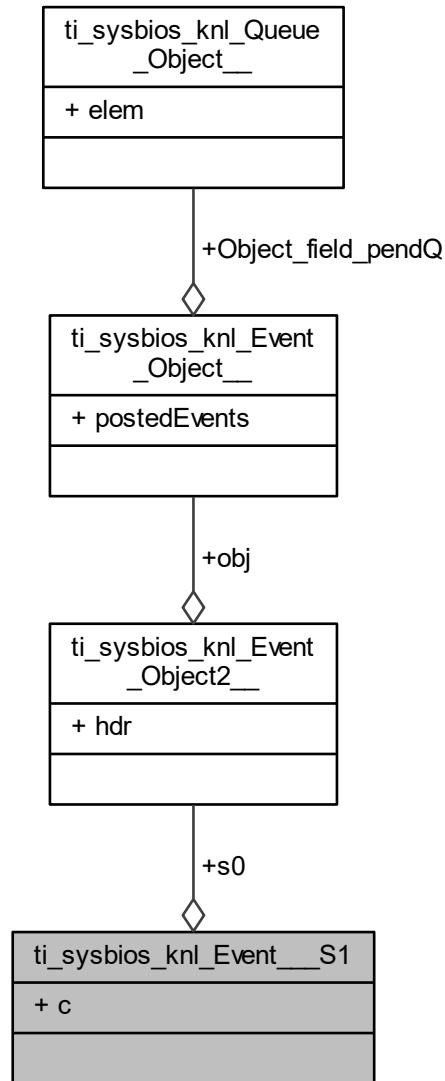
3.85.2.2 **obj**

`ti_sysbios_knl_Clock_TimerProxy_Object__ ti_sysbios_knl_Clock_TimerProxy_Object2__::obj`
Definition at line 430 of file **dss_mrr_pe674.c**.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.86 ti_sysbios_knl_Event__S1 Struct Reference

Collaboration diagram for ti_sysbios_knl_Event__S1:



Data Fields

- `ti_sysbios_knl_Event_Object2__ s0`
- `char c`

3.86.1 Detailed Description

Definition at line 9299 of file `dss_mrr_pe674.c`.

3.86.2 Field Documentation

3.86.2.1 c

```
char ti_sysbios_knl_Event____S1::c  
Definition at line 9299 of file dss_mrr_pe674.c.
```

3.86.2.2 s0

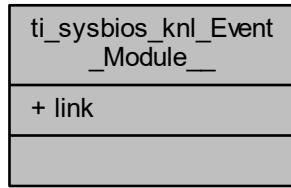
```
ti_sysbios_knl_Event_Object2__ ti_sysbios_knl_Event____S1::s0  
Definition at line 9299 of file dss_mrr_pe674.c.
```

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.87 ti_sysbios_knl_Event_Module__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Event_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.87.1 Detailed Description

Definition at line 439 of file dss_mrr_pe674.c.

3.87.2 Field Documentation

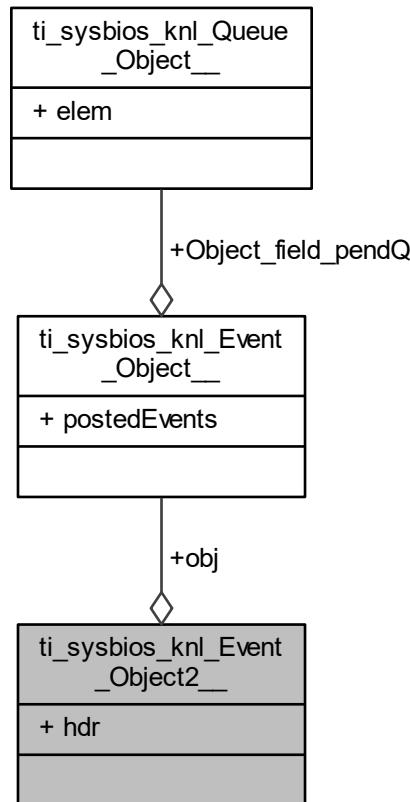
3.87.2.1 link

```
xdc_runtime_Types_Link ti_sysbios_knl_Event_Module__::link  
Definition at line 440 of file dss_mrr_pe674.c.  
Referenced by ti_sysbios_knl_Event_Object__first__S(), and ti_sysbios_knl_Event_Object__next__S().  
The documentation for this struct was generated from the following file:
```

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.88 ti_sysbios_knl_Event_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Event_Object2__:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`
- `ti_sysbios_knl_Event_Object__ obj`

3.88.1 Detailed Description

Definition at line 455 of file `dss_mrr_pe674.c`.

3.88.2 Field Documentation

3.88.2.1 `hdr`

`xdc_runtime_Types_InstHdr ti_sysbios_knl_Event_Object2__::hdr`
 Definition at line 456 of file `dss_mrr_pe674.c`.

3.88.2.2 `obj`

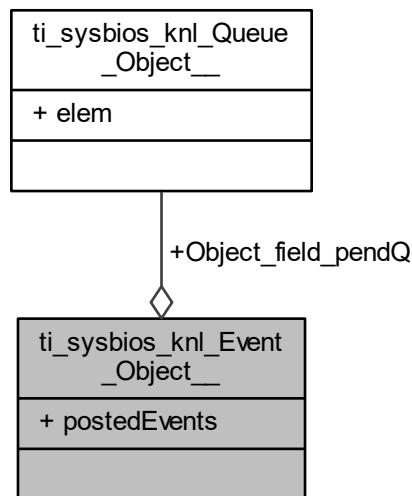
`ti_sysbios_knl_Event_Object__` `ti_sysbios_knl_Event_Object2__::obj`
 Definition at line 457 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.89 `ti_sysbios_knl_Event_Object__` Struct Reference

Collaboration diagram for `ti_sysbios_knl_Event_Object__`:



Data Fields

- `volatile xdc_UInt postedEvents`
- `ti_sysbios_knl_Queue_Object__ Object_field_pendQ`

3.89.1 Detailed Description

Definition at line 449 of file `dss_mrr_pe674.c`.

3.89.2 Field Documentation

3.89.2.1 `Object_field_pendQ`

`ti_sysbios_knl_Queue_Object__` `ti_sysbios_knl_Event_Object__::Object_field_pendQ`
 Definition at line 451 of file `dss_mrr_pe674.c`.

3.89.2.2 `postedEvents`

`volatile xdc_UInt ti_sysbios_knl_Event_Object__::postedEvents`

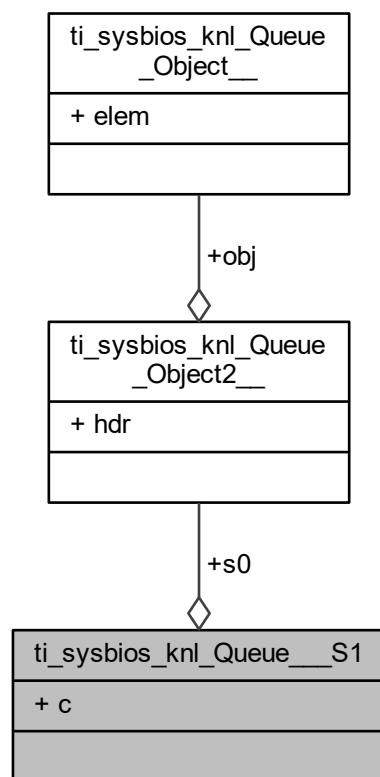
Definition at line 450 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.90 ti_sysbios_knl_Queue____S1 Struct Reference

Collaboration diagram for ti_sysbios_knl_Queue____S1:



Data Fields

- **ti_sysbios_knl_Queue_Object2__ s0**
- **char c**

3.90.1 Detailed Description

Definition at line 9318 of file dss_mrr_pe674.c.

3.90.2 Field Documentation

3.90.2.1 c

```
char ti_sysbios_knl_Queue____S1::c
```

Definition at line 9318 of file dss_mrr_pe674.c.

3.90.2.2 s0

ti_sysbios_knl_Queue_Object2__ ti_sysbios_knl_Queue____S1::s0

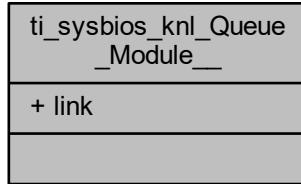
Definition at line 9318 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.91 **ti_sysbios_knl_Queue_Module__ Struct Reference**

Collaboration diagram for **ti_sysbios_knl_Queue_Module__**:



Data Fields

- `xdc_runtime_Types_Link link`

3.91.1 Detailed Description

Definition at line 481 of file dss_mrr_pe674.c.

3.91.2 Field Documentation

3.91.2.1 link

`xdc_runtime_Types_Link ti_sysbios_knl_Queue_Module__::link`

Definition at line 482 of file dss_mrr_pe674.c.

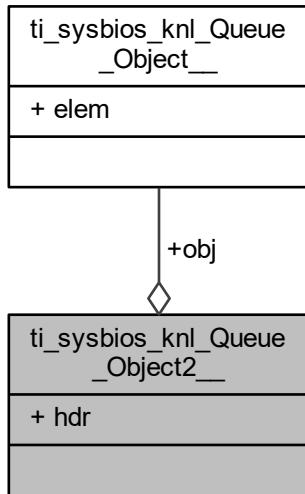
Referenced by `ti_sysbios_knl_Queue_Object__first__S()`, and `ti_sysbios_knl_Queue_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.92 ti_sysbios_knl_Queue_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Queue_Object2__:



Data Fields

- `xdc_runtime_Types_InstHdr` `hdr`
- `ti_sysbios_knl_Queue_Object__` `obj`

3.92.1 Detailed Description

Definition at line 113 of file `dss_mrr_pe674.c`.

3.92.2 Field Documentation

3.92.2.1 `hdr`

`xdc_runtime_Types_InstHdr` `ti_sysbios_knl_Queue_Object2__::hdr`
Definition at line 114 of file `dss_mrr_pe674.c`.

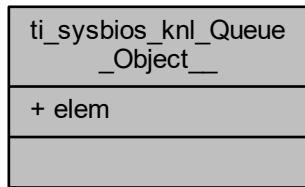
3.92.2.2 `obj`

`ti_sysbios_knl_Queue_Object__` `ti_sysbios_knl_Queue_Object2__::obj`
Definition at line 115 of file `dss_mrr_pe674.c`.
The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.93 ti_sysbios_knl_Queue_Object__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Queue_Object__:



Data Fields

- `ti_sysbios_knl_Queue_Elem elem`

3.93.1 Detailed Description

Definition at line 108 of file `dss_mrr_pe674.c`.

3.93.2 Field Documentation

3.93.2.1 elem

`ti_sysbios_knl_Queue_Elem ti_sysbios_knl_Queue_Object__::elem`

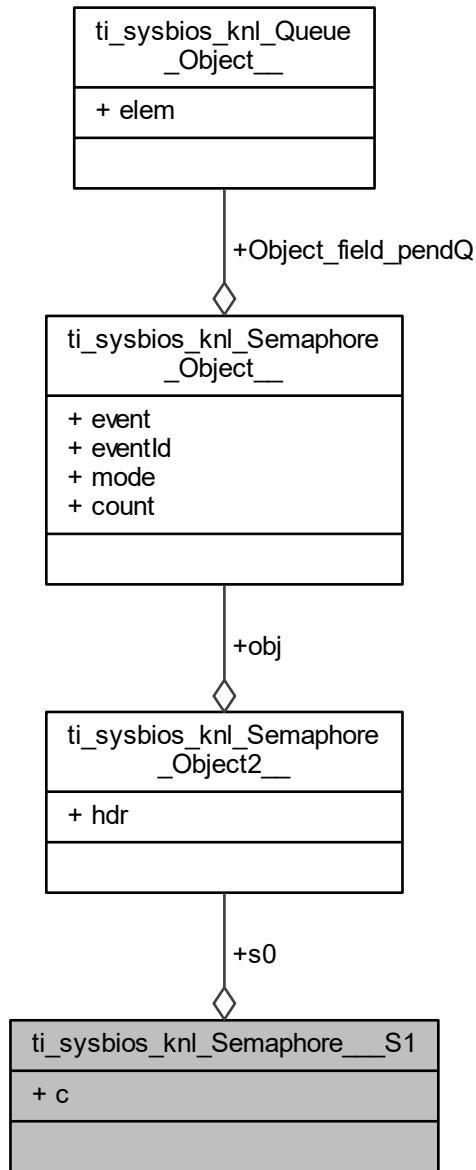
Definition at line 109 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.94 ti_sysbios_knl_Semaphore____S1 Struct Reference

Collaboration diagram for ti_sysbios_knl_Semaphore____S1:



Data Fields

- `ti_sysbios_knl_Semaphore_Object2__ s0`
- `char c`

3.94.1 Detailed Description

Definition at line 9337 of file `dss_mrr_pe674.c`.

3.94.2 Field Documentation

3.94.2.1 c

`char ti_sysbios_knl_Semaphore____S1::c`
Definition at line 9337 of file `dss_mrr_pe674.c`.

3.94.2.2 s0

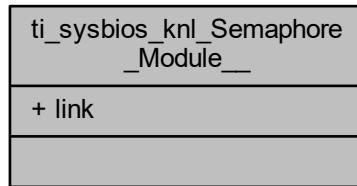
`ti_sysbios_knl_Semaphore_Object2__ ti_sysbios_knl_Semaphore____S1::s0`
Definition at line 9337 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.95 ti_sysbios_knl_Semaphore_Module__ Struct Reference

Collaboration diagram for `ti_sysbios_knl_Semaphore_Module__`:



Data Fields

- `xdc_runtime_Types_Link link`

3.95.1 Detailed Description

Definition at line 496 of file `dss_mrr_pe674.c`.

3.95.2 Field Documentation

3.95.2.1 link

`xdc_runtime_Types_Link ti_sysbios_knl_Semaphore_Module__::link`
Definition at line 497 of file `dss_mrr_pe674.c`.

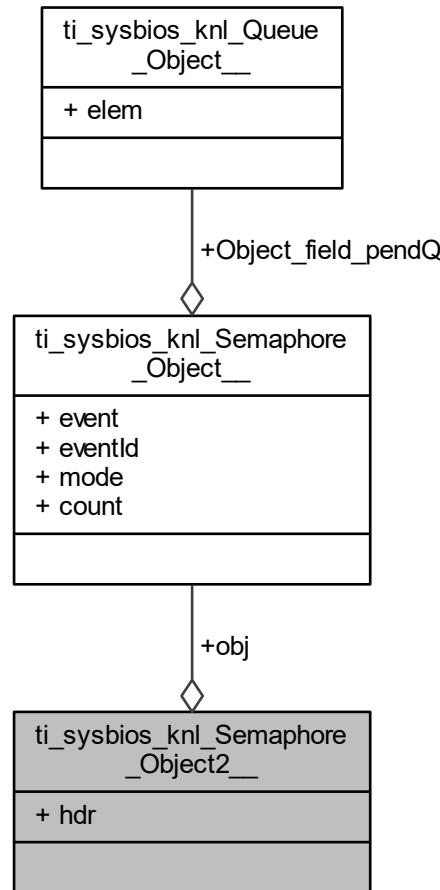
Referenced by `ti_sysbios_knl_Semaphore_Object__first__S()`, and `ti_sysbios_knl_Semaphore_Object__next__←S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.96 ti_sysbios_knl_Semaphore_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Semaphore_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_knl_Semaphore_Object__ obj**

3.96.1 Detailed Description

Definition at line 128 of file dss_mrr_pe674.c.

3.96.2 Field Documentation

3.96.2.1 **hdr**

`xdc_runtime_Types_InstHdr ti_sysbios_knl_Semaphore_Object2__::hdr`
Definition at line 129 of file dss_mrr_pe674.c.

3.96.2.2 obj

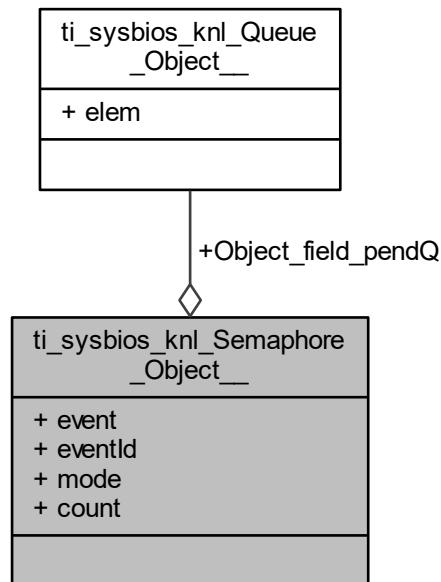
`ti_sysbios_knl_Semaphore_Object__` `ti_sysbios_knl_Semaphore_Object2__::obj`
 Definition at line 130 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.97 ti_sysbios_knl_Semaphore_Object__ Struct Reference

Collaboration diagram for `ti_sysbios_knl_Semaphore_Object__`:



Data Fields

- `ti_sysbios_knl_Event_Handle event`
- `xdc_UInt eventId`
- `ti_sysbios_knl_Semaphore_Mode mode`
- `volatile xdc_UInt16 count`
- `ti_sysbios_knl_Queue_Object__ Object_field_pendQ`

3.97.1 Detailed Description

Definition at line 119 of file `dss_mrr_pe674.c`.

3.97.2 Field Documentation

3.97.2.1 count

`volatile xdc_UInt16 ti_sysbios_knl_Semaphore_Object__::count`

Definition at line 123 of file dss_mrr_pe674.c.

3.97.2.2 event

`ti_sysbios_knl_Event_Handle ti_sysbios_knl_Semaphore_Object__::event`
Definition at line 120 of file dss_mrr_pe674.c.

3.97.2.3 eventId

`xdc_UInt ti_sysbios_knl_Semaphore_Object__::eventId`
Definition at line 121 of file dss_mrr_pe674.c.

3.97.2.4 mode

`ti_sysbios_knl_Semaphore_Mode ti_sysbios_knl_Semaphore_Object__::mode`
Definition at line 122 of file dss_mrr_pe674.c.

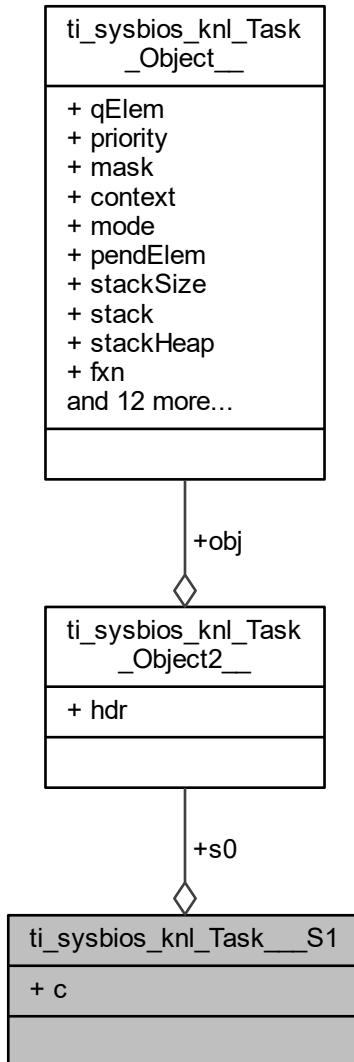
3.97.2.5 Object_field_pendQ

`ti_sysbios_knl_Queue_Object__ ti_sysbios_knl_Semaphore_Object__::Object_field_pendQ`
Definition at line 124 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.98 ti_sysbios_knl_Task__S1 Struct Reference

Collaboration diagram for ti_sysbios_knl_Task__S1:



Data Fields

- `ti_sysbios_knl_Task_Object2__ s0`
- `char c`

3.98.1 Detailed Description

Definition at line 9356 of file `dss_mrr_pe674.c`.

3.98.2 Field Documentation

3.98.2.1 c

```
char ti_sysbios_knl_Task___S1::c
Definition at line 9356 of file dss_mrr_pe674.c.
```

3.98.2.2 s0

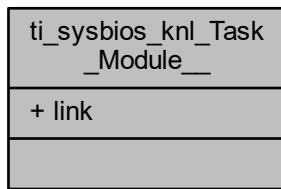
```
ti_sysbios_knl_Task_Object2__ ti_sysbios_knl_Task___S1::s0
Definition at line 9356 of file dss_mrr_pe674.c.
```

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.99 ti_sysbios_knl_Task_Module__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Task_Module__:



Data Fields

- xdc_runtime_Types_Link **link**

3.99.1 Detailed Description

Definition at line 511 of file dss_mrr_pe674.c.

3.99.2 Field Documentation

3.99.2.1 link

```
xdc_runtime_Types_Link ti_sysbios_knl_Task_Module__::link
```

Definition at line 512 of file dss_mrr_pe674.c.

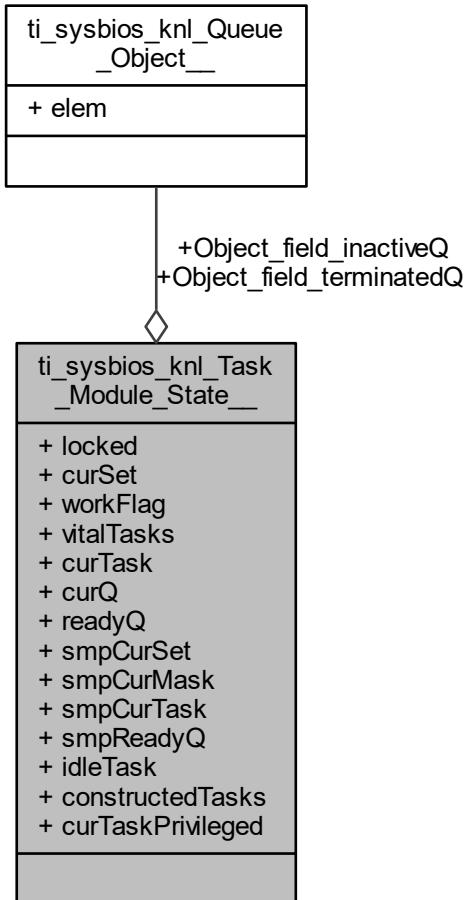
Referenced by `ti_sysbios_knl_Task_Object__first__S()`, and `ti_sysbios_knl_Task_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.100 `ti_sysbios_knl_Task_Module_State` Struct Reference

Collaboration diagram for `ti_sysbios_knl_Task_Module_State`:



Data Fields

- volatile xdc_Bool **locked**
- volatile xdc_UInt **curSet**
- volatile xdc_Bool **workFlag**
- xdc_UInt **vitalTasks**
- `ti_sysbios_knl_Task_Handle` **curTask**
- `ti_sysbios_knl_Queue_Handle` **curQ**
- `__TA_ti_sysbios_knl_Task_Module_State_readyQ` **readyQ**
- `__TA_ti_sysbios_knl_Task_Module_State_smpCurSet` **smpCurSet**
- `__TA(ti_sysbios_knl_Task_Module_State_smpCurMask)` **smpCurMask**
- `__TA(ti_sysbios_knl_Task_Module_State_smpCurTask)` **smpCurTask**
- `__TA(ti_sysbios_knl_Task_Module_State_smpReadyQ)` **smpReadyQ**
- `__TA(ti_sysbios_knl_Task_Module_State_idleTask)` **idleTask**
- `__TA(ti_sysbios_knl_Task_Module_State_constructedTasks)` **constructedTasks**
- xdc_Bool **curTaskPrivileged**
- `ti_sysbios_knl_Queue_Object` **Object_field_inactiveQ**
- `ti_sysbios_knl_Queue_Object` **Object_field_terminatedQ**

3.100.1 Detailed Description

Definition at line 1592 of file dss_mrr_pe674.c.

3.100.2 Field Documentation

3.100.2.1 constructedTasks

```
__TA_ti_sysbios_knl_Task_Module_State__constructedTasks ti_sysbios_knl_Task_Module_State__::constructedTasks
```

Definition at line 1605 of file dss_mrr_pe674.c.

3.100.2.2 curQ

```
ti_sysbios_knl_Queue_Handle ti_sysbios_knl_Task_Module_State__::curQ
```

Definition at line 1598 of file dss_mrr_pe674.c.

3.100.2.3 curSet

```
volatile xdc_UInt ti_sysbios_knl_Task_Module_State__::curSet
```

Definition at line 1594 of file dss_mrr_pe674.c.

3.100.2.4 curTask

```
ti_sysbios_knl_Task_Handle ti_sysbios_knl_Task_Module_State__::curTask
```

Definition at line 1597 of file dss_mrr_pe674.c.

3.100.2.5 curTaskPrivileged

```
xdc_Bool ti_sysbios_knl_Task_Module_State__::curTaskPrivileged
```

Definition at line 1606 of file dss_mrr_pe674.c.

3.100.2.6 idleTask

```
__TA_ti_sysbios_knl_Task_Module_State__idleTask ti_sysbios_knl_Task_Module_State__::idleTask
```

Definition at line 1604 of file dss_mrr_pe674.c.

3.100.2.7 locked

```
volatile xdc_Bool ti_sysbios_knl_Task_Module_State__::locked
```

Definition at line 1593 of file dss_mrr_pe674.c.

3.100.2.8 Object_field_inactiveQ

```
ti_sysbios_knl_Queue_Object__ ti_sysbios_knl_Task_Module_State__::Object_field_inactiveQ
```

Definition at line 1607 of file dss_mrr_pe674.c.

3.100.2.9 Object_field_terminatedQ

`__TA_ti_sysbios_knl_Task_Module_State__Object ti_sysbios_knl_Task_Module_State__::Object_field_terminatedQ`
Definition at line 1608 of file dss_mrr_pe674.c.

3.100.2.10 readyQ

`__TA_ti_sysbios_knl_Task_Module_State__readyQ ti_sysbios_knl_Task_Module_State__::readyQ`
Definition at line 1599 of file dss_mrr_pe674.c.

3.100.2.11 smpCurMask

`__TA_ti_sysbios_knl_Task_Module_State__smpCurMask ti_sysbios_knl_Task_Module_State__::smpCurMask`
Definition at line 1601 of file dss_mrr_pe674.c.

3.100.2.12 smpCurSet

`__TA_ti_sysbios_knl_Task_Module_State__smpCurSet ti_sysbios_knl_Task_Module_State__::smpCurSet`
Definition at line 1600 of file dss_mrr_pe674.c.

3.100.2.13 smpCurTask

`__TA_ti_sysbios_knl_Task_Module_State__smpCurTask ti_sysbios_knl_Task_Module_State__::smpCurTask`
Definition at line 1602 of file dss_mrr_pe674.c.

3.100.2.14 smpReadyQ

`__TA_ti_sysbios_knl_Task_Module_State__smpReadyQ ti_sysbios_knl_Task_Module_State__::smpReadyQ`
Definition at line 1603 of file dss_mrr_pe674.c.

3.100.2.15 vitalTasks

`xdc_UInt ti_sysbios_knl_Task_Module_State__::vitalTasks`
Definition at line 1596 of file dss_mrr_pe674.c.

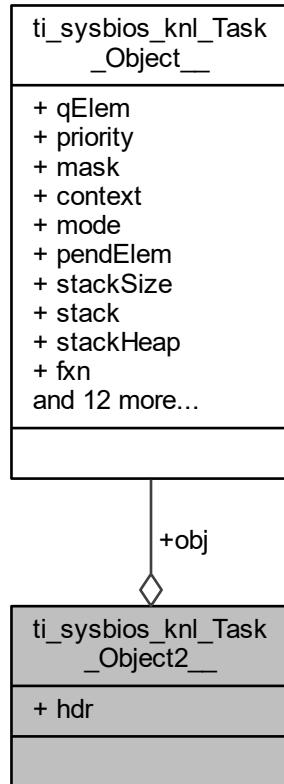
3.100.2.16 workFlag

`volatile xdc_Bool ti_sysbios_knl_Task_Module_State__::workFlag`
Definition at line 1595 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.101 ti_sysbios_knl_Task_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Task_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_knl_Task_Object__ obj**

3.101.1 Detailed Description

Definition at line 547 of file dss_mrr_pe674.c.

3.101.2 Field Documentation

3.101.2.1 **hdr**

`xdc_runtime_Types_InstHdr ti_sysbios_knl_Task_Object2__::hdr`
Definition at line 548 of file dss_mrr_pe674.c.

3.101.2.2 **obj**

`ti_sysbios_knl_Task_Object__ ti_sysbios_knl_Task_Object2__::obj`

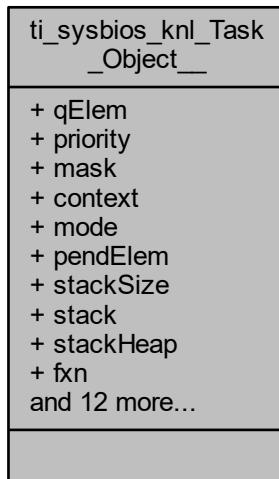
Definition at line 549 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.102 ti_sysbios_knl_Task_Object__ Struct Reference

Collaboration diagram for ti_sysbios_knl_Task_Object__:



Data Fields

- `ti_sysbios_knl_Task Queue_Elem qElem`
- volatile `xdc_Int priority`
- `xdc_UInt mask`
- `xdc_Ptr context`
- `ti_sysbios_knl_Task_Mode mode`
- `ti_sysbios_knl_Task_PendElem * pendElem`
- `xdc_SizeT stackSize`
- `__TA_ti_sysbios_knl_Task_Instance_State_stack stack`
- `xdc_runtime_IHeap_Handle stackHeap`
- `ti_sysbios_knl_Task_FuncPtr fxn`
- `xdc_UArg arg0`
- `xdc_UArg arg1`
- `xdc_Ptr env`
- `__TA_ti_sysbios_knl_Task_Instance_State_hookEnv hookEnv`
- `xdc_Bool vitalTaskFlag`
- `ti_sysbios_knl_Task_Queue_Handle readyQ`
- `xdc_UInt curCoreId`
- `xdc_UInt affinity`
- `xdc_Bool privileged`
- `xdc_Ptr domain`
- `xdc_UInt32 checkValue`
- `xdc_Ptr tls`

3.102.1 Detailed Description

Definition at line 521 of file dss_mrr_pe674.c.

3.102.2 Field Documentation

3.102.2.1 affinity

```
xdc_UInt ti_sysbios_knl_Task_Object__::affinity
```

Definition at line 539 of file dss_mrr_pe674.c.

3.102.2.2 arg0

```
xdc_UArg ti_sysbios_knl_Task_Object__::arg0
```

Definition at line 532 of file dss_mrr_pe674.c.

3.102.2.3 arg1

```
xdc_UArg ti_sysbios_knl_Task_Object__::arg1
```

Definition at line 533 of file dss_mrr_pe674.c.

3.102.2.4 checkValue

```
xdc_UInt32 ti_sysbios_knl_Task_Object__::checkValue
```

Definition at line 542 of file dss_mrr_pe674.c.

3.102.2.5 context

```
xdc_Ptr ti_sysbios_knl_Task_Object__::context
```

Definition at line 525 of file dss_mrr_pe674.c.

3.102.2.6 curCoreId

```
xdc_UInt ti_sysbios_knl_Task_Object__::curCoreID
```

Definition at line 538 of file dss_mrr_pe674.c.

3.102.2.7 domain

```
xdc_Ptr ti_sysbios_knl_Task_Object__::domain
```

Definition at line 541 of file dss_mrr_pe674.c.

3.102.2.8 env

```
xdc_Ptr ti_sysbios_knl_Task_Object__::env
```

Definition at line 534 of file dss_mrr_pe674.c.

3.102.2.9 fxn

```
ti_sysbios_knl_Task_FuncPtr ti_sysbios_knl_Task_Object__::fxn
```

Definition at line 531 of file dss_mrr_pe674.c.

3.102.2.10 hookEnv

`__TA_ti_sysbios_knl_Task_Instance_State__hookEnv ti_sysbios_knl_Task_Object__::hookEnv`
Definition at line 535 of file dss_mrr_pe674.c.

3.102.2.11 mask

`xdc_UInt ti_sysbios_knl_Task_Object__::mask`
Definition at line 524 of file dss_mrr_pe674.c.

3.102.2.12 mode

`ti_sysbios_knl_Task_Mode ti_sysbios_knl_Task_Object__::mode`
Definition at line 526 of file dss_mrr_pe674.c.

3.102.2.13 pendElem

`ti_sysbios_knl_Task_PendElem* ti_sysbios_knl_Task_Object__::pendElem`
Definition at line 527 of file dss_mrr_pe674.c.

3.102.2.14 priority

`volatile xdc_Int ti_sysbios_knl_Task_Object__::priority`
Definition at line 523 of file dss_mrr_pe674.c.

3.102.2.15 privileged

`xdc_Bool ti_sysbios_knl_Task_Object__::privileged`
Definition at line 540 of file dss_mrr_pe674.c.

3.102.2.16 qElem

`ti_sysbios_knl_Queue_Elem ti_sysbios_knl_Task_Object__::qElem`
Definition at line 522 of file dss_mrr_pe674.c.

3.102.2.17 readyQ

`ti_sysbios_knl_Queue_Handle ti_sysbios_knl_Task_Object__::readyQ`
Definition at line 537 of file dss_mrr_pe674.c.

3.102.2.18 stack

`__TA_ti_sysbios_knl_Task_Instance_State__stack ti_sysbios_knl_Task_Object__::stack`
Definition at line 529 of file dss_mrr_pe674.c.

3.102.2.19 stackHeap

`xdc_runtime_IHeap_Handle ti_sysbios_knl_Task_Object__::stackHeap`
Definition at line 530 of file dss_mrr_pe674.c.

3.102.2.20 stackSize

`xdc_SizeT ti_sysbios_knl_Task_Object__::stackSize`
 Definition at line 528 of file dss_mrr_pe674.c.

3.102.2.21 tls

`xdc_Ptr ti_sysbios_knl_Task_Object__::tls`
 Definition at line 543 of file dss_mrr_pe674.c.

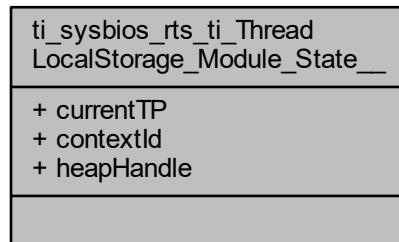
3.102.2.22 vitalTaskFlag

`xdc_Bool ti_sysbios_knl_Task_Object__::vitalTaskFlag`
 Definition at line 536 of file dss_mrr_pe674.c.
 The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.103 **ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__ Struct Reference**

Collaboration diagram for `ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__`:



Data Fields

- `xdc_Ptr currentTP`
- `xdc_UInt contextId`
- `xdc_runtime_IHeap_Handle heapHandle`

3.103.1 Detailed Description

Definition at line 1637 of file dss_mrr_pe674.c.

3.103.2 Field Documentation

3.103.2.1 contextId

```
xdc_UInt ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__::contextId  
Definition at line 1639 of file dss_mrr_pe674.c.
```

3.103.2.2 currentTP

```
xdc_Ptr ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__::currentTP  
Definition at line 1638 of file dss_mrr_pe674.c.
```

3.103.2.3 heapHandle

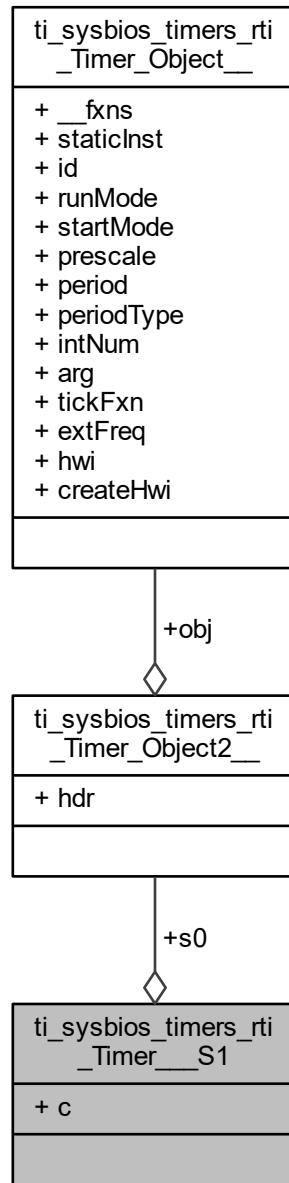
```
xdc_runtime_IHeap_Handle ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__::heapHandle  
Definition at line 1640 of file dss_mrr_pe674.c.
```

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.104 ti_sysbios_timers_rti_Timer____S1 Struct Reference

Collaboration diagram for ti_sysbios_timers_rti_Timer____S1:



Data Fields

- `ti_sysbios_timers_rti_Timer_Object2__ s0`
- `char c`

3.104.1 Detailed Description

Definition at line 9375 of file `dss_mrr_pe674.c`.

3.104.2 Field Documentation

3.104.2.1 c

`char ti_sysbios_timers_rti_Timer____S1::c`
 Definition at line 9375 of file dss_mrr_pe674.c.

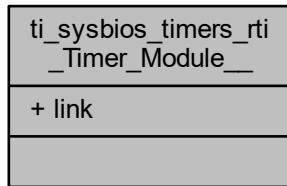
3.104.2.2 s0

`ti_sysbios_timers_rti_Timer_Object2__ ti_sysbios_timers_rti_Timer____S1::s0`
 Definition at line 9375 of file dss_mrr_pe674.c.
 The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.105 ti_sysbios_timers_rti_Timer_Module__ Struct Reference

Collaboration diagram for `ti_sysbios_timers_rti_Timer_Module__`:



Data Fields

- `xdc_runtime_Types_Link link`

3.105.1 Detailed Description

Definition at line 568 of file dss_mrr_pe674.c.

3.105.2 Field Documentation

3.105.2.1 link

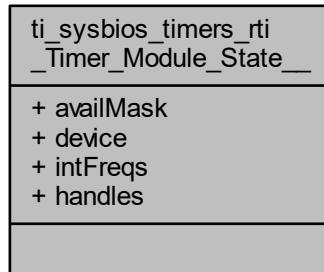
`xdc_runtime_Types_Link ti_sysbios_timers_rti_Timer_Module__::link`
 Definition at line 569 of file dss_mrr_pe674.c.
 Referenced by `ti_sysbios_timers_rti_Timer_Object__first__S()`, and `ti_sysbios_timers_rti_Timer_Object__next__S()`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.106 `ti_sysbios_timers_rti_Timer_Module_State__` Struct Reference

Collaboration diagram for `ti_sysbios_timers_rti_Timer_Module_State__`:



Data Fields

- `xdc_UInt availMask`
- `__TA_ti_sysbios_timers_rti_Timer_Module_State__device device`
- `__TA_ti_sysbios_timers_rti_Timer_Module_State__intFreqs intFreqs`
- `__TA_ti_sysbios_timers_rti_Timer_Module_State__handles handles`

3.106.1 Detailed Description

Definition at line 1655 of file `dss_mrr_pe674.c`.

3.106.2 Field Documentation

3.106.2.1 `availMask`

`xdc_UInt ti_sysbios_timers_rti_Timer_Module_State__::availMask`
Definition at line 1656 of file `dss_mrr_pe674.c`.

3.106.2.2 `device`

`__TA_ti_sysbios_timers_rti_Timer_Module_State__device ti_sysbios_timers_rti_Timer_Module__->State__::device`
Definition at line 1657 of file `dss_mrr_pe674.c`.

3.106.2.3 `handles`

`__TA_ti_sysbios_timers_rti_Timer_Module_State__handles ti_sysbios_timers_rti_Timer_Module__->State__::handles`
Definition at line 1659 of file `dss_mrr_pe674.c`.

3.106.2.4 intFreqs

```
__TA_ti_sysbios_timers_rti_Timer_Module_State__intFreqs ti_sysbios_timers_rti_Timer_Module_<-
State__::intFreqs
```

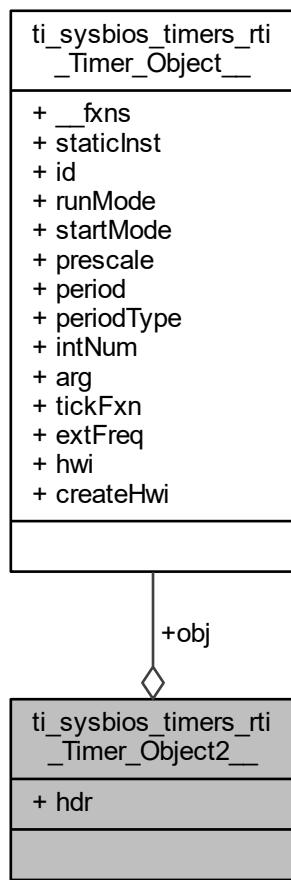
Definition at line 1658 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.107 ti_sysbios_timers_rti_Timer_Object2__ Struct Reference

Collaboration diagram for ti_sysbios_timers_rti_Timer_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **ti_sysbios_timers_rti_Timer_Object__ obj**

3.107.1 Detailed Description

Definition at line 419 of file dss_mrr_pe674.c.

3.107.2 Field Documentation

3.107.2.1 hdr

`xdc_runtime_Types_InstHdr ti_sysbios_timers_rti_Timer_Object2__::hdr`
 Definition at line 420 of file `dss_mrr_pe674.c`.

3.107.2.2 obj

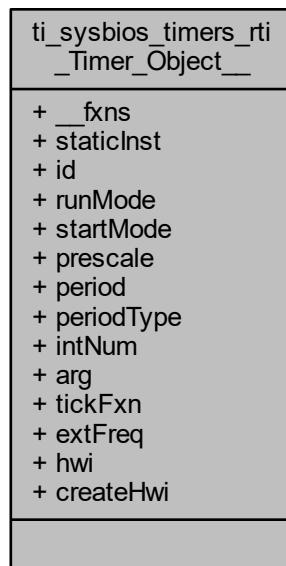
`ti_sysbios_timers_rti_Timer_Object__ ti_sysbios_timers_rti_Timer_Object2__::obj`
 Definition at line 421 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.108 `ti_sysbios_timers_rti_Timer_Object__` Struct Reference

Collaboration diagram for `ti_sysbios_timers_rti_Timer_Object__`:



Data Fields

- `const ti_sysbios_timers_rti_Timer_Fxns__ * __fxns`
- `xdc_Bool staticInst`
- `xdc_Int id`
- `ti_sysbios_interfaces_ITimer_RunMode runMode`
- `ti_sysbios_interfaces_ITimer_StartMode startMode`
- `xdc_UInt prescale`
- `xdc_UInt period`
- `ti_sysbios_interfaces_ITimer_PeriodType periodType`

- `xdc_UInt intNum`
- `xdc_UArg arg`
- `ti_sysbios_hal_Hwi_FuncPtr tickFxn`
- `xdc_runtime_Types_FreqHz extFreq`
- `ti_sysbios_hal_Hwi_Handle hwi`
- `xdc_Bool createHwi`

3.108.1 Detailed Description

Definition at line 401 of file `dss_mrr_pe674.c`.

3.108.2 Field Documentation

3.108.2.1 __fxns

`const ti_sysbios_timers_rti_Timer_Fxns__* ti_sysbios_timers_rti_Timer_Object__::__fxns`
Definition at line 402 of file `dss_mrr_pe674.c`.

3.108.2.2 arg

`xdc_UArg ti_sysbios_timers_rti_Timer_Object__::arg`
Definition at line 411 of file `dss_mrr_pe674.c`.

3.108.2.3 createHwi

`xdc_Bool ti_sysbios_timers_rti_Timer_Object__::createHwi`
Definition at line 415 of file `dss_mrr_pe674.c`.

3.108.2.4 extFreq

`xdc_runtime_Types_FreqHz ti_sysbios_timers_rti_Timer_Object__::extFreq`
Definition at line 413 of file `dss_mrr_pe674.c`.

3.108.2.5 hwi

`ti_sysbios_hal_Hwi_Handle ti_sysbios_timers_rti_Timer_Object__::hwi`
Definition at line 414 of file `dss_mrr_pe674.c`.

3.108.2.6 id

`xdc_Int ti_sysbios_timers_rti_Timer_Object__::id`
Definition at line 404 of file `dss_mrr_pe674.c`.

3.108.2.7 intNum

`xdc_UInt ti_sysbios_timers_rti_Timer_Object__::intNum`
Definition at line 410 of file `dss_mrr_pe674.c`.

3.108.2.8 period

```
xdc_UInt ti_sysbios_timers_rti_Timer_Object__::period  
Definition at line 408 of file dss_mrr_pe674.c.
```

3.108.2.9 periodType

```
ti_sysbios_interfaces_ITimer_PeriodType ti_sysbios_timers_rti_Timer_Object__::periodType  
Definition at line 409 of file dss_mrr_pe674.c.
```

3.108.2.10 prescale

```
xdc_UInt ti_sysbios_timers_rti_Timer_Object__::prescale  
Definition at line 407 of file dss_mrr_pe674.c.
```

3.108.2.11 runMode

```
ti_sysbios_interfaces_ITimer_RunMode ti_sysbios_timers_rti_Timer_Object__::runMode  
Definition at line 405 of file dss_mrr_pe674.c.
```

3.108.2.12 startMode

```
ti_sysbios_interfaces_ITimer_StartMode ti_sysbios_timers_rti_Timer_Object__::startMode  
Definition at line 406 of file dss_mrr_pe674.c.
```

3.108.2.13 staticInst

```
xdc_Bool ti_sysbios_timers_rti_Timer_Object__::staticInst  
Definition at line 403 of file dss_mrr_pe674.c.
```

3.108.2.14 tickFxn

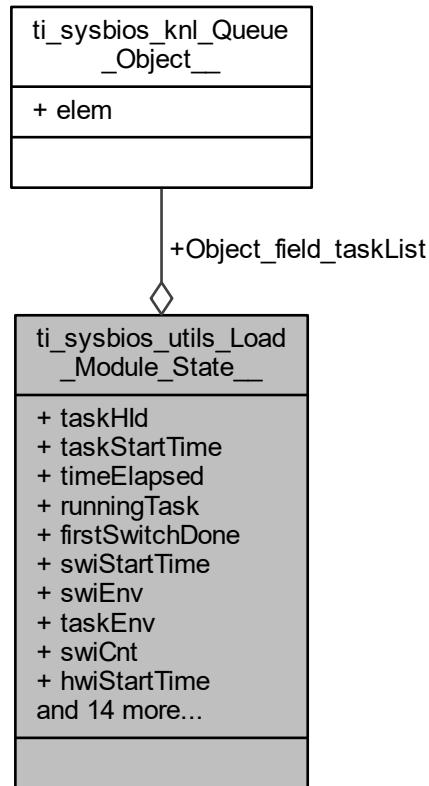
```
ti_sysbios_hal_Hwi_FuncPtr ti_sysbios_timers_rti_Timer_Object__::tickFxn  
Definition at line 412 of file dss_mrr_pe674.c.
```

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.109 `ti_sysbios_utils_Load_Module_State` Struct Reference

Collaboration diagram for `ti_sysbios_utils_Load_Module_State`:



Data Fields

- `xdc_Int taskHld`
- `__TA_ti_sysbios_utils_Load_Module_State_taskStartTime taskStartTime`
- `xdc_UInt32 timeElapsed`
- `__TA(ti_sysbios_utils_Load_Module_State_runningTask runningTask`
- `xdc_Bool firstSwitchDone`
- `xdc_UInt32 swiStartTime`
- `ti_sysbios_utils_Load_HookContext swiEnv`
- `__TA(ti_sysbios_utils_Load_Module_State_taskEnv taskEnv`
- `xdc_UInt32 swiCnt`
- `xdc_UInt32 hwiStartTime`
- `ti_sysbios_utils_Load_HookContext hwiEnv`
- `xdc_UInt32 hwiCnt`
- `xdc_UInt32 timeSlotCnt`
- `xdc_UInt32 minLoop`
- `xdc_UInt32 minIdle`
- `xdc_UInt32 t0`
- `xdc_UInt32 idleCnt`
- `xdc_UInt32 cpuLoad`

- `xdc_UInt32 taskEnvLen`
- `xdc_UInt32 taskNum`
- `xdc_Bool powerEnabled`
- `xdc_UInt32 idleStartTime`
- `xdc_UInt32 busyStartTime`
- `xdc_UInt32 busyTime`
- `ti_sysbios_knl_Queue_Object__ Object_field_taskList`

3.109.1 Detailed Description

Definition at line 1680 of file `dss_mrr_pe674.c`.

3.109.2 Field Documentation

3.109.2.1 `busyStartTime`

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::busyStartTime`

Definition at line 1703 of file `dss_mrr_pe674.c`.

3.109.2.2 `busyTime`

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::busyTime`

Definition at line 1704 of file `dss_mrr_pe674.c`.

3.109.2.3 `cpuLoad`

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::cpuLoad`

Definition at line 1698 of file `dss_mrr_pe674.c`.

3.109.2.4 `firstSwitchDone`

`xdc_Bool ti_sysbios_utils_Load_Module_State__::firstSwitchDone`

Definition at line 1685 of file `dss_mrr_pe674.c`.

3.109.2.5 `hwiCnt`

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::hwiCnt`

Definition at line 1692 of file `dss_mrr_pe674.c`.

3.109.2.6 `hwiEnv`

`ti_sysbios_utils_Load_HookContext ti_sysbios_utils_Load_Module_State__::hwiEnv`

Definition at line 1691 of file `dss_mrr_pe674.c`.

3.109.2.7 `hwiStartTime`

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::hwiStartTime`

Definition at line 1690 of file `dss_mrr_pe674.c`.

3.109.2.8 idleCnt

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::idleCnt  
Definition at line 1697 of file dss_mrr_pe674.c.
```

3.109.2.9 idleStartTime

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::idleStartTime  
Definition at line 1702 of file dss_mrr_pe674.c.
```

3.109.2.10 minIdle

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::minIdle  
Definition at line 1695 of file dss_mrr_pe674.c.
```

3.109.2.11 minLoop

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::minLoop  
Definition at line 1694 of file dss_mrr_pe674.c.
```

3.109.2.12 Object_field_taskList

```
ti_sysbios_knl_Queue_Object__ ti_sysbios_utils_Load_Module_State__::Object_field_taskList  
Definition at line 1705 of file dss_mrr_pe674.c.
```

3.109.2.13 powerEnabled

```
xdc_Bool ti_sysbios_utils_Load_Module_State__::powerEnabled  
Definition at line 1701 of file dss_mrr_pe674.c.
```

3.109.2.14 runningTask

```
__TA_ti_sysbios_utils_Load_Module_State__runningTask ti_sysbios_utils_Load_Module_State__←  
::runningTask  
Definition at line 1684 of file dss_mrr_pe674.c.
```

3.109.2.15 swiCnt

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::swiCnt  
Definition at line 1689 of file dss_mrr_pe674.c.
```

3.109.2.16 swiEnv

```
ti_sysbios_utils_Load_HookContext ti_sysbios_utils_Load_Module_State__::swiEnv  
Definition at line 1687 of file dss_mrr_pe674.c.
```

3.109.2.17 swiStartTime

```
xdc_UInt32 ti_sysbios_utils_Load_Module_State__::swiStartTime  
Definition at line 1686 of file dss_mrr_pe674.c.
```

3.109.2.18 t0

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::t0`
Definition at line 1696 of file `dss_mrr_pe674.c`.

3.109.2.19 taskEnv

`__TA_ti_sysbios_utils_Load_Module_State__taskEnv ti_sysbios_utils_Load_Module_State__::taskEnv`
Definition at line 1688 of file `dss_mrr_pe674.c`.

3.109.2.20 taskEnvLen

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::taskEnvLen`
Definition at line 1699 of file `dss_mrr_pe674.c`.

3.109.2.21 taskHId

`xdc_Int ti_sysbios_utils_Load_Module_State__::taskHId`
Definition at line 1681 of file `dss_mrr_pe674.c`.

3.109.2.22 taskNum

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::taskNum`
Definition at line 1700 of file `dss_mrr_pe674.c`.

3.109.2.23 taskStartTime

`__TA_ti_sysbios_utils_Load_Module_State__taskStartTime ti_sysbios_utils_Load_Module_State__::taskStartTime`
Definition at line 1682 of file `dss_mrr_pe674.c`.

3.109.2.24 timeElapsed

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::timeElapsed`
Definition at line 1683 of file `dss_mrr_pe674.c`.

3.109.2.25 timeSlotCnt

`xdc_UInt32 ti_sysbios_utils_Load_Module_State__::timeSlotCnt`
Definition at line 1693 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

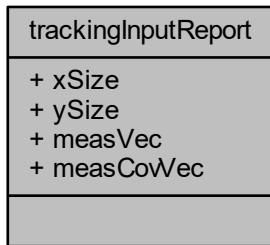
- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.110 trackingInputReport Struct Reference

Input to tracking from the clustering output.

```
#include <dss_data_path.h>
```

Collaboration diagram for trackingInputReport:



Data Fields

- int16_t **xSize**
- int16_t **ySize**
- float **measVec** [3]
- float **measCovVec** [3]

3.110.1 Detailed Description

Input to tracking from the clustering output.
Definition at line 358 of file dss_data_path.h.

3.110.2 Field Documentation

3.110.2.1 measCovVec

```
float trackingInputReport::measCovVec[3]
```

Measurement covariance.
Definition at line 363 of file dss_data_path.h.

3.110.2.2 measVec

```
float trackingInputReport::measVec[3]
```

Measurement input
Definition at line 362 of file dss_data_path.h.

3.110.2.3 xSize

```
int16_t trackingInputReport::xSize
```

the clustering size on x direction
Definition at line 360 of file dss_data_path.h.

3.110.2.4 ySize

`int16_t trackingInputReport::ySize`

the clustering size on y direction

Definition at line 361 of file dss_data_path.h.

The documentation for this struct was generated from the following file:

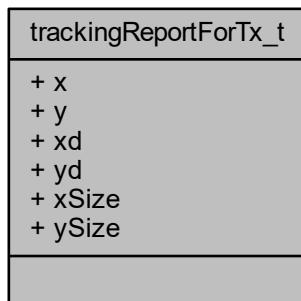
- `dss_data_path.h`

3.111 trackingReportForTx_t Struct Reference

Structure for tracking report.

`#include <dss_data_path.h>`

Collaboration diagram for trackingReportForTx_t:



Data Fields

- `int16_t x`
- `int16_t y`
- `int16_t xd`
- `int16_t yd`
- `int16_t xSize`
- `int16_t ySize`

3.111.1 Detailed Description

Structure for tracking report.

Definition at line 191 of file dss_data_path.h.

3.111.2 Field Documentation

3.111.2.1 x

`int16_t trackingReportForTx_t::x`

the tracking output -> x co-ordinate

Definition at line 193 of file dss_data_path.h.

3.111.2.2 xd

```
int16_t trackingReportForTx_t::xd  
velocity in the x direction  
Definition at line 195 of file dss_data_path.h.
```

3.111.2.3 xSize

```
int16_t trackingReportForTx_t::xSize  
cluster size (x direction).  
Definition at line 197 of file dss_data_path.h.
```

3.111.2.4 y

```
int16_t trackingReportForTx_t::y  
the tracking output -> y co-ordinate  
Definition at line 194 of file dss_data_path.h.
```

3.111.2.5 yd

```
int16_t trackingReportForTx_t::yd  
velocity in the y direction  
Definition at line 196 of file dss_data_path.h.
```

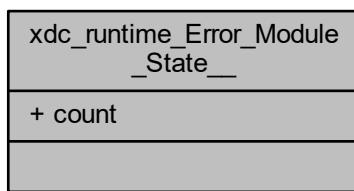
3.111.2.6 ySize

```
int16_t trackingReportForTx_t::ySize  
cluster size (y direction).  
Definition at line 198 of file dss_data_path.h.  
The documentation for this struct was generated from the following file:
```

- [dss_data_path.h](#)

3.112 xdc_runtime_Error_Module_State__ Struct Reference

Collaboration diagram for xdc_runtime_Error_Module_State__:



Data Fields

- `xdc_UInt16 count`

3.112.1 Detailed Description

Definition at line 1746 of file dss_mrr_pe674.c.

3.112.2 Field Documentation

3.112.2.1 count

`xdc_UInt16 xdc_runtime_Error_Module_State__::count`

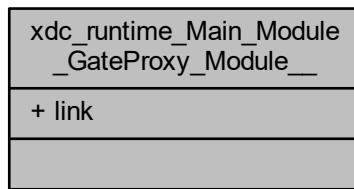
Definition at line 1747 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.113 `xdc_runtime_Main_Module_GateProxy_Module__` Struct Reference

Collaboration diagram for `xdc_runtime_Main_Module_GateProxy_Module__`:



Data Fields

- `xdc_runtime_Types_Link link`

3.113.1 Detailed Description

Definition at line 628 of file dss_mrr_pe674.c.

3.113.2 Field Documentation

3.113.2.1 link

`xdc_runtime_Types_Link xdc_runtime_Main_Module_GateProxy_Module__::link`

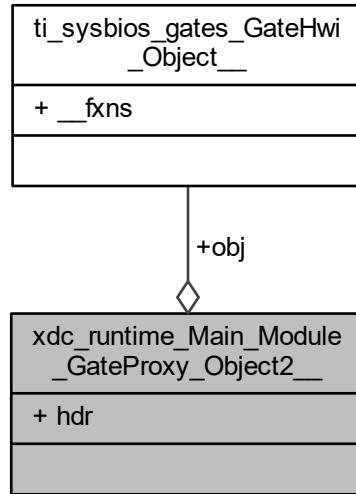
Definition at line 629 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.114 xdc_runtime_Main_Module_GateProxy_Object2__ Struct Reference

Collaboration diagram for xdc_runtime_Main_Module_GateProxy_Object2__:



Data Fields

- xdc_runtime_Types_InstHdr **hdr**
- **xdc_runtime_Main_Module_GateProxy_Object__ obj**

3.114.1 Detailed Description

Definition at line 641 of file dss_mrr_pe674.c.

3.114.2 Field Documentation

3.114.2.1 **hdr**

`xdc_runtime_Types_InstHdr xdc_runtime_Main_Module_GateProxy_Object2__::hdr`
Definition at line 642 of file dss_mrr_pe674.c.

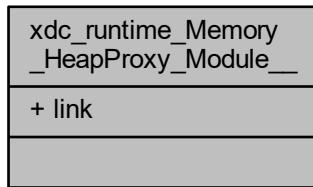
3.114.2.2 **obj**

`xdc_runtime_Main_Module_GateProxy_Object__ xdc_runtime_Main_Module_GateProxy_Object2__::obj`
Definition at line 643 of file dss_mrr_pe674.c.
The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.115 xdc_runtime_Memory_HeapProxy_Module__ Struct Reference

Collaboration diagram for xdc_runtime_Memory_HeapProxy_Module__:



Data Fields

- `xdc_runtime_Types_Link link`

3.115.1 Detailed Description

Definition at line 657 of file `dss_mrr_pe674.c`.

3.115.2 Field Documentation

3.115.2.1 link

`xdc_runtime_Types_Link xdc_runtime_Memory_HeapProxy_Module__::link`

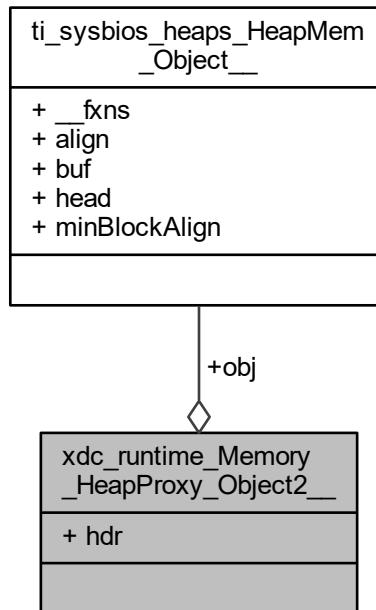
Definition at line 658 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.116 xdc_runtime_Memory_HeapProxy_Object2__ Struct Reference

Collaboration diagram for xdc_runtime_Memory_HeapProxy_Object2__:



Data Fields

- `xdc_runtime_Types_InstHdr` `hdr`
- `xdc_runtime_Memory_HeapProxy_Object__` `obj`

3.116.1 Detailed Description

Definition at line 670 of file `dss_mrr_pe674.c`.

3.116.2 Field Documentation

3.116.2.1 `hdr`

`xdc_runtime_Types_InstHdr` `xdc_runtime_Memory_HeapProxy_Object2__::hdr`
Definition at line 671 of file `dss_mrr_pe674.c`.

3.116.2.2 `obj`

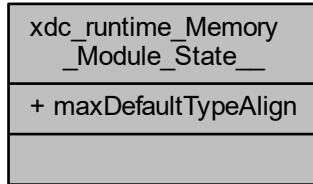
`xdc_runtime_Memory_HeapProxy_Object__` `xdc_runtime_Memory_HeapProxy_Object2__::obj`
Definition at line 672 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

3.117 xdc_runtime_Memory_Module_State__ Struct Reference

Collaboration diagram for xdc_runtime_Memory_Module_State__:



Data Fields

- `xdc_SizeT maxDefaultTypeAlign`

3.117.1 Detailed Description

Definition at line 1779 of file `dss_mrr_pe674.c`.

3.117.2 Field Documentation

3.117.2.1 maxDefaultTypeAlign

`xdc_SizeT xdc_runtime_Memory_Module_State__::maxDefaultTypeAlign`

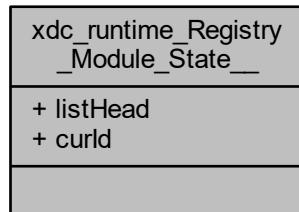
Definition at line 1780 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.118 xdc_runtime_Registry_Module_State__ Struct Reference

Collaboration diagram for xdc_runtime_Registry_Module_State__:



Data Fields

- `xdc_runtime_Registry_Desc * listHead`
- `xdc_runtime_Types_ModuleId curId`

3.118.1 Detailed Description

Definition at line 1797 of file `dss_mrr_pe674.c`.

3.118.2 Field Documentation

3.118.2.1 curId

`xdc_runtime_Types_ModuleId xdc_runtime_Registry_Module_State__::curId`
 Definition at line 1799 of file `dss_mrr_pe674.c`.

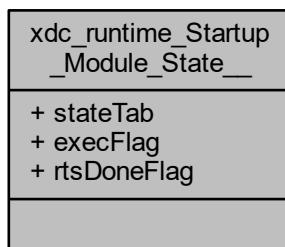
3.118.2.2 listHead

`xdc_runtime_Registry_Desc* xdc_runtime_Registry_Module_State__::listHead`
 Definition at line 1798 of file `dss_mrr_pe674.c`.
 The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.119 xdc_runtime_Startup_Module_State__ Struct Reference

Collaboration diagram for `xdc_runtime_Startup_Module_State__`:



Data Fields

- `xdc_Int * stateTab`
- `xdc_Bool execFlag`
- `xdc_Bool rtsDoneFlag`

3.119.1 Detailed Description

Definition at line 1811 of file `dss_mrr_pe674.c`.

3.119.2 Field Documentation

3.119.2.1 execFlag

`xdc_Bool xdc_runtime_Startup_Module_State__::execFlag`

Definition at line 1813 of file `dss_mrr_pe674.c`.

3.119.2.2 rtsDoneFlag

`xdc_Bool xdc_runtime_Startup_Module_State__::rtsDoneFlag`

Definition at line 1814 of file `dss_mrr_pe674.c`.

3.119.2.3 stateTab

`xdc_Int* xdc_runtime_Startup_Module_State__::stateTab`

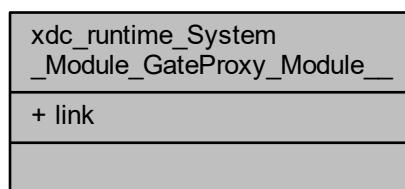
Definition at line 1812 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- `Debug/configPkg/package/cfg/ dss_mrr_pe674.c`

3.120 `xdc_runtime_System_Module_GateProxy_Module__` Struct Reference

Collaboration diagram for `xdc_runtime_System_Module_GateProxy_Module__`:



Data Fields

- `xdc_runtime_Types_Link link`

3.120.1 Detailed Description

Definition at line 701 of file `dss_mrr_pe674.c`.

3.120.2 Field Documentation

3.120.2.1 link

`xdc_runtime_Types_Link xdc_runtime_System_Module_GateProxy_Module__::link`

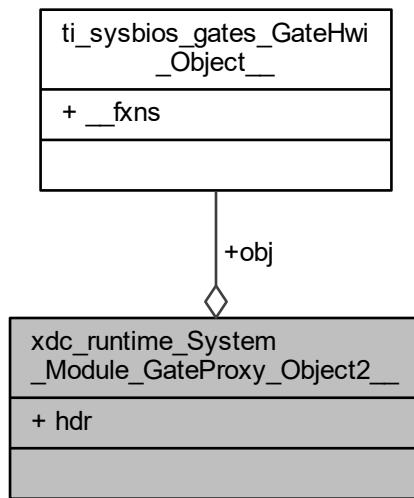
Definition at line 702 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.121 xdc_runtime_System_Module_GateProxy_Object2__ Struct Reference

Collaboration diagram for `xdc_runtime_System_Module_GateProxy_Object2__`:



Data Fields

- `xdc_runtime_Types_InstHdr hdr`
- `xdc_runtime_System_Module_GateProxy_Object__ obj`

3.121.1 Detailed Description

Definition at line 714 of file `dss_mrr_pe674.c`.

3.121.2 Field Documentation

3.121.2.1 hdr

`xdc_runtime_Types_InstHdr xdc_runtime_System_Module_GateProxy_Object2__::hdr`

Definition at line 715 of file `dss_mrr_pe674.c`.

3.121.2.2 obj

```
xdc_runtime_System_Module_GateProxy_Object__ xdc_runtime_System_Module_GateProxy_Object2__←
::obj
```

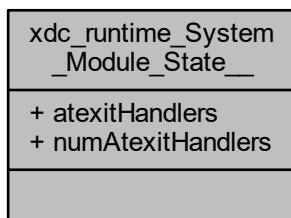
Definition at line 716 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.122 xdc_runtime_System_Module_State__ Struct Reference

Collaboration diagram for xdc_runtime_System_Module_State__:



Data Fields

- `_TA_xdc_runtime_System_Module_State__atexitHandlers atexitHandlers`
- `xdc_Int numAtexitHandlers`

3.122.1 Detailed Description

Definition at line 1873 of file dss_mrr_pe674.c.

3.122.2 Field Documentation

3.122.2.1 atexitHandlers

```
_TA_xdc_runtime_System_Module_State__atexitHandlers xdc_runtime_System_Module_State__::atexit←
Handlers
```

Definition at line 1874 of file dss_mrr_pe674.c.

3.122.2.2 numAtexitHandlers

```
xdc_Int xdc_runtime_System_Module_State__::numAtexitHandlers
```

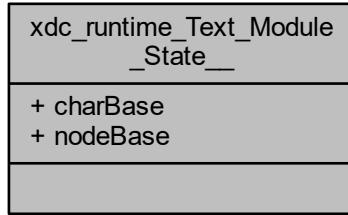
Definition at line 1875 of file dss_mrr_pe674.c.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ **dss_mrr_pe674.c**

3.123 xdc_runtime_Text_Module_State__ Struct Reference

Collaboration diagram for xdc_runtime_Text_Module_State__:



Data Fields

- `xdc_CPtr charBase`
- `xdc_CPtr nodeBase`

3.123.1 Detailed Description

Definition at line 1900 of file `dss_mrr_pe674.c`.

3.123.2 Field Documentation

3.123.2.1 `charBase`

`xdc_CPtr xdc_runtime_Text_Module_State__::charBase`

Definition at line 1901 of file `dss_mrr_pe674.c`.

3.123.2.2 `nodeBase`

`xdc_CPtr xdc_runtime_Text_Module_State__::nodeBase`

Definition at line 1902 of file `dss_mrr_pe674.c`.

The documentation for this struct was generated from the following file:

- Debug/configPkg/package/cfg/ `dss_mrr_pe674.c`

Chapter 4

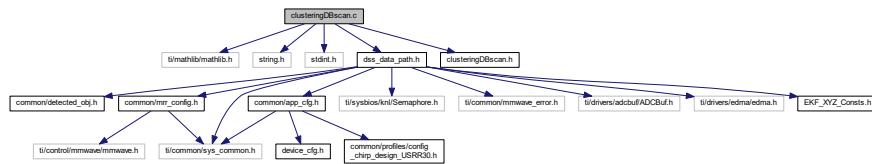
File Documentation

4.1 clusteringDBscan.c File Reference

DBscan clustering module.

```
#include <ti/mathlib/mathlib.h>
#include <string.h>
#include <stdint.h>
#include "dss_data_path.h"
#include "clusteringDBscan.h"
```

Include dependency graph for clusteringDBscan.c:



Functions

- int32_t **clusteringDBscanRun** (clusteringDBscanInstance_t *inst, DSS_DataPathObj *obj, uint16_t numDetectedObj, clusteringDBscanOutput_t *output, trackingInputReport_t *trackingInput)
- uint16_t **clusteringDBscan_findNeighbors2Fixed** (MmwDemo_detectedObjActual *restrict detObj2D, uint16_t point, uint16_t *restrict neigh, uint16_t numPoints, int32_t epsilon2, int32_t epsilon, float weight, int32_t vFactor, char *restrict visited, uint16_t *restrict newCount, uint16_t dBScanNeighbourLim)
- void **clusteringDBscan_calcInfoFixed** (uint16_t clusterOriginator, MmwDemo_detectedObjActual *restrict detObj2D, uint16_t *restrict neighStart, uint16_t *restrict neighLast, clusteringDBscanReport_t *restrict report, trackingInputReport_t *restrict trackingInput, DSS_DataPathObj *obj)

4.1.1 Detailed Description

DBscan clustering module.

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4.1.2 Function Documentation

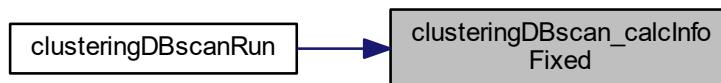
4.1.2.1 clusteringDBscan_calcInfoFixed()

```
void clusteringDBscan_calcInfoFixed (
    uint16_t clusterOriginator,
    MmwDemo_detectedObjActual *restrict detObj2D,
    uint16_t *restrict neighStart,
    uint16_t *restrict neighLast,
    clusteringDBscanReport_t *restrict report,
    trackingInputReport_t *restrict trackingInput,
    DSS_DataPathObj * obj )
```

Definition at line 355 of file clusteringDBscan.c.

Referenced by clusteringDBscanRun().

Here is the caller graph for this function:



4.1.2.2 clusteringDBscan_findNeighbors2Fixed()

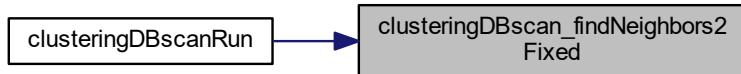
```
uint16_t clusteringDBscan_findNeighbors2Fixed (
    MmwDemo_detectedObjActual *restrict detObj2D,
    uint16_t point,
    uint16_t *restrict neigh,
    uint16_t numPoints,
    int32_t epsilon2,
    int32_t epsilon,
    float weight,
    int32_t vFactor,
    char *restrict visited,
    uint16_t *restrict newCount,
    uint16_t dBScanNeighbourLim )
```

Definition at line 247 of file clusteringDBscan.c.

References POINT_VISITED.

Referenced by clusteringDBscanRun().

Here is the caller graph for this function:



4.1.2.3 clusteringDBscanRun()

```

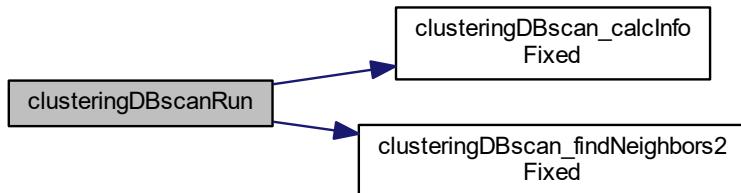
int32_t clusteringDBscanRun (
    clusteringDBscanInstance_t * inst,
    DSS_DataPathObj * obj,
    uint16_t numDetectedObj,
    clusteringDBscanOutput_t * output,
    trackingInputReport_t * trackingInput )

```

Definition at line 79 of file clusteringDBscan.c.

References clusteringDBscan_calcInfoFixed(), clusteringDBscan_findNeighbors2Fixed(), DBSCAN_ERROR_C←
LUSTER_LIMIT_REACHED, DBSCAN_OK, clusteringDBscanInstance::dBScanNeighbourLim, DSS_DataPath←
Obj_t::detObj2D, clusteringDBscanInstance::epsilon, clusteringDBscanInstance::fixedPointScale, clusteringD←
BscanOutput::IndexArray, clusteringDBscanInstance::maxClusters, clusteringDBscanInstance::minPointsInCluster,
clusteringDBscanInstance::neighbors, clusteringDBscanOutput::numCluster, POINT_UNKNOWN, POINT_VI←
SITED, clusteringDBscanOutput::report, clusteringDBscanInstance::scope, clusteringDBscanInstance::vFactor,
clusteringDBscanInstance::visited, and clusteringDBscanInstance::weight.

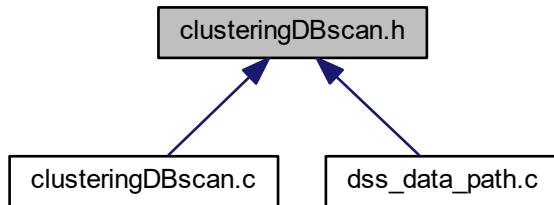
Here is the call graph for this function:



4.2 clusteringDBscan.h File Reference

DBscan clustering module.

This graph shows which files directly or indirectly include this file:



Macros

- `#define POINT_UNKNOWN 0`
#defines for the state of the detected points w.r.t to the clustering algorithms.
- `#define POINT_VISITED 1`

Functions

- `uint16_t clusteringDBscan_findNeighbors2Fixed (MmwDemo_detectedObjActual *restrict detObj2D, uint16_t point, uint16_t *restrict neigh, uint16_t numPoints, int32_t epsilon2, int32_t epsilon, float weight, int32_t vFactor, char *restrict visited, uint16_t *restrict newCount, uint16_t dBScanNeighbourLim)`
- `void clusteringDBscan_calcInfoFixed (uint16_t clusterOriginator, MmwDemo_detectedObjActual *restrict detObj2D, uint16_t *restrict neighStart, uint16_t *restrict neighLast, clusteringDBscanReport_t *restrict report, trackingInputReport_t *restrict trackingInput, DSS_DataPathObj *obj)`
- `int32_t clusteringDBscanRun (clusteringDBscanInstance_t *inst, DSS_DataPathObj *obj, uint16_t numDetectedObj, clusteringDBscanOutput_t *output, trackingInputReport_t *trackingInput)`

4.2.1 Detailed Description

DBscan clustering module.

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4.2.2 Macro Definition Documentation

4.2.2.1 POINT_UNKNOWN

```
#define POINT_UNKNOWN 0
#define for the state of the detected points w.r.t to the clustering algorithms.
```

Definition at line 45 of file clusteringDBscan.h.

4.2.2.2 POINT_VISITED

```
#define POINT_VISITED 1
Definition at line 46 of file clusteringDBscan.h.
```

4.2.3 Function Documentation

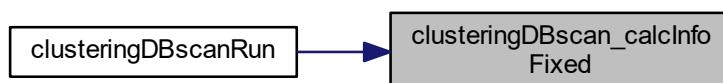
4.2.3.1 clusteringDBscan_calcInfoFixed()

```
void clusteringDBscan_calcInfoFixed (
    uint16_t clusterOriginator,
    MmwDemo_detectedObjActual *restrict detObj2D,
    uint16_t *restrict neighStart,
    uint16_t *restrict neighLast,
    clusteringDBscanReport_t *restrict report,
    trackingInputReport_t *restrict trackingInput,
    DSS_DataPathObj * obj )
```

Definition at line 355 of file clusteringDBscan.c.

Referenced by clusteringDBscanRun().

Here is the caller graph for this function:



4.2.3.2 clusteringDBscan_findNeighbors2Fixed()

```
uint16_t clusteringDBscan_findNeighbors2Fixed (
    MmwDemo_detectedObjActual *restrict detObj2D,
    uint16_t point,
    uint16_t *restrict neigh,
    uint16_t numPoints,
    int32_t epsilon2,
    int32_t epsilon,
    float weight,
    int32_t vFactor,
    char *restrict visited,
```

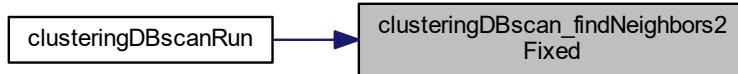
```
    uint16_t *restrict newCount,
    uint16_t dBSscanNeighbourLim )
```

Definition at line 247 of file clusteringDBscan.c.

References POINT_VISITED.

Referenced by clusteringDBscanRun().

Here is the caller graph for this function:



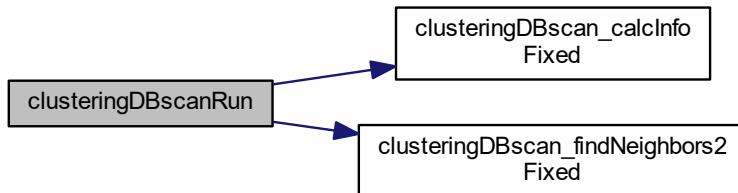
4.2.3.3 clusteringDBscanRun()

```
int32_t clusteringDBscanRun (
    clusteringDBscanInstance_t * inst,
    DSS_DataPathObj * obj,
    uint16_t numDetectedObj,
    clusteringDBscanOutput_t * output,
    trackingInputReport_t * trackingInput )
```

Definition at line 79 of file clusteringDBscan.c.

References clusteringDBscan_calcInfoFixed(), clusteringDBscan_findNeighbors2Fixed(), DBSCAN_ERROR_C←LUSTER_LIMIT_REACHED, DBSCAN_OK, clusteringDBscanInstance::dBSscanNeighbourLim, DSS_DataPath←Obj_t::detObj2D, clusteringDBscanInstance::epsilon, clusteringDBscanInstance::fixedPointScale, clusteringD←BscanOutput::IndexArray, clusteringDBscanInstance::maxClusters, clusteringDBscanInstance::minPointsInCluster, clusteringDBscanInstance::neighbors, clusteringDBscanOutput::numCluster, POINT_UNKNOWN, POINT_VI←SITED, clusteringDBscanOutput::report, clusteringDBscanInstance::scope, clusteringDBscanInstance::vFactor, clusteringDBscanInstance::visited, and clusteringDBscanInstance::weight.

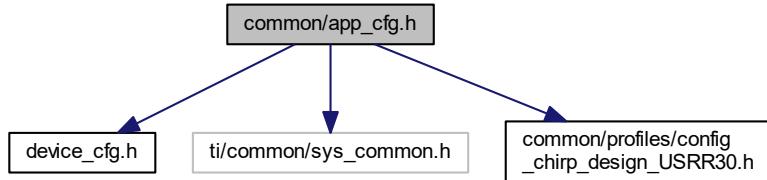
Here is the call graph for this function:



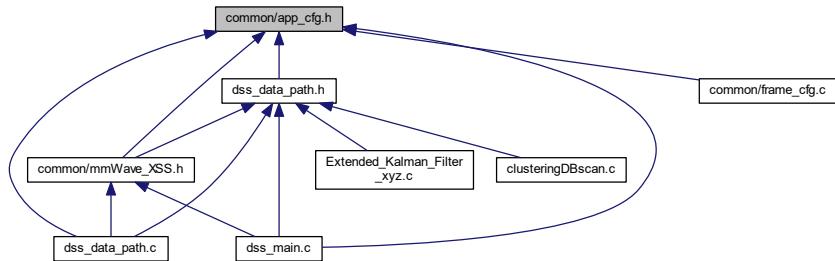
4.3 common/app_cfg.h File Reference

```
#include "device_cfg.h"
#include <ti/common/sys_common.h>
#include <common/profiles/config_chirp_design_USRR30.h>
```

Include dependency graph for app_cfg.h:



This graph shows which files directly or indirectly include this file:



Macros

- `#define NUM_RX_CHANNELS (4U)`

Reduced Thresholds. The following line (if uncommented) reduces thresholds for the USRR detection algorithm to enable the detection of weak targets.

- `#define NUM_CHIRP_PROG (3U) /* 3TX antennas so Three for USRR20 */`
- `#define NUM_PROFILES (1U) /* one profile for USRR mode of operation*/`
- `#define NUM_SUBFRAMES (1U) /* one subframe for USRR mode operation: No advanced frame configuration */`
- `#define SUBFRAME_CONF_USRR`

ENABLE USRR configuraion.

- `#define ADCBUFF_CHIRP_THRESHOLD (1U)`

Number of chirps to be collected in the ADC buffer, before the chirp available interrupt.

- `#define FRAME_PERIODICITY_VAL (SUBFRAME_USRR_PERIODICITY_VAL)`

Which subframe is used to do max-vel-enhancement.

- `#define FRAME_PERIODICITY_SEC (FRAME_PERIODICITY_VAL*5e-9)`

The total frame periodicity in seconds.

- `#define MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS (3U)`

The number of SNR Thresholds - used to vary the SNR requirement as a function of range.

- `#define MAX_VEL_ENH_PROCESSING (0U)`

There are two processing paths in the MRR Demo.

- `#define POINT_CLOUD_PROCESSING (1U)`

- `#define MAX_NUM_CLUSTER_USRR (24U)`

The maximum number of clusters out of the dbSCAN algorithm (for the USRR subframe).

- **#define MAX_NUM_CLUSTER_MRR (32U)**
The maximum number of clusters out of the dbscan algorithm (for the MRR subframe).
- **#define MAX_TRK_OBJS (32U)**
The maximum number of tracked objects from the Kalman filter.
- **#define REPORT_N_BIT_FRAC (7U)**
Fractional bit width for most of the report data (range, velocity, x, y, etc).
- **#define CFARTHRESHOLD_N_BIT_FRAC (8U)**
Fractional bit width for Thresholds for CFAR data (rangeSNRdB, dopplerSNRdB, AzimSNR, etc).
- **#define MIN_RANGE_OFFSET_METERS (0.075f)**
The radar's range estimate has a constant error due to the finite distance from the antenna to the LO.
- **#define MIN_TICK_FOR_TX (10U)**
Wait for MIN_TICK_FOR_TX before letting the tracker results out.
- **#define SIN_55_DEGREES (0.8192f)**
We discard objects at extreme angles (greater than 55 degrees) from the tracking procedure.
- **#define TRK_SIN_AZIM_THRESH (1.0f/256.0f)**
We discard objects with poor azimuth SNR from the tracking procedure.
- **#define CHECK_FOR_DET_MATRIX_TX 1**
In processing the max-velocity enhancement subframe we need to check for det matrix transfer only after the second set of chirps are processed.
- **#define DO_NOT_CHECK_FOR_DET_MATRIX_TX 0**
- **#define MRR_MAX_OBJ_OUT 200**
The maximum number of objects to be send out per frame. This number is upper bounded by the transfer rate to the external device.
- **#define MAX_DET_OBJECTS_RAW_MAX_VEL_ENH_PROCESSING 200**
The maximum number of objects detected in the 'Max velocity enhanced' processing path. Because of all the pruning, and higher thresholds, and lower resolution, fewer objects are detected in MAX_VEL_ENH_PROCESSING.
- **#define MAX_DET_OBJECTS_RAW_POINT_CLOUD_PROCESSING 900**
The maximum number of objects detected in the 'point cloud' processing path.
- **#define MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH_DB (3U)**
The two peaks (from the 'fast chirp' and the 'slow chirp' should be within 2 dB.
- **#define MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH (((1U << CFARTHRESHOLD_N_BIT_FRAC) * NUM_RX_CHANNELS * MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH_DB) / 6U)**
Convert the threshold to a CFAR magnitude difference.
- **#define MAX_VEL_IMPROVEMENT_NUM_SPREAD (2U)**
Search across 2 bins in the slow chirp.
- **#define MAX_VEL_ENH_NUM_NYQUIST (2U)**
Max velocity improvement of 3x.
- **#define MAX_NUM_DET_PER_RANGE_GATE (3U)**
Restrict the number of detected objects per range-gate to 3.
- **#define FRAME_CHIRP_START_IDX (0U)**
Unused #defines?? THEY ARE USED.
- **#define FRAME_CHIRP_END_IDX (1U)**
- **#define FRAME_COUNT_VAL (0U)**
- **#define FRAME_LOOP_COUNT (64U)**
- **#define FRAME_TRIGGER_DELAY_VAL (0U)**
- **#define FRAME_NUM_REAL_ADC_SAMPLES (512U)**
- **#define FRAME_NUM_CMPLX_ADC_SAMPLES (256U)**

- **#define EDMA_INSTANCE_A (0U)**
There are two TPCCs available on the 16xx.
- **#define EDMA_INSTANCE_B (1U)**

- `#define MRR_SF0_EDMA_CH_1D_IN_PING EDMA_TPCC0_REQ_FREE_0`
EDMA allocation and configuration table for FFT processing of subframe 0 AKA: (The whole frame)
- `#define MRR_SF0_EDMA_CH_1D_IN_PONG EDMA_TPCC0_REQ_FREE_1`
- `#define MRR_SF0_EDMA_CH_1D_OUT_PING EDMA_TPCC0_REQ_FREE_2`
- `#define MRR_SF0_EDMA_CH_1D_OUT_PONG EDMA_TPCC0_REQ_FREE_3`
- `#define MRR_SF0_EDMA_CH_2D_IN_PING EDMA_TPCC0_REQ_FREE_4`
- `#define MRR_SF0_EDMA_CH_2D_IN_PONG EDMA_TPCC0_REQ_FREE_5`
- `#define MRR_SF0_EDMA_CH_DET_MATRIX EDMA_TPCC0_REQ_FREE_6`
- `#define MRR_SF0_EDMA_CH_DET_MATRIX2 EDMA_TPCC0_REQ_FREE_7`
- `#define MRR_SF0_EDMA_CH_3D_IN_PING EDMA_TPCC0_REQ_FREE_8`
- `#define MRR_SF0_EDMA_CH_3D_IN_PONG EDMA_TPCC0_REQ_FREE_9`
- `#define EDMA_INSTANCE_DSS EDMA_INSTANCE_A`
- `#define EDMA_INSTANCE_MSS EDMA_INSTANCE_B`
- `#define MRR_EDMA_TRIGGER_ENABLE 1`
- `#define MRR_EDMA_TRIGGER_DISABLE 0`
- `#define MAX_VEL_POINT_CLOUD_PROCESSING_IS_ENABLED 0`

Flag to enable the max velocity enhancement in point cloud processing.

4.3.1 Macro Definition Documentation

4.3.1.1 ADCBUFF_CHIRP_THRESHOLD

```
#define ADCBUFF_CHIRP_THRESHOLD (1U)
```

Number of chirps to be collected in the ADC buffer, before the chirp available interrupt.

Definition at line 88 of file app_cfg.h.

4.3.1.2 CFARTHRESHOLD_N_BIT_FRAC

```
#define CFARTHRESHOLD_N_BIT_FRAC (8U)
```

Fractional bit width for Thresholds for CFAR data (rangeSNRdB, dopplerSNRdB, AzimSNR, etc).

Definition at line 127 of file app_cfg.h.

4.3.1.3 CHECK_FOR_DET_MATRIX_TX

```
#define CHECK_FOR_DET_MATRIX_TX 1
```

In processing the max-velocity enhancement subframe we need to check for det matrix transfer only after the second set of chirps are processed.

Definition at line 145 of file app_cfg.h.

4.3.1.4 DO_NOT_CHECK_FOR_DET_MATRIX_TX

```
#define DO_NOT_CHECK_FOR_DET_MATRIX_TX 0
```

Definition at line 146 of file app_cfg.h.

4.3.1.5 EDMA_INSTANCE_A

```
#define EDMA_INSTANCE_A (0U)
```

There are two TPCCs available on the 16xx.

Definition at line 181 of file app_cfg.h.

4.3.1.6 EDMA_INSTANCE_B

```
#define EDMA_INSTANCE_B (1U)
Definition at line 182 of file app_cfg.h.
```

4.3.1.7 EDMA_INSTANCE_DSS

```
#define EDMA_INSTANCE_DSS EDMA_INSTANCE_A
Definition at line 200 of file app_cfg.h.
```

4.3.1.8 EDMA_INSTANCE_MSS

```
#define EDMA_INSTANCE_MSS EDMA_INSTANCE_B
Definition at line 201 of file app_cfg.h.
```

4.3.1.9 FRAME_CHIRP_END_IDX

```
#define FRAME_CHIRP_END_IDX (1U)
Definition at line 173 of file app_cfg.h.
```

4.3.1.10 FRAME_CHIRP_START_IDX

```
#define FRAME_CHIRP_START_IDX (0U)
Unused #defines?? THEY ARE USED.
Definition at line 172 of file app_cfg.h.
```

4.3.1.11 FRAME_COUNT_VAL

```
#define FRAME_COUNT_VAL (0U)
Definition at line 174 of file app_cfg.h.
```

4.3.1.12 FRAME_LOOP_COUNT

```
#define FRAME_LOOP_COUNT (64U)
Definition at line 175 of file app_cfg.h.
```

4.3.1.13 FRAME_NUM_CMPLX_ADC_SAMPLES

```
#define FRAME_NUM_CMPLX_ADC_SAMPLES (256U)
Definition at line 178 of file app_cfg.h.
```

4.3.1.14 FRAME_NUM_REAL_ADC_SAMPLES

```
#define FRAME_NUM_REAL_ADC_SAMPLES (512U)
Definition at line 177 of file app_cfg.h.
```

4.3.1.15 FRAME_PERIODICITY_SEC

```
#define FRAME_PERIODICITY_SEC ( FRAME_PERIODICITY_VAL*5e-9 )
The total frame periodicity in seconds.
Definition at line 105 of file app_cfg.h.
```

4.3.1.16 FRAME_PERIODICITY_VAL

```
#define FRAME_PERIODICITY_VAL ( SUBFRAME_USRR_PERIODICITY_VAL )
```

Which subframe is used to do max-vel-enhancement.

Add in the USRR20 profile for > Max Range 20m; Better Range Resolution

Add in the USRR30 profile for > Max Range 30m; Lower Range Resolution

Definition at line 100 of file app_cfg.h.

4.3.1.17 FRAME_TRIGGER_DELAY_VAL

```
#define FRAME_TRIGGER_DELAY_VAL ( 0U )
```

Definition at line 176 of file app_cfg.h.

4.3.1.18 MAX_DET_OBJECTS_RAW_MAX_VEL_ENH_PROCESSING

```
#define MAX_DET_OBJECTS_RAW_MAX_VEL_ENH_PROCESSING 200
```

The maximum number of objects detected in the 'Max velocity enhanced' processing path. Because of all the pruning, and higher thresholds, and lower resolution, fewer objects are detected in MAX_VEL_ENH_PROCESSING.

Definition at line 156 of file app_cfg.h.

4.3.1.19 MAX_DET_OBJECTS_RAW_POINT_CLOUD_PROCESSING

```
#define MAX_DET_OBJECTS_RAW_POINT_CLOUD_PROCESSING 900
```

The maximum number of objects detected in the 'point cloud ' processing path.

Definition at line 158 of file app_cfg.h.

4.3.1.20 MAX_NUM_CLUSTER_MRR

```
#define MAX_NUM_CLUSTER_MRR ( 32U )
```

The maximum number of clusters out of the dbscan algorithm (for the MRR subframe).

Definition at line 118 of file app_cfg.h.

4.3.1.21 MAX_NUM_CLUSTER_USRR

```
#define MAX_NUM_CLUSTER_USRR ( 24U )
```

The maximum number of clusters out of the dbscan algorithm (for the USRR subframe).

Definition at line 115 of file app_cfg.h.

4.3.1.22 MAX_NUM_DET_PER_RANGE_GATE

```
#define MAX_NUM_DET_PER_RANGE_GATE ( 3U )
```

Restrict the number of detected objects per range-gate to 3.

Definition at line 168 of file app_cfg.h.

4.3.1.23 MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS

```
#define MAX_NUM_RANGE_DEPENDANT_SNR_THRESHOLDS ( 3U )
```

The number of SNR Thresholds - used to vary the SNR requirement as a function of range.

Definition at line 108 of file app_cfg.h.

4.3.1.24 MAX_TRK_OBJS

```
#define MAX_TRK_OBJS (32U)
```

The maximum number of tracked objects from the Kalman filter.

Definition at line 121 of file app_cfg.h.

4.3.1.25 MAX_VEL_ENH_NUM_NYQUIST

```
#define MAX_VEL_ENH_NUM_NYQUIST (2U)
```

Max velocity improvement of 3x.

Definition at line 166 of file app_cfg.h.

4.3.1.26 MAX_VEL_ENH_PROCESSING

```
#define MAX_VEL_ENH_PROCESSING (0U)
```

There are two processing paths in the MRR Demo.

Definition at line 111 of file app_cfg.h.

4.3.1.27 MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH

```
#define MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH (((1U << CFARTHRESHOLD_N_BIT_FRAC) * NUM_RX_CHANNELS * MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH_DB) / 6U)
```

Convert the threshold to a CFAR magnitude difference.

Definition at line 162 of file app_cfg.h.

4.3.1.28 MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH_DB

```
#define MAX_VEL_IMPROVEMENT_ASSOCIATION_THRESH_DB (3U)
```

The two peaks (from the 'fast chirp' and the 'slow chirp' should be within 2 dB.

Definition at line 160 of file app_cfg.h.

4.3.1.29 MAX_VEL_IMPROVEMENT_NUM_SPREAD

```
#define MAX_VEL_IMPROVEMENT_NUM_SPREAD (2U)
```

Search across 2 bins in the slow chirp.

Definition at line 164 of file app_cfg.h.

4.3.1.30 MAX_VEL_POINT_CLOUD_PROCESSING_IS_ENABLED

```
#define MAX_VEL_POINT_CLOUD_PROCESSING_IS_ENABLED 0
```

Flag to enable the max velocity enhancement in point cloud processing.

Definition at line 211 of file app_cfg.h.

4.3.1.31 MIN_RANGE_OFFSET_METERS

```
#define MIN_RANGE_OFFSET_METERS (0.075f)
```

The radar's range estimate has a constant error due to the finite distance from the antenna to the LO.

Definition at line 130 of file app_cfg.h.

4.3.1.32 MIN_TICK_FOR_TX

```
#define MIN_TICK_FOR_TX (10U)  
Wait for MIN_TICK_FOR_TX before letting the tracker results out.  
Definition at line 133 of file app_cfg.h.
```

4.3.1.33 MRR_EDMA_TRIGGER_DISABLE

```
#define MRR_EDMA_TRIGGER_DISABLE 0  
Definition at line 205 of file app_cfg.h.
```

4.3.1.34 MRR_EDMA_TRIGGER_ENABLE

```
#define MRR_EDMA_TRIGGER_ENABLE 1  
Definition at line 204 of file app_cfg.h.
```

4.3.1.35 MRR_MAX_OBJ_OUT

```
#define MRR_MAX_OBJ_OUT 200  
The maximum number of objects to be send out per frame. This number is upper bounded by the transfer rate to  
the external device.  
Definition at line 150 of file app_cfg.h.
```

4.3.1.36 MRR_SF0_EDMA_CH_1D_IN_PING

```
#define MRR_SF0_EDMA_CH_1D_IN_PING EDMA_TPCC0_REQ_FREE_0  
EDMA allocation and configuration table for FFT processing of subframe 0 AKA: (The whole frame)  
Definition at line 187 of file app_cfg.h.
```

4.3.1.37 MRR_SF0_EDMA_CH_1D_IN_PONG

```
#define MRR_SF0_EDMA_CH_1D_IN_PONG EDMA_TPCC0_REQ_FREE_1  
Definition at line 188 of file app_cfg.h.
```

4.3.1.38 MRR_SF0_EDMA_CH_1D_OUT_PING

```
#define MRR_SF0_EDMA_CH_1D_OUT_PING EDMA_TPCC0_REQ_FREE_2  
Definition at line 189 of file app_cfg.h.
```

4.3.1.39 MRR_SF0_EDMA_CH_1D_OUT_PONG

```
#define MRR_SF0_EDMA_CH_1D_OUT_PONG EDMA_TPCC0_REQ_FREE_3  
Definition at line 190 of file app_cfg.h.
```

4.3.1.40 MRR_SF0_EDMA_CH_2D_IN_PING

```
#define MRR_SF0_EDMA_CH_2D_IN_PING EDMA_TPCC0_REQ_FREE_4  
Definition at line 191 of file app_cfg.h.
```

4.3.1.41 MRR_SF0_EDMA_CH_2D_IN_PONG

```
#define MRR_SF0_EDMA_CH_2D_IN_PONG EDMA_TPCC0_REQ_FREE_5
Definition at line 192 of file app_cfg.h.
```

4.3.1.42 MRR_SF0_EDMA_CH_3D_IN_PING

```
#define MRR_SF0_EDMA_CH_3D_IN_PING EDMA_TPCC0_REQ_FREE_8
Definition at line 195 of file app_cfg.h.
```

4.3.1.43 MRR_SF0_EDMA_CH_3D_IN_PONG

```
#define MRR_SF0_EDMA_CH_3D_IN_PONG EDMA_TPCC0_REQ_FREE_9
Definition at line 196 of file app_cfg.h.
```

4.3.1.44 MRR_SF0_EDMA_CH_DET_MATRIX

```
#define MRR_SF0_EDMA_CH_DET_MATRIX EDMA_TPCC0_REQ_FREE_6
Definition at line 193 of file app_cfg.h.
```

4.3.1.45 MRR_SF0_EDMA_CH_DET_MATRIX2

```
#define MRR_SF0_EDMA_CH_DET_MATRIX2 EDMA_TPCC0_REQ_FREE_7
Definition at line 194 of file app_cfg.h.
```

4.3.1.46 NUM_CHIRP_PROG

```
#define NUM_CHIRP_PROG (3U) /* 3TX antennas so Three for USRR20 */
Definition at line 78 of file app_cfg.h.
```

4.3.1.47 NUM_PROFILES

```
#define NUM_PROFILES (1U) /* one profile for USRR mode of operation*/
Definition at line 79 of file app_cfg.h.
```

4.3.1.48 NUM_RX_CHANNELS

```
#define NUM_RX_CHANNELS (4U)
Reduced Thresholds. The following line (if uncommented) reduces thresholds for the USRR detection algorithm to
enable the detection of weak targets.
```

120m max range. The MRR was initially designed for 80m, but with a slight configuration change will work at 120m (boresight). Uncomment the following line for enabling that config.
 Definition at line 77 of file app_cfg.h.

4.3.1.49 NUM_SUBFRAMES

```
#define NUM_SUBFRAMES (1U) /* one subframe for USRR mode operation: No advanced frame configuration */
Definition at line 80 of file app_cfg.h.
```

4.3.1.50 POINT_CLOUD_PROCESSING

```
#define POINT_CLOUD_PROCESSING (1U)
Definition at line 112 of file app_cfg.h.
```

4.3.1.51 REPORT_N_BIT_FRAC

```
#define REPORT_N_BIT_FRAC (7U)
Fractional bit width for most of the report data (range, velocity, x, y, etc).
Definition at line 124 of file app_cfg.h.
```

4.3.1.52 SIN_55_DEGREES

```
#define SIN_55_DEGREES (0.8192f)
We discard objects at extreme angles (greater than 55 degrees) from the tracking procedure.
Definition at line 137 of file app_cfg.h.
```

4.3.1.53 SUBFRAME_CONF_USRR

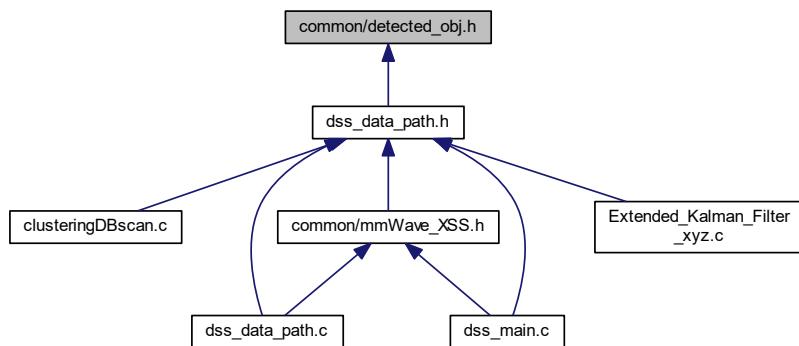
```
#define SUBFRAME_CONF_USRR
ENABLE USRR configuraion.
Definition at line 83 of file app_cfg.h.
```

4.3.1.54 TRK_SIN_AZIM_THRESH

```
#define TRK_SIN_AZIM_THRESH (1.0f/256.0f)
We discard objects with poor azimuth SNR from the tracking procedure.
Definition at line 141 of file app_cfg.h.
```

4.4 common/detected_obj.h File Reference

This graph shows which files directly or indirectly include this file:



Data Structures

- struct **MmwDemo_detectedObj_t**
Detected object estimated parameters.

Macros

- `#define MMW_MAX_OBJ_OUT 100`
Maximum number of detected objects by HWA.
- `#define DOPPLER_IDX_TO_SIGNED(_idx, _fftSize)`
Converts Doppler index to signed variable. Value greater than or equal half the Doppler FFT size will become negative. Needed for extended maximum velocity.
- `#define DOPPLER_IDX_TO_UNSIGNED(_idx, _fftSize) ((_idx) & (_fftSize - 1))`
Converts signed Doppler index to unsigned variable (zero to FFT size -1).

Typedefs

- `typedef volatile struct MmwDemo_detectedObj_t MmwDemo_detectedObj`
Detected object estimated parameters.

4.4.1 Macro Definition Documentation

4.4.1.1 DOPPLER_IDX_TO_SIGNED

```
#define DOPPLER_IDX_TO_SIGNED (
    _idx,
    _fftSize )
```

Value:

```
((_idx) < (_fftSize)/2 ? \
    ((int16_t) (_idx)) : ((int16_t) (_idx) - (int16_t) (_fftSize)))
```

Converts Doppler index to signed variable. Value greater than or equal half the Doppler FFT size will become negative. Needed for extended maximum velocity.

Definition at line 16 of file detected_obj.h.

4.4.1.2 DOPPLER_IDX_TO_UNSIGNED

```
#define DOPPLER_IDX_TO_UNSIGNED (
    _idx,
    _fftSize ) ((_idx) & (_fftSize - 1))
```

Converts signed Doppler index to unsigned variable (zero to FFT size -1).

Definition at line 21 of file detected_obj.h.

4.4.1.3 MMW_MAX_OBJ_OUT

```
#define MMW_MAX_OBJ_OUT 100
```

Maximum number of detected objects by HWA.

Definition at line 10 of file detected_obj.h.

4.4.2 Typedef Documentation

4.4.2.1 MmwDemo_detectedObj

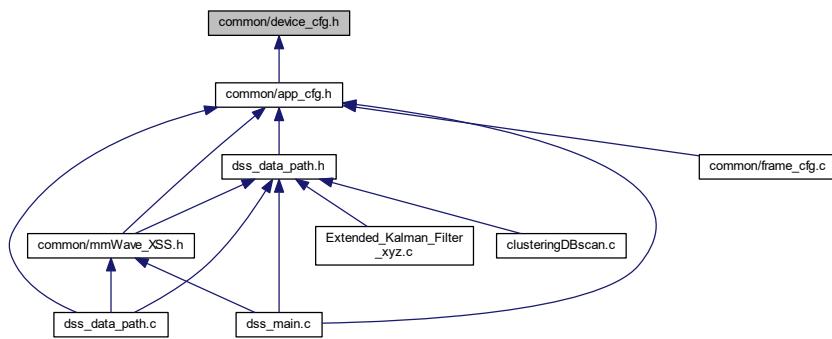
```
typedef volatile struct MmwDemo_detectedObj_t MmwDemo_detectedObj
```

Detected object estimated parameters.

4.5 common/device_cfg.h File Reference

This file holds constants related to the system chirp configuration as well as the calling the profiles configs such as: `config_chirp_design_USRR20.h` (p. ??) `config_chirp_design_USRR30.h` (p. ??) To Add profiles, create the profile in the following path `common/profiles/` under the following name convention: `config_chirp_design_XRRxx.h` X where S:Short, M: Medium, US: Ultrashort .. etc xx is the number of chips within the frame.

This graph shows which files directly or indirectly include this file:



Macros

- `#define TX_CHANNEL_1_ENABLE (1U << 0U)`
- `#define TX_CHANNEL_2_ENABLE (1U << 1U)`
- `#define TX_CHANNEL_3_ENABLE (1U << 2U)`
- `#define TX_CHANNEL_1_2_ENABLE (TX_CHANNEL_1_ENABLE | TX_CHANNEL_2_ENABLE)`
- `#define TX_CHANNEL_2_3_ENABLE (TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE)`
- `#define TX_CHANNEL_1_3_ENABLE (TX_CHANNEL_1_ENABLE | TX_CHANNEL_3_ENABLE)`
- `#define TX_CHANNEL_1_2_3_ENABLE (TX_CHANNEL_1_ENABLE | TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE)`
- `#define RX_CHANNEL_1_ENABLE (1U << 0U)`
- `#define RX_CHANNEL_2_ENABLE (1U << 1U)`
- `#define RX_CHANNEL_3_ENABLE (1U << 2U)`
- `#define RX_CHANNEL_4_ENABLE (1U << 3U)`
- `#define RX_CHANNEL_1_2_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE)`
- `#define RX_CHANNEL_1_3_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_3_ENABLE)`
- `#define RX_CHANNEL_1_4_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define RX_CHANNEL_2_3_ENABLE (RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE)`
- `#define RX_CHANNEL_2_4_ENABLE (RX_CHANNEL_2_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define RX_CHANNEL_3_4_ENABLE (RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define RX_CHANNEL_1_2_3_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE)`
- `#define RX_CHANNEL_2_3_4_ENABLE (RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define RX_CHANNEL_1_3_4_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define RX_CHANNEL_1_2_3_4_ENABLE (RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE)`
- `#define ADC_BITS_12 (0U)`
- `#define ADC_BITS_14 (1U)`
- `#define ADC_BITS_16 (2U)`
- `#define ADC_FORMAT_REAL (0U)`
- `#define ADC_FORMAT_COMPLEX (1U)`

- #define **ADC_FORMAT_CPMLEX_WITH_IMG_BAND** (2U)
- #define **ADC_I_FIRST** (0U)
- #define **ADC_Q_FIRST** (1U)
- #define **ADC_INTERLEAVED_MODE** (0U)
- #define **ADC_NON_INTERLEAVED_MODE** (1U)
- #define **DATA_PATH_CSI2** (0U)
- #define **DATA_PATH_LVDS** (1U)
- #define **DATA_PATH_FMT1_SUPRESS** (0U)
- #define **DATA_PATH_FMT1_CP_CQ** (1U)
- #define **DATA_PATH_FMT1_CQ_CP** (2U)
- #define **DATA_PATH_FMT0_ADC_DATA_ONLY** (0U)
- #define **DATA_PATH_FMT0_CP_ADC_DATA** (1U)
- #define **DATA_PATH_FMT0_ADC_CP_DATA** (2U)
- #define **DATA_PATH_FMT0_CP_ADC_CQ_DATA** (3U)
- #define **DATA_PATH_CQ_FMT_BITS_12** (0U)
- #define **DATA_PATH_CQ_FMT_BITS_14** (1U)
- #define **DATA_PATH_CQ_FMT_BITS_16** (2U)
- #define **LVDS_LANE_CLOCK_SDR** (0U)
- #define **LVDS_LANE_CLOCK_DDR** (1U)
- #define **LVDS_ALL_LANE_EN** (0xFU)
- #define **LVDS_DATA_RATE_900** (0U)
- #define **LVDS_DATA_RATE_600** (1U)
- #define **LVDS_DATA_RATE_450** (2U)
- #define **LVDS_DATA_RATE_400** (3U)
- #define **LVDS_DATA_RATE_300** (4U)
- #define **LVDS_DATA_RATE_225** (5U)
- #define **LVDS_DATA_RATE_150** (6U)
- #define **LVDS_LANE1_DISABLE** (0U)
- #define **LVDS_LANE1_FORMAT_0** (1U)
- #define **LVDS_LANE1_FORMAT_1** (2U)
- #define **LVDS_LANE2_DISABLE** (0U)
- #define **LVDS_LANE2_FORMAT_0** (1U)
- #define **LVDS_LANE2_FORMAT_1** (2U)
- #define **LVDS_LANE3_DISABLE** (0U)
- #define **LVDS_LANE3_FORMAT_0** (1U)
- #define **LVDS_LANE3_FORMAT_1** (2U)
- #define **LVDS_LANE4_DISABLE** (0U)
- #define **LVDS_LANE4_FORMAT_0** (1U)
- #define **LVDS_LANE4_FORMAT_1** (2U)
- #define **LVDS_LANE_MSB_FIRST_ENABLE** (1U)
- #define **LVDS_LANE_MSB_FIRST_DISABLE** (0U)
- #define **LVDS_LANE_PACKET_END_PULSE_ENABLE** (1U)
- #define **LVDS_LANE_PACKET_END_PULSE_DISABLE** (0U)
- #define **LVDS_LANE_CRC_ENABLE** (1U)
- #define **LVDS_LANE_CRC_DISABLE** (0U)
- #define **LVDS_LANE_TI_MODE_ENABLE** (1U)
- #define **LVDS_LANE_TI_MODE_DISABLE** (0U)
- #define **ANA_CHANNEL_COMPLEX_CHAIN** (0U)
- #define **ANA_CHANNEL_REAL_CHAIN** (1U)
- #define **LP_ADC_MODE_REGULAR** (0U)
- #define **LP_ADC_MODE_LOW_POWER** (1U)
- #define **NOISE FIGURE LOW** (0U)
- #define **NOISE FIGURE HIGH** (1U)
- #define **CHIRP_HPF1_CORNER_FREQ_175K** (0U)
- #define **CHIRP_HPF1_CORNER_FREQ_235K** (1U)

- #define CHIRP_HPF1_CORNER_FREQ_350K (2U)
- #define CHIRP_HPF1_CORNER_FREQ_700K (3U)
- #define CHIRP_HPF2_CORNER_FREQ_350K (0U)
- #define CHIRP_HPF2_CORNER_FREQ_700K (1U)
- #define CHIRP_HPF2_CORNER_FREQ_1_4M (2U)
- #define CHIRP_HPF2_CORNER_FREQ_2_8M (3U)
- #define CHIRP_HPF2_CORNER_FREQ_5M (4U)
- #define CHIRP_HPF2_CORNER_FREQ_7_5M (5U)
- #define CHIRP_HPF2_CORNER_FREQ_10M (6U)
- #define CHIRP_HPF2_CORNER_FREQ_15M (7U)
- #define ROUND_TO_INT32(X) ((int32_t)(X))
- #define CONV_FREQ_GHZ_TO_CODEWORD(X) ROUND_TO_INT32(X * (1.0e9/53.644))
- #define CONV_SLOPE_MHZ_PER_US_TO_CODEWORD(X) (ROUND_TO_INT32(X * (1000.0/48.279)))
- #define LOG2_APPROX(X) ((X <= 1)? 0:(X <= 2)? 1:(X <= 4)? 2:(X <= 8)? 3:(X <= 16)? 4:(X <= 32)? 5:(X <= 64)? 6:(X <= 128)? 7:(X <= 256)? 8:(X <= 512)? 9:(X <= 1024)? 10:11))))))))
- #define SPEED_OF_LIGHT_IN_METERS_PER_SEC (3.0e8)
- #define SPEED_OF_LIGHT_IN_METERS_PER_USEC (3.0e2)

4.5.1 Detailed Description

This file holds constants related to the system chirp configuration as well as the calling the profiles configs such as: **config_chirp_design_USRR20.h** (p. ??) **config_chirp_design_USRR30.h** (p. ??) To Add profiles, create the profile in the following path common/profiles/ under the following name convention: config_chirp_design_XRxx.h X where S:Short, M: Medium, US: Ultrashort .. etc xx is the number of chips within the frame.

4.5.2 Macro Definition Documentation

4.5.2.1 ADC_BITS_12

```
#define ADC_BITS_12 (0U)
Definition at line 31 of file device_cfg.h.
```

4.5.2.2 ADC_BITS_14

```
#define ADC_BITS_14 (1U)
Definition at line 32 of file device_cfg.h.
```

4.5.2.3 ADC_BITS_16

```
#define ADC_BITS_16 (2U)
Definition at line 33 of file device_cfg.h.
```

4.5.2.4 ADC_FORMAT_COMPLEX

```
#define ADC_FORMAT_COMPLEX (1U)
Definition at line 36 of file device_cfg.h.
```

4.5.2.5 ADC_FORMAT_CPMLEX_WITH_IMG_BAND

```
#define ADC_FORMAT_CPMLEX_WITH_IMG_BAND (2U)
Definition at line 37 of file device_cfg.h.
```

4.5.2.6 ADC_FORMAT_REAL

```
#define ADC_FORMAT_REAL (0U)
```

Definition at line 35 of file device_cfg.h.

4.5.2.7 ADC_I_FIRST

```
#define ADC_I_FIRST (0U)
```

Definition at line 39 of file device_cfg.h.

4.5.2.8 ADC_INTERLEAVED_MODE

```
#define ADC_INTERLEAVED_MODE (0U)
```

Definition at line 42 of file device_cfg.h.

4.5.2.9 ADC_NON_INTERLEAVED_MODE

```
#define ADC_NON_INTERLEAVED_MODE (1U)
```

Definition at line 43 of file device_cfg.h.

4.5.2.10 ADC_Q_FIRST

```
#define ADC_Q_FIRST (1U)
```

Definition at line 40 of file device_cfg.h.

4.5.2.11 ANA_CHANNEL_COMPLEX_CHAIN

```
#define ANA_CHANNEL_COMPLEX_CHAIN (0U)
```

Definition at line 104 of file device_cfg.h.

4.5.2.12 ANA_CHANNEL_REAL_CHAIN

```
#define ANA_CHANNEL_REAL_CHAIN (1U)
```

Definition at line 105 of file device_cfg.h.

4.5.2.13 CHIRP_HPF1_CORNER_FREQ_175K

```
#define CHIRP_HPF1_CORNER_FREQ_175K (0U)
```

Definition at line 114 of file device_cfg.h.

4.5.2.14 CHIRP_HPF1_CORNER_FREQ_235K

```
#define CHIRP_HPF1_CORNER_FREQ_235K (1U)
```

Definition at line 115 of file device_cfg.h.

4.5.2.15 CHIRP_HPF1_CORNER_FREQ_350K

```
#define CHIRP_HPF1_CORNER_FREQ_350K (2U)
```

Definition at line 116 of file device_cfg.h.

4.5.2.16 CHIRP_HPF1_CORNER_FREQ_700K

```
#define CHIRP_HPF1_CORNER_FREQ_700K (3U)
```

Definition at line 117 of file device_cfg.h.

4.5.2.17 CHIRP_HPF2_CORNER_FREQ_10M

```
#define CHIRP_HPF2_CORNER_FREQ_10M (6U)
```

Definition at line 125 of file device_cfg.h.

4.5.2.18 CHIRP_HPF2_CORNER_FREQ_15M

```
#define CHIRP_HPF2_CORNER_FREQ_15M (7U)
```

Definition at line 126 of file device_cfg.h.

4.5.2.19 CHIRP_HPF2_CORNER_FREQ_1_4M

```
#define CHIRP_HPF2_CORNER_FREQ_1_4M (2U)
```

Definition at line 121 of file device_cfg.h.

4.5.2.20 CHIRP_HPF2_CORNER_FREQ_2_8M

```
#define CHIRP_HPF2_CORNER_FREQ_2_8M (3U)
```

Definition at line 122 of file device_cfg.h.

4.5.2.21 CHIRP_HPF2_CORNER_FREQ_350K

```
#define CHIRP_HPF2_CORNER_FREQ_350K (0U)
```

Definition at line 119 of file device_cfg.h.

4.5.2.22 CHIRP_HPF2_CORNER_FREQ_5M

```
#define CHIRP_HPF2_CORNER_FREQ_5M (4U)
```

Definition at line 123 of file device_cfg.h.

4.5.2.23 CHIRP_HPF2_CORNER_FREQ_700K

```
#define CHIRP_HPF2_CORNER_FREQ_700K (1U)
```

Definition at line 120 of file device_cfg.h.

4.5.2.24 CHIRP_HPF2_CORNER_FREQ_7_5M

```
#define CHIRP_HPF2_CORNER_FREQ_7_5M (5U)
```

Definition at line 124 of file device_cfg.h.

4.5.2.25 CONV_FREQ_GHZ_TO_CODEWORD

```
#define CONV_FREQ_GHZ_TO_CODEWORD (
```

```
    X )  ROUND_TO_INT32(X * (1.0e9/53.644))
```

Definition at line 130 of file device_cfg.h.

4.5.2.26 CONV_SLOPE_MHZ_PER_US_TO_CODEWORD

```
#define CONV_SLOPE_MHZ_PER_US_TO_CODEWORD( X ) ( ROUND_TO_INT32(X * (1000.0/48.279)) )
```

Definition at line 131 of file device_cfg.h.

4.5.2.27 DATA_PATH_CQ_FMT_BITS_12

```
#define DATA_PATH_CQ_FMT_BITS_12 (0U)
```

Definition at line 59 of file device_cfg.h.

4.5.2.28 DATA_PATH_CQ_FMT_BITS_14

```
#define DATA_PATH_CQ_FMT_BITS_14 (1U)
```

Definition at line 60 of file device_cfg.h.

4.5.2.29 DATA_PATH_CQ_FMT_BITS_16

```
#define DATA_PATH_CQ_FMT_BITS_16 (2U)
```

Definition at line 61 of file device_cfg.h.

4.5.2.30 DATA_PATH_CSI2

```
#define DATA_PATH_CSI2 (0U)
```

Definition at line 46 of file device_cfg.h.

4.5.2.31 DATA_PATH_FMT0_ADC_CP_DATA

```
#define DATA_PATH_FMT0_ADC_CP_DATA (2U)
```

Definition at line 56 of file device_cfg.h.

4.5.2.32 DATA_PATH_FMT0_ADC_DATA_ONLY

```
#define DATA_PATH_FMT0_ADC_DATA_ONLY (0U)
```

Definition at line 54 of file device_cfg.h.

4.5.2.33 DATA_PATH_FMT0_CP_ADC_CQ_DATA

```
#define DATA_PATH_FMT0_CP_ADC_CQ_DATA (3U)
```

Definition at line 57 of file device_cfg.h.

4.5.2.34 DATA_PATH_FMT0_CP_ADC_DATA

```
#define DATA_PATH_FMT0_CP_ADC_DATA (1U)
```

Definition at line 55 of file device_cfg.h.

4.5.2.35 DATA_PATH_FMT1_CP_CQ

```
#define DATA_PATH_FMT1_CP_CQ (1U)
```

Definition at line 51 of file device_cfg.h.

4.5.2.36 DATA_PATH_FMT1_CQ_CP

```
#define DATA_PATH_FMT1_CQ_CP (2U)
```

Definition at line 52 of file device_cfg.h.

4.5.2.37 DATA_PATH_FMT1_SUPRESS

```
#define DATA_PATH_FMT1_SUPRESS (0U)
```

Definition at line 50 of file device_cfg.h.

4.5.2.38 DATA_PATH_LVDS

```
#define DATA_PATH_LVDS (1U)
```

Definition at line 47 of file device_cfg.h.

4.5.2.39 LOG2_APPROX

```
#define LOG2_APPROX(
```

$$\begin{aligned} X) & ((X <= 1) ? 0 : ((X <= 2) ? 1 : ((X <= 4) ? 2 : ((X <= 8) ? 3 : ((X <= 16) ? 4 \\ \leftarrow \\ : ((X <= 32) ? 5 : ((X <= 64) ? 6 : ((X <= 128) ? 7 : ((X <= 256) ? 8 : ((X <= 512) ? 9 : ((X <= 1024) ? 10 : 11))))))) \end{aligned})$$

Definition at line 133 of file device_cfg.h.

4.5.2.40 LP_ADC_MODE_LOW_POWER

```
#define LP_ADC_MODE_LOW_POWER (1U)
```

Definition at line 108 of file device_cfg.h.

4.5.2.41 LP_ADC_MODE_REGULAR

```
#define LP_ADC_MODE_REGULAR (0U)
```

Definition at line 107 of file device_cfg.h.

4.5.2.42 LVDS_ALL_LANE_EN

```
#define LVDS_ALL_LANE_EN (0xFU)
```

Definition at line 67 of file device_cfg.h.

4.5.2.43 LVDS_DATA_RATE_150

```
#define LVDS_DATA_RATE_150 (6U)
```

Definition at line 75 of file device_cfg.h.

4.5.2.44 LVDS_DATA_RATE_225

```
#define LVDS_DATA_RATE_225 (5U)
```

Definition at line 74 of file device_cfg.h.

4.5.2.45 LVDS_DATA_RATE_300

```
#define LVDS_DATA_RATE_300 (4U)
```

Definition at line 73 of file device_cfg.h.

4.5.2.46 LVDS_DATA_RATE_400

```
#define LVDS_DATA_RATE_400 (3U)
```

Definition at line 72 of file device_cfg.h.

4.5.2.47 LVDS_DATA_RATE_450

```
#define LVDS_DATA_RATE_450 (2U)
```

Definition at line 71 of file device_cfg.h.

4.5.2.48 LVDS_DATA_RATE_600

```
#define LVDS_DATA_RATE_600 (1U)
```

Definition at line 70 of file device_cfg.h.

4.5.2.49 LVDS_DATA_RATE_900

```
#define LVDS_DATA_RATE_900 (0U)
```

Definition at line 69 of file device_cfg.h.

4.5.2.50 LVDS_LANE1_DISABLE

```
#define LVDS_LANE1_DISABLE (0U)
```

Definition at line 78 of file device_cfg.h.

4.5.2.51 LVDS_LANE1_FORMAT_0

```
#define LVDS_LANE1_FORMAT_0 (1U)
```

Definition at line 79 of file device_cfg.h.

4.5.2.52 LVDS_LANE1_FORMAT_1

```
#define LVDS_LANE1_FORMAT_1 (2U)
```

Definition at line 80 of file device_cfg.h.

4.5.2.53 LVDS_LANE2_DISABLE

```
#define LVDS_LANE2_DISABLE (0U)
```

Definition at line 82 of file device_cfg.h.

4.5.2.54 LVDS_LANE2_FORMAT_0

```
#define LVDS_LANE2_FORMAT_0 (1U)
```

Definition at line 83 of file device_cfg.h.

4.5.2.55 LVDS_LANE2_FORMAT_1

```
#define LVDS_LANE2_FORMAT_1 (2U)
```

Definition at line 84 of file device_cfg.h.

4.5.2.56 LVDS_LANE3_DISABLE

```
#define LVDS_LANE3_DISABLE (0U)
```

Definition at line 86 of file device_cfg.h.

4.5.2.57 LVDS_LANE3_FORMAT_0

```
#define LVDS_LANE3_FORMAT_0 (1U)
```

Definition at line 87 of file device_cfg.h.

4.5.2.58 LVDS_LANE3_FORMAT_1

```
#define LVDS_LANE3_FORMAT_1 (2U)
```

Definition at line 88 of file device_cfg.h.

4.5.2.59 LVDS_LANE4_DISABLE

```
#define LVDS_LANE4_DISABLE (0U)
```

Definition at line 90 of file device_cfg.h.

4.5.2.60 LVDS_LANE4_FORMAT_0

```
#define LVDS_LANE4_FORMAT_0 (1U)
```

Definition at line 91 of file device_cfg.h.

4.5.2.61 LVDS_LANE4_FORMAT_1

```
#define LVDS_LANE4_FORMAT_1 (2U)
```

Definition at line 92 of file device_cfg.h.

4.5.2.62 LVDS_LANE_CLOCK_DDR

```
#define LVDS_LANE_CLOCK_DDR (1U)
```

Definition at line 65 of file device_cfg.h.

4.5.2.63 LVDS_LANE_CLOCK_SDR

```
#define LVDS_LANE_CLOCK_SDR (0U)
```

Definition at line 64 of file device_cfg.h.

4.5.2.64 LVDS_LANE_CRC_DISABLE

```
#define LVDS_LANE_CRC_DISABLE (0U)
```

Definition at line 99 of file device_cfg.h.

4.5.2.65 LVDS_LANE_CRC_ENABLE

```
#define LVDS_LANE_CRC_ENABLE (1U)
```

Definition at line 98 of file device_cfg.h.

4.5.2.66 LVDS_LANE_MSB_FIRST_DISABLE

```
#define LVDS_LANE_MSB_FIRST_DISABLE (0U)
```

Definition at line 95 of file device_cfg.h.

4.5.2.67 LVDS_LANE_MSB_FIRST_ENABLE

```
#define LVDS_LANE_MSB_FIRST_ENABLE (1U)
```

Definition at line 94 of file device_cfg.h.

4.5.2.68 LVDS_LANE_PACKET_END_PULSE_DISABLE

```
#define LVDS_LANE_PACKET_END_PULSE_DISABLE (0U)
```

Definition at line 97 of file device_cfg.h.

4.5.2.69 LVDS_LANE_PACKET_END_PULSE_ENABLE

```
#define LVDS_LANE_PACKET_END_PULSE_ENABLE (1U)
```

Definition at line 96 of file device_cfg.h.

4.5.2.70 LVDS_LANE_TI_MODE_DISABLE

```
#define LVDS_LANE_TI_MODE_DISABLE (0U)
```

Definition at line 101 of file device_cfg.h.

4.5.2.71 LVDS_LANE_TI_MODE_ENABLE

```
#define LVDS_LANE_TI_MODE_ENABLE (1U)
```

Definition at line 100 of file device_cfg.h.

4.5.2.72 NOISE FIGURE HIGH

```
#define NOISE FIGURE HIGH (1U)
```

Definition at line 111 of file device_cfg.h.

4.5.2.73 NOISE FIGURE LOW

```
#define NOISE FIGURE LOW (0U)
```

Definition at line 110 of file device_cfg.h.

4.5.2.74 ROUND_TO_INT32

```
#define ROUND_TO_INT32(
```

```
    X ) ((int32_t) (X))
```

Definition at line 129 of file device_cfg.h.

4.5.2.75 RX_CHANNEL_1_2_3_4_ENABLE

```
#define RX_CHANNEL_1_2_3_4_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE )
```

Definition at line 28 of file device_cfg.h.

4.5.2.76 RX_CHANNEL_1_2_3_ENABLE

```
#define RX_CHANNEL_1_2_3_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE )
```

Definition at line 25 of file device_cfg.h.

4.5.2.77 RX_CHANNEL_1_2_ENABLE

```
#define RX_CHANNEL_1_2_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_2_ENABLE )
```

Definition at line 19 of file device_cfg.h.

4.5.2.78 RX_CHANNEL_1_3_4_ENABLE

```
#define RX_CHANNEL_1_3_4_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE )
```

Definition at line 27 of file device_cfg.h.

4.5.2.79 RX_CHANNEL_1_3_ENABLE

```
#define RX_CHANNEL_1_3_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_3_ENABLE )
```

Definition at line 20 of file device_cfg.h.

4.5.2.80 RX_CHANNEL_1_4_ENABLE

```
#define RX_CHANNEL_1_4_ENABLE ( RX_CHANNEL_1_ENABLE | RX_CHANNEL_4_ENABLE )
```

Definition at line 21 of file device_cfg.h.

4.5.2.81 RX_CHANNEL_1_ENABLE

```
#define RX_CHANNEL_1_ENABLE (1U << 0U)
```

Definition at line 15 of file device_cfg.h.

4.5.2.82 RX_CHANNEL_2_3_4_ENABLE

```
#define RX_CHANNEL_2_3_4_ENABLE ( RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE )
```

Definition at line 26 of file device_cfg.h.

4.5.2.83 RX_CHANNEL_2_3_ENABLE

```
#define RX_CHANNEL_2_3_ENABLE ( RX_CHANNEL_2_ENABLE | RX_CHANNEL_3_ENABLE )
```

Definition at line 22 of file device_cfg.h.

4.5.2.84 RX_CHANNEL_2_4_ENABLE

```
#define RX_CHANNEL_2_4_ENABLE ( RX_CHANNEL_2_ENABLE | RX_CHANNEL_4_ENABLE)
```

Definition at line 23 of file device_cfg.h.

4.5.2.85 RX_CHANNEL_2_ENABLE

```
#define RX_CHANNEL_2_ENABLE (1U << 1U)
```

Definition at line 16 of file device_cfg.h.

4.5.2.86 RX_CHANNEL_3_4_ENABLE

```
#define RX_CHANNEL_3_4_ENABLE ( RX_CHANNEL_3_ENABLE | RX_CHANNEL_4_ENABLE)
```

Definition at line 24 of file device_cfg.h.

4.5.2.87 RX_CHANNEL_3_ENABLE

```
#define RX_CHANNEL_3_ENABLE (1U << 2U)
```

Definition at line 17 of file device_cfg.h.

4.5.2.88 RX_CHANNEL_4_ENABLE

```
#define RX_CHANNEL_4_ENABLE (1U << 3U)
```

Definition at line 18 of file device_cfg.h.

4.5.2.89 SPEED_OF_LIGHT_IN_METERS_PER_SEC

```
#define SPEED_OF_LIGHT_IN_METERS_PER_SEC (3.0e8)
```

Definition at line 135 of file device_cfg.h.

4.5.2.90 SPEED_OF_LIGHT_IN_METERS_PER_USEC

```
#define SPEED_OF_LIGHT_IN_METERS_PER_USEC (3.0e2)
```

Definition at line 137 of file device_cfg.h.

4.5.2.91 TX_CHANNEL_1_2_3_ENABLE

```
#define TX_CHANNEL_1_2_3_ENABLE ( TX_CHANNEL_1_ENABLE | TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE)
```

Definition at line 12 of file device_cfg.h.

4.5.2.92 TX_CHANNEL_1_2_ENABLE

```
#define TX_CHANNEL_1_2_ENABLE ( TX_CHANNEL_1_ENABLE | TX_CHANNEL_2_ENABLE)
```

Definition at line 9 of file device_cfg.h.

4.5.2.93 TX_CHANNEL_1_3_ENABLE

```
#define TX_CHANNEL_1_3_ENABLE ( TX_CHANNEL_1_ENABLE | TX_CHANNEL_3_ENABLE)
```

Definition at line 11 of file device_cfg.h.

4.5.2.94 TX_CHANNEL_1_ENABLE

```
#define TX_CHANNEL_1_ENABLE (1U << 0U)
Definition at line 6 of file device_cfg.h.
```

4.5.2.95 TX_CHANNEL_2_3_ENABLE

```
#define TX_CHANNEL_2_3_ENABLE ( TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE )
Definition at line 10 of file device_cfg.h.
```

4.5.2.96 TX_CHANNEL_2_ENABLE

```
#define TX_CHANNEL_2_ENABLE (1U << 1U)
Definition at line 7 of file device_cfg.h.
```

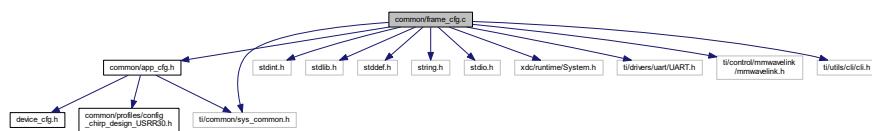
4.5.2.97 TX_CHANNEL_3_ENABLE

```
#define TX_CHANNEL_3_ENABLE (1U << 2U)
Definition at line 8 of file device_cfg.h.
```

4.6 common/frame_cfg.c File Reference

```
#include <common/app_cfg.h>
#include <stdint.h>
#include <stdlib.h>
#include <stddef.h>
#include <string.h>
#include <stdio.h>
#include <xdc/runtime/System.h>
#include <ti/common/sys_common.h>
#include <ti/drivers/uart/UART.h>
#include <ti/control/mmwavelink/mmwavelink.h>
#include <ti/utils/cli/cli.h>
```

Include dependency graph for frame_cfg.c:



Functions

- void **Cfg_AdvFrameCfgInitParams** (rlAdvFrameCfg_t *ptrAdvFrameCfg)
The function initializes the frame configuration with the default parameters.
- void **Cfg_FrameCfgInitParams** (rlFrameCfg_t *ptrFrameCfg)
The function initializes the frame configuration with the default parameters.
- void **Cfg_ProfileCfgInitParams** (uint8_t profileNum, rlProfileCfg_t *ptrProfileCfg)
The function initializes the profile configuration with the default parameters.
- void **Cfg_ChirpCfgInitParams** (uint8_t chirpNum, rlChirpCfg_t *ptrChirpCfg)
The function initializes the chirp configuration with the default parameters.
- void **Cfg_LowPowerModeInitParams** (rlLowPowerModeCfg_t *ptrLowPowerMode)

The function initializes the low power configuration with the default parameters.

- void **Cfg_ChannelCfgInitParams** (rlChanCfg_t *ptrChannelCfg)

The function initializes the channel configuration with the default parameters.

- void **Cfg_ADCOutCfgInitParams** (rlAdcOutCfg_t *ptrADCOutCfg)

The function initializes the ADCOut configuration with the default parameters.

4.6.1 Function Documentation

4.6.1.1 Cfg_ADCOutCfgInitParams()

```
void Cfg_ADCOutCfgInitParams (
    rlAdcOutCfg_t * ptrADCOutCfg )
```

The function initializes the ADCOut configuration with the default parameters.

Parameters

out	<i>ptrADCOutCfg</i>	Pointer to the ADCOutput configuration
-----	---------------------	--

Return values

N/A	
-----	--

Definition at line 308 of file frame_cfg.c.

4.6.1.2 Cfg_AdvFrameCfgInitParams()

```
void Cfg_AdvFrameCfgInitParams (
    rlAdvFrameCfg_t * ptrAdvFrameCfg )
```

The function initializes the frame configuration with the default parameters.

Parameters

out	<i>ptrAdvFrameCfg</i>	Pointer to the adavance frame configuration
-----	-----------------------	---

Return values

N/A	
-----	--

Definition at line 42 of file frame_cfg.c.

4.6.1.3 Cfg_ChannelCfgInitParams()

```
void Cfg_ChannelCfgInitParams (
    rlChanCfg_t * ptrChannelCfg )
```

The function initializes the channel configuration with the default parameters.

Parameters

out	<i>ptrChannelCfg</i>	Pointer to the channel configuration
-----	----------------------	--------------------------------------

Return values

N/A	
-----	--

Definition at line 285 of file frame_cfg.c.

4.6.1.4 Cfg_ChirpCfgInitParams()

```
void Cfg_ChirpCfgInitParams (
    uint8_t chirpNum,
    rlChirpCfg_t * ptrChirpCfg )
```

The function initializes the chirp configuration with the default parameters.

Parameters

out	chirpNum	Chirp Number to be configured
out	ptrChirpCfg	Pointer to the chirp configuration

Return values

N/A	
-----	--

Definition at line 204 of file frame_cfg.c.

4.6.1.5 Cfg_FrameCfgInitParams()

```
void Cfg_FrameCfgInitParams (
    rlFrameCfg_t * ptrFrameCfg )
```

The function initializes the frame configuration with the default parameters.

Parameters

out	ptrFrameCfg	Pointer to the frame configuration
-----	-------------	------------------------------------

Return values

N/A	
-----	--

Definition at line 138 of file frame_cfg.c.

4.6.1.6 Cfg_LowPowerModeInitParams()

```
void Cfg_LowPowerModeInitParams (
    rlLowPowerModeCfg_t * ptrLowPowerMode )
```

The function initializes the low power configuration with the default parameters.

Parameters

out	ptrLowPowerMode	Pointer to the low power mode configuration
-----	-----------------	---

Return values

N/A	
-----	--

Definition at line 264 of file frame_cfg.c.

4.6.1.7 Cfg_ProfileCfgInitParams()

```
void Cfg_ProfileCfgInitParams (
    uint8_t profileNum,
    rrlProfileCfg_t * ptrProfileCfg )
```

The function initializes the profile configuration with the default parameters.

Parameters

in	<i>profileNum</i>	Profile number to be initialized
out	<i>ptrProfileCfg</i>	Pointer to the profile configuration

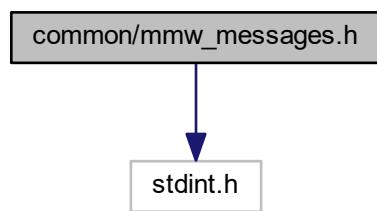
Return values

N/A	
-----	--

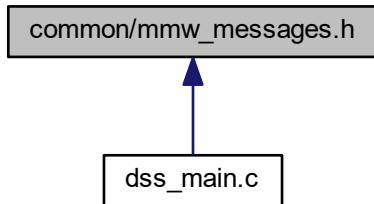
Definition at line 167 of file frame_cfg.c.

4.7 common/mmw_messages.h File Reference

```
#include <stdint.h>
Include dependency graph for mmw_messages.h:
```



This graph shows which files directly or indirectly include this file:



Data Structures

- struct **mmWave_OUT_MSG_header_t**
The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS.
- struct **mmWave_OUT_MSG_stats_dataObjDescr_t**
Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS.
- struct **mmWave_OUT_MSG_tl_t**
The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS.
- struct **mmWaveMSG_TLV_t**
The structure describes the TLV part of the message from DSS to MSS on data path detection information.
- struct **mmWave_detObjMsg_t**
The structure defines the message body for reporting detection information from data path to MSS.
- struct **mmWave_dssAssertInfoMsg_t**
The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information.
- union **mmWaveMSG_body_u**
The union defines the message body for various configuration messages. For passing configuration from MSS to DSS.
- struct **mmWaveMSG_t**
The structure defines the message structure used for communication between MSS and DSS.

Macros

- #define **MMW_OUTPUT_MSG_SEGMENT_LEN** 32
Output packet length is a multiple of this value, must be power of 2.
- #define **MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_DSS** 1
Software interrupt number used by DSS to signal exception from DSS to MSS.
- #define **MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_MSS** SOC_XWR16XX_MSS_DSS2MSS_SW1_INT
*Software interrupt ID on MSS corresponding to **MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_DSS** (p. ??).*
- #define **MMW_DSS2MSS_CHIRP_PROC_DEADLINE_MISS_EXCEPTION** 0
Exception ID definitions for DSS to MSS urgent exception signalling through software interrupt. DSS to MSS chirp processing deadline miss exception ID.
- #define **MMW_DSS2MSS_FRAME_PROC_DEADLINE_MISS_EXCEPTION** 1

Exception ID definitions for DSS to MSS urgent exception signalling through software interrupt. DSS to MSS frame processing deadline miss exception ID.

- `#define MMWAVE_MAX_FILE_NAME_SIZE 128`
- `#define MMW_SUBFRAME_NUM_FRAME_LEVEL_CONFIG (-1)`

For advanced frame config, below define means the configuration given is global at frame level and therefore it is broadcast to all sub-frames.

Typedefs

- **typedef enum mmWaveMSG_OUT_TYPE_e mmWaveMSG_OUT_TYPE**
Message types used in Millimeter Wave Demo for the communication between target and host, and also for Mailbox communication between MSS and DSS on the XWR16xx platform. Message types are used to indicate different type detection information sent out from the target.
- **typedef struct mmWave_OUT_MSG_header_t mmWave_OUT_MSG_header**
The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS.
- **typedef struct mmWave_OUT_MSG_stats_dataObjDescr_t mmWave_OUT_MSG_stats_dataObjDescr**
Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS.
- **typedef struct mmWave_OUT_MSG_tl_t mmWave_OUT_MSG_tl**
The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS.
- **typedef enum mbox_message_type_e mbox_message_type**
The enum is used to hold all the messages types used for Mailbox communication between MSS and DSS in mmw Demo.
- **typedef struct mmWaveMSG_TLV_t mmWaveMSG_TLV**
The structure describes the TLV part of the message from DSS to MSS on data path detection information.
- **typedef struct mmWave_detObjMsg_t mmWave_detInfoMsg**
The structure defines the message body for reporting detection information from data path to MSS.
- **typedef struct mmWave_dssAssertInfoMsg_t mmWave_dssAssertInfoMsg**
The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information.
- **typedef union mmWaveMSG_body_u mmWaveMSG_body**
The union defines the message body for various configuration messages. For passing configuration from MSS to DSS.
- **typedef struct mmWaveMSG_t mmWaveMSG**
The structure defines the message structure used for communication between MSS and DSS.

Enumerations

- **enum mmWaveMSG_OUT_TYPE_e {**
OUTPUT_MSG_DETECTED_POINTS = 1, OUTPUT_MSG_RANGE_PROFILE, OUTPUT_MSG_NOISE_PROFILE, OUTPUT_MSG_AZIMUT_STATIC_HEAT_MAP, OUTPUT_MSG_RANGE_DOPPLER_HEAT_MAP, OUTPUT_MSG_STATS, OUTPUT_MSG_MAX }
Message types used in Millimeter Wave Demo for the communication between target and host, and also for Mailbox communication between MSS and DSS on the XWR16xx platform. Message types are used to indicate different type detection information sent out from the target.
- **enum mbox_message_type_e {**
MBOX_MSS2DSS_GUIMON_CFG = 0xFEED0001, MBOX_MSS2DSS_CFAR_RANGE_CFG, MBOX_MSS2DSS_CFAR_DOPPLER_CFG, MBOX_MSS2DSS_PEAK_GROUPING_CFG, MBOX_MSS2DSS_MULTI_OBJ_BEAM_FORM, MBOX_MSS2DSS_CALIB_DC_RANGE_SIG, MBOX_MSS2DSS_DETOBJ_SHIPPED, MBOX_MSS2DSS_SET_DATALOGGER, MBOX_MSS2DSS_ADCBUFCFG, MBOX_MSS2DSS_EXTENDED_MAX_VELOCITY, MBOX_MSS2DSS_CLUTTER_REMOVAL, MBOX_MSS2DSS_COMP_RANGE_BIAS_AND_RX_CHAN_PHASE, MBOX_MSS2DSS_MEASURE_RANGE_BIAS_AND_RX_CHAN_PHASE, MBOX_DSS2MSS_CONFIG_DONE = 0xFEED0100, MBOX_DSS2MSS_DETOBJ_READY, MBOX_DSS2MSS_STOPDONE,

```
MBOX_DSS2MSS_ASSERT_INFO, MBOX_DSS2MSS_ISR_INFO_ADDRESS, MBOX_DSS2MSS_ME-  
ASUREMENT_INFO }
```

The enum is used to hold all the messages types used for Mailbox communication between MSS and DSS in mmw Demo.

4.7.1 Macro Definition Documentation

4.7.1.1 MMW_DSS2MSS_CHIRP_PROC_DEADLINE_MISS_EXCEPTION

```
#define MMW_DSS2MSS_CHIRP_PROC_DEADLINE_MISS_EXCEPTION 0
```

Exception ID definitions for DSS to MSS urgent exception signalling through software interrupt. DSS to MSS chirp processing deadline miss exception ID.

Definition at line 167 of file mmw_messages.h.

4.7.1.2 MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_DSS

```
#define MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_DSS 1
```

Software interrupt number used by DSS to signal exception from DSS to MSS.

Definition at line 156 of file mmw_messages.h.

4.7.1.3 MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_MSS

```
#define MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_MSS SOC_XWR16XX_MSS_DSS2MSS_SW1_INT
```

Software interrupt ID on MSS corresponding to **MMW_DSS2MSS_EXCEPTION_SIGNALLING_SW_INT_DSS** (p. ??).

Definition at line 159 of file mmw_messages.h.

4.7.1.4 MMW_DSS2MSS_FRAME_PROC_DEADLINE_MISS_EXCEPTION

```
#define MMW_DSS2MSS_FRAME_PROC_DEADLINE_MISS_EXCEPTION 1
```

Exception ID definitions for DSS to MSS urgent exception signalling through software interrupt. DSS to MSS frame processing deadline miss exception ID.

Definition at line 174 of file mmw_messages.h.

4.7.1.5 MMW_OUTPUT_MSG_SEGMENT_LEN

```
#define MMW_OUTPUT_MSG_SEGMENT_LEN 32
```

Output packet length is a multiple of this value, must be power of 2.

Definition at line 24 of file mmw_messages.h.

4.7.1.6 MMW_SUBFRAME_NUM_FRAME_LEVEL_CONFIG

```
#define MMW_SUBFRAME_NUM_FRAME_LEVEL_CONFIG (-1)
```

For advanced frame config, below define means the configuration given is global at frame level and therefore it is broadcast to all sub-frames.

Definition at line 228 of file mmw_messages.h.

4.7.1.7 MMWAVE_MAX_FILE_NAME_SIZE

```
#define MMWAVE_MAX_FILE_NAME_SIZE 128
```

Definition at line 199 of file mmw_messages.h.

4.7.2 Typedef Documentation

4.7.2.1 mbox_message_type

```
typedef enum mbox_message_type_e mbox_message_type
```

The enum is used to hold all the messages types used for Mailbox communication between MSS and DSS in mmw Demo.

4.7.2.2 mmWave_detInfoMsg

```
typedef struct mmWave_detObjMsg_t mmWave_detInfoMsg
```

The structure defines the message body for reporting detection information from data path to MSS.

4.7.2.3 mmWave_dssAssertInfoMsg

```
typedef struct mmWave_dssAssertInfoMsg_t mmWave_dssAssertInfoMsg
```

The structure defines the message body for the information on a DSS exception that should be forwarded to the MSS reporting the DSS assertion information.

4.7.2.4 mmWave_OUT_MSG_header

```
typedef struct mmWave_OUT_MSG_header_t mmWave_OUT_MSG_header
```

The structure defines the message header for reporting detection information from data path. Processed by both MSS and DSS.

4.7.2.5 mmWave_OUT_MSG_stats_dataObjDescr

```
typedef struct mmWave_OUT_MSG_stats_dataObjDescr_t mmWave_OUT_MSG_stats_dataObjDescr
```

Structure holds information about detected objects. This information is sent in front of the array of detected objects Sent by DSS.

4.7.2.6 mmWave_OUT_MSG_t1

```
typedef struct mmWave_OUT_MSG_t1_t mmWave_OUT_MSG_t1
```

The structure defines the message body for reporting detected objects from data path. Processed by both MSS and DSS.

4.7.2.7 mmWaveMSG

```
typedef struct mmWaveMSG_t mmWaveMSG
```

The structure defines the message structure used for communication between MSS and DSS.

4.7.2.8 mmWaveMSG_body

```
typedef union mmWaveMSG_body_u mmWaveMSG_body
```

The union defines the message body for various configuration messages. For passing configuration from MSS to DSS.

4.7.2.9 mmWaveMSG_OUT_TYPE

```
typedef enum mmWaveMSG_OUT_TYPE_e mmWaveMSG_OUT_TYPE
```

Message types used in Millimeter Wave Demo for the communication between target and host, and also for Mailbox communication between MSS and DSS on the XWR16xx platform. Message types are used to indicate different type detection information sent out from the target.

4.7.2.10 mmWaveMSG_TLV

```
typedef struct mmWaveMSG_TLV_t mmWaveMSG_TLV
```

The structure describes the TLV part of the message from DSS to MSS on data path detection information.

4.7.3 Enumeration Type Documentation

4.7.3.1 mbox_message_type_e

```
enum mbox_message_type_e
```

The enum is used to hold all the messages types used for Mailbox communication between MSS and DSS in mmw Demo.

Enumerator

MBOX_MSS2DSS_GUIMON_CFG	message types for MSS to DSS communication
MBOX_MSS2DSS_CFAR_RANGE_CFG	
MBOX_MSS2DSS_CFAR_DOPPLER_CFG	
MBOX_MSS2DSS_PEAK_GROUPING_CFG	
MBOX_MSS2DSS_MULTI_OBJ_BEAM_FORM	
MBOX_MSS2DSS_CALIB_DC_RANGE_SIG	
MBOX_MSS2DSS_DETOBJ_SHIPPED	
MBOX_MSS2DSS_SET_DATALOGGER	
MBOX_MSS2DSS_ADCBUFCFG	
MBOX_MSS2DSS_EXTENDED_MAX_VELOCITY	
MBOX_MSS2DSS_CLUTTER_REMOVAL	
MBOX_MSS2DSS_COMP_RANGE_BIAS_AND_RX_CH↔_AN_PHASE	
MBOX_MSS2DSS_MEASURE_RANGE_BIAS_AND_RX↔_CHAN_PHASE	
MBOX_DSS2MSS_CONFIGDONE	message types for DSS to MSS communication
MBOX_DSS2MSS_DETOBJ_READY	
MBOX_DSS2MSS_STOPDONE	
MBOX_DSS2MSS_ASSERT_INFO	
MBOX_DSS2MSS_ISR_INFO_ADDRESS	
MBOX_DSS2MSS_MEASUREMENT_INFO	

Definition at line 128 of file mmw_messages.h.

4.7.3.2 mmWaveMSG_OUT_TYPE_e

```
enum mmWaveMSG_OUT_TYPE_e
```

Message types used in Millimeter Wave Demo for the communication between target and host, and also for Mailbox communication between MSS and DSS on the XWR16xx platform. Message types are used to indicate different type detection information sent out from the target.

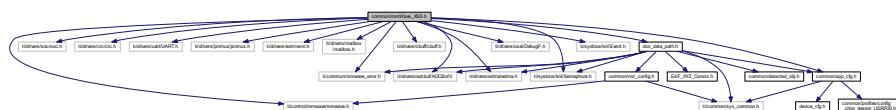
Enumerator

OUTPUT_MSG_DETECTED_POINTS	List of detected points.
OUTPUT_MSG_RANGE_PROFILE	Range profile.
OUTPUT_MSG_NOISE_PROFILE	Noise floor profile.
OUTPUT_MSG_AZIMUT_STATIC_HEAT_MAP	Samples to calculate static azimuth heatmap.
OUTPUT_MSG_RANGE_DOPPLER_HEAT_MAP	Range/Doppler detection matrix.
OUTPUT_MSG_STATS	Stats information.
OUTPUT_MSG_MAX	Max TLV output message.

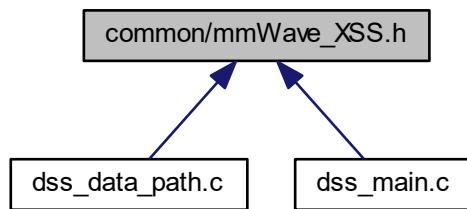
Definition at line 34 of file mmw_messages.h.

4.8 common/mmWave_XSS.h File Reference

```
#include <ti/common/mmwave_error.h>
#include <ti/drivers/soc/soc.h>
#include <ti/drivers/crc/crc.h>
#include <ti/drivers/uart/UART.h>
#include <ti/drivers/pinmux/pinmux.h>
#include <ti/drivers/esm/esm.h>
#include <ti/drivers/mailbox/mailbox.h>
#include <ti/control/mmwave/mmwave.h>
#include <ti/drivers/cbuff/cbuff.h>
#include <ti/drivers/adcbuf/ADCBuf.h>
#include <ti/drivers/edma/edma.h>
#include <ti/drivers/osal/DebugP.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Event.h>
#include <common/app_cfg.h>
#include "dss_data_path.h"
Include dependency graph for mmWave_XSS.h:
```



This graph shows which files directly or indirectly include this file:



Data Structures

- struct **MmwDemo_DSS_STATS_t**
- struct **MCB_t**

DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design.

Macros

- #define **DSS**
- #define **DSS_START_COMPLETED_EVT** Event_Id_07
sensor start CLI event SYS/BIOS events are a means of communication between Tasks and threads SYS/BIOS objects include semaphores, mailboxes, message queues, etc. Only tasks can WAIT for events; whereas tasks, Hwis, Swis, or SYS/BIOS objects can POST them. -ref http://software-dl.ti.com/dspsw/dspsw_public_sw/sdo_sb/targetcontent/sysbios/6_41_02_41/exports/bios_6_41_02_41/docs/cdoc/ti/sysbios/knl/Event.html
- #define **MMWDEMO_CLI_EVENTS**
- #define **MMWDEMO_BSS_FAULT_EVENTS**
- #define **MmwDemo_dssAssert**(expression)

Typedefs

- typedef enum **MmwDemo_DSS_STATE_e** **MmwDemo_DSS_STATE**
Millimeter Wave Demo state.
- typedef struct **MmwDemo_DSS_STATS_t** **MmwDemo_DSS_STATS**
- typedef struct **MCB_t** **MCB**
DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design.

Enumerations

- enum **MmwDemo_DSS_STATE_e** { **MmwDemo_DSS_STATE_INIT** = 0, **MmwDemo_DSS_STATE_STARTED**, **MmwDemo_DSS_STATE_STOPPED**, **MmwDemo_DSS_STATE_STOP_PENDING** }
Millimeter Wave Demo state.

Functions

- void **_MmwDemo_dssAssert** (int32_t expression, const char *file, int32_t line)
- void **Cfg_AdvFrameCfgInitParams** (rlAdvFrameCfg_t *ptrAdvFrameCfg)
The function initializes the frame configuration with the default parameters.
- void **Cfg_FrameCfgInitParams** (rlFrameCfg_t *ptrFrameCfg)
The function initializes the frame configuration with the default parameters.
- void **Cfg_ProfileCfgInitParams** (uint8_t profileNum, rlProfileCfg_t *ptrProfileCfg)
The function initializes the profile configuration with the default parameters.
- void **Cfg_ChirpCfgInitParams** (uint8_t chirpNum, rlChirpCfg_t *ptrChirpCfg)
The function initializes the chirp configuration with the default parameters.
- void **Cfg_LowPowerModeInitParams** (rlLowPowerModeCfg_t *ptrLowPowerMode)
The function initializes the low power configuration with the default parameters.
- void **Cfg_ChannelCfgInitParams** (rlChanCfg_t *ptrChannelCfg)
The function initializes the channel configuration with the default parameters.
- void **Cfg_ADCOutCfgInitParams** (rlAdcOutCfg_t *ptrADCOutCfg)
The function initializes the ADCOut configuration with the default parameters.

Variables

- **MCB gMCB**

Global Variable for tracking information required by the design.

4.8.1 Macro Definition Documentation

4.8.1.1 DSS

```
#define DSS
Definition at line 13 of file mmWave_XSS.h.
```

4.8.1.2 DSS_START_COMPLETED_EVT

```
#define DSS_START_COMPLETED_EVT Event_Id_07
sensor start CLI event SYS/BIOS events are a means of communication between Tasks and threads SYS/BIOS
objects include semaphores, mailboxes, message queues, etc. Only tasks can WAIT for events; whereas tasks,
HwIs, SwIs, or SYS/BIOS objects can POST them. -ref http://software-dl.ti.com/dspsw/dspss-
_public_sw/sdo_sb/targetcontent/sysbios/6_41_02_41/exports/bios_6_41_02-
41/docs/cdoc/ti/sysbios/knl/Event.html
Definition at line 80 of file mmWave_XSS.h.
```

4.8.1.3 MMWDEMO_BSS_FAULT_EVENTS

```
#define MMWDEMO_BSS_FAULT_EVENTS
Value:
(MMWDEMO_BSS_CPUFAULT_EVT | \
MMWDEMO_BSS_ESMFAULT_EVT )
```

Definition at line 90 of file mmWave_XSS.h.

4.8.1.4 MMWDEMO_CLI_EVENTS

```
#define MMWDEMO_CLI_EVENTS
Value:
(MMWDEMO_CLI_SENSORSTART_EVT | \
MMWDEMO_CLI_SENSORSTOP_EVT | \
MMWDEMO_CLI_FRAMESTART_EVT)
```

Definition at line 84 of file mmWave_XSS.h.

4.8.1.5 MmwDemo_dssAssert

```
#define MmwDemo_dssAssert(
    expression )
Value:
{ \
    _MmwDemo_dssAssert(expression,
        __FILE__, __LINE__);
    DebugP_assert(expression);
}
```

Definition at line 259 of file mmWave_XSS.h.

4.8.2 Typedef Documentation

4.8.2.1 MCB

```
typedef struct MCB_t MCB
```

DSP-Subsystem (DSS) Master control block (MCB) The structure is used to hold handling information, flags and stats relative to the radar design.

4.8.2.2 MmwDemo_DSS_STATE

```
typedef enum MmwDemo_DSS_STATE_e MmwDemo_DSS_STATE
```

Millimeter Wave Demo state.

The enumeration is used to hold the data path states for the Millimeter Wave demo

4.8.2.3 MmwDemo_DSS_STATS

```
typedef struct MmwDemo_DSS_STATS_t MmwDemo_DSS_STATS
```

4.8.3 Enumeration Type Documentation

4.8.3.1 MmwDemo_DSS_STATE_e

```
enum MmwDemo_DSS_STATE_e
```

Millimeter Wave Demo state.

The enumeration is used to hold the data path states for the Millimeter Wave demo

Enumerator

MmwDemo_DSS_STATE_INIT	State after data path is initialized.
MmwDemo_DSS_STATE_STARTED	State after data path is started.
MmwDemo_DSS_STATE_STOPPED	State after data path is stopped.
MmwDemo_DSS_STATE_STOP_PENDING	State after STOP request was received by DSP but complete stop is on-going.

Definition at line 105 of file mmWave_XSS.h.

4.8.4 Function Documentation

4.8.4.1 _MmwDemo_dssAssert()

```
void _MmwDemo_dssAssert (
    int32_t expression,
    const char * file,
    int32_t line )
```

Description

Sends DSS assert information to MSS

Return values

Not	Applicable.
-----	-------------

Definition at line 302 of file dss_main.c.

4.8.4.2 Cfg_ADCOutCfgInitParams()

```
void Cfg_ADCOutCfgInitParams (
    rlAdcOutCfg_t * ptrADCOutCfg )
```

The function initializes the ADCOut configuration with the default parameters.

Parameters

out	<i>ptrADCOutCfg</i>	Pointer to the ADCOutput configuration
-----	---------------------	--

Return values

N/A	
-----	--

Definition at line 308 of file frame_cfg.c.

4.8.4.3 Cfg_AdvFrameCfgInitParams()

```
void Cfg_AdvFrameCfgInitParams (
    rlAdvFrameCfg_t * ptrAdvFrameCfg )
```

The function initializes the frame configuration with the default parameters.

Parameters

out	<i>ptrAdvFrameCfg</i>	Pointer to the adavance frame configuration
-----	-----------------------	---

Return values

N/A	
-----	--

Definition at line 42 of file frame_cfg.c.

4.8.4.4 Cfg_ChannelCfgInitParams()

```
void Cfg_ChannelCfgInitParams (
    rlChanCfg_t * ptrChannelCfg )
```

The function initializes the channel configuration with the default parameters.

Parameters

out	<i>ptrChannelCfg</i>	Pointer to the channel configuration
-----	----------------------	--------------------------------------

Return values

N/A	
-----	--

Definition at line 285 of file frame_cfg.c.

4.8.4.5 Cfg_ChirpCfgInitParams()

```
void Cfg_ChirpCfgInitParams (
    uint8_t chirpNum,
```

```
rlChirpCfg_t * ptrChirpCfg )
```

The function initializes the chirp configuration with the default parameters.

Parameters

out	<i>chirpNum</i>	Chirp Number to be configured
out	<i>ptrChirpCfg</i>	Pointer to the chirp configuration

Return values

N/A	
-----	--

Definition at line 204 of file frame_cfg.c.

4.8.4.6 Cfg_FrameCfgInitParams()

```
void Cfg_FrameCfgInitParams (
    rlFrameCfg_t * ptrFrameCfg )
```

The function initializes the frame configuration with the default parameters.

Parameters

out	<i>ptrFrameCfg</i>	Pointer to the frame configuration
-----	--------------------	------------------------------------

Return values

N/A	
-----	--

Definition at line 138 of file frame_cfg.c.

4.8.4.7 Cfg_LowPowerModelInitParams()

```
void Cfg_LowPowerModeInitParams (
    rlLowPowerModeCfg_t * ptrLowPowerMode )
```

The function initializes the low power configuration with the default parameters.

Parameters

out	<i>ptrLowPowerMode</i>	Pointer to the low power mode configuration
-----	------------------------	---

Return values

N/A	
-----	--

Definition at line 264 of file frame_cfg.c.

4.8.4.8 Cfg_ProfileCfgInitParams()

```
void Cfg_ProfileCfgInitParams (
    uint8_t profileNum,
    rlProfileCfg_t * ptrProfileCfg )
```

The function initializes the profile configuration with the default parameters.

Parameters

in	<i>profileNum</i>	Profile number to be initialized
out	<i>ptrProfileCfg</i>	Pointer to the profile configuration

Return values

N/A	
-----	--

Definition at line 167 of file frame_cfg.c.

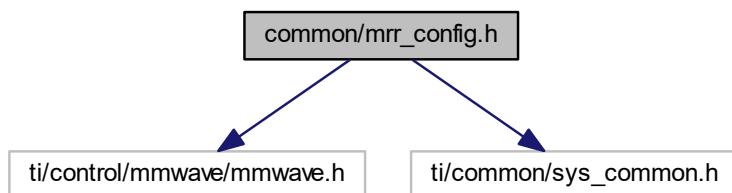
4.8.5 Variable Documentation

4.8.5.1 gMCB

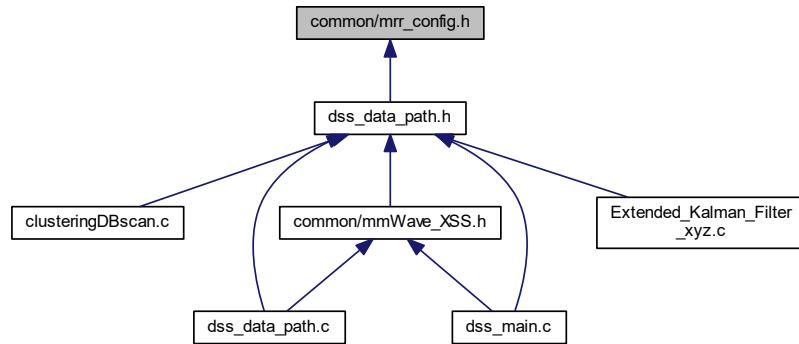
MCB gMCB
 Global Variable for tracking information required by the design.
 Definition at line 97 of file dss_main.c.

4.9 common/mrr_config.h File Reference

```
#include <ti/control/mmwave/mmwave.h>
#include <ti/common/sys_common.h>
Include dependency graph for mrr_config.h:
```



This graph shows which files directly or indirectly include this file:



Data Structures

- struct **MmwDemo_CfarCfg_t**
Millimeter Wave Demo CFAR Configuration.
- struct **MmwDemo_PeakGroupingCfg_t**
Millimeter Wave Demo Peak grouping Configuration.
- struct **MmwDemo_MultiObjBeamFormingCfg_t**
Millimeter Wave Demo multi object beam formaing Configuration.
- struct **MmwDemo_ExtendedMaxVelocityCfg_t**
Millimeter Wave Demo Velocity Disambiguation.
- struct **MmwDemo_NearFieldCorrectionCfg_t**
Millimeter Wave Demo near field correction.
- struct **MmwDemo_ClutterRemovalCfg_t**
Clutter removal configuration.
- struct **MmwDemo_CalibDcRangeSigCfg_t**
Millimeter Wave Demo DC range signature compensation.
- struct **MmwDemo_ADCBufCfg_t**
ADCBUF configuration.
- struct **MmwDemo_LvdsStreamCfg_t**
LVDS streaming configuration.
- struct **MmwDemo_AnaMonitorCfg_t**
Millimeter Wave Demo analog monitor configuration.
- struct **MmwDemo_GuiMonSel_t**
Millimeter Wave Demo Gui Monitor Selection.
- struct **MmwDemo_CliCfg_t**
Millimeter Wave Demo CLI related configuration.
- struct **MmwDemo_compRxChannelBiasCfg_t**
Range Bias and rx channel gain/phase compensation configuration.
- struct **MmwDemo_measureRxChannelBiasCfg_t**
Range Bias and rx channel gain/phase measurement configuration.
- struct **MmwDemo_CliCommonCfg_t**
Millimeter Wave Demo CLI related configuration common across all subframes.
- struct **MmwDemo_Cfg_t**
Millimeter Wave Demo configuration.

Macros

- #define **DC_RANGE_SIGNATURE_COMP_MAX_BIN_SIZE** 32
Maximum number of 1D FFT bins in DC range antenna signature compensation.

Typedefs

- typedef struct **MmwDemo_CfarCfg_t** **MmwDemo_CfarCfg**
Millimeter Wave Demo CFAR Configuration.
- typedef struct **MmwDemo_PeakGroupingCfg_t** **MmwDemo_PeakGroupingCfg**
Millimeter Wave Demo Peak grouping Configuration.
- typedef struct **MmwDemo_MultiObjBeamFormingCfg_t** **MmwDemo_MultiObjBeamFormingCfg**
Millimeter Wave Demo multi object beam formaing Configuration.
- typedef struct **MmwDemo_ExtendedMaxVelocityCfg_t** **MmwDemo_ExtendedMaxVelocityCfg**
Millimeter Wave Demo Velocity Disambiguation.
- typedef struct **MmwDemo_NearFieldCorrectionCfg_t** **MmwDemo_NearFieldCorrectionCfg**
Millimeter Wave Demo near field correction.
- typedef struct **MmwDemo_ClutterRemovalCfg_t** **MmwDemo_ClutterRemovalCfg**
Clutter removal configuration.
- typedef struct **MmwDemo_CalibDcRangeSigCfg_t** **MmwDemo_CalibDcRangeSigCfg**
Millimeter Wave Demo DC range signature compensation.
- typedef struct **MmwDemo_ADCBufCfg_t** **MmwDemo_ADCBufCfg**
ADCBUF configuration.
- typedef struct **MmwDemo_LvdsStreamCfg_t** **MmwDemo_LvdsStreamCfg**
LVDS streaming configuration.
- typedef struct **MmwDemo_AnaMonitorCfg_t** **MmwDemo_AnaMonitorCfg**
Millimeter Wave Demo analog monitor configuration.
- typedef struct **MmwDemo_GuiMonSel_t** **MmwDemo_GuiMonSel**
Millimeter Wave Demo Gui Monitor Selection.
- typedef struct **MmwDemo_CliCfg_t** **MmwDemo_CliCfg**
Millimeter Wave Demo CLI related configuration.
- typedef struct **MmwDemo_compRxChannelBiasCfg_t** **MmwDemo_compRxChannelBiasCfg**
Range Bias and rx channel gain/phase compensation configuration.
- typedef struct **MmwDemo_measureRxChannelBiasCfg_t** **MmwDemo_measureRxChannelBiasCfg**
Range Bias and rx channel gain/phase measurement configuration.
- typedef struct **MmwDemo_CliCommonCfg_t** **MmwDemo_CliCommonCfg**
Millimeter Wave Demo CLI related configuration common across all subframes.
- typedef struct **MmwDemo_Cfg_t** **MmwDemo_Cfg**
Millimeter Wave Demo configuration.

4.9.1 Macro Definition Documentation

4.9.1.1 DC_RANGE_SIGNATURE_COMP_MAX_BIN_SIZE

```
#define DC_RANGE_SIGNATURE_COMP_MAX_BIN_SIZE 32
Maximum number of 1D FFT bins in DC range antenna signature compensation.
Definition at line 52 of file mrr_config.h.
```

4.9.2 Typedef Documentation

4.9.2.1 MmwDemo_ADCBufCfg

```
typedef struct MmwDemo_ADCBufCfg_t MmwDemo_ADCBufCfg
```

ADCBUF configuration.

The structure is used to hold all the relevant configuration which is used to configure ADCBUF.

4.9.2.2 MmwDemo_AnaMonitorCfg

```
typedef struct MmwDemo_AnaMonitorCfg_t MmwDemo_AnaMonitorCfg
```

Millimeter Wave Demo analog monitor configuration.

The structure contains the flags that select analog monitors to be enabled.

4.9.2.3 MmwDemo_CalibDcRangeSigCfg

```
typedef struct MmwDemo_CalibDcRangeSigCfg_t MmwDemo_CalibDcRangeSigCfg
```

Millimeter Wave Demo DC range signature compensation.

The structure contains the DC range antenna signature removal configuration used in data path

4.9.2.4 MmwDemo_CfarCfg

```
typedef struct MmwDemo_CfarCfg_t MmwDemo_CfarCfg
```

Millimeter Wave Demo CFAR Configuration.

The structure contains the cfar configuration used in data path

4.9.2.5 MmwDemo_Cfg

```
typedef struct MmwDemo_Cfg_t MmwDemo_Cfg
```

Millimeter Wave Demo configuration.

The structure is used to hold all the relevant configuration which is used to execute the Millimeter Wave Demo.

4.9.2.6 MmwDemo_CliCfg_t

```
typedef struct MmwDemo_CliCfg_t_ MmwDemo_CliCfg_t
```

Millimeter Wave Demo CLI related configuration.

4.9.2.7 MmwDemo_CliCommonCfg_t

```
typedef struct MmwDemo_CliCommonCfg_t_ MmwDemo_CliCommonCfg_t
```

Millimeter Wave Demo CLI related configuration common across all subframes.

4.9.2.8 MmwDemo_ClutterRemovalCfg

```
typedef struct MmwDemo_ClutterRemovalCfg_t MmwDemo_ClutterRemovalCfg
```

Clutter removal configuration.

The structure contains clutter removal configuration

4.9.2.9 MmwDemo_compRxChannelBiasCfg_t

```
typedef struct MmwDemo_compRxChannelBiasCfg_t_ MmwDemo_compRxChannelBiasCfg_t
```

Range Bias and rx channel gain/phase compensation configuration.

4.9.2.10 MmwDemo_ExtendedMaxVelocityCfg

```
typedef struct MmwDemo_ExtendedMaxVelocityCfg_t MmwDemo_ExtendedMaxVelocityCfg
```

Millimeter Wave Demo Velocity Disambiguation.

The structure contains Velocity Disambiguation configuration

4.9.2.11 MmwDemo_GuiMonSel

```
typedef struct MmwDemo_GuiMonSel_t MmwDemo_GuiMonSel
Millimeter Wave Demo Gui Monitor Selection.
```

The structure contains the flags which select what information is placed to the output packet, and sent out to GUI. If the flag is set to 1, information is sent out. If the flag is set to 0, information is not sent out.

4.9.2.12 MmwDemo_LvdsStreamCfg

```
typedef struct MmwDemo_LvdsStreamCfg_t MmwDemo_LvdsStreamCfg
LVDS streaming configuration.
```

The structure is used to hold all the relevant configuration for the LVDS streaming.

4.9.2.13 MmwDemo_measureRxChannelBiasCfg_t

```
typedef struct MmwDemo_measureRxChannelBiasCfg_t MmwDemo_measureRxChannelBiasCfg_t
Range Bias and rx channel gain/phase measurement configuration.
```

4.9.2.14 MmwDemo_MultiObjBeamFormingCfg

```
typedef struct MmwDemo_MultiObjBeamFormingCfg_t MmwDemo_MultiObjBeamFormingCfg
Millimeter Wave Demo multi object beam formaing Configuration.
```

The structure contains the Peak grouping configuration used in data path

4.9.2.15 MmwDemo_NearFieldCorrectionCfg

```
typedef struct MmwDemo_NearFieldCorrectionCfg_t MmwDemo_NearFieldCorrectionCfg
Millimeter Wave Demo near field correction.
```

The structure contains Near Field Correction configuration

4.9.2.16 MmwDemo_PeakGroupingCfg

```
typedef struct MmwDemo_PeakGroupingCfg_t MmwDemo_PeakGroupingCfg
Millimeter Wave Demo Peak grouping Configuration.
```

The structure contains the Peak grouping configuration used in data path

4.10 common/profiles/config_chirp_design_MRR120.h File Reference

Macros

- #define **PROFILE_MRR_PROFILE_ID** (0U)
MRR profile ID.
- #define **PROFILE_MRR_HPFCORNER_FREQ1_VAL** RL_RX_HPF1_175_KHz
HPF 1 corner frequency.
- #define **PROFILE_MRR_HPFCORNER_FREQ2_VAL** RL_RX_HPF2_350_KHz
HPF 2 corner frequency.
- #define **PROFILE_MRR_RX_GAIN_VAL** (44U)
Rx gain is kept at the maximum .
- #define **PROFILE_MRR_DIGOUT_SAMPLERATE_VAL** (4652U)
ADC Output rate is 5Mhz.
- #define **PROFILE_MRR_ADC_SAMPLE_VAL** (256U)
- #define **PROFILE_MRR_IDLE_TIME_VAL** (500U)
- #define **PROFILE_MRR_RAMP_END_TIME_VAL** (6000U)
- #define **PROFILE_MRR_START_FREQ_GHZ** (76.01f)
- #define **PROFILE_MRR_START_FREQ_VAL** (CONV_FREQ_GHZ_TO_CODEWORD(**PROFILE_MRR_START_FREQ_GHZ**))

- #define PROFILE_MRR_TXOUT_POWER_BACKOFF (0U)
- #define PROFILE_MRR_TXPHASESHIFTER_VAL (0U)
- #define PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US (4.0f)
- #define PROFILE_MRR_FREQ_SLOPE_VAL (CONV_SLOPE_MHZ_PER_US_TO_CODEWORD(PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US))
- #define PROFILE_MRR_TX_START_TIME_VAL (100U)
- #define PROFILE_MRR_ADC_START_TIME_VAL (480U)
- #define PROFILE_MRR_LAMBDA_MILLIMETER (MMWDemo_Speed_of_Light_in_Meters_Per_usec/ PROFILE_MRR_START_FREQ_GHZ)
- #define CHIRP_MRR_0_PROFILE_ID (0U)

Define 128 chirps, the first 64 will have an idle time of 3us, and the remaining 64 will have an idle time of 14.8us (11.8us extra 'idle time')
- #define CHIRP_MRR_0_START_INDEX (0U)
- #define CHIRP_MRR_0_END_INDEX (127U)
- #define CHIRP_MRR_0_START_FREQ_VAL (0U)
- #define CHIRP_MRR_0_FREQ_SLOPE_VAL (0U)
- #define CHIRP_MRR_0_IDLE_TIME_VAL (0U)
- #define CHIRP_MRR_0_ADC_START_TIME_VAL (0U)
- #define CHIRP_MRR_0_TX_CHANNEL (TX_CHANNEL_1_ENABLE| TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE)
- #define CHIRP_MRR_1_PROFILE_ID (0U)
- #define CHIRP_MRR_1_START_INDEX (128U)
- #define CHIRP_MRR_1_END_INDEX (255U)
- #define CHIRP_MRR_1_START_FREQ_VAL (0U)
- #define CHIRP_MRR_1_FREQ_SLOPE_VAL (0U)
- #define CHIRP_MRR_1_IDLE_TIME_VAL (1300U)
- #define CHIRP_MRR_1_ADC_START_TIME_VAL (0U)
- #define CHIRP_MRR_1_TX_CHANNEL (TX_CHANNEL_1_ENABLE| TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE)
- #define SUBFRAME_MRR_CHIRP_START_IDX (0U)

SUBFRAME configuration.
- #define SUBFRAME_MRR_CHIRP_END_IDX (255U)
- #define SUBFRAME_MRR_LOOP_COUNT (1U)
- #define SUBFRAME_MRR_PERIODICITY_VAL (6000000U)
- #define SUBFRAME_MRR_TRIGGER_DELAY_VAL (0U)
- #define SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES (PROFILE_MRR_ADC_SAMPLE_VAL * 2)
- #define SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES (PROFILE_MRR_ADC_SAMPLE_VAL)
- #define SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS ((CHIRP_MRR_0_END_INDEX - CHIRP_MRR_0_START_INDEX + 1)* SUBFRAME_MRR_LOOP_COUNT)
- #define SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS ((CHIRP_MRR_1_END_INDEX - CHIRP_MRR_1_START_INDEX + 1)* SUBFRAME_MRR_LOOP_COUNT)
- #define SUBFRAME_MRR_NUM_TX (1U)
- #define SUBFRAME_MRR_NUM_VIRT_ANT (SUBFRAME_MRR_NUM_TX* NUM_RX_CHANNELS)
- #define SUBFRAME_MRR_NUM_ANGLE_BINS (32U)
- #define SUBFRAME_MRR_NUM_CHIRPS_TOTAL ((SUBFRAME_MRR_CHIRP_END_IDX - SUBFRAME_MRR_CHIRP_START_IDX + 1) * SUBFRAME_MRR_LOOP_COUNT)
- #define PROFILE_MRR_RANGE_RESOLUTION_METERS ((MMWDemo_Speed_of_Light_in_Meters_Per_usec * PROFILE_MRR_DIGOUT_SAMPLERATE_VAL)/ (2000.0f * PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES))
- #define SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US ((CHIRP_MRR_0_IDLE_TIME_VAL + PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
- #define SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US)/ SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS)*(PROFILE_MRR_LAMBDA_MILLIMETER/2))

- `#define SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S (SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S* SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS/2)`
- `#define INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S)`
- `#define SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US ((CHIRP_MRR_1_IDLE_TIME_VAL + PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)`
- `#define SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US)/ SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS)*(PROFILE_MRR_LAMBDA_MILLIMETER/2))`
- `#define SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S ((SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S* SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS/2)`
- `#define INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S)`
- `#define SUBFRAME_MRR_MIN_SNR_dB (14.0f)`
- `#define SUBFRAME_MRR_NUM_CHIRPTYPES (2U)`

4.10.1 Macro Definition Documentation

4.10.1.1 CHIRP_MRR_0_ADC_START_TIME_VAL

```
#define CHIRP_MRR_0_ADC_START_TIME_VAL (0U)
```

Definition at line 75 of file config_chirp_design_MRR120.h.

4.10.1.2 CHIRP_MRR_0_END_INDEX

```
#define CHIRP_MRR_0_END_INDEX (127U)
```

Definition at line 71 of file config_chirp_design_MRR120.h.

4.10.1.3 CHIRP_MRR_0_FREQ_SLOPE_VAL

```
#define CHIRP_MRR_0_FREQ_SLOPE_VAL (0U)
```

Definition at line 73 of file config_chirp_design_MRR120.h.

4.10.1.4 CHIRP_MRR_0_IDLE_TIME_VAL

```
#define CHIRP_MRR_0_IDLE_TIME_VAL (0U)
```

Definition at line 74 of file config_chirp_design_MRR120.h.

4.10.1.5 CHIRP_MRR_0_PROFILE_ID

```
#define CHIRP_MRR_0_PROFILE_ID (0U)
```

Define 128 chirps, the first 64 will have an idle time of 3us, and the remaining 64 will have an idle time of 14.8us (11.8us extra 'idle time')

Definition at line 69 of file config_chirp_design_MRR120.h.

4.10.1.6 CHIRP_MRR_0_START_FREQ_VAL

```
#define CHIRP_MRR_0_START_FREQ_VAL (0U)
```

Definition at line 72 of file config_chirp_design_MRR120.h.

4.10.1.7 CHIRP_MRR_0_START_INDEX

```
#define CHIRP_MRR_0_START_INDEX (0U)
Definition at line 70 of file config_chirp_design_MRR120.h.
```

4.10.1.8 CHIRP_MRR_0_TX_CHANNEL

```
#define CHIRP_MRR_0_TX_CHANNEL ( TX_CHANNEL_1_ENABLE| TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE )
Definition at line 76 of file config_chirp_design_MRR120.h.
```

4.10.1.9 CHIRP_MRR_1_ADC_START_TIME_VAL

```
#define CHIRP_MRR_1_ADC_START_TIME_VAL (0U)
Definition at line 85 of file config_chirp_design_MRR120.h.
```

4.10.1.10 CHIRP_MRR_1_END_INDEX

```
#define CHIRP_MRR_1_END_INDEX (255U)
Definition at line 81 of file config_chirp_design_MRR120.h.
```

4.10.1.11 CHIRP_MRR_1_FREQ_SLOPE_VAL

```
#define CHIRP_MRR_1_FREQ_SLOPE_VAL (0U)
Definition at line 83 of file config_chirp_design_MRR120.h.
```

4.10.1.12 CHIRP_MRR_1_IDLE_TIME_VAL

```
#define CHIRP_MRR_1_IDLE_TIME_VAL (1300U)
Definition at line 84 of file config_chirp_design_MRR120.h.
```

4.10.1.13 CHIRP_MRR_1_PROFILE_ID

```
#define CHIRP_MRR_1_PROFILE_ID (0U)
Definition at line 79 of file config_chirp_design_MRR120.h.
```

4.10.1.14 CHIRP_MRR_1_START_FREQ_VAL

```
#define CHIRP_MRR_1_START_FREQ_VAL (0U)
Definition at line 82 of file config_chirp_design_MRR120.h.
```

4.10.1.15 CHIRP_MRR_1_START_INDEX

```
#define CHIRP_MRR_1_START_INDEX (128U)
Definition at line 80 of file config_chirp_design_MRR120.h.
```

4.10.1.16 CHIRP_MRR_1_TX_CHANNEL

```
#define CHIRP_MRR_1_TX_CHANNEL ( TX_CHANNEL_1_ENABLE| TX_CHANNEL_2_ENABLE | TX_CHANNEL_3_ENABLE )
Definition at line 86 of file config_chirp_design_MRR120.h.
```

4.10.1.17 INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S

```
#define INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S)
```

Definition at line 109 of file config_chirp_design_MRR120.h.

4.10.1.18 INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S

```
#define INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S)
```

Definition at line 114 of file config_chirp_design_MRR120.h.

4.10.1.19 PROFILE_MRR_ADC_SAMPLE_VAL

```
#define PROFILE_MRR_ADC_SAMPLE_VAL (256U)
```

Definition at line 52 of file config_chirp_design_MRR120.h.

4.10.1.20 PROFILE_MRR_ADC_START_TIME_VAL

```
#define PROFILE_MRR_ADC_START_TIME_VAL (480U)
```

Definition at line 62 of file config_chirp_design_MRR120.h.

4.10.1.21 PROFILE_MRR_DIGOUT_SAMPLERATE_VAL

```
#define PROFILE_MRR_DIGOUT_SAMPLERATE_VAL (4652U)
```

ADC Output rate is 5Mhz.

Definition at line 51 of file config_chirp_design_MRR120.h.

4.10.1.22 PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US

```
#define PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US (4.0f)
```

Definition at line 59 of file config_chirp_design_MRR120.h.

4.10.1.23 PROFILE_MRR_FREQ_SLOPE_VAL

```
#define PROFILE_MRR_FREQ_SLOPE_VAL ( CONV_SLOPE_MHZ_PER_US_TO_CODEWORD( PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US ) )
```

Definition at line 60 of file config_chirp_design_MRR120.h.

4.10.1.24 PROFILE_MRR_HPFCORNER_FREQ1_VAL

```
#define PROFILE_MRR_HPFCORNER_FREQ1_VAL RL_RX_HPF1_175_KHz
```

HPF 1 corner frequency.

Definition at line 45 of file config_chirp_design_MRR120.h.

4.10.1.25 PROFILE_MRR_HPFCORNER_FREQ2_VAL

```
#define PROFILE_MRR_HPFCORNER_FREQ2_VAL RL_RX_HPF2_350_KHz
```

HPF 2 corner frequency.

Definition at line 47 of file config_chirp_design_MRR120.h.

4.10.1.26 PROFILE_MRR_IDLE_TIME_VAL

```
#define PROFILE_MRR_IDLE_TIME_VAL (500U)
Definition at line 53 of file config_chirp_design_MRR120.h.
```

4.10.1.27 PROFILE_MRR_LAMBDA_MILLIMETER

```
#define PROFILE_MRR_LAMBDA_MILLIMETER (MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC/ PROFILE_MR<-
R_START_FREQ_GHZ)
Definition at line 64 of file config_chirp_design_MRR120.h.
```

4.10.1.28 PROFILE_MRR_PROFILE_ID

```
#define PROFILE_MRR_PROFILE_ID (0U)
MRR profile ID.
Definition at line 43 of file config_chirp_design_MRR120.h.
```

4.10.1.29 PROFILE_MRR_RAMP_END_TIME_VAL

```
#define PROFILE_MRR_RAMP_END_TIME_VAL (6000U)
Definition at line 54 of file config_chirp_design_MRR120.h.
```

4.10.1.30 PROFILE_MRR_RANGE_RESOLUTION_METERS

```
#define PROFILE_MRR_RANGE_RESOLUTION_METERS ((MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC * PR<-
OFILE_MRR_DIGOUT_SAMPLERATE_VAL) / (2000.0f * PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_M<-
RR_NUM_CMPLX_ADC_SAMPLES) )
Definition at line 104 of file config_chirp_design_MRR120.h.
```

4.10.1.31 PROFILE_MRR_RX_GAIN_VAL

```
#define PROFILE_MRR_RX_GAIN_VAL (44U)
Rx gain is kept at the maximum .
Definition at line 49 of file config_chirp_design_MRR120.h.
```

4.10.1.32 PROFILE_MRR_START_FREQ_GHZ

```
#define PROFILE_MRR_START_FREQ_GHZ (76.01f)
Definition at line 55 of file config_chirp_design_MRR120.h.
```

4.10.1.33 PROFILE_MRR_START_FREQ_VAL

```
#define PROFILE_MRR_START_FREQ_VAL ( CONV_FREQ_GHZ_TO_CODEWORD( PROFILE_MRR_START_FREQ_GHZ ) )
Definition at line 56 of file config_chirp_design_MRR120.h.
```

4.10.1.34 PROFILE_MRR_TX_START_TIME_VAL

```
#define PROFILE_MRR_TX_START_TIME_VAL (100U)
Definition at line 61 of file config_chirp_design_MRR120.h.
```

4.10.1.35 PROFILE_MRR_TXOUT_POWER_BACKOFF

```
#define PROFILE_MRR_TXOUT_POWER_BACKOFF (0U)
Definition at line 57 of file config_chirp_design_MRR120.h.
```

4.10.1.36 PROFILE_MRR_TXPHASESHIFTER_VAL

```
#define PROFILE_MRR_TXPHASESHIFTER_VAL (0U)
Definition at line 58 of file config_chirp_design_MRR120.h.
```

4.10.1.37 SUBFRAME_MRR_CHIRP_END_IDX

```
#define SUBFRAME_MRR_CHIRP_END_IDX (255U)
Definition at line 90 of file config_chirp_design_MRR120.h.
```

4.10.1.38 SUBFRAME_MRR_CHIRP_START_IDX

```
#define SUBFRAME_MRR_CHIRP_START_IDX (0U)
SUBFRAME configuration.
Definition at line 89 of file config_chirp_design_MRR120.h.
```

4.10.1.39 SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US

```
#define SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US (((CHIRP_MRR_0_IDLE_TIME_VAL +
PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
Definition at line 106 of file config_chirp_design_MRR120.h.
```

4.10.1.40 SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S (SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S *
SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS/2)
Definition at line 108 of file config_chirp_design_MRR120.h.
```

4.10.1.41 SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS

```
#define SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS ((CHIRP_MRR_0_END_INDEX - CHIRP_MRR_0_START_INDEX +
1)*SUBFRAME_MRR_LOOP_COUNT)
Definition at line 96 of file config_chirp_design_MRR120.h.
```

4.10.1.42 SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (((1000.0f / SUBFRAME_MRR_CHIRPTYPE_0_C_HIRP_REPETITION_PERIOD_US) /
SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS)*(PROFILE_MRR_LAMBDA_MILLIMETER/2))
Definition at line 107 of file config_chirp_design_MRR120.h.
```

4.10.1.43 SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US

```
#define SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US (((CHIRP_MRR_1_IDLE_TIME_VAL +
PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
Definition at line 111 of file config_chirp_design_MRR120.h.
```

4.10.1.44 SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S (( SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S* SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS/2)
```

Definition at line 113 of file config_chirp_design_MRR120.h.

4.10.1.45 SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS

```
#define SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS (( CHIRP_MRR_1_END_INDEX - CHIRP_MRR_1_START_INDEX + 1)* SUBFRAME_MRR_LOOP_COUNT)
```

Definition at line 97 of file config_chirp_design_MRR120.h.

4.10.1.46 SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_1_C_HIRP_REPETITION_PERIOD_US) / SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS)*( PROFILE_MRR_LAMBDA_MILLIMETER/2))
```

Definition at line 112 of file config_chirp_design_MRR120.h.

4.10.1.47 SUBFRAME_MRR_LOOP_COUNT

```
#define SUBFRAME_MRR_LOOP_COUNT (1U)
```

Definition at line 91 of file config_chirp_design_MRR120.h.

4.10.1.48 SUBFRAME_MRR_MIN_SNR_dB

```
#define SUBFRAME_MRR_MIN_SNR_dB (14.0f)
```

Definition at line 116 of file config_chirp_design_MRR120.h.

4.10.1.49 SUBFRAME_MRR_NUM_ANGLE_BINS

```
#define SUBFRAME_MRR_NUM_ANGLE_BINS (32U)
```

Definition at line 101 of file config_chirp_design_MRR120.h.

4.10.1.50 SUBFRAME_MRR_NUM_CHIRPS_TOTAL

```
#define SUBFRAME_MRR_NUM_CHIRPS_TOTAL (( SUBFRAME_MRR_CHIRP_END_IDX - SUBFRAME_MRR_CHIRP_START_IDX + 1) * SUBFRAME_MRR_LOOP_COUNT)
```

Definition at line 102 of file config_chirp_design_MRR120.h.

4.10.1.51 SUBFRAME_MRR_NUM_CHIRPTYPES

```
#define SUBFRAME_MRR_NUM_CHIRPTYPES (2U)
```

Definition at line 118 of file config_chirp_design_MRR120.h.

4.10.1.52 SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES

```
#define SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES ( PROFILE_MRR_ADC_SAMPLE_VAL)
```

Definition at line 95 of file config_chirp_design_MRR120.h.

4.10.1.53 SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES

```
#define SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES ( PROFILE_MRR_ADC_SAMPLE_VAL * 2 )
Definition at line 94 of file config_chirp_design_MRR120.h.
```

4.10.1.54 SUBFRAME_MRR_NUM_TX

```
#define SUBFRAME_MRR_NUM_TX (1U)
Definition at line 98 of file config_chirp_design_MRR120.h.
```

4.10.1.55 SUBFRAME_MRR_NUM_VIRT_ANT

```
#define SUBFRAME_MRR_NUM_VIRT_ANT ( SUBFRAME_MRR_NUM_TX* NUM_RX_CHANNELS )
Definition at line 100 of file config_chirp_design_MRR120.h.
```

4.10.1.56 SUBFRAME_MRR_PERIODICITY_VAL

```
#define SUBFRAME_MRR_PERIODICITY_VAL (6000000U)
Definition at line 92 of file config_chirp_design_MRR120.h.
```

4.10.1.57 SUBFRAME_MRR_TRIGGER_DELAY_VAL

```
#define SUBFRAME_MRR_TRIGGER_DELAY_VAL (0U)
Definition at line 93 of file config_chirp_design_MRR120.h.
```

4.11 common/profiles/config_chirp_design_MRR80.h File Reference

Macros

- #define **PROFILE_MRR_PROFILE_ID** (0U)
MRR profile ID.
- #define **PROFILE_MRR_HPFCORNER_FREQ1_VAL** RL_RX_HPF1_175_KHz
HPF 1 corner frequency.
- #define **PROFILE_MRR_HPFCORNER_FREQ2_VAL** RL_RX_HPF2_350_KHz
HPF 2 corner frequency.
- #define **PROFILE_MRR_RX_GAIN_VAL** (44U)
Rx gain is kept at the maximum .
- #define **PROFILE_MRR_DIGOUT_SAMPLERATE_VAL** (5000U)
ADC Output rate is 5Mhz.
- #define **PROFILE_MRR_ADC_SAMPLE_VAL** (256U)
- #define **PROFILE_MRR_IDLE_TIME_VAL** (600U)
- #define **PROFILE_MRR_RAMP_END_TIME_VAL** (5600U)
- #define **PROFILE_MRR_START_FREQ_GHZ** (76.01f)
- #define **PROFILE_MRR_START_FREQ_VAL** (CONV_FREQ_GHZ_TO_CODEWORD(PROFILE_MRR←
 _Start_Freq_Ghz))
- #define **PROFILE_MRR_TXOUT_POWER_BACKOFF** (0U)
- #define **PROFILE_MRR_TXPHASESHIFTER_VAL** (0U)
- #define **PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US** (8.0f)
- #define **PROFILE_MRR_FREQ_SLOPE_VAL** (CONV_SLOPE_MHZ_PER_US_TO_CODEWORD(PR←
 OFILE_MRR_FREQ_SLOPE_MHZ_PER_US))
- #define **PROFILE_MRR_TX_START_TIME_VAL** (100U)
- #define **PROFILE_MRR_ADC_START_TIME_VAL** (480U)

- #define PROFILE_MRR_LAMBDA_MILLIMETER (MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PE↔R_USEC/ PROFILE_MRR_START_FREQ_GHZ)
- #define CHIRP_MRR_0_PROFILE_ID (0U)

Define 128 chirps, the first 64 will have an idle time of 3us, and the remaining 64 will have an idle time of 14.8us (11.8us extra 'idle time')

 - #define CHIRP_MRR_0_START_INDEX (0U)
 - #define CHIRP_MRR_0_END_INDEX (63U)
 - #define CHIRP_MRR_0_START_FREQ_VAL (0U)
 - #define CHIRP_MRR_0_FREQ_SLOPE_VAL (0U)
 - #define CHIRP_MRR_0_IDLE_TIME_VAL (0U)
 - #define CHIRP_MRR_0_ADC_START_TIME_VAL (0U)
 - #define CHIRP_MRR_0_TX_CHANNEL (TX_CHANNEL_1_ENABLE)
 - #define CHIRP_MRR_1_PROFILE_ID (0U)
 - #define CHIRP_MRR_1_START_INDEX (64U)
 - #define CHIRP_MRR_1_END_INDEX (127U)
 - #define CHIRP_MRR_1_START_FREQ_VAL (0U)
 - #define CHIRP_MRR_1_FREQ_SLOPE_VAL (0U)
 - #define CHIRP_MRR_1_IDLE_TIME_VAL (1180U)
 - #define CHIRP_MRR_1_ADC_START_TIME_VAL (0U)
 - #define CHIRP_MRR_1_TX_CHANNEL (TX_CHANNEL_1_ENABLE)
 - #define SUBFRAME_MRR_CHIRP_START_IDX (0U)

SUBFRAME configuration.

 - #define SUBFRAME_MRR_CHIRP_END_IDX (127U)
 - #define SUBFRAME_MRR_LOOP_COUNT (1U)
 - #define SUBFRAME_MRR_PERIODICITY_VAL (6000000U)
 - #define SUBFRAME_MRR_TRIGGER_DELAY_VAL (0U)
 - #define SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES (PROFILE_MRR_ADC_SAMPLE_VAL * 2)
 - #define SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES (PROFILE_MRR_ADC_SAMPLE_VAL)
 - #define SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS ((CHIRP_MRR_0_END_INDEX - CHIRP_MRR_0_START_INDEX + 1)* SUBFRAME_MRR_LOOP_COUNT)
 - #define SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS ((CHIRP_MRR_1_END_INDEX - CHIRP_MRR_1_START_INDEX + 1)* SUBFRAME_MRR_LOOP_COUNT)
 - #define SUBFRAME_MRR_NUM_TX (1U)
 - #define SUBFRAME_MRR_NUM_VIRT_ANT (SUBFRAME_MRR_NUM_TX* NUM_RX_CHANNELS)
 - #define SUBFRAME_MRR_NUM_ANGLE_BINS (32U)
 - #define SUBFRAME_MRR_NUM_CHIRPS_TOTAL ((SUBFRAME_MRR_CHIRP_END_IDX - SUBFRAME_MRR_CHIRP_START_IDX + 1) * SUBFRAME_MRR_LOOP_COUNT)
 - #define PROFILE_MRR_RANGE_RESOLUTION_METERS ((MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC * PROFILE_MRR_DIGOUT_SAMPLERATE_VAL)/ (2000.0f * PROFILE_MRR_EQ_SLOPE_MHZ_PER_US * SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES))
 - #define SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US ((CHIRP_MRR_0_IDLE_TIME_VAL + PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
 - #define SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US)/ SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS)*(PROFILE_MRR_LAMBDA_MILLIMETER/2))
 - #define SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S (SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S* SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS/2)
 - #define INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S)
 - #define SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US ((CHIRP_MRR_1_IDLE_TIME_VAL + PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
 - #define SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US)/ SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS)*(PROFILE_MRR_LAMBDA_MILLIMETER/2))
 - #define SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S ((SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S* SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS/2))

- #define INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (1.0f/ **SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S**)
- #define SUBFRAME_MRR_MIN_SNR_dB (14.0f)
- #define SUBFRAME_MRR_NUM_CHIRPTYPES (2U)

4.11.1 Macro Definition Documentation

4.11.1.1 CHIRP_MRR_0_ADC_START_TIME_VAL

```
#define CHIRP_MRR_0_ADC_START_TIME_VAL (0U)  
Definition at line 75 of file config_chirp_design_MRR80.h.
```

4.11.1.2 CHIRP_MRR_0_END_INDEX

```
#define CHIRP_MRR_0_END_INDEX (63U)  
Definition at line 71 of file config_chirp_design_MRR80.h.
```

4.11.1.3 CHIRP_MRR_0_FREQ_SLOPE_VAL

```
#define CHIRP_MRR_0_FREQ_SLOPE_VAL (0U)  
Definition at line 73 of file config_chirp_design_MRR80.h.
```

4.11.1.4 CHIRP_MRR_0_IDLE_TIME_VAL

```
#define CHIRP_MRR_0_IDLE_TIME_VAL (0U)  
Definition at line 74 of file config_chirp_design_MRR80.h.
```

4.11.1.5 CHIRP_MRR_0_PROFILE_ID

```
#define CHIRP_MRR_0_PROFILE_ID (0U)  
Define 128 chirps, the first 64 will have an idle time of 3us, and the remaining 64 will have an idle time of 14.8us  
(11.8us extra 'idle time')  
Definition at line 69 of file config_chirp_design_MRR80.h.
```

4.11.1.6 CHIRP_MRR_0_START_FREQ_VAL

```
#define CHIRP_MRR_0_START_FREQ_VAL (0U)  
Definition at line 72 of file config_chirp_design_MRR80.h.
```

4.11.1.7 CHIRP_MRR_0_START_INDEX

```
#define CHIRP_MRR_0_START_INDEX (0U)  
Definition at line 70 of file config_chirp_design_MRR80.h.
```

4.11.1.8 CHIRP_MRR_0_TX_CHANNEL

```
#define CHIRP_MRR_0_TX_CHANNEL ( TX_CHANNEL_1_ENABLE )  
Definition at line 76 of file config_chirp_design_MRR80.h.
```

4.11.1.9 CHIRP_MRR_1_ADC_START_TIME_VAL

```
#define CHIRP_MRR_1_ADC_START_TIME_VAL (0U)
```

Definition at line 84 of file config_chirp_design_MRR80.h.

4.11.1.10 CHIRP_MRR_1_END_INDEX

```
#define CHIRP_MRR_1_END_INDEX (127U)
```

Definition at line 80 of file config_chirp_design_MRR80.h.

4.11.1.11 CHIRP_MRR_1_FREQ_SLOPE_VAL

```
#define CHIRP_MRR_1_FREQ_SLOPE_VAL (0U)
```

Definition at line 82 of file config_chirp_design_MRR80.h.

4.11.1.12 CHIRP_MRR_1_IDLE_TIME_VAL

```
#define CHIRP_MRR_1_IDLE_TIME_VAL (1180U)
```

Definition at line 83 of file config_chirp_design_MRR80.h.

4.11.1.13 CHIRP_MRR_1_PROFILE_ID

```
#define CHIRP_MRR_1_PROFILE_ID (0U)
```

Definition at line 78 of file config_chirp_design_MRR80.h.

4.11.1.14 CHIRP_MRR_1_START_FREQ_VAL

```
#define CHIRP_MRR_1_START_FREQ_VAL (0U)
```

Definition at line 81 of file config_chirp_design_MRR80.h.

4.11.1.15 CHIRP_MRR_1_START_INDEX

```
#define CHIRP_MRR_1_START_INDEX (64U)
```

Definition at line 79 of file config_chirp_design_MRR80.h.

4.11.1.16 CHIRP_MRR_1_TX_CHANNEL

```
#define CHIRP_MRR_1_TX_CHANNEL ( TX_CHANNEL_1_ENABLE )
```

Definition at line 85 of file config_chirp_design_MRR80.h.

4.11.1.17 INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S

```
#define INV_SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_0_VE←  
L_RESOLUTION_M_P_S)
```

Definition at line 107 of file config_chirp_design_MRR80.h.

4.11.1.18 INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S

```
#define INV_SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (1.0f/ SUBFRAME_MRR_CHIRPTYPE_1_VE←  
L_RESOLUTION_M_P_S)
```

Definition at line 112 of file config_chirp_design_MRR80.h.

4.11.1.19 PROFILE_MRR_ADC_SAMPLE_VAL

```
#define PROFILE_MRR_ADC_SAMPLE_VAL (256U)
Definition at line 52 of file config_chirp_design_MRR80.h.
```

4.11.1.20 PROFILE_MRR_ADC_START_TIME_VAL

```
#define PROFILE_MRR_ADC_START_TIME_VAL (480U)
Definition at line 62 of file config_chirp_design_MRR80.h.
```

4.11.1.21 PROFILE_MRR_DIGOUT_SAMPLERATE_VAL

```
#define PROFILE_MRR_DIGOUT_SAMPLERATE_VAL (5000U)
ADC Output rate is 5Mhz.
Definition at line 51 of file config_chirp_design_MRR80.h.
```

4.11.1.22 PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US

```
#define PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US (8.0f)
Definition at line 59 of file config_chirp_design_MRR80.h.
```

4.11.1.23 PROFILE_MRR_FREQ_SLOPE_VAL

```
#define PROFILE_MRR_FREQ_SLOPE_VAL ( CONV_SLOPE_MHZ_PER_US_TO_CODEWORD( PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US ) )
Definition at line 60 of file config_chirp_design_MRR80.h.
```

4.11.1.24 PROFILE_MRR_HPFCORNER_FREQ1_VAL

```
#define PROFILE_MRR_HPFCORNER_FREQ1_VAL RL_RX_HPF1_175_KHz
HPF 1 corner frequency.
Definition at line 45 of file config_chirp_design_MRR80.h.
```

4.11.1.25 PROFILE_MRR_HPFCORNER_FREQ2_VAL

```
#define PROFILE_MRR_HPFCORNER_FREQ2_VAL RL_RX_HPF2_350_KHz
HPF 2 corner frequency.
Definition at line 47 of file config_chirp_design_MRR80.h.
```

4.11.1.26 PROFILE_MRR_IDLE_TIME_VAL

```
#define PROFILE_MRR_IDLE_TIME_VAL (600U)
Definition at line 53 of file config_chirp_design_MRR80.h.
```

4.11.1.27 PROFILE_MRR_LAMBDA_MILLIMETER

```
#define PROFILE_MRR_LAMBDA_MILLIMETER (MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC/ PROFILE_MR_START_FREQ_GHZ )
Definition at line 64 of file config_chirp_design_MRR80.h.
```

4.11.1.28 PROFILE_MRR_PROFILE_ID

```
#define PROFILE_MRR_PROFILE_ID (0U)
```

MRR profile ID.

Definition at line 43 of file config_chirp_design_MRR80.h.

4.11.1.29 PROFILE_MRR_RAMP_END_TIME_VAL

```
#define PROFILE_MRR_RAMP_END_TIME_VAL (5600U)
```

Definition at line 54 of file config_chirp_design_MRR80.h.

4.11.1.30 PROFILE_MRR_RANGE_RESOLUTION_METERS

```
#define PROFILE_MRR_RANGE_RESOLUTION_METERS ((MMWDemo_Speed_of_Light_in_Meters_Per_Usec * PROFILE_MRR_DIGOUT_SAMPLERATE_VAL) / (2000.0f * PROFILE_MRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES))
```

Definition at line 102 of file config_chirp_design_MRR80.h.

4.11.1.31 PROFILE_MRR_RX_GAIN_VAL

```
#define PROFILE_MRR_RX_GAIN_VAL (44U)
```

Rx gain is kept at the maximum .

Definition at line 49 of file config_chirp_design_MRR80.h.

4.11.1.32 PROFILE_MRR_START_FREQ_GHZ

```
#define PROFILE_MRR_START_FREQ_GHZ (76.01f)
```

Definition at line 55 of file config_chirp_design_MRR80.h.

4.11.1.33 PROFILE_MRR_START_FREQ_VAL

```
#define PROFILE_MRR_START_FREQ_VAL (CONV_FREQ_GHZ_TO_CODEWORD( PROFILE_MRR_START_FREQ_GHZ ))
```

Definition at line 56 of file config_chirp_design_MRR80.h.

4.11.1.34 PROFILE_MRR_TX_START_TIME_VAL

```
#define PROFILE_MRR_TX_START_TIME_VAL (100U)
```

Definition at line 61 of file config_chirp_design_MRR80.h.

4.11.1.35 PROFILE_MRR_TXOUT_POWER_BACKOFF

```
#define PROFILE_MRR_TXOUT_POWER_BACKOFF (0U)
```

Definition at line 57 of file config_chirp_design_MRR80.h.

4.11.1.36 PROFILE_MRR_TXPHASESHIFTER_VAL

```
#define PROFILE_MRR_TXPHASESHIFTER_VAL (0U)
```

Definition at line 58 of file config_chirp_design_MRR80.h.

4.11.1.37 SUBFRAME_MRR_CHIRP_END_IDX

```
#define SUBFRAME_MRR_CHIRP_END_IDX (127U)
Definition at line 89 of file config_chirp_design_MRR80.h.
```

4.11.1.38 SUBFRAME_MRR_CHIRP_START_IDX

```
#define SUBFRAME_MRR_CHIRP_START_IDX (0U)
SUBFRAME configuration.
Definition at line 88 of file config_chirp_design_MRR80.h.
```

4.11.1.39 SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US

```
#define SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US (( CHIRP_MRR_0_IDLE_TIME_VAL +
PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
Definition at line 104 of file config_chirp_design_MRR80.h.
```

4.11.1.40 SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_0_MAX_VEL_M_P_S ( SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S *
SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS/2)
Definition at line 106 of file config_chirp_design_MRR80.h.
```

4.11.1.41 SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS

```
#define SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS (( CHIRP_MRR_0_END_INDEX - CHIRP_MRR_0_START_INDEX +
1)* SUBFRAME_MRR_LOOP_COUNT)
Definition at line 95 of file config_chirp_design_MRR80.h.
```

4.11.1.42 SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_0_VEL_RESOLUTION_M_P_S (((1000.0f / SUBFRAME_MRR_CHIRPTYPE_0_CHIRP_REPETITION_PERIOD_US) /
SUBFRAME_MRR_CHIRPTYPE_0_NUM_CHIRPS)*( PROFILE_MRR_LAMBDA_MILLIMETER/2))
Definition at line 105 of file config_chirp_design_MRR80.h.
```

4.11.1.43 SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US

```
#define SUBFRAME_MRR_CHIRPTYPE_1_CHIRP_REPETITION_PERIOD_US (( CHIRP_MRR_1_IDLE_TIME_VAL +
PROFILE_MRR_IDLE_TIME_VAL + PROFILE_MRR_RAMP_END_TIME_VAL)/100.0f)
Definition at line 109 of file config_chirp_design_MRR80.h.
```

4.11.1.44 SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_1_MAX_VEL_M_P_S ( SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S *
SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS/2)
Definition at line 111 of file config_chirp_design_MRR80.h.
```

4.11.1.45 SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS

```
#define SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS (( CHIRP_MRR_1_END_INDEX - CHIRP_MRR_1_START_INDEX +
1)* SUBFRAME_MRR_LOOP_COUNT)
```

Definition at line 96 of file config_chirp_design_MRR80.h.

4.11.1.46 SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_MRR_CHIRPTYPE_1_VEL_RESOLUTION_M_P_S (((1000.0f/ SUBFRAME_MRR_CHIRPTYPE_1_C←
HIRP_REPETITION_PERIOD_US) / SUBFRAME_MRR_CHIRPTYPE_1_NUM_CHIRPS)*( PROFILE_MRR_LAMBDA_MILLIME←
TER/2))
```

Definition at line 110 of file config_chirp_design_MRR80.h.

4.11.1.47 SUBFRAME_MRR_LOOP_COUNT

```
#define SUBFRAME_MRR_LOOP_COUNT (1U)
```

Definition at line 90 of file config_chirp_design_MRR80.h.

4.11.1.48 SUBFRAME_MRR_MIN_SNR_dB

```
#define SUBFRAME_MRR_MIN_SNR_dB (14.0f)
```

Definition at line 114 of file config_chirp_design_MRR80.h.

4.11.1.49 SUBFRAME_MRR_NUM_ANGLE_BINS

```
#define SUBFRAME_MRR_NUM_ANGLE_BINS (32U)
```

Definition at line 99 of file config_chirp_design_MRR80.h.

4.11.1.50 SUBFRAME_MRR_NUM_CHIRPS_TOTAL

```
#define SUBFRAME_MRR_NUM_CHIRPS_TOTAL (( SUBFRAME_MRR_CHIRP_END_IDX - SUBFRAME_MRR_CHIRP_ST←
ART_IDX + 1) * SUBFRAME_MRR_LOOP_COUNT)
```

Definition at line 100 of file config_chirp_design_MRR80.h.

4.11.1.51 SUBFRAME_MRR_NUM_CHIRPTYPES

```
#define SUBFRAME_MRR_NUM_CHIRPTYPES (2U)
```

Definition at line 116 of file config_chirp_design_MRR80.h.

4.11.1.52 SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES

```
#define SUBFRAME_MRR_NUM_CMPLX_ADC_SAMPLES ( PROFILE_MRR_ADC_SAMPLE_VAL)
```

Definition at line 94 of file config_chirp_design_MRR80.h.

4.11.1.53 SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES

```
#define SUBFRAME_MRR_NUM_REAL_ADC_SAMPLES ( PROFILE_MRR_ADC_SAMPLE_VAL * 2)
```

Definition at line 93 of file config_chirp_design_MRR80.h.

4.11.1.54 SUBFRAME_MRR_NUM_TX

```
#define SUBFRAME_MRR_NUM_TX (1U)
```

Definition at line 97 of file config_chirp_design_MRR80.h.

4.11.1.55 SUBFRAME_MRR_NUM_VIRT_ANT

```
#define SUBFRAME_MRR_NUM_VIRT_ANT ( SUBFRAME_MRR_NUM_TX* NUM_RX_CHANNELS)
```

Definition at line 98 of file config_chirp_design_MRR80.h.

4.11.1.56 SUBFRAME_MRR_PERIODICITY_VAL

```
#define SUBFRAME_MRR_PERIODICITY_VAL (6000000U)
```

Definition at line 91 of file config_chirp_design_MRR80.h.

4.11.1.57 SUBFRAME_MRR_TRIGGER_DELAY_VAL

```
#define SUBFRAME_MRR_TRIGGER_DELAY_VAL (0U)
```

Definition at line 92 of file config_chirp_design_MRR80.h.

4.12 common/profiles/config_chirp_design_USRR20.h File Reference

Macros

- #define **PROFILE_USRR_PROFILE_ID** (1U)

Ultra short range chirp profile - 20 m range, 4.3cm resolution. better angular resolution, approximately 18kmph max - vel.
- #define **PROFILE_USRR_HPFCORNER_FREQ1_VAL** RL_RX_HPF1_175_KHz
- #define **PROFILE_USRR_HPFCORNER_FREQ2_VAL** RL_RX_HPF2_350_KHz
- #define **PROFILE_USRR_RX_GAIN_VAL** (30U)
- #define **PROFILE_USRR_DIGOUT_SAMPLERATE_VAL** (6222U)
- #define **PROFILE_USRR_ADC_SAMPLE_VAL** (512U)
- #define **PROFILE_USRR_IDLE_TIME_VAL** (700U)
- #define **PROFILE_USRR_RAMP_END_TIME_VAL** (8728U)
- #define **PROFILE_USRR_START_FREQ_GHZ** (77.01f)
- #define **PROFILE_USRR_START_FREQ_VAL** (CONV_FREQ_GHZ_TO_CODEWORD(**PROFILE_USRR_START_FREQ_GHZ**))
- #define **PROFILE_USRR_TXOUT_POWER_BACKOFF** (0U)
- #define **PROFILE_USRR_TXPHASESHIFTER_VAL** (0U)
- #define **PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US** (42.0f)
- #define **PROFILE_USRR_FREQ_SLOPE_VAL** (CONV_SLOPE_MHZ_PER_US_TO_CODEWORD(**PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US**))
- #define **PROFILE_USRR_TX_START_TIME_VAL** (100U)
- #define **PROFILE_USRR_ADC_START_TIME_VAL** (480U)
- #define **PROFILE_USRR_LAMBDA_MILLIMETER** (MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC/ **PROFILE_USRR_START_FREQ_GHZ**)
- #define **CHIRP_USRR_0_PROFILE_ID** (1U)
- #define **CHIRP_USRR_0_START_INDEX** (256U + 0U)
- #define **CHIRP_USRR_0_END_INDEX** (256U + 0U)
- #define **CHIRP_USRR_0_START_FREQ_VAL** (0U)
- #define **CHIRP_USRR_0_FREQ_SLOPE_VAL** (0U)
- #define **CHIRP_USRR_0_IDLE_TIME_VAL** (0U)
- #define **CHIRP_USRR_0_ADC_START_TIME_VAL** (0U)
- #define **CHIRP_USRR_0_TX_CHANNEL** (TX_CHANNEL_1_ENABLE)
- #define **CHIRP_USRR_1_PROFILE_ID** (1U)
- #define **CHIRP_USRR_1_START_INDEX** (256U + 1U)
- #define **CHIRP_USRR_1_END_INDEX** (256U + 1U)
- #define **CHIRP_USRR_1_START_FREQ_VAL** (0U)
- #define **CHIRP_USRR_1_FREQ_SLOPE_VAL** (0U)

- #define CHIRP_USRR_1_IDLE_TIME_VAL (0U)
- #define CHIRP_USRR_1_ADC_START_TIME_VAL (0U)
- #define CHIRP_USRR_1_TX_CHANNEL (TX_CHANNEL_2_ENABLE)
- #define CHIRP_USRR_2_PROFILE_ID (1U)
- #define CHIRP_USRR_2_START_INDEX (256U + 2U)
- #define CHIRP_USRR_2_END_INDEX (256U + 2U)
- #define CHIRP_USRR_2_START_FREQ_VAL (0U)
- #define CHIRP_USRR_2_FREQ_SLOPE_VAL (0U)
- #define CHIRP_USRR_2_IDLE_TIME_VAL (0U)
- #define CHIRP_USRR_2_ADC_START_TIME_VAL (0U)
- #define CHIRP_USRR_2_TX_CHANNEL (TX_CHANNEL_3_ENABLE)
- #define SUBFRAME_USRR_CHIRP_START_IDX (CHIRP_USRR_0_START_INDEX)
- #define SUBFRAME_USRR_CHIRP_END_IDX (CHIRP_USRR_2_END_INDEX)
- #define SUBFRAME_USRR_LOOP_COUNT (32U)
- #define SUBFRAME_USRR_PERIODICITY_VAL (6000000U)
- #define SUBFRAME_USRR_TRIGGER_DELAY_VAL (0U)
- #define SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES (PROFILE_USRR_ADC_SAMPLE_VAL * 2)
- #define SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES (PROFILE_USRR_ADC_SAMPLE_VAL)
- #define SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS ((CHIRP_USRR_0_END_INDEX - CHIRP_USRR_0_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
- #define SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS ((CHIRP_USRR_1_END_INDEX - CHIRP_USRR_1_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
- #define SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS ((CHIRP_USRR_2_END_INDEX - CHIRP_USRR_2_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
- #define SUBFRAME_USRR_NUM_TX (3U)
- #define SUBFRAME_USRR_NUM_VIRT_ANT (SUBFRAME_USRR_NUM_TX* NUM_RX_CHANNELS)
- #define SUBFRAME_USRR_NUM_ANGLE_BINS (64U)
- #define SUBFRAME_USRR_NUM_CHIRPS_TOTAL ((SUBFRAME_USRR_CHIRP_END_IDX - SUBFRAME_USRR_CHIRP_START_IDX + 1) * SUBFRAME_USRR_LOOP_COUNT)
- #define PROFILE_USRR_RANGE_RESOLUTION_METERS (((MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC * PROFILE_USRR_DIGOUT_SAMPLERATE_VAL)/(2000.0f * PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES)))

Derived parameters.

- #define SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US (((PROFILE_USRR_IDLE_TIME_VAL + PROFILE_USRR_RAMP_END_TIME_VAL)/100.0f))
- #define SUBFRAME_USRR_VEL_RESOLUTION_M_P_S (((1000.0f/(3.0f* SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US))/ SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS)*(PROFILE_USRR_LAMBDA_MILLIMETER/2.0f))
- #define SUBFRAME_USRR_MAX_VEL_M_P_S (SUBFRAME_USRR_VEL_RESOLUTION_M_P_S* SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS/2)
- #define SUBFRAME_USRR_MIN_SNR_dB (22.0f)
- #define SUBFRAME_USRR_NUM_CHIRPTYPES (3U)

4.12.1 Macro Definition Documentation

4.12.1.1 CHIRP_USRR_0_ADC_START_TIME_VAL

```
#define CHIRP_USRR_0_ADC_START_TIME_VAL (0U)
Definition at line 65 of file config_chirp_design_USRR20.h.
```

4.12.1.2 CHIRP_USRR_0_END_INDEX

```
#define CHIRP_USRR_0_END_INDEX (256U + 0U)
Definition at line 61 of file config_chirp_design_USRR20.h.
```

4.12.1.3 CHIRP_USRR_0_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_0_FREQ_SLOPE_VAL (0U)
Definition at line 63 of file config_chirp_design_USRR20.h.
```

4.12.1.4 CHIRP_USRR_0_IDLE_TIME_VAL

```
#define CHIRP_USRR_0_IDLE_TIME_VAL (0U)
Definition at line 64 of file config_chirp_design_USRR20.h.
```

4.12.1.5 CHIRP_USRR_0_PROFILE_ID

```
#define CHIRP_USRR_0_PROFILE_ID (1U)
Definition at line 59 of file config_chirp_design_USRR20.h.
```

4.12.1.6 CHIRP_USRR_0_START_FREQ_VAL

```
#define CHIRP_USRR_0_START_FREQ_VAL (0U)
Definition at line 62 of file config_chirp_design_USRR20.h.
```

4.12.1.7 CHIRP_USRR_0_START_INDEX

```
#define CHIRP_USRR_0_START_INDEX (256U + 0U)
Definition at line 60 of file config_chirp_design_USRR20.h.
```

4.12.1.8 CHIRP_USRR_0_TX_CHANNEL

```
#define CHIRP_USRR_0_TX_CHANNEL ( TX_CHANNEL_1_ENABLE )
Definition at line 66 of file config_chirp_design_USRR20.h.
```

4.12.1.9 CHIRP_USRR_1_ADC_START_TIME_VAL

```
#define CHIRP_USRR_1_ADC_START_TIME_VAL (0U)
Definition at line 74 of file config_chirp_design_USRR20.h.
```

4.12.1.10 CHIRP_USRR_1_END_INDEX

```
#define CHIRP_USRR_1_END_INDEX (256U + 1U)
Definition at line 70 of file config_chirp_design_USRR20.h.
```

4.12.1.11 CHIRP_USRR_1_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_1_FREQ_SLOPE_VAL (0U)
Definition at line 72 of file config_chirp_design_USRR20.h.
```

4.12.1.12 CHIRP_USRR_1_IDLE_TIME_VAL

```
#define CHIRP_USRR_1_IDLE_TIME_VAL (0U)
Definition at line 73 of file config_chirp_design_USRR20.h.
```

4.12.1.13 CHIRP_USRR_1_PROFILE_ID

```
#define CHIRP_USRR_1_PROFILE_ID (1U)
```

Definition at line 68 of file config_chirp_design_USRR20.h.

4.12.1.14 CHIRP_USRR_1_START_FREQ_VAL

```
#define CHIRP_USRR_1_START_FREQ_VAL (0U)
```

Definition at line 71 of file config_chirp_design_USRR20.h.

4.12.1.15 CHIRP_USRR_1_START_INDEX

```
#define CHIRP_USRR_1_START_INDEX (256U + 1U)
```

Definition at line 69 of file config_chirp_design_USRR20.h.

4.12.1.16 CHIRP_USRR_1_TX_CHANNEL

```
#define CHIRP_USRR_1_TX_CHANNEL ( TX_CHANNEL_2_ENABLE )
```

Definition at line 75 of file config_chirp_design_USRR20.h.

4.12.1.17 CHIRP_USRR_2_ADC_START_TIME_VAL

```
#define CHIRP_USRR_2_ADC_START_TIME_VAL (0U)
```

Definition at line 84 of file config_chirp_design_USRR20.h.

4.12.1.18 CHIRP_USRR_2_END_INDEX

```
#define CHIRP_USRR_2_END_INDEX (256U + 2U)
```

Definition at line 80 of file config_chirp_design_USRR20.h.

4.12.1.19 CHIRP_USRR_2_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_2_FREQ_SLOPE_VAL (0U)
```

Definition at line 82 of file config_chirp_design_USRR20.h.

4.12.1.20 CHIRP_USRR_2_IDLE_TIME_VAL

```
#define CHIRP_USRR_2_IDLE_TIME_VAL (0U)
```

Definition at line 83 of file config_chirp_design_USRR20.h.

4.12.1.21 CHIRP_USRR_2_PROFILE_ID

```
#define CHIRP_USRR_2_PROFILE_ID (1U)
```

Definition at line 78 of file config_chirp_design_USRR20.h.

4.12.1.22 CHIRP_USRR_2_START_FREQ_VAL

```
#define CHIRP_USRR_2_START_FREQ_VAL (0U)
```

Definition at line 81 of file config_chirp_design_USRR20.h.

4.12.1.23 CHIRP_USRR_2_START_INDEX

```
#define CHIRP_USRR_2_START_INDEX (256U + 2U)  
Definition at line 79 of file config_chirp_design_USRR20.h.
```

4.12.1.24 CHIRP_USRR_2_TX_CHANNEL

```
#define CHIRP_USRR_2_TX_CHANNEL ( TX_CHANNEL_3_ENABLE)  
Definition at line 85 of file config_chirp_design_USRR20.h.
```

4.12.1.25 PROFILE_USRR_ADC_SAMPLE_VAL

```
#define PROFILE_USRR_ADC_SAMPLE_VAL (512U)  
Definition at line 46 of file config_chirp_design_USRR20.h.
```

4.12.1.26 PROFILE_USRR_ADC_START_TIME_VAL

```
#define PROFILE_USRR_ADC_START_TIME_VAL (480U)  
Definition at line 56 of file config_chirp_design_USRR20.h.
```

4.12.1.27 PROFILE_USRR_DIGOUT_SAMPLERATE_VAL

```
#define PROFILE_USRR_DIGOUT_SAMPLERATE_VAL (6222U)  
Definition at line 45 of file config_chirp_design_USRR20.h.
```

4.12.1.28 PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US

```
#define PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US (42.0f)  
Definition at line 53 of file config_chirp_design_USRR20.h.
```

4.12.1.29 PROFILE_USRR_FREQ_SLOPE_VAL

```
#define PROFILE_USRR_FREQ_SLOPE_VAL ( CONV_SLOPE_MHZ_PER_US_TO_CODEWORD( PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US ) )  
Definition at line 54 of file config_chirp_design_USRR20.h.
```

4.12.1.30 PROFILE_USRR_HPFCORNER_FREQ1_VAL

```
#define PROFILE_USRR_HPFCORNER_FREQ1_VAL RL_RX_HPF1_175_KHz  
Definition at line 42 of file config_chirp_design_USRR20.h.
```

4.12.1.31 PROFILE_USRR_HPFCORNER_FREQ2_VAL

```
#define PROFILE_USRR_HPFCORNER_FREQ2_VAL RL_RX_HPF2_350_KHz  
Definition at line 43 of file config_chirp_design_USRR20.h.
```

4.12.1.32 PROFILE_USRR_IDLE_TIME_VAL

```
#define PROFILE_USRR_IDLE_TIME_VAL (700U)  
Definition at line 47 of file config_chirp_design_USRR20.h.
```

4.12.1.33 PROFILE_USRR_LAMBDA_MILLIMETER

```
#define PROFILE_USRR_LAMBDA_MILLIMETER (MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC/ PROFILE_U←
SRR_START_FREQ_GHZ)
```

Definition at line 57 of file config_chirp_design_USRR20.h.

4.12.1.34 PROFILE_USRR_PROFILE_ID

```
#define PROFILE_USRR_PROFILE_ID (1U)
```

Ultra short range chirp profile - 20 m range, 4.3cm resolution. better angular resolution, approximately 18kmph max - vel.

Definition at line 41 of file config_chirp_design_USRR20.h.

4.12.1.35 PROFILE_USRR_RAMP_END_TIME_VAL

```
#define PROFILE_USRR_RAMP_END_TIME_VAL (8728U)
```

Definition at line 48 of file config_chirp_design_USRR20.h.

4.12.1.36 PROFILE_USRR_RANGE_RESOLUTION_METERS

```
#define PROFILE_USRR_RANGE_RESOLUTION_METERS ((MMWDEMO_SPEED_OF_LIGHT_IN_METERS_PER_USEC * P←
ROFILE_USRR_DIGOUT_SAMPLERATE_VAL) / (2000.0f * PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAM←
E_USRR_NUM_CMPLX_ADC_SAMPLES))
```

Derived parameters.

Definition at line 105 of file config_chirp_design_USRR20.h.

4.12.1.37 PROFILE_USRR_RX_GAIN_VAL

```
#define PROFILE_USRR_RX_GAIN_VAL (30U)
```

Definition at line 44 of file config_chirp_design_USRR20.h.

4.12.1.38 PROFILE_USRR_START_FREQ_GHZ

```
#define PROFILE_USRR_START_FREQ_GHZ (77.01f)
```

Definition at line 49 of file config_chirp_design_USRR20.h.

4.12.1.39 PROFILE_USRR_START_FREQ_VAL

```
#define PROFILE_USRR_START_FREQ_VAL ( CONV_FREQ_GHZ_TO_CODEWORD( PROFILE_USRR_START_FREQ_GHZ ) )
```

Definition at line 50 of file config_chirp_design_USRR20.h.

4.12.1.40 PROFILE_USRR_TX_START_TIME_VAL

```
#define PROFILE_USRR_TX_START_TIME_VAL (100U)
```

Definition at line 55 of file config_chirp_design_USRR20.h.

4.12.1.41 PROFILE_USRR_TXOUT_POWER_BACKOFF

```
#define PROFILE_USRR_TXOUT_POWER_BACKOFF (0U)
```

Definition at line 51 of file config_chirp_design_USRR20.h.

4.12.1.42 PROFILE_USRR_TXPHASESHIFTER_VAL

```
#define PROFILE_USRR_TXPHASESHIFTER_VAL (0U)
Definition at line 52 of file config_chirp_design_USRR20.h.
```

4.12.1.43 SUBFRAME_USRR_CHIRP_END_IDX

```
#define SUBFRAME_USRR_CHIRP_END_IDX ( CHIRP_USRR_2_END_INDEX)
Definition at line 89 of file config_chirp_design_USRR20.h.
```

4.12.1.44 SUBFRAME_USRR_CHIRP_REPEATITION_PERIOD_US

```
#define SUBFRAME_USRR_CHIRP_REPEATITION_PERIOD_US ((( PROFILE_USRR_IDLE_TIME_VAL + PROFILE_USRR_RAMP_END_TIME_VAL)/100.0f))
Definition at line 108 of file config_chirp_design_USRR20.h.
```

4.12.1.45 SUBFRAME_USRR_CHIRP_START_IDX

```
#define SUBFRAME_USRR_CHIRP_START_IDX ( CHIRP_USRR_0_START_INDEX)
Definition at line 88 of file config_chirp_design_USRR20.h.
```

4.12.1.46 SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS (( CHIRP_USRR_0_END_INDEX - CHIRP_USRR_0_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
Definition at line 95 of file config_chirp_design_USRR20.h.
```

4.12.1.47 SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS (( CHIRP_USRR_1_END_INDEX - CHIRP_USRR_1_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
Definition at line 96 of file config_chirp_design_USRR20.h.
```

4.12.1.48 SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS (( CHIRP_USRR_2_END_INDEX - CHIRP_USRR_2_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
Definition at line 97 of file config_chirp_design_USRR20.h.
```

4.12.1.49 SUBFRAME_USRR_LOOP_COUNT

```
#define SUBFRAME_USRR_LOOP_COUNT (32U)
Definition at line 90 of file config_chirp_design_USRR20.h.
```

4.12.1.50 SUBFRAME_USRR_MAX_VEL_M_P_S

```
#define SUBFRAME_USRR_MAX_VEL_M_P_S ( SUBFRAME_USRR_VEL_RESOLUTION_M_P_S* SUBFRAME_USRR_CHIRP_TYPE_0_NUM_CHIRPS/2)
Definition at line 111 of file config_chirp_design_USRR20.h.
```

4.12.1.51 SUBFRAME_USRR_MIN_SNR_dB

```
#define SUBFRAME_USRR_MIN_SNR_dB (22.0f)
```

Definition at line 113 of file config_chirp_design_USRR20.h.

4.12.1.52 SUBFRAME_USRR_NUM_ANGLE_BINS

```
#define SUBFRAME_USRR_NUM_ANGLE_BINS (64U)
```

Definition at line 101 of file config_chirp_design_USRR20.h.

4.12.1.53 SUBFRAME_USRR_NUM_CHIRPS_TOTAL

```
#define SUBFRAME_USRR_NUM_CHIRPS_TOTAL (( SUBFRAME_USRR_CHIRP_END_IDX - SUBFRAME_USRR_CHIRP_START_IDX + 1) * SUBFRAME_USRR_LOOP_COUNT)
```

Definition at line 102 of file config_chirp_design_USRR20.h.

4.12.1.54 SUBFRAME_USRR_NUM_CHIRPTYPES

```
#define SUBFRAME_USRR_NUM_CHIRPTYPES (3U)
```

Definition at line 115 of file config_chirp_design_USRR20.h.

4.12.1.55 SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES

```
#define SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES ( PROFILE_USRR_ADC_SAMPLE_VAL )
```

Definition at line 94 of file config_chirp_design_USRR20.h.

4.12.1.56 SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES

```
#define SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES ( PROFILE_USRR_ADC_SAMPLE_VAL * 2 )
```

Definition at line 93 of file config_chirp_design_USRR20.h.

4.12.1.57 SUBFRAME_USRR_NUM_TX

```
#define SUBFRAME_USRR_NUM_TX (3U)
```

Definition at line 99 of file config_chirp_design_USRR20.h.

4.12.1.58 SUBFRAME_USRR_NUM_VIRT_ANT

```
#define SUBFRAME_USRR_NUM_VIRT_ANT ( SUBFRAME_USRR_NUM_TX* NUM_RX_CHANNELS )
```

Definition at line 100 of file config_chirp_design_USRR20.h.

4.12.1.59 SUBFRAME_USRR_PERIODICITY_VAL

```
#define SUBFRAME_USRR_PERIODICITY_VAL (6000000U)
```

Definition at line 91 of file config_chirp_design_USRR20.h.

4.12.1.60 SUBFRAME_USRR_TRIGGER_DELAY_VAL

```
#define SUBFRAME_USRR_TRIGGER_DELAY_VAL (0U)
```

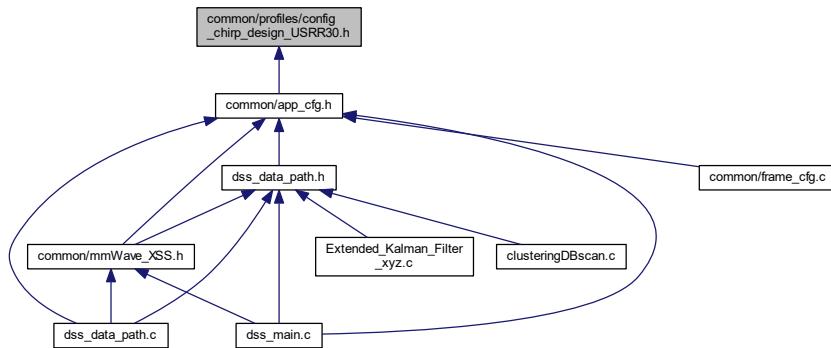
Definition at line 92 of file config_chirp_design_USRR20.h.

4.12.1.61 SUBFRAME_USRR_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_USRR_VEL_RESOLUTION_M_P_S (((1000.0f/(3.0f* SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US)) / SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS)*( PROFILE_USRR_LAMBDA_MILLIMETER/2.0f))
Definition at line 110 of file config_chirp_design_USRR20.h.
```

4.13 common/profiles/config_chirp_design_USRR30.h File Reference

This graph shows which files directly or indirectly include this file:



Macros

- #define **PROFILE_USRR_PROFILE_ID** (1U)

Ultra short range chirp profile - 20 m range, 4.3cm resolution. better angular resolution, approximately 18kmph max - vel.
- #define **PROFILE_USRR_HPFCORNER_FREQ1_VAL** RL_RX_HPF1_175_KHz
- #define **PROFILE_USRR_HPFCORNER_FREQ2_VAL** RL_RX_HPF2_350_KHz
- #define **PROFILE_USRR_RX_GAIN_VAL** (30U)
- #define **PROFILE_USRR_DIGOUT_SAMPLERATE_VAL** (12500U)
- #define **PROFILE_USRR_ADC_SAMPLE_VAL** (512U)
- #define **PROFILE_USRR_IDLE_TIME_VAL** (500U)
- #define **PROFILE_USRR_RAMP_END_TIME_VAL** (4400U)
- #define **PROFILE_USRR_START_FREQ_GHZ** (77.01f)
- #define **PROFILE_USRR_START_FREQ_VAL** (CONV_FREQ_GHZ_TO_CODEWORD(**PROFILE_USRR_START_FREQ_GHZ**))
- #define **PROFILE_USRR_TXOUT_POWER_BACKOFF** (0U)
- #define **PROFILE_USRR_TXPHASESHIFTER_VAL** (0U)
- #define **PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US** (56.25f)
- #define **PROFILE_USRR_FREQ_SLOPE_VAL** (CONV_SLOPE_MHZ_PER_US_TO_CODEWORD(**PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US**))
- #define **PROFILE_USRR_TX_START_TIME_VAL** (100U)
- #define **PROFILE_USRR_ADC_START_TIME_VAL** (300U)
- #define **PROFILE_USRR_LAMBDA_MILLIMETER** (SPEED_OF_LIGHT_IN_METERS_PER_USEC/ PROFILE_USRR_START_FREQ_GHZ)
- #define **CHIRP_USRR_0_PROFILE_ID** (1U)
- #define **CHIRP_USRR_0_START_INDEX** (256U + 0U)
- #define **CHIRP_USRR_0_END_INDEX** (256U + 0U)
- #define **CHIRP_USRR_0_START_FREQ_VAL** (0U)
- #define **CHIRP_USRR_0_FREQ_SLOPE_VAL** (0U)
- #define **CHIRP_USRR_0_IDLE_TIME_VAL** (0U)

- #define CHIRP_USRR_0_ADC_START_TIME_VAL (0U)
 - #define CHIRP_USRR_0_TX_CHANNEL (TX_CHANNEL_1_ENABLE)
 - #define CHIRP_USRR_1_PROFILE_ID (1U)
 - #define CHIRP_USRR_1_START_INDEX (256U + 1U)
 - #define CHIRP_USRR_1_END_INDEX (256U + 1U)
 - #define CHIRP_USRR_1_START_FREQ_VAL (0U)
 - #define CHIRP_USRR_1_FREQ_SLOPE_VAL (0U)
 - #define CHIRP_USRR_1_IDLE_TIME_VAL (0U)
 - #define CHIRP_USRR_1_ADC_START_TIME_VAL (0U)
 - #define CHIRP_USRR_1_TX_CHANNEL (TX_CHANNEL_2_ENABLE)
 - #define CHIRP_USRR_2_PROFILE_ID (1U)
 - #define CHIRP_USRR_2_START_INDEX (256U + 2U)
 - #define CHIRP_USRR_2_END_INDEX (256U + 2U)
 - #define CHIRP_USRR_2_START_FREQ_VAL (0U)
 - #define CHIRP_USRR_2_FREQ_SLOPE_VAL (0U)
 - #define CHIRP_USRR_2_IDLE_TIME_VAL (0U)
 - #define CHIRP_USRR_2_ADC_START_TIME_VAL (0U)
 - #define CHIRP_USRR_2_TX_CHANNEL (TX_CHANNEL_3_ENABLE)
 - #define SUBFRAME_USRR_CHIRP_START_IDX (CHIRP_USRR_0_START_INDEX)
 - #define SUBFRAME_USRR_CHIRP_END_IDX (CHIRP_USRR_2_END_INDEX)
 - #define SUBFRAME_USRR_LOOP_COUNT (32U)
 - #define SUBFRAME_USRR_PERIODICITY_VAL (6000000U)
 - #define SUBFRAME_USRR_TRIGGER_DELAY_VAL (0U)
 - #define SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES (PROFILE_USRR_ADC_SAMPLE_VAL * 2)
 - #define SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES (PROFILE_USRR_ADC_SAMPLE_VAL)
 - #define SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS ((CHIRP_USRR_0_END_INDEX - CHIRP_USRR_0_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
 - #define SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS ((CHIRP_USRR_1_END_INDEX - CHIRP_USRR_1_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
 - #define SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS ((CHIRP_USRR_2_END_INDEX - CHIRP_USRR_2_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)
 - #define SUBFRAME_USRR_NUM_TX (3U)
 - #define SUBFRAME_USRR_NUM_VIRT_ANT (SUBFRAME_USRR_NUM_TX* NUM_RX_CHANNELS)
 - #define SUBFRAME_USRR_NUM_ANGLE_BINS (64U)
 - #define SUBFRAME_USRR_NUM_CHIRPS_TOTAL ((SUBFRAME_USRR_CHIRP_END_IDX - SUBFRAME_USRR_CHIRP_START_IDX + 1) * SUBFRAME_USRR_LOOP_COUNT)
 - #define PROFILE_USRR_RANGE_RESOLUTION_METERS ((SPEED_OF_LIGHT_IN_METERS_PER_USEC * PROFILE_USRR_DIGOUT_SAMPLERATE_VAL)/ (2000.0f * PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES))
- Derived parameters.*
- #define SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US (((PROFILE_USRR_IDLE_TIME_VAL + PROFILE_USRR_RAMP_END_TIME_VAL)/100.0f))
 - #define SUBFRAME_USRR_VEL_RESOLUTION_M_P_S (((1000.0f/(3.0f* SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US))/ SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS)*(PROFILE_USRR_LAMBDA_MILLIMETER/2.0f))
 - #define SUBFRAME_USRR_MAX_VEL_M_P_S (SUBFRAME_USRR_VEL_RESOLUTION_M_P_S* SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS/2)
 - #define SUBFRAME_USRR_MIN_SNR_dB (22.0f)
 - #define SUBFRAME_USRR_NUM_CHIRPTYPES (3U)

4.13.1 Macro Definition Documentation

4.13.1.1 CHIRP_USRR_0_ADC_START_TIME_VAL

```
#define CHIRP_USRR_0_ADC_START_TIME_VAL (0U)
```

Definition at line 65 of file config_chirp_design_USRR30.h.

4.13.1.2 CHIRP_USRR_0_END_INDEX

```
#define CHIRP_USRR_0_END_INDEX (256U + 0U)
```

Definition at line 61 of file config_chirp_design_USRR30.h.

4.13.1.3 CHIRP_USRR_0_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_0_FREQ_SLOPE_VAL (0U)
```

Definition at line 63 of file config_chirp_design_USRR30.h.

4.13.1.4 CHIRP_USRR_0_IDLE_TIME_VAL

```
#define CHIRP_USRR_0_IDLE_TIME_VAL (0U)
```

Definition at line 64 of file config_chirp_design_USRR30.h.

4.13.1.5 CHIRP_USRR_0_PROFILE_ID

```
#define CHIRP_USRR_0_PROFILE_ID (1U)
```

Definition at line 59 of file config_chirp_design_USRR30.h.

4.13.1.6 CHIRP_USRR_0_START_FREQ_VAL

```
#define CHIRP_USRR_0_START_FREQ_VAL (0U)
```

Definition at line 62 of file config_chirp_design_USRR30.h.

4.13.1.7 CHIRP_USRR_0_START_INDEX

```
#define CHIRP_USRR_0_START_INDEX (256U + 0U)
```

Definition at line 60 of file config_chirp_design_USRR30.h.

4.13.1.8 CHIRP_USRR_0_TX_CHANNEL

```
#define CHIRP_USRR_0_TX_CHANNEL ( TX_CHANNEL_1_ENABLE )
```

Definition at line 66 of file config_chirp_design_USRR30.h.

4.13.1.9 CHIRP_USRR_1_ADC_START_TIME_VAL

```
#define CHIRP_USRR_1_ADC_START_TIME_VAL (0U)
```

Definition at line 74 of file config_chirp_design_USRR30.h.

4.13.1.10 CHIRP_USRR_1_END_INDEX

```
#define CHIRP_USRR_1_END_INDEX (256U + 1U)
```

Definition at line 70 of file config_chirp_design_USRR30.h.

4.13.1.11 CHIRP_USRR_1_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_1_FREQ_SLOPE_VAL (0U)
Definition at line 72 of file config_chirp_design_USRR30.h.
```

4.13.1.12 CHIRP_USRR_1_IDLE_TIME_VAL

```
#define CHIRP_USRR_1_IDLE_TIME_VAL (0U)
Definition at line 73 of file config_chirp_design_USRR30.h.
```

4.13.1.13 CHIRP_USRR_1_PROFILE_ID

```
#define CHIRP_USRR_1_PROFILE_ID (1U)
Definition at line 68 of file config_chirp_design_USRR30.h.
```

4.13.1.14 CHIRP_USRR_1_START_FREQ_VAL

```
#define CHIRP_USRR_1_START_FREQ_VAL (0U)
Definition at line 71 of file config_chirp_design_USRR30.h.
```

4.13.1.15 CHIRP_USRR_1_START_INDEX

```
#define CHIRP_USRR_1_START_INDEX (256U + 1U)
Definition at line 69 of file config_chirp_design_USRR30.h.
```

4.13.1.16 CHIRP_USRR_1_TX_CHANNEL

```
#define CHIRP_USRR_1_TX_CHANNEL ( TX_CHANNEL_2_ENABLE )
Definition at line 75 of file config_chirp_design_USRR30.h.
```

4.13.1.17 CHIRP_USRR_2_ADC_START_TIME_VAL

```
#define CHIRP_USRR_2_ADC_START_TIME_VAL (0U)
Definition at line 84 of file config_chirp_design_USRR30.h.
```

4.13.1.18 CHIRP_USRR_2_END_INDEX

```
#define CHIRP_USRR_2_END_INDEX (256U + 2U)
Definition at line 80 of file config_chirp_design_USRR30.h.
```

4.13.1.19 CHIRP_USRR_2_FREQ_SLOPE_VAL

```
#define CHIRP_USRR_2_FREQ_SLOPE_VAL (0U)
Definition at line 82 of file config_chirp_design_USRR30.h.
```

4.13.1.20 CHIRP_USRR_2_IDLE_TIME_VAL

```
#define CHIRP_USRR_2_IDLE_TIME_VAL (0U)
Definition at line 83 of file config_chirp_design_USRR30.h.
```

4.13.1.21 CHIRP_USRR_2_PROFILE_ID

```
#define CHIRP_USRR_2_PROFILE_ID (1U)
Definition at line 78 of file config_chirp_design_USRR30.h.
```

4.13.1.22 CHIRP_USRR_2_START_FREQ_VAL

```
#define CHIRP_USRR_2_START_FREQ_VAL (0U)
Definition at line 81 of file config_chirp_design_USRR30.h.
```

4.13.1.23 CHIRP_USRR_2_START_INDEX

```
#define CHIRP_USRR_2_START_INDEX (256U + 2U)
Definition at line 79 of file config_chirp_design_USRR30.h.
```

4.13.1.24 CHIRP_USRR_2_TX_CHANNEL

```
#define CHIRP_USRR_2_TX_CHANNEL ( TX_CHANNEL_3_ENABLE )
Definition at line 85 of file config_chirp_design_USRR30.h.
```

4.13.1.25 PROFILE_USRR_ADC_SAMPLE_VAL

```
#define PROFILE_USRR_ADC_SAMPLE_VAL (512U)
Definition at line 46 of file config_chirp_design_USRR30.h.
```

4.13.1.26 PROFILE_USRR_ADC_START_TIME_VAL

```
#define PROFILE_USRR_ADC_START_TIME_VAL (300U)
Definition at line 56 of file config_chirp_design_USRR30.h.
```

4.13.1.27 PROFILE_USRR_DIGOUT_SAMPLERATE_VAL

```
#define PROFILE_USRR_DIGOUT_SAMPLERATE_VAL (12500U)
Definition at line 45 of file config_chirp_design_USRR30.h.
```

4.13.1.28 PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US

```
#define PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US (56.25f)
Definition at line 53 of file config_chirp_design_USRR30.h.
```

4.13.1.29 PROFILE_USRR_FREQ_SLOPE_VAL

```
#define PROFILE_USRR_FREQ_SLOPE_VAL ( CONV_SLOPE_MHZ_PER_US_TO_CODEWORD( PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US ) )
Definition at line 54 of file config_chirp_design_USRR30.h.
```

4.13.1.30 PROFILE_USRR_HPFCORNER_FREQ1_VAL

```
#define PROFILE_USRR_HPFCORNER_FREQ1_VAL RL_RX_HPF1_175_KHz
Definition at line 42 of file config_chirp_design_USRR30.h.
```

4.13.1.31 PROFILE_USRR_HPFCORNER_FREQ2_VAL

```
#define PROFILE_USRR_HPFCORNER_FREQ2_VAL RL_RX_HPF2_350_KHz
Definition at line 43 of file config_chirp_design_USRR30.h.
```

4.13.1.32 PROFILE_USRR_IDLE_TIME_VAL

```
#define PROFILE_USRR_IDLE_TIME_VAL (500U)
Definition at line 47 of file config_chirp_design_USRR30.h.
```

4.13.1.33 PROFILE_USRR_LAMBDA_MILLIMETER

```
#define PROFILE_USRR_LAMBDA_MILLIMETER ( SPEED_OF_LIGHT_IN_METERS_PER_USEC / PROFILE_USRR_STA-
RT_FREQ_GHZ )
Definition at line 57 of file config_chirp_design_USRR30.h.
```

4.13.1.34 PROFILE_USRR_PROFILE_ID

```
#define PROFILE_USRR_PROFILE_ID (1U)
Ultra short range chirp profile - 20 m range, 4.3cm resolution. better angular resolution, approximately 18kmph max
- vel.
Definition at line 41 of file config_chirp_design_USRR30.h.
```

4.13.1.35 PROFILE_USRR_RAMP_END_TIME_VAL

```
#define PROFILE_USRR_RAMP_END_TIME_VAL (4400U)
Definition at line 48 of file config_chirp_design_USRR30.h.
```

4.13.1.36 PROFILE_USRR_RANGE_RESOLUTION_METERS

```
#define PROFILE_USRR_RANGE_RESOLUTION_METERS (( SPEED_OF_LIGHT_IN_METERS_PER_USEC * PROFILE_USRR_DIGOUT_SAMPLERATE_VAL ) / (2000.0f * PROFILE_USRR_FREQ_SLOPE_MHZ_PER_US * SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES ))
Derived parameters.
Definition at line 105 of file config_chirp_design_USRR30.h.
```

4.13.1.37 PROFILE_USRR_RX_GAIN_VAL

```
#define PROFILE_USRR_RX_GAIN_VAL (30U)
Definition at line 44 of file config_chirp_design_USRR30.h.
```

4.13.1.38 PROFILE_USRR_START_FREQ_GHZ

```
#define PROFILE_USRR_START_FREQ_GHZ (77.01f)
Definition at line 49 of file config_chirp_design_USRR30.h.
```

4.13.1.39 PROFILE_USRR_START_FREQ_VAL

```
#define PROFILE_USRR_START_FREQ_VAL ( CONV_FREQ_GHZ_TO_CODEWORD( PROFILE_USRR_START_FREQ_GHZ ) )
Definition at line 50 of file config_chirp_design_USRR30.h.
```

4.13.1.40 PROFILE_USRR_TX_START_TIME_VAL

```
#define PROFILE_USRR_TX_START_TIME_VAL (100U)  
Definition at line 55 of file config_chirp_design_USRR30.h.
```

4.13.1.41 PROFILE_USRR_TXOUT_POWER_BACKOFF

```
#define PROFILE_USRR_TXOUT_POWER_BACKOFF (0U)  
Definition at line 51 of file config_chirp_design_USRR30.h.
```

4.13.1.42 PROFILE_USRR_TXPHASESHIFTER_VAL

```
#define PROFILE_USRR_TXPHASESHIFTER_VAL (0U)  
Definition at line 52 of file config_chirp_design_USRR30.h.
```

4.13.1.43 SUBFRAME_USRR_CHIRP_END_IDX

```
#define SUBFRAME_USRR_CHIRP_END_IDX ( CHIRP_USRR_2_END_INDEX)  
Definition at line 89 of file config_chirp_design_USRR30.h.
```

4.13.1.44 SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US

```
#define SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US ((( PROFILE_USRR_IDLE_TIME_VAL + PROFILE_USRR_RAMP_END_TIME_VAL)/100.0f))  
Definition at line 108 of file config_chirp_design_USRR30.h.
```

4.13.1.45 SUBFRAME_USRR_CHIRP_START_IDX

```
#define SUBFRAME_USRR_CHIRP_START_IDX ( CHIRP_USRR_0_START_INDEX)  
Definition at line 88 of file config_chirp_design_USRR30.h.
```

4.13.1.46 SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS (( CHIRP_USRR_0_END_INDEX - CHIRP_USRR_0_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)  
Definition at line 95 of file config_chirp_design_USRR30.h.
```

4.13.1.47 SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_1_NUM_CHIRPS (( CHIRP_USRR_1_END_INDEX - CHIRP_USRR_1_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)  
Definition at line 96 of file config_chirp_design_USRR30.h.
```

4.13.1.48 SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS

```
#define SUBFRAME_USRR_CHIRPTYPE_2_NUM_CHIRPS (( CHIRP_USRR_2_END_INDEX - CHIRP_USRR_2_START_INDEX + 1)* SUBFRAME_USRR_LOOP_COUNT)  
Definition at line 97 of file config_chirp_design_USRR30.h.
```

4.13.1.49 SUBFRAME_USRR_LOOP_COUNT

```
#define SUBFRAME_USRR_LOOP_COUNT (32U)
```

Definition at line 90 of file config_chirp_design_USRR30.h.

4.13.1.50 SUBFRAME_USRR_MAX_VEL_M_P_S

```
#define SUBFRAME_USRR_MAX_VEL_M_P_S ( SUBFRAME_USRR_VEL_RESOLUTION_M_P_S* SUBFRAME_USRR_CHIRP_TYPE_0_NUM_CHIRPS/2)
```

Definition at line 111 of file config_chirp_design_USRR30.h.

4.13.1.51 SUBFRAME_USRR_MIN_SNR_dB

```
#define SUBFRAME_USRR_MIN_SNR_dB (22.0f)
```

Definition at line 113 of file config_chirp_design_USRR30.h.

4.13.1.52 SUBFRAME_USRR_NUM_ANGLE_BINS

```
#define SUBFRAME_USRR_NUM_ANGLE_BINS (64U)
```

Definition at line 101 of file config_chirp_design_USRR30.h.

4.13.1.53 SUBFRAME_USRR_NUM_CHIRPS_TOTAL

```
#define SUBFRAME_USRR_NUM_CHIRPS_TOTAL (( SUBFRAME_USRR_CHIRP_END_IDX - SUBFRAME_USRR_CHIRP_START_IDX + 1) * SUBFRAME_USRR_LOOP_COUNT)
```

Definition at line 102 of file config_chirp_design_USRR30.h.

4.13.1.54 SUBFRAME_USRR_NUM_CHIRPTYPES

```
#define SUBFRAME_USRR_NUM_CHIRPTYPES (3U)
```

Definition at line 115 of file config_chirp_design_USRR30.h.

4.13.1.55 SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES

```
#define SUBFRAME_USRR_NUM_CMPLX_ADC_SAMPLES ( PROFILE_USRR_ADC_SAMPLE_VAL)
```

Definition at line 94 of file config_chirp_design_USRR30.h.

4.13.1.56 SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES

```
#define SUBFRAME_USRR_NUM_REAL_ADC_SAMPLES ( PROFILE_USRR_ADC_SAMPLE_VAL * 2)
```

Definition at line 93 of file config_chirp_design_USRR30.h.

4.13.1.57 SUBFRAME_USRR_NUM_TX

```
#define SUBFRAME_USRR_NUM_TX (3U)
```

Definition at line 99 of file config_chirp_design_USRR30.h.

4.13.1.58 SUBFRAME_USRR_NUM_VIRT_ANT

```
#define SUBFRAME_USRR_NUM_VIRT_ANT ( SUBFRAME_USRR_NUM_TX* NUM_RX_CHANNELS)
```

Definition at line 100 of file config_chirp_design_USRR30.h.

4.13.1.59 SUBFRAME_USRR_PERIODICITY_VAL

```
#define SUBFRAME_USRR_PERIODICITY_VAL (6000000U)
Definition at line 91 of file config_chirp_design_USRR30.h.
```

4.13.1.60 SUBFRAME_USRR_TRIGGER_DELAY_VAL

```
#define SUBFRAME_USRR_TRIGGER_DELAY_VAL (0U)
Definition at line 92 of file config_chirp_design_USRR30.h.
```

4.13.1.61 SUBFRAME_USRR_VEL_RESOLUTION_M_P_S

```
#define SUBFRAME_USRR_VEL_RESOLUTION_M_P_S (((1000.0f/(3.0f* SUBFRAME_USRR_CHIRP_REPETITION_PERIOD_US)) / SUBFRAME_USRR_CHIRPTYPE_0_NUM_CHIRPS)*( PROFILE_USRR_LAMBDA_MILLIMETER/2.0f))
Definition at line 110 of file config_chirp_design_USRR30.h.
```

4.14 Debug/clusteringDBscan.d File Reference

4.15 Debug/common/frame_cfg.d File Reference

4.16 Debug/configPkg/package/cfg/dss_mrr_pe674.c File Reference

```
#include <xdc/std.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/family/c62/IntrinsicsSupport.h>
#include <ti/sysbios/family/c62/TaskSupport.h>
#include <ti/sysbios/family/c64p/Cache.h>
#include <ti/sysbios/family/c64p/EventCombiner.h>
#include <ti/sysbios/family/c64p/Exception.h>
#include <ti/sysbios/family/c64p/Hwi.h>
#include <ti/sysbios/family/c64p/TimestampProvider.h>
#include <ti/sysbios/gates/GateHwi.h>
#include <ti/sysbios/gates/GateMutex.h>
#include <ti/sysbios/hal/Hwi.h>
#include <ti/sysbios/heaps/HeapMem.h>
#include <ti/sysbios/knl/Clock.h>
#include <ti/sysbios/knl/Event.h>
#include <ti/sysbios/knl/Idle.h>
#include <ti/sysbios/knl/Intrinsics.h>
#include <ti/sysbios/knl/Queue.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/rts/ti/ThreadLocalStorage.h>
#include <ti/sysbios/timers/rti/Timer.h>
#include <ti/sysbios/utils/Load.h>
#include <xdc/runtime/Assert.h>
#include <xdc/runtime/Core.h>
#include <xdc/runtime/Defaults.h>
#include <xdc/runtime/Diags.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/Gate.h>
#include <xdc/runtime/Log.h>
#include <xdc/runtime/Main.h>
#include <xdc/runtime/Memory.h>
#include <xdc/runtime/Registry.h>
```

```
#include <xdc/runtime/Startup.h>
#include <xdc/runtime/SysStd.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Text.h>
#include <xdc/runtime/Timestamp.h>
#include <xdc/runtime/TimestampNull.h>
#include <limits.h>
#include <xdc/runtime/Types.h>
#include <xdc/runtime/System_internal.h>
#include <_lock.h>
#include <ti/sysbios/hal/Core.h>
#include <string.h>
Include dependency graph for dss_mrr_pe674.c:
```



Data Structures

- struct **ti_sysbios_BIOS_RtsGateProxy_Module**_
- struct **ti_sysbios_knl_Queue_Object**_
- struct **ti_sysbios_knl_Queue_Object2**_
- struct **ti_sysbios_knl_Semaphore_Object**_
- struct **ti_sysbios_knl_Semaphore_Object2**_
- struct **ti_sysbios_gates_GateMutex_Object**_
- struct **ti_sysbios_gates_GateMutex_Object2**_
- struct **ti_sysbios_BIOS_RtsGateProxy_Object2**_
- struct **ti_sysbios_family_c64p_Hwi_Module**_
- struct **ti_sysbios_family_c64p_Hwi_Object**_
- struct **ti_sysbios_family_c64p_Hwi_Object2**_
- struct **ti_sysbios_gates_GateHwi_Module**_
- struct **ti_sysbios_gates_GateHwi_Object**_
- struct **ti_sysbios_gates_GateHwi_Object2**_
- struct **ti_sysbios_gates_GateMutex_Module**_
- struct **ti_sysbios_hal_Hwi_Module**_
- struct **ti_sysbios_hal_Hwi_Object**_
- struct **ti_sysbios_hal_Hwi_Object2**_
- struct **ti_sysbios_hal_Hwi_HwiProxy_Module**_
- struct **ti_sysbios_hal_Hwi_HwiProxy_Object2**_
- struct **ti_sysbios_heaps_HeapMem_Module**_
- struct **ti_sysbios_heaps_HeapMem_Object**_
- struct **ti_sysbios_heaps_HeapMem_Object2**_
- struct **ti_sysbios_heaps_HeapMem_Module_GateProxy_Module**_
- struct **ti_sysbios_heaps_HeapMem_Module_GateProxy_Object2**_
- struct **ti_sysbios_knl_Clock_Module**_
- struct **ti_sysbios_knl_Clock_Object**_
- struct **ti_sysbios_knl_Clock_Object2**_
- struct **ti_sysbios_knl_Clock_TimerProxy_Module**_
- struct **ti_sysbios_timers_rti_Timer_Object**_
- struct **ti_sysbios_timers_rti_Timer_Object2**_
- struct **ti_sysbios_knl_Clock_TimerProxy_Object2**_
- struct **ti_sysbios_knl_Event_Module**_
- struct **ti_sysbios_knl_Event_Object**_
- struct **ti_sysbios_knl_Event_Object2**_
- struct **ti_sysbios_knl_Queue_Module**_
- struct **ti_sysbios_knl_Semaphore_Module**_

- struct `ti_sysbios_knl_Task_Module`__
- struct `ti_sysbios_knl_Task_Object`__
- struct `ti_sysbios_knl_Task_Object2`__
- struct `ti_sysbios_timers_rti_Timer_Module`__
- struct `xdc_runtime_Main_Module_GateProxy_Module`__
- struct `xdc_runtime_Main_Module_GateProxy_Object2`__
- struct `xdc_runtime_Memory_HeapProxy_Module`__
- struct `xdc_runtime_Memory_HeapProxy_Object2`__
- struct `xdc_runtime_System_Module_GateProxy_Module`__
- struct `xdc_runtime_System_Module_GateProxy_Object2`__
- struct `ti_sysbios_BIOS_Module_State`__
- struct `ti_sysbios_family_c64p_Cache_Module_State`__
- struct `ti_sysbios_family_c64p_EventCombiner_Module_State`__
- struct `ti_sysbios_family_c64p_Exception_Module_State`__
- struct `ti_sysbios_family_c64p_Hwi_Module_State`__
- struct `ti_sysbios_knl_Clock_Module_State`__
- struct `ti_sysbios_knl_Task_Module_State`__
- struct `ti_sysbios_rts_ti_ThreadLocalStorage_Module_State`__
- struct `ti_sysbios_timers_rti_Timer_Module_State`__
- struct `ti_sysbios_utils_Load_Module_State`__
- struct `xdc_runtime_Error_Module_State`__
- struct `xdc_runtime_Memory_Module_State`__
- struct `xdc_runtime_Registry_Module_State`__
- struct `xdc_runtime_Startup_Module_State`__
- struct `xdc_runtime_System_Module_State`__
- struct `xdc_runtime_Text_Module_State`__
- union `Header`
- struct `ti_sysbios_family_c64p_Hwi_S1`
- struct `ti_sysbios_gates_GateHwi_S1`
- struct `ti_sysbios_gates_GateMutex_S1`
- struct `ti_sysbios_hal_Hwi_S1`
- struct `ti_sysbios_heaps_HeapMem_S1`
- struct `ti_sysbios_knl_Clock_S1`
- struct `ti_sysbios_knl_Event_S1`
- struct `ti_sysbios_knl_Queue_S1`
- struct `ti_sysbios_knl_Semaphore_S1`
- struct `ti_sysbios_knl_Task_S1`
- struct `ti_sysbios_timers_rti_Timer_S1`

Macros

- `#define __nested__`
- `#define __config__`
- `#define ATTRIBUTE __attribute__ ((used))`
- `#define Module_MID ti_sysbios_BIOS_RtsGateProxy_Module_id_C`
- `#define Module_DGSINCL ti_sysbios_BIOS_RtsGateProxy_Module_diagsIncluded_C`
- `#define Module_DGSENAB ti_sysbios_BIOS_RtsGateProxy_Module_diagsEnabled_C`
- `#define Module_DGSMASK ti_sysbios_BIOS_RtsGateProxy_Module_diagsMask_C`
- `#define Module_LOGDEF ti_sysbios_BIOS_RtsGateProxy_Module_loggerDefined_C`
- `#define Module_LOGOBJ ti_sysbios_BIOS_RtsGateProxy_Module_loggerObj_C`
- `#define Module_LOGFXN0 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn0_C`
- `#define Module_LOGFXN1 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn1_C`
- `#define Module_LOGFXN2 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn2_C`
- `#define Module_LOGFXN4 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn4_C`
- `#define Module_LOGFXN8 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn8_C`

```
• #define Module__G_OBJ ti_sysbios_BIOS_RtsGateProxy_Module_gateObj_C
• #define Module__G_PRMS ti_sysbios_BIOS_RtsGateProxy_Module_gatePrms_C
• #define Module__GP_create ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_query
• #define Module__MID ti_sysbios_family_c64p_Hwi_Module_id_C
• #define Module__DGSINCL ti_sysbios_family_c64p_Hwi_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_family_c64p_Hwi_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_family_c64p_Hwi_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_family_c64p_Hwi_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_family_c64p_Hwi_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_family_c64p_Hwi_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_family_c64p_Hwi_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_family_c64p_Hwi_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_family_c64p_Hwi_Module_loggerFxn4_C
• #define Module__LOGFXN8 ti_sysbios_family_c64p_Hwi_Module_loggerFxn8_C
• #define Module__G_OBJ ti_sysbios_family_c64p_Hwi_Module_gateObj_C
• #define Module__G_PRMS ti_sysbios_family_c64p_Hwi_Module_gatePrms_C
• #define Module__GP_create ti_sysbios_family_c64p_Hwi_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_family_c64p_Hwi_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_family_c64p_Hwi_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_family_c64p_Hwi_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_family_c64p_Hwi_Module_GateProxy_query
• #define Module__MID ti_sysbios_gates_GateHwi_Module_id_C
• #define Module__DGSINCL ti_sysbios_gates_GateHwi_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_gates_GateHwi_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_gates_GateHwi_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_gates_GateHwi_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_gates_GateHwi_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_gates_GateHwi_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_gates_GateHwi_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_gates_GateHwi_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_gates_GateHwi_Module_loggerFxn4_C
• #define Module__LOGFXN8 ti_sysbios_gates_GateHwi_Module_loggerFxn8_C
• #define Module__G_OBJ ti_sysbios_gates_GateHwi_Module_gateObj_C
• #define Module__G_PRMS ti_sysbios_gates_GateHwi_Module_gatePrms_C
• #define Module__GP_create ti_sysbios_gates_GateHwi_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_gates_GateHwi_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_gates_GateHwi_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_gates_GateHwi_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_gates_GateHwi_Module_GateProxy_query
• #define Module__MID ti_sysbios_gates_GateMutex_Module_id_C
• #define Module__DGSINCL ti_sysbios_gates_GateMutex_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_gates_GateMutex_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_gates_GateMutex_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_gates_GateMutex_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_gates_GateMutex_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_gates_GateMutex_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_gates_GateMutex_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_gates_GateMutex_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_gates_GateMutex_Module_loggerFxn4_C
• #define Module__LOGFXN8 ti_sysbios_gates_GateMutex_Module_loggerFxn8_C
• #define Module__G_OBJ ti_sysbios_gates_GateMutex_Module_gateObj_C
```

```
• #define Module__G_PRMS ti_sysbios_gates_GateMutex_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_gates_GateMutex_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_gates_GateMutex_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_gates_GateMutex_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_gates_GateMutex_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_gates_GateMutex_Module_GateProxy_query
• #define Module__MID ti_sysbios_hal_Hwi_Module_id__C
• #define Module__DGSINCL ti_sysbios_hal_Hwi_Module_diagsIncluded__C
• #define Module__DGSENAB ti_sysbios_hal_Hwi_Module_diagsEnabled__C
• #define Module__DGSMASK ti_sysbios_hal_Hwi_Module_diagsMask__C
• #define Module__LOGDEF ti_sysbios_hal_Hwi_Module_loggerDefined__C
• #define Module__LOGOBJ ti_sysbios_hal_Hwi_Module_loggerObj__C
• #define Module__LOGFXN0 ti_sysbios_hal_Hwi_Module_loggerFxn0__C
• #define Module__LOGFXN1 ti_sysbios_hal_Hwi_Module_loggerFxn1__C
• #define Module__LOGFXN2 ti_sysbios_hal_Hwi_Module_loggerFxn2__C
• #define Module__LOGFXN4 ti_sysbios_hal_Hwi_Module_loggerFxn4__C
• #define Module__LOGFXN8 ti_sysbios_hal_Hwi_Module_loggerFxn8__C
• #define Module__G_OBJ ti_sysbios_hal_Hwi_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_hal_Hwi_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_hal_Hwi_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_hal_Hwi_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_hal_Hwi_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_hal_Hwi_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_hal_Hwi_Module_GateProxy_query
• #define Module__MID ti_sysbios_hal_Hwi_HwiProxy_Module_id__C
• #define Module__DGSINCL ti_sysbios_hal_Hwi_HwiProxy_Module_diagsIncluded__C
• #define Module__DGSENAB ti_sysbios_hal_Hwi_HwiProxy_Module_diagsEnabled__C
• #define Module__DGSMASK ti_sysbios_hal_Hwi_HwiProxy_Module_diagsMask__C
• #define Module__LOGDEF ti_sysbios_hal_Hwi_HwiProxy_Module_loggerDefined__C
• #define Module__LOGOBJ ti_sysbios_hal_Hwi_HwiProxy_Module_loggerObj__C
• #define Module__LOGFXN0 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn0__C
• #define Module__LOGFXN1 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn1__C
• #define Module__LOGFXN2 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn2__C
• #define Module__LOGFXN4 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn4__C
• #define Module__LOGFXN8 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn8__C
• #define Module__G_OBJ ti_sysbios_hal_Hwi_HwiProxy_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_hal_Hwi_HwiProxy_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_query
• #define Module__MID ti_sysbios_heaps_HeapMem_Module_id__C
• #define Module__DGSINCL ti_sysbios_heaps_HeapMem_Module_diagsIncluded__C
• #define Module__DGSENAB ti_sysbios_heaps_HeapMem_Module_diagsEnabled__C
• #define Module__DGSMASK ti_sysbios_heaps_HeapMem_Module_diagsMask__C
• #define Module__LOGDEF ti_sysbios_heaps_HeapMem_Module_loggerDefined__C
• #define Module__LOGOBJ ti_sysbios_heaps_HeapMem_Module_loggerObj__C
• #define Module__LOGFXN0 ti_sysbios_heaps_HeapMem_Module_loggerFxn0__C
• #define Module__LOGFXN1 ti_sysbios_heaps_HeapMem_Module_loggerFxn1__C
• #define Module__LOGFXN2 ti_sysbios_heaps_HeapMem_Module_loggerFxn2__C
• #define Module__LOGFXN4 ti_sysbios_heaps_HeapMem_Module_loggerFxn4__C
• #define Module__LOGFXN8 ti_sysbios_heaps_HeapMem_Module_loggerFxn8__C
• #define Module__G_OBJ ti_sysbios_heaps_HeapMem_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_heaps_HeapMem_Module_gatePrms__C
```

```
• #define Module__GP_create ti_sysbios_heaps_HeapMem_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_heaps_HeapMem_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_heaps_HeapMem_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_heaps_HeapMem_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_heaps_HeapMem_Module_GateProxy_query
• #define Module__MID ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_id_C
• #define Module__DGSINCL ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn4_C
• #define Module__LOGFXN8 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn8_C
• #define Module__G_OBJ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_gateObj_C
• #define Module__G_PRMS ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_gatePrms_C
• #define Module__GP_create ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_query
• #define Module__MID ti_sysbios_knl_Clock_Module_id_C
• #define Module__DGSINCL ti_sysbios_knl_Clock_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_knl_Clock_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_knl_Clock_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_knl_Clock_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_knl_Clock_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_knl_Clock_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_knl_Clock_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_knl_Clock_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_knl_Clock_Module_loggerFxn4_C
• #define Module__LOGFXN8 ti_sysbios_knl_Clock_Module_loggerFxn8_C
• #define Module__G_OBJ ti_sysbios_knl_Clock_Module_gateObj_C
• #define Module__G_PRMS ti_sysbios_knl_Clock_Module_gatePrms_C
• #define Module__GP_create ti_sysbios_knl_Clock_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_knl_Clock_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_knl_Clock_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_knl_Clock_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_knl_Clock_Module_GateProxy_query
• #define Module__MID ti_sysbios_knl_Clock_TimerProxy_Module_id_C
• #define Module__DGSINCL ti_sysbios_knl_Clock_TimerProxy_Module_diagsIncluded_C
• #define Module__DGSENAB ti_sysbios_knl_Clock_TimerProxy_Module_diagsEnabled_C
• #define Module__DGSMASK ti_sysbios_knl_Clock_TimerProxy_Module_diagsMask_C
• #define Module__LOGDEF ti_sysbios_knl_Clock_TimerProxy_Module_loggerDefined_C
• #define Module__LOGOBJ ti_sysbios_knl_Clock_TimerProxy_Module_loggerObj_C
• #define Module__LOGFXN0 ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn0_C
• #define Module__LOGFXN1 ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn1_C
• #define Module__LOGFXN2 ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn2_C
• #define Module__LOGFXN4 ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn4_C
```

- #define **Module__LOGFXN8** ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn8_C
- #define **Module__G_OBJ** ti_sysbios_knl_Clock_TimerProxy_Module_gateObj_C
- #define **Module__G_PRMS** ti_sysbios_knl_Clock_TimerProxy_Module_gatePrms_C
- #define **Module__GP_create** ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_create
- #define **Module__GP_delete** ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_delete
- #define **Module__GP_enter** ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_enter
- #define **Module__GP_leave** ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_leave
- #define **Module__GP_query** ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_query
- #define **Module__MID** ti_sysbios_knl_Event_Module_id_C
- #define **Module__DGSINCL** ti_sysbios_knl_Event_Module_diagsIncluded_C
- #define **Module__DGSENAB** ti_sysbios_knl_Event_Module_diagsEnabled_C
- #define **Module__DGSMASK** ti_sysbios_knl_Event_Module_diagsMask_C
- #define **Module__LOGDEF** ti_sysbios_knl_Event_Module_loggerDefined_C
- #define **Module__LOGOBJ** ti_sysbios_knl_Event_Module_loggerObj_C
- #define **Module__LOGFXN0** ti_sysbios_knl_Event_Module_loggerFxn0_C
- #define **Module__LOGFXN1** ti_sysbios_knl_Event_Module_loggerFxn1_C
- #define **Module__LOGFXN2** ti_sysbios_knl_Event_Module_loggerFxn2_C
- #define **Module__LOGFXN4** ti_sysbios_knl_Event_Module_loggerFxn4_C
- #define **Module__LOGFXN8** ti_sysbios_knl_Event_Module_loggerFxn8_C
- #define **Module__G_OBJ** ti_sysbios_knl_Event_Module_gateObj_C
- #define **Module__G_PRMS** ti_sysbios_knl_Event_Module_gatePrms_C
- #define **Module__GP_create** ti_sysbios_knl_Event_Module_GateProxy_create
- #define **Module__GP_delete** ti_sysbios_knl_Event_Module_GateProxy_delete
- #define **Module__GP_enter** ti_sysbios_knl_Event_Module_GateProxy_enter
- #define **Module__GP_leave** ti_sysbios_knl_Event_Module_GateProxy_leave
- #define **Module__GP_query** ti_sysbios_knl_Event_Module_GateProxy_query
- #define **Module__MID** ti_sysbios_knl_Queue_Module_id_C
- #define **Module__DGSINCL** ti_sysbios_knl_Queue_Module_diagsIncluded_C
- #define **Module__DGSENAB** ti_sysbios_knl_Queue_Module_diagsEnabled_C
- #define **Module__DGSMASK** ti_sysbios_knl_Queue_Module_diagsMask_C
- #define **Module__LOGDEF** ti_sysbios_knl_Queue_Module_loggerDefined_C
- #define **Module__LOGOBJ** ti_sysbios_knl_Queue_Module_loggerObj_C
- #define **Module__LOGFXN0** ti_sysbios_knl_Queue_Module_loggerFxn0_C
- #define **Module__LOGFXN1** ti_sysbios_knl_Queue_Module_loggerFxn1_C
- #define **Module__LOGFXN2** ti_sysbios_knl_Queue_Module_loggerFxn2_C
- #define **Module__LOGFXN4** ti_sysbios_knl_Queue_Module_loggerFxn4_C
- #define **Module__LOGFXN8** ti_sysbios_knl_Queue_Module_loggerFxn8_C
- #define **Module__G_OBJ** ti_sysbios_knl_Queue_Module_gateObj_C
- #define **Module__G_PRMS** ti_sysbios_knl_Queue_Module_gatePrms_C
- #define **Module__GP_create** ti_sysbios_knl_Queue_Module_GateProxy_create
- #define **Module__GP_delete** ti_sysbios_knl_Queue_Module_GateProxy_delete
- #define **Module__GP_enter** ti_sysbios_knl_Queue_Module_GateProxy_enter
- #define **Module__GP_leave** ti_sysbios_knl_Queue_Module_GateProxy_leave
- #define **Module__GP_query** ti_sysbios_knl_Queue_Module_GateProxy_query
- #define **Module__MID** ti_sysbios_knl_Semaphore_Module_id_C
- #define **Module__DGSINCL** ti_sysbios_knl_Semaphore_Module_diagsIncluded_C
- #define **Module__DGSENAB** ti_sysbios_knl_Semaphore_Module_diagsEnabled_C
- #define **Module__DGSMASK** ti_sysbios_knl_Semaphore_Module_diagsMask_C
- #define **Module__LOGDEF** ti_sysbios_knl_Semaphore_Module_loggerDefined_C
- #define **Module__LOGOBJ** ti_sysbios_knl_Semaphore_Module_loggerObj_C
- #define **Module__LOGFXN0** ti_sysbios_knl_Semaphore_Module_loggerFxn0_C
- #define **Module__LOGFXN1** ti_sysbios_knl_Semaphore_Module_loggerFxn1_C
- #define **Module__LOGFXN2** ti_sysbios_knl_Semaphore_Module_loggerFxn2_C
- #define **Module__LOGFXN4** ti_sysbios_knl_Semaphore_Module_loggerFxn4_C
- #define **Module__LOGFXN8** ti_sysbios_knl_Semaphore_Module_loggerFxn8_C

```

• #define Module__G_OBJ ti_sysbios_knl_Semaphore_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_knl_Semaphore_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_knl_Semaphore_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_knl_Semaphore_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_knl_Semaphore_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_knl_Semaphore_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_knl_Semaphore_Module_GateProxy_query
• #define Module__MID ti_sysbios_knl_Task_Module_id__C
• #define Module__DGSINCL ti_sysbios_knl_Task_Module_diagsIncluded__C
• #define Module__DGSENAB ti_sysbios_knl_Task_Module_diagsEnabled__C
• #define Module__DGSMASK ti_sysbios_knl_Task_Module_diagsMask__C
• #define Module__LOGDEF ti_sysbios_knl_Task_Module_loggerDefined__C
• #define Module__LOGOBJ ti_sysbios_knl_Task_Module_loggerObj__C
• #define Module__LOGFXN0 ti_sysbios_knl_Task_Module_loggerFxn0__C
• #define Module__LOGFXN1 ti_sysbios_knl_Task_Module_loggerFxn1__C
• #define Module__LOGFXN2 ti_sysbios_knl_Task_Module_loggerFxn2__C
• #define Module__LOGFXN4 ti_sysbios_knl_Task_Module_loggerFxn4__C
• #define Module__LOGFXN8 ti_sysbios_knl_Task_Module_loggerFxn8__C
• #define Module__G_OBJ ti_sysbios_knl_Task_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_knl_Task_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_knl_Task_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_knl_Task_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_knl_Task_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_knl_Task_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_knl_Task_Module_GateProxy_query
• #define Module__MID ti_sysbios_timers_rti_Timer_Module_id__C
• #define Module__DGSINCL ti_sysbios_timers_rti_Timer_Module_diagsIncluded__C
• #define Module__DGSENAB ti_sysbios_timers_rti_Timer_Module_diagsEnabled__C
• #define Module__DGSMASK ti_sysbios_timers_rti_Timer_Module_diagsMask__C
• #define Module__LOGDEF ti_sysbios_timers_rti_Timer_Module_loggerDefined__C
• #define Module__LOGOBJ ti_sysbios_timers_rti_Timer_Module_loggerObj__C
• #define Module__LOGFXN0 ti_sysbios_timers_rti_Timer_Module_loggerFxn0__C
• #define Module__LOGFXN1 ti_sysbios_timers_rti_Timer_Module_loggerFxn1__C
• #define Module__LOGFXN2 ti_sysbios_timers_rti_Timer_Module_loggerFxn2__C
• #define Module__LOGFXN4 ti_sysbios_timers_rti_Timer_Module_loggerFxn4__C
• #define Module__LOGFXN8 ti_sysbios_timers_rti_Timer_Module_loggerFxn8__C
• #define Module__G_OBJ ti_sysbios_timers_rti_Timer_Module_gateObj__C
• #define Module__G_PRMS ti_sysbios_timers_rti_Timer_Module_gatePrms__C
• #define Module__GP_create ti_sysbios_timers_rti_Timer_Module_GateProxy_create
• #define Module__GP_delete ti_sysbios_timers_rti_Timer_Module_GateProxy_delete
• #define Module__GP_enter ti_sysbios_timers_rti_Timer_Module_GateProxy_enter
• #define Module__GP_leave ti_sysbios_timers_rti_Timer_Module_GateProxy_leave
• #define Module__GP_query ti_sysbios_timers_rti_Timer_Module_GateProxy_query
• #define Module__MID xdc_runtime_Main_Module_GateProxy_Module_id__C
• #define Module__DGSINCL xdc_runtime_Main_Module_GateProxy_Module_diagsIncluded__C
• #define Module__DGSENAB xdc_runtime_Main_Module_GateProxy_Module_diagsEnabled__C
• #define Module__DGSMASK xdc_runtime_Main_Module_GateProxy_Module_diagsMask__C
• #define Module__LOGDEF xdc_runtime_Main_Module_GateProxy_Module_loggerDefined__C
• #define Module__LOGOBJ xdc_runtime_Main_Module_GateProxy_Module_loggerObj__C
• #define Module__LOGFXN0 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn0__C
• #define Module__LOGFXN1 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn1__C
• #define Module__LOGFXN2 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn2__C
• #define Module__LOGFXN4 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn4__C
• #define Module__LOGFXN8 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn8__C
• #define Module__G_OBJ xdc_runtime_Main_Module_GateProxy_Module_gateObj__C

```

```

• #define Module__G_PRMS xdc_runtime_Main_Module_GateProxy_Module_gatePrms_C
• #define Module__GP_create xdc_runtime_Main_Module_GateProxy_Module_GateProxy_create
• #define Module__GP_delete xdc_runtime_Main_Module_GateProxy_Module_GateProxy_delete
• #define Module__GP_enter xdc_runtime_Main_Module_GateProxy_Module_GateProxy_enter
• #define Module__GP_leave xdc_runtime_Main_Module_GateProxy_Module_GateProxy_leave
• #define Module__GP_query xdc_runtime_Main_Module_GateProxy_Module_GateProxy_query
• #define Module__MID xdc_runtime_Memory_HeapProxy_Module_id_C
• #define Module__DGSINCL xdc_runtime_Memory_HeapProxy_Module_diagsIncluded_C
• #define Module__DGSENAB xdc_runtime_Memory_HeapProxy_Module_diagsEnabled_C
• #define Module__DGSMASK xdc_runtime_Memory_HeapProxy_Module_diagsMask_C
• #define Module__LOGDEF xdc_runtime_Memory_HeapProxy_Module_loggerDefined_C
• #define Module__LOGOBJ xdc_runtime_Memory_HeapProxy_Module_loggerObj_C
• #define Module__LOGFXN0 xdc_runtime_Memory_HeapProxy_Module_loggerFxn0_C
• #define Module__LOGFXN1 xdc_runtime_Memory_HeapProxy_Module_loggerFxn1_C
• #define Module__LOGFXN2 xdc_runtime_Memory_HeapProxy_Module_loggerFxn2_C
• #define Module__LOGFXN4 xdc_runtime_Memory_HeapProxy_Module_loggerFxn4_C
• #define Module__LOGFXN8 xdc_runtime_Memory_HeapProxy_Module_loggerFxn8_C
• #define Module__G_OBJ xdc_runtime_Memory_HeapProxy_Module_gateObj_C
• #define Module__G_PRMS xdc_runtime_Memory_HeapProxy_Module_gatePrms_C
• #define Module__GP_create xdc_runtime_Memory_HeapProxy_Module_GateProxy_create
• #define Module__GP_delete xdc_runtime_Memory_HeapProxy_Module_GateProxy_delete
• #define Module__GP_enter xdc_runtime_Memory_HeapProxy_Module_GateProxy_enter
• #define Module__GP_leave xdc_runtime_Memory_HeapProxy_Module_GateProxy_leave
• #define Module__GP_query xdc_runtime_Memory_HeapProxy_Module_GateProxy_query
• #define Module__MID xdc_runtime_System_Module_GateProxy_Module_id_C
• #define Module__DGSINCL xdc_runtime_System_Module_GateProxy_Module_diagsIncluded_C
• #define Module__DGSENAB xdc_runtime_System_Module_GateProxy_Module_diagsEnabled_C
• #define Module__DGSMASK xdc_runtime_System_Module_GateProxy_Module_diagsMask_C
• #define Module__LOGDEF xdc_runtime_System_Module_GateProxy_Module_loggerDefined_C
• #define Module__LOGOBJ xdc_runtime_System_Module_GateProxy_Module_loggerObj_C
• #define Module__LOGFXN0 xdc_runtime_System_Module_GateProxy_Module_loggerFxn0_C
• #define Module__LOGFXN1 xdc_runtime_System_Module_GateProxy_Module_loggerFxn1_C
• #define Module__LOGFXN2 xdc_runtime_System_Module_GateProxy_Module_loggerFxn2_C
• #define Module__LOGFXN4 xdc_runtime_System_Module_GateProxy_Module_loggerFxn4_C
• #define Module__LOGFXN8 xdc_runtime_System_Module_GateProxy_Module_loggerFxn8_C
• #define Module__G_OBJ xdc_runtime_System_Module_GateProxy_Module_gateObj_C
• #define Module__G_PRMS xdc_runtime_System_Module_GateProxy_Module_gatePrms_C
• #define Module__GP_create xdc_runtime_System_Module_GateProxy_Module_GateProxy_create
• #define Module__GP_delete xdc_runtime_System_Module_GateProxy_Module_GateProxy_delete
• #define Module__GP_enter xdc_runtime_System_Module_GateProxy_Module_GateProxy_enter
• #define Module__GP_leave xdc_runtime_System_Module_GateProxy_Module_GateProxy_leave
• #define Module__GP_query xdc_runtime_System_Module_GateProxy_Module_GateProxy_query

```

Typedefs

- `typedef struct ti_sysbios_BIOS_RtsGateProxy_Module__ ti_sysbios_BIOS_RtsGateProxy_Module__`
- `—`
- `typedef struct ti_sysbios_knl_Queue_Object__ ti_sysbios_knl_Queue_Object__`
- `typedef struct ti_sysbios_knl_Semaphore_Object__ ti_sysbios_knl_Semaphore_Object__`
- `typedef struct ti_sysbios_gates_GateMutex_Object__ ti_sysbios_gates_GateMutex_Object__`
- `typedef ti_sysbios_gates_GateMutex_Object__ ti_sysbios_BIOS_RtsGateProxy_Object__`
- `typedef struct ti_sysbios_family_c64p_Hwi_Module__ ti_sysbios_family_c64p_Hwi_Module__`
- `typedef struct ti_sysbios_family_c64p_Hwi_Object__ ti_sysbios_family_c64p_Hwi_Object__`
- `typedef struct ti_sysbios_gates_GateHwi_Module__ ti_sysbios_gates_GateHwi_Module__`
- `typedef struct ti_sysbios_gates_GateHwi_Object__ ti_sysbios_gates_GateHwi_Object__`

- `typedef struct ti_sysbios_gates_GateMutex_Module__ ti_sysbios_gates_GateMutex_Module__`
- `typedef struct ti_sysbios_hal_Hwi_Module__ ti_sysbios_hal_Hwi_Module__`
- `typedef struct ti_sysbios_hal_Hwi_Object__ ti_sysbios_hal_Hwi_Object__`
- `typedef struct ti_sysbios_hal_HwiProxy_Module__ ti_sysbios_hal_HwiProxy_Module__`
- `typedef ti_sysbios_family_c64p_Hwi_Object__ ti_sysbios_hal_HwiProxy_Object__`
- `typedef struct ti_sysbios_heaps_HeapMem_Module__ ti_sysbios_heaps_HeapMem_Module__`
- `typedef struct ti_sysbios_heaps_HeapMem_Object__ ti_sysbios_heaps_HeapMem_Object__`
- `typedef struct ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__`
- `typedef ti_sysbios_gates_GateMutex_Object__ ti_sysbios_heaps_HeapMem_Module_GateProxy_Object__`
- `typedef struct ti_sysbios_knl_Clock_Module__ ti_sysbios_knl_Clock_Module__`
- `typedef struct ti_sysbios_knl_Clock_Object__ ti_sysbios_knl_Clock_Object__`
- `typedef struct ti_sysbios_knl_Clock_TimerProxy_Module__ ti_sysbios_knl_Clock_TimerProxy_Module__`
- `typedef struct ti_sysbios_timers_rti_Timer_Object__ ti_sysbios_timers_rti_Timer_Object__`
- `typedef ti_sysbios_timers_rti_Timer_Object__ ti_sysbios_knl_Clock_TimerProxy_Object__`
- `typedef struct ti_sysbios_knl_Event_Module__ ti_sysbios_knl_Event_Module__`
- `typedef struct ti_sysbios_knl_Event_Object__ ti_sysbios_knl_Event_Object__`
- `typedef struct ti_sysbios_knl_Queue_Module__ ti_sysbios_knl_Queue_Module__`
- `typedef struct ti_sysbios_knl_Semaphore_Module__ ti_sysbios_knl_Semaphore_Module__`
- `typedef struct ti_sysbios_knl_Task_Module__ ti_sysbios_knl_Task_Module__`
- `typedef struct ti_sysbios_knl_Task_Object__ ti_sysbios_knl_Task_Object__`
- `typedef struct ti_sysbios_timers_rti_Timer_Module__ ti_sysbios_timers_rti_Timer_Module__`
- `typedef struct xdc_runtime_Main_Module_GateProxy_Module__ xdc_runtime_Main_Module_GateProxy_Module__`
- `typedef ti_sysbios_gates_GateHwi_Object__ xdc_runtime_Main_Module_GateProxy_Object__`
- `typedef struct xdc_runtime_Memory_HeapProxy_Module__ xdc_runtime_Memory_HeapProxy_Module__`
- `typedef ti_sysbios_heaps_HeapMem_Object__ xdc_runtime_Memory_HeapProxy_Object__`
- `typedef struct xdc_runtime_System_Module_GateProxy_Module__ xdc_runtime_System_Module_GateProxy_Module__`
- `typedef ti_sysbios_gates_GateHwi_Object__ xdc_runtime_System_Module_GateProxy_Object__`
- `typedef struct ti_sysbios_BIOS_Module_State__ ti_sysbios_BIOS_Module_State__`
- `typedef struct ti_sysbios_family_c64p_Cache_Module_State__ ti_sysbios_family_c64p_Cache_Module_State__`
- `typedef struct ti_sysbios_family_c64p_EventCombiner_Module_State__ ti_sysbios_family_c64p_EventCombiner_Module_State__`
- `typedef struct ti_sysbios_family_c64p_Exception_Module_State__ ti_sysbios_family_c64p_Exception_Module_State__`
- `typedef struct ti_sysbios_family_c64p_Hwi_Module_State__ ti_sysbios_family_c64p_Hwi_Module_State__`
- `typedef struct ti_sysbios_knl_Clock_Module_State__ ti_sysbios_knl_Clock_Module_State__`
- `typedef struct ti_sysbios_knl_Task_Module_State__ ti_sysbios_knl_Task_Module_State__`
- `typedef struct ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__ ti_sysbios_rts_ti_ThreadLocalStorage_Module_State__`
- `typedef struct ti_sysbios_timers_rti_Timer_Module_State__ ti_sysbios_timers_rti_Timer_Module_State__`
- `typedef struct ti_sysbios_utils_Load_Module_State__ ti_sysbios_utils_Load_Module_State__`
- `typedef struct xdc_runtime_Error_Module_State__ xdc_runtime_Error_Module_State__`
- `typedef struct xdc_runtime_Memory_Module_State__ xdc_runtime_Memory_Module_State__`
- `typedef struct xdc_runtime_Registry_Module_State__ xdc_runtime_Registry_Module_State__`
- `typedef struct xdc_runtime_Startup_Module_State__ xdc_runtime_Startup_Module_State__`
- `typedef struct xdc_runtime_System_Module_State__ xdc_runtime_System_Module_State__`
- `typedef struct xdc_runtime_Text_Module_State__ xdc_runtime_Text_Module_State__`
- `typedef union Header Header`

Functions

- `xdc_Void ti_sysbios_BIOS_startFunc (xdc_Void)`
- `xdc_Void ti_sysbios_BIOS_exitFunc (xdc_Int)`
- `xdc_Void ti_sysbios_family_c64p_EventCombiner_unused (xdc_UArg)`
- `xdc_Void ti_sysbios_family_c64p_EventCombiner_dispatch (xdc_UArg)`
- `xdc_UInt ti_sysbios_knl_Task_disable_E (xdc_Void)`
- `xdc_Void ti_sysbios_knl_Task_restoreHwi_E (xdc_UInt)`
- `xdc_Void ti_sysbios_knl_Clock_doTick_I (xdc_UArg)`
- `xdc_Void DSP_sleep (xdc_Void)`
- `xdc_Void ti_sysbios_hal_Hwi_checkStack (xdc_Void)`
- `xdc_Void ti_sysbios_utils_Load_taskRegHook_E (xdc_Int)`
- `xdc_Void ti_sysbios_hal_Hwi_initStack (xdc_Void)`
- `xdc_Int xdc_runtime_System_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_family_c64p_EventCombiner_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_family_c64p_Exception_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_family_c64p_Hwi_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_family_c64p_TimestampProvider_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_family_c64p_Cache_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_knl_Clock_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_knl_Task_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_hal_Hwi_Module_startup_E (xdc_Int)`
- `xdc_Int ti_sysbios_timers_rti_Timer_Module_startup_E (xdc_Int)`
- `xdc_Int xdc_runtime_System_printfExtend_I (xdc_Char **, xdc_CString *, xdc_VaList *, xdc_runtime->_System_ParseData *)`
- `xdc_META (__ASM__, "@(#)_ASM__ = C:/Users/astro/workspace_v9_2/mrr_18xx_dss/Debug/ config->Pkg/package/cfg/dss_mrr_pe674")`
- `xdc_META (__ISA__, "@(#)_ISA__ = 674")`
- `xdc_META (__PLAT__, "@(#)_PLAT__ = ti.platforms.c6x")`
- `xdc_META (__TARG__, "@(#)_TARG__ = ti.targets.elf.C674")`
- `xdc_META (__TRDR__, "@(#)_TRDR__ = ti.targets.omf.elf.Elf32")`
- `xdc_Bool xdc_runtime_System_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_family_c64p_EventCombiner_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_family_c64p_Exception_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_family_c64p_Hwi_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_family_c64p_Cache_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_knl_Clock_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_knl_Task_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_hal_Hwi_Module_startupDone_F (void)`
- `xdc_Bool ti_sysbios_timers_rti_Timer_Module_startupDone_F (void)`
- `xdc_Void xdc_runtime_Startup_exec_I (void)`
- `xdc_Void xdc_runtime_Startup_reset_I (void)`
- `void xdc_runtime_Text_visitRope_I (xdc_runtime_Text_RopeId rope, xdc_Fxn visFxn, xdc_Ptr visState)`
- `asm (.sect \".vecs\")`
- `asm (.align 0x400 ")`
- `asm (.nocmp ")`
- `asm (.global ti_sysbios_family_c64p_Hwi0 ")`
- `asm ("ti_sysbios_family_c64p_Hwi0: ")`
- `asm (.global ti_sysbios_family_c64p_Hwi_int0 ")`
- `asm (.global _c_int00 ")`
- `asm ("ti_sysbios_family_c64p_Hwi_int0: ")`
- `asm ("nop ")`
- `asm ("mvkl _c_int00, b0 ")`
- `asm ("mvkh _c_int00, b0 ")`

- **asm (" b b0 ")**
- **asm (" nop 4 ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int1 ")**
- **asm (" .global ti_sysbios_family_c64p_Exception_dispatch__E ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int1: ")**
- **asm (" stw b0, *b15--[2] ")**
- **asm (" mvk 1, b0 ")**
- **asm (" stw b0, *b15[1] ")**
- **asm (" ldw *++b15[2], b0 ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int2 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int2: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int3 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int3: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int4 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int4: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int5 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int5: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int6 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int6: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int7 ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_dispatchAlways ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int7: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int8 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int8: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int9 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int9: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int10 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int10: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int11 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int11: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int12 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int12: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int13 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int13: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int14 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int14: ")**
- **asm (" .global ti_sysbios_family_c64p_Hwi_int15 ")**
- **asm ("ti_sysbios_family_c64p_Hwi_int15: ")**
- **Void *ti_sysbios_BIOS_atExitFunc_I* (Int)**
- **Void *ti_sysbios_BIOS_registerRTSLock* ()**
- **Void *ti_sysbios_timers_rti_Timer_startup_E* ()**
- **Void *ti_sysbios_BIOS_startFunc_I* ()**
- **Void *ti_sysbios_BIOS_rtsLock* ()**
- **Void *ti_sysbios_BIOS_rtsUnlock* ()**
- **Void *ti_sysbios_BIOS_nullFunc_I* ()**
- **Void *ti_sysbios_BIOS_registerRTSLock* (Void)**
- **Void *ti_sysbios_BIOS_removeRTSLock* (Void)**
- **Void *ti_sysbios_BIOS_exitFunc* (Int stat)**
- **Void *ti_sysbios_BIOS_errorRaiseHook* (xdc_runtime_Error_Block *eb)**
- **Void *ti_sysbios_knl_Clock_doTick_I* (UArg arg)**
- **Void *ti_sysbios_utils_Load_update_E* ()**
- **static Void * *ti_sysbios_rts_MemAlloc_alloc* (SizeT size)**
- **Void **ATTRIBUTE** * *malloc* (SizeT size)**
- **Void **ATTRIBUTE** * *memalign* (SizeT alignment, SizeT size)**
- **Void **ATTRIBUTE** * *calloc* (SizeT nmemb, SizeT size)**

- Void **ATTRIBUTE free** (Void *ptr)
- Void **ATTRIBUTE *** **realloc** (Void *ptr, SizeT size)
- xdc_Int **xdc_runtime_System_printf_va__E** (xdc_CString fmt, va_list __va)
- xdc_Int **xdc_runtime_System_printf__E** (xdc_CString fmt,...)
- xdc_Int **xdc_runtime_System_aprintf_va__E** (xdc_CString fmt, va_list __va)
- xdc_Int **xdc_runtime_System_aprintf__E** (xdc_CString fmt,...)
- xdc_Int **xdc_runtime_System_sprintf_va__E** (xdc_Char buf[], xdc_CString fmt, va_list __va)
- xdc_Int **xdc_runtime_System_sprintf__E** (xdc_Char buf[], xdc_CString fmt,...)
- xdc_Int **xdc_runtime_System_asprintf_va__E** (xdc_Char buf[], xdc_CString fmt, va_list __va)
- xdc_Int **xdc_runtime_System_asprintf__E** (xdc_Char buf[], xdc_CString fmt,...)
- xdc_Int **xdc_runtime_System_snprintf_va__E** (xdc_Char buf[], xdc_SizeT n, xdc_CString fmt, va_list __va)
- xdc_Int **xdc_runtime_System_snprintf__E** (xdc_Char buf[], xdc_SizeT n, xdc_CString fmt,...)
- xdc_Bool **ti_sysbios_BIOS_RtsGateProxy_Module_startupDone__S** (void)
- ti_sysbios_BIOS_RtsGateProxy_Handle **ti_sysbios_BIOS_RtsGateProxy_create** (const ti_sysbios_BIOS_RtsGateProxy_Params *prms, xdc_runtime_Error_Block *eb)
- void **ti_sysbios_BIOS_RtsGateProxy_delete** (ti_sysbios_BIOS_RtsGateProxy_Handle *instp)
- void **ti_sysbios_BIOS_RtsGateProxy_Params_init__S** (xdc_Ptr dst, const void *src, xdc_SizeT psz, xdc_SizeT isz)
- xdc_runtime_Types_Label * **ti_sysbios_BIOS_RtsGateProxy_Handle_label__S** (xdc_Ptr obj, xdc_runtime_Types_Label *lab)
- xdc_Bool **ti_sysbios_BIOS_RtsGateProxy_query__E** (xdc_Int qual)
- xdc_IArg **ti_sysbios_BIOS_RtsGateProxy_enter__E** (ti_sysbios_BIOS_RtsGateProxy_Handle __inst)
- xdc_Void **ti_sysbios_BIOS_RtsGateProxy_leave__E** (ti_sysbios_BIOS_RtsGateProxy_Handle __inst, xdc_IArg key)
- xdc_Bool **ti_sysbios_hal_Hwi_HwiProxy_Module_startupDone__S** (void)
- ti_sysbios_hal_Hwi_HwiProxy_Handle **ti_sysbios_hal_Hwi_HwiProxy_create** (xdc_Int intNum, ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn, const ti_sysbios_hal_Hwi_HwiProxy_Params *prms, xdc_runtime_Error_Block *eb)
- void **ti_sysbios_hal_Hwi_HwiProxy_delete** (ti_sysbios_hal_Hwi_HwiProxy_Handle *instp)
- void **ti_sysbios_hal_Hwi_HwiProxy_Params_init__S** (xdc_Ptr dst, const void *src, xdc_SizeT psz, xdc_SizeT isz)
- xdc_runtime_Types_Label * **ti_sysbios_hal_Hwi_HwiProxy_Handle_label__S** (xdc_Ptr obj, xdc_runtime_Types_Label *lab)
- xdc_Bool **ti_sysbios_hal_Hwi_HwiProxy_getStackInfo__E** (ti_sysbios_interfaces_IHwi_StackInfo *stkInfo, xdc_Bool computeStackDepth)
- xdc_Bool **ti_sysbios_hal_Hwi_HwiProxy_getCoreStackInfo__E** (ti_sysbios_interfaces_IHwi_StackInfo *stkInfo, xdc_UInt coreId, xdc_Bool computeStackDepth, xdc_UInt coreId)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_startup__E** (void)
- xdc_UInt **ti_sysbios_hal_Hwi_HwiProxy_disable__E** (void)
- xdc_UInt **ti_sysbios_hal_Hwi_HwiProxy_enable__E** (void)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_restore__E** (xdc_UInt key)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_switchFromBootStack__E** (void)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_post__E** (xdc_UInt intNum)
- xdc_Char * **ti_sysbios_hal_Hwi_HwiProxy_getTaskSP__E** (void)
- xdc_UInt **ti_sysbios_hal_Hwi_HwiProxy_disableInterrupt__E** (xdc_UInt intNum)
- xdc_UInt **ti_sysbios_hal_Hwi_HwiProxy_enableInterrupt__E** (xdc_UInt intNum)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_restoreInterrupt__E** (xdc_UInt intNum, xdc_UInt key)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_clearInterrupt__E** (xdc_UInt intNum)
- ti_sysbios_interfaces_IHwi_FuncPtr **ti_sysbios_hal_Hwi_HwiProxy_getFunc__E** (ti_sysbios_hal_Hwi_HwiProxy_Handle __inst, xdc_UArg *arg)
- xdc_Void **ti_sysbios_hal_Hwi_HwiProxy_setFunc__E** (ti_sysbios_hal_Hwi_HwiProxy_Handle __inst, ti_sysbios_interfaces_IHwi_FuncPtr fxn, xdc_UArg arg)
- xdc_Ptr **ti_sysbios_hal_Hwi_HwiProxy_getHookContext__E** (ti_sysbios_hal_Hwi_HwiProxy_Handle __inst, xdc_Int id)

- `xdc_Void ti_sysbios_hal_Hwi_HwiProxy_setHookContext_E (ti_sysbios_hal_Hwi_HwiProxy_Handle ← __inst, xdc_Int id, xdc_Ptr hookContext)`
- `ti_sysbios_interfaces_IHwi_Irp ti_sysbios_hal_Hwi_HwiProxy_getIrp_E (ti_sysbios_hal_Hwi_Hwi← Proxy_Handle __inst)`
- `xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_startupDone_S (void)`
- `ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle ti_sysbios_heaps_HeapMem_Module_Gate← Proxy_create (const ti_sysbios_heaps_HeapMem_Module_GateProxy_Params *prms, xdc_runtime← Error_Block *eb)`
- `void ti_sysbios_heaps_HeapMem_Module_GateProxy_delete (ti_sysbios_heaps_HeapMem_Module ← GateProxy_Handle *instp)`
- `void ti_sysbios_heaps_HeapMem_Module_GateProxy_Params_init_S (xdc_Ptr dst, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_runtime_Types_Label * ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle_label__← S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_query_E (xdc_Int qual)`
- `xdc_IArg ti_sysbios_heaps_HeapMem_Module_GateProxy_enter_E (ti_sysbios_heaps_HeapMem ← Module_GateProxy_Handle __inst)`
- `xdc_Void ti_sysbios_heaps_HeapMem_Module_GateProxy_leave_E (ti_sysbios_heaps_HeapMem ← Module_GateProxy_Handle __inst, xdc_IArg key)`
- `xdc_Bool ti_sysbios_knl_Clock_TimerProxy_Module_startupDone_S (void)`
- `ti_sysbios_knl_Clock_TimerProxy_Handle ti_sysbios_knl_Clock_TimerProxy_create (xdc_Int id, ti ← sysbios_interfaces_ITimer_FuncPtr tickFxn, const ti_sysbios_knl_Clock_TimerProxy_Params *prms, xdc← _runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Clock_TimerProxy_delete (ti_sysbios_knl_Clock_TimerProxy_Handle *instp)`
- `void ti_sysbios_knl_Clock_TimerProxy_Params_init_S (xdc_Ptr dst, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Clock_TimerProxy_Handle_label__S (xdc_Ptr obj, xdc← runtime_Types_Label *lab)`
- `xdc_UInt ti_sysbios_knl_Clock_TimerProxy_getNumTimers_E (void)`
- `ti_sysbios_interfaces_ITimer_Status ti_sysbios_knl_Clock_TimerProxy_getStatus_E (xdc_UInt id)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_startup_E (void)`
- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getMaxTicks_E (ti_sysbios_knl_Clock_TimerProxy ← Handle __inst)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_setNextTick_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst, xdc_UInt32 ticks)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_start_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_stop_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_setPeriod_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst, xdc_UInt32 period)`
- `xdc_Bool ti_sysbios_knl_Clock_TimerProxy_setPeriodMicroSecs_E (ti_sysbios_knl_Clock_Timer← Proxy_Handle __inst, xdc_UInt32 microsecs)`
- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getPeriod_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst)`
- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getCount_E (ti_sysbios_knl_Clock_TimerProxy_Handle __inst)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_getFreq_E (ti_sysbios_knl_Clock_TimerProxy_Handle ← __inst, xdc_runtime_Types_FreqHz *freq)`
- `ti_sysbios_interfaces_ITimer_FuncPtr ti_sysbios_knl_Clock_TimerProxy_getFunc_E (ti_sysbios_knl← _Clock_TimerProxy_Handle __inst, xdc_UArg *arg)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_setFunc_E (ti_sysbios_knl_Clock_TimerProxy_Handle ← __inst, ti_sysbios_interfaces_ITimer_FuncPtr fxn, xdc_UArg arg)`
- `xdc_Void ti_sysbios_knl_Clock_TimerProxy_trigger_E (ti_sysbios_knl_Clock_TimerProxy_Handle ← __inst, xdc_UInt32 cycles)`
- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getExpiredCounts_E (ti_sysbios_knl_Clock_Timer← Proxy_Handle __inst)`

- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getExpiredTicks__E` (`ti_sysbios_knl_Clock_TimerProxy_Handle __inst`, `xdc_UInt32 tickPeriod`)
- `xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getCurrentTick__E` (`ti_sysbios_knl_Clock_TimerProxy_Handle __inst`, `xdc_Bool save`)
- `xdc_Bool ti_sysbios_knl_Intrinsics_SupportProxy_Module_startupDone__S` (`void`)
- `xdc_UInt ti_sysbios_knl_Intrinsics_SupportProxy_maxbit__E` (`xdc_UInt bits`)
- `xdc_Bool ti_sysbios_knl_Task_SupportProxy_Module_startupDone__S` (`void`)
- `xdc_Ptr ti_sysbios_knl_Task_SupportProxy_start__E` (`xdc_Ptr curTask`, `ti_sysbios_interfaces_ITaskSupport_FuncPtr enterFxn`, `ti_sysbios_interfaces_ITaskSupport_FuncPtr exitFxn`, `xdc_runtime_Error_Block *eb`)
- `xdc_Void ti_sysbios_knl_Task_SupportProxy_swap__E` (`xdc_Ptr *oldtskContext`, `xdc_Ptr *newtskContext`)
- `xdc_Bool ti_sysbios_knl_Task_SupportProxy_checkStack__E` (`xdc_Char *stack`, `xdc_SizeT size`)
- `xdc_SizeT ti_sysbios_knl_Task_SupportProxy_stackUsed__E` (`xdc_Char *stack`, `xdc_SizeT size`)
- `xdc_UInt ti_sysbios_knl_Task_SupportProxy_getStackAlignment__E` (`void`)
- `xdc_SizeT ti_sysbios_knl_Task_SupportProxy_getDefaultStackSize__E` (`void`)
- `xdc_Bool xdc_runtime_Main_Module_GateProxy_Module_startupDone__S` (`void`)
- `xdc_runtime_Main_Module_GateProxy_Handle xdc_runtime_Main_Module_GateProxy_create` (`const xdc_runtime_Main_Module_GateProxy_Params *prms`, `xdc_runtime_Error_Block *eb`)
- `void xdc_runtime_Main_Module_GateProxy_delete` (`xdc_runtime_Main_Module_GateProxy_Handle *instp`)
- `void xdc_runtime_Main_Module_GateProxy_Params_init__S` (`xdc_Ptr dst`, `const void *src`, `xdc_SizeT psz`, `xdc_SizeT isz`)
- `xdc_runtime_Types_Label * xdc_runtime_Main_Module_GateProxy_Handle_label__S` (`xdc_Ptr obj`, `xdc_runtime_Types_Label *lab`)
- `xdc_Bool xdc_runtime_Main_Module_GateProxy_query__E` (`xdc_Int qual`)
- `xdc_IArg xdc_runtime_Main_Module_GateProxy_enter__E` (`xdc_runtime_Main_Module_GateProxy_Handle __inst`)
- `xdc_Void xdc_runtime_Main_Module_GateProxy_leave__E` (`xdc_runtime_Main_Module_GateProxy_Handle __inst`, `xdc_IArg key`)
- `xdc_Bool xdc_runtime_Memory_HeapProxy_Module_startupDone__S` (`void`)
- `xdc_runtime_Memory_HeapProxy_Handle xdc_runtime_Memory_HeapProxy_create` (`const xdc_runtime_Memory_HeapProxy_Params *prms`, `xdc_runtime_Error_Block *eb`)
- `void xdc_runtime_Memory_HeapProxy_delete` (`xdc_runtime_Memory_HeapProxy_Handle *instp`)
- `void xdc_runtime_Memory_HeapProxy_Params_init__S` (`xdc_Ptr dst`, `const void *src`, `xdc_SizeT psz`, `xdc_SizeT isz`)
- `xdc_runtime_Types_Label * xdc_runtime_Memory_HeapProxy_Handle_label__S` (`xdc_Ptr obj`, `xdc_runtime_Types_Label *lab`)
- `xdc_Ptr xdc_runtime_Memory_HeapProxy_alloc__E` (`xdc_runtime_Memory_HeapProxy_Handle __inst`, `xdc_SizeT size`, `xdc_SizeT align`, `xdc_runtime_Error_Block *eb`)
- `xdc_Void xdc_runtime_Memory_HeapProxy_free__E` (`xdc_runtime_Memory_HeapProxy_Handle __inst`, `xdc_Ptr block`, `xdc_SizeT size`)
- `xdc_Bool xdc_runtime_Memory_HeapProxy_isBlocking__E` (`xdc_runtime_Memory_HeapProxy_Handle __inst`)
- `xdc_Void xdc_runtime_Memory_HeapProxy_getStats__E` (`xdc_runtime_Memory_HeapProxy_Handle __inst`, `xdc_runtime_Memory_Stats *stats`)
- `xdc_Bool xdc_runtime_System_Module_GateProxy_Module_startupDone__S` (`void`)
- `xdc_runtime_System_Module_GateProxy_Handle xdc_runtime_System_Module_GateProxy_create` (`const xdc_runtime_System_Module_GateProxy_Params *prms`, `xdc_runtime_Error_Block *eb`)
- `void xdc_runtime_System_Module_GateProxy_delete` (`xdc_runtime_System_Module_GateProxy_Handle *instp`)
- `void xdc_runtime_System_Module_GateProxy_Params_init__S` (`xdc_Ptr dst`, `const void *src`, `xdc_SizeT psz`, `xdc_SizeT isz`)
- `xdc_runtime_Types_Label * xdc_runtime_System_Module_GateProxy_Handle_label__S` (`xdc_Ptr obj`, `xdc_runtime_Types_Label *lab`)
- `xdc_Bool xdc_runtime_System_Module_GateProxy_query__E` (`xdc_Int qual`)

- `xdc_IArg xdc_runtime_System_Module_GateProxy_enter_E` (`xdc_runtime_System_Module_GateProxy_Handle __inst`)
- `xdc_Void xdc_runtime_System_Module_GateProxy_leave_E` (`xdc_runtime_System_Module_GateProxy_Handle __inst, xdc_IArg key`)
- `xdc_Bool xdc_runtime_System_SupportProxy_Module_startupDone_S` (`void`)
- `xdc_Void xdc_runtime_System_SupportProxy_abort_E` (`xdc_CString str`)
- `xdc_Void xdc_runtime_System_SupportProxy_exit_E` (`xdc_Int stat`)
- `xdc_Void xdc_runtime_System_SupportProxy_flush_E` (`void`)
- `xdc_Void xdc_runtime_System_SupportProxy_putch_E` (`xdc_Char ch`)
- `xdc_Bool xdc_runtime_System_SupportProxy_ready_E` (`void`)
- `xdc_Bool xdc_runtime_Timestamp_SupportProxy_Module_startupDone_S` (`void`)
- `xdc_Bits32 xdc_runtime_Timestamp_SupportProxy_get32_E` (`void`)
- `xdc_Void xdc_runtime_Timestamp_SupportProxy_get64_E` (`xdc_runtime_Types_Timestamp64 *result`)
- `xdc_Void xdc_runtime_Timestamp_SupportProxy_getFreq_E` (`xdc_runtime_Types_FreqHz *freq`)
- `xdc_runtime_IHeap_Handle xdc_runtime_IHeap_create` (`xdc_runtime_IHeap_Module mod, const xdc_runtime_IHeap_Parms *prms, xdc_runtime_Error_Block *eb`)
- `void xdc_runtime_IHeap_delete` (`xdc_runtime_IHeap_Handle *instp`)
- `xdc_runtime_IGateProvider_Handle xdc_runtime_IGateProvider_create` (`xdc_runtime_IGateProvider_Module mod, const xdc_runtime_IGateProvider_Parms *prms, xdc_runtime_Error_Block *eb`)
- `void xdc_runtime_IGateProvider_delete` (`xdc_runtime_IGateProvider_Handle *instp`)
- `xdc_Bool ti_sysbios_BIOS_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_BIOS_RtsGateProxy_Proxy_abstract_S` (`void`)
- `xdc_CPtr ti_sysbios_BIOS_RtsGateProxy_Proxy_delegate_S` (`void`)
- `xdc_Bool ti_sysbios_family_c62_IntrinsicsSupport_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_family_c62_TaskSupport_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_family_c64p_Cache_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_family_c64p_EventCombiner_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_family_c64p_Exception_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_family_c64p_Hwi_Module_startupDone_S` (`void`)
- `xdc_Runtime_Types_Label * ti_sysbios_family_c64p_Hwi_Handle_label_S` (`xdc_Ptr obj, xdc_runtime_Types_Label *lab`)
- `xdc_Void ti_sysbios_family_c64p_Hwi_Params_init_S` (`xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz`)
- `xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_get_S` (`xdc_Ptr oa, xdc_Int i`)
- `xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_first_S` (`void`)
- `xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_next_S` (`xdc_Ptr obj`)
- `xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_create_S` (`xdc_CPtr __aa, const xdc_UChar *__paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb`)
- `ti_sysbios_family_c64p_Hwi_Handle ti_sysbios_family_c64p_Hwi_create` (`xdc_Int intNum, ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn, const ti_sysbios_family_c64p_Hwi_Parms *__paramsPtr, xdc_runtime_Error_Block *eb`)
- `void ti_sysbios_family_c64p_Hwi_construct` (`ti_sysbios_family_c64p_Hwi_Struct *__obj, xdc_Int intNum, ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn, const ti_sysbios_family_c64p_Hwi_Parms *__paramsPtr, xdc_runtime_Error_Block *eb`)
- `void ti_sysbios_family_c64p_Hwi_destruct` (`ti_sysbios_family_c64p_Hwi_Struct *obj`)
- `xdc_Void ti_sysbios_family_c64p_Hwi_Object_delete_S` (`xdc_Ptr instp`)
- `void ti_sysbios_family_c64p_Hwi_delete` (`ti_sysbios_family_c64p_Hwi_Handle *instp`)
- `xdc_Bool ti_sysbios_timestampProvider_Module_startupDone_S` (`void`)
- `xdc_Bool ti_sysbios_gates_GateHwi_Module_startupDone_S` (`void`)
- `xdc_Runtime_Types_Label * ti_sysbios_gates_GateHwi_Handle_label_S` (`xdc_Ptr obj, xdc_runtime_Types_Label *lab`)
- `xdc_Void ti_sysbios_gates_GateHwi_Params_init_S` (`xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz`)
- `xdc_Ptr ti_sysbios_gates_GateHwi_Object_get_S` (`xdc_Ptr oa, xdc_Int i`)
- `xdc_Ptr ti_sysbios_gates_GateHwi_Object_first_S` (`void`)

- `xdc_Ptr ti_sysbios_gates_GateHwi_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_gates_GateHwi_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __params, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_gates_GateHwi_Handle ti_sysbios_gates_GateHwi_create (const ti_sysbios_gates_GateHwi_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_gates_GateHwi_construct (ti_sysbios_gates_GateHwi_Struct * __obj, const ti_sysbios_gates_GateHwi_Params * __paramsPtr)`
- `void ti_sysbios_gates_GateHwi_destruct (ti_sysbios_gates_GateHwi_Struct *obj)`
- `xdc_Void ti_sysbios_gates_GateHwi_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_gates_GateHwi_delete (ti_sysbios_gates_GateHwi_Handle *instp)`
- `xdc_Bool ti_sysbios_gates_GateMutex_Module_startupDone_S (void)`
- `xdc(runtime_Types_Label * ti_sysbios_gates_GateMutex_Handle_label_S (xdc_Ptr obj, xdc(runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_gates_GateMutex_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_gates_GateMutex_Object_get_S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_gates_GateMutex_Object_first_S (void)`
- `xdc_Ptr ti_sysbios_gates_GateMutex_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_gates_GateMutex_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_gates_GateMutex_Handle ti_sysbios_gates_GateMutex_create (const ti_sysbios_gates_GateMutex_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_gates_GateMutex_construct (ti_sysbios_gates_GateMutex_Struct * __obj, const ti_sysbios_gates_GateMutex_Params * __paramsPtr)`
- `void ti_sysbios_gates_GateMutex_destruct (ti_sysbios_gates_GateMutex_Struct *obj)`
- `xdc_Void ti_sysbios_gates_GateMutex_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_gates_GateMutex_delete (ti_sysbios_gates_GateMutex_Handle *instp)`
- `xdc_Bool ti_sysbios_hal_Hwi_Module_startupDone_S (void)`
- `xdc(runtime_Types_Label * ti_sysbios_hal_Hwi_Handle_label_S (xdc_Ptr obj, xdc(runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_hal_Hwi_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_hal_Hwi_Object_get_S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_hal_Hwi_Object_first_S (void)`
- `xdc_Ptr ti_sysbios_hal_Hwi_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_hal_Hwi_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_hal_Hwi_Handle ti_sysbios_hal_Hwi_create (xdc_Int intNum, ti_sysbios_hal_Hwi_FuncPtr hwiFxn, const ti_sysbios_hal_Hwi_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_hal_Hwi_construct (ti_sysbios_hal_Hwi_Struct * __obj, xdc_Int intNum, ti_sysbios_hal_Hwi_FuncPtr hwiFxn, const ti_sysbios_hal_Hwi_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_hal_Hwi_destruct (ti_sysbios_hal_Hwi_Struct *obj)`
- `xdc_Void ti_sysbios_hal_Hwi_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_hal_Hwi_delete (ti_sysbios_hal_Hwi_Handle *instp)`
- `xdc_Bool ti_sysbios_hal_Hwi_HwiProxy_Proxy_abstract_S (void)`
- `xdc_CPtr ti_sysbios_hal_Hwi_HwiProxy_Proxy_delegate_S (void)`
- `xdc_Bool ti_sysbios_heaps_HeapMem_Module_startupDone_S (void)`
- `xdc(runtime_Types_Label * ti_sysbios_heaps_HeapMem_Handle_label_S (xdc_Ptr obj, xdc(runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_heaps_HeapMem_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_heaps_HeapMem_Object_get_S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_heaps_HeapMem_Object_first_S (void)`
- `xdc_Ptr ti_sysbios_heaps_HeapMem_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_heaps_HeapMem_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`

- `ti_sysbios_heaps_HeapMem_Handle ti_sysbios_heaps_HeapMem_create (const ti_sysbios_heaps_HeapMem_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_heaps_HeapMem_construct (ti_sysbios_heaps_HeapMem_Struct * __obj, const ti_sysbios_heaps_HeapMem_Params * __paramsPtr)`
- `void ti_sysbios_heaps_HeapMem_destruct (ti_sysbios_heaps_HeapMem_Struct *obj)`
- `xdc_Void ti_sysbios_heaps_HeapMem_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_heaps_HeapMem_delete (ti_sysbios_heaps_HeapMem_Handle *instp)`
- `xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_abstract_S (void)`
- `xdc_CPtr ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_delegate_S (void)`
- `xdc_Bool ti_sysbios_knl_Clock_Module_startupDone_S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Clock_Handle_label_S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_knl_Clock_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_knl_Clock_Object_get_S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_knl_Clock_Object_first_S (void)`
- `xdc_Ptr ti_sysbios_knl_Clock_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_knl_Clock_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_knl_Clock_Handle ti_sysbios_knl_Clock_create (ti_sysbios_knl_Clock_FuncPtr clockFxn, xdc_UInt timeout, const ti_sysbios_knl_Clock_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Clock_construct (ti_sysbios_knl_Clock_Struct * __obj, ti_sysbios_knl_Clock_Params * __paramsPtr)`
- `void ti_sysbios_knl_Clock_destruct (ti_sysbios_knl_Clock_Struct *obj)`
- `xdc_Void ti_sysbios_knl_Clock_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_knl_Clock_delete (ti_sysbios_knl_Clock_Handle *instp)`
- `xdc_Bool ti_sysbios_knl_Clock_TimerProxy_Proxy_abstract_S (void)`
- `xdc_CPtr ti_sysbios_knl_Clock_TimerProxy_Proxy_delegate_S (void)`
- `xdc_Bool ti_sysbios_knl_Event_Module_startupDone_S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Event_Handle_label_S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_knl_Event_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_knl_Event_Object_get_S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_knl_Event_Object_first_S (void)`
- `xdc_Ptr ti_sysbios_knl_Event_Object_next_S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_knl_Event_Object_create_S (xdc_CPtr __aa, const xdc_UChar * __paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_knl_Event_Handle ti_sysbios_knl_Event_create (const ti_sysbios_knl_Event_Params * __paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Event_construct (ti_sysbios_knl_Event_Struct * __obj, const ti_sysbios_knl_Event_Params * __paramsPtr)`
- `void ti_sysbios_knl_Event_destruct (ti_sysbios_knl_Event_Struct *obj)`
- `xdc_Void ti_sysbios_knl_Event_Object_delete_S (xdc_Ptr instp)`
- `void ti_sysbios_knl_Event_delete (ti_sysbios_knl_Event_Handle *instp)`
- `xdc_Bool ti_sysbios_knl_Idle_Module_startupDone_S (void)`
- `xdc_Bool ti_sysbios_knl_Intrinsics_Module_startupDone_S (void)`
- `xdc_Bool ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_abstract_S (void)`
- `xdc_CPtr ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_delegate_S (void)`
- `xdc_Bool ti_sysbios_knl_Queue_Module_startupDone_S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Queue_Handle_label_S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_knl_Queue_Params_init_S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_knl_Queue_Object_get_S (xdc_Ptr oa, xdc_Int i)`

- `xdc_Ptr ti_sysbios_knl_Queue_Object__first__S (void)`
- `xdc_Ptr ti_sysbios_knl_Queue_Object__next__S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_knl_Queue_Object__create__S (xdc_CPtr __aa, const xdc_UChar *__paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_knl_Queue_Handle ti_sysbios_knl_Queue_create (const ti_sysbios_knl_Queue_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Queue_construct (ti_sysbios_knl_Queue_Struct *__obj, const ti_sysbios_knl_Queue_Params *__paramsPtr)`
- `void ti_sysbios_knl_Queue_destruct (ti_sysbios_knl_Queue_Struct *obj)`
- `xdc_Void ti_sysbios_knl_Queue_Object__delete__S (xdc_Ptr instp)`
- `void ti_sysbios_knl_Queue_delete (ti_sysbios_knl_Queue_Handle *instp)`
- `xdc_Bool ti_sysbios_knl_Semaphore_Module__startupDone__S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Semaphore_Handle__label__S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_knl_Semaphore_Params__init__S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_knl_Semaphore_Object__get__S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_knl_Semaphore_Object__first__S (void)`
- `xdc_Ptr ti_sysbios_knl_Semaphore_Object__next__S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_knl_Semaphore_Object__create__S (xdc_CPtr __aa, const xdc_UChar *__paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_knl_Semaphore_Handle ti_sysbios_knl_Semaphore_create (xdc_Int count, const ti_sysbios_knl_Semaphore_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Semaphore_construct (ti_sysbios_knl_Semaphore_Struct *__obj, xdc_Int count, const ti_sysbios_knl_Semaphore_Params *__paramsPtr)`
- `void ti_sysbios_knl_Semaphore_destruct (ti_sysbios_knl_Semaphore_Struct *obj)`
- `xdc_Void ti_sysbios_knl_Semaphore_Object__delete__S (xdc_Ptr instp)`
- `void ti_sysbios_knl_Semaphore_delete (ti_sysbios_knl_Semaphore_Handle *instp)`
- `xdc_Bool ti_sysbios_knl_Task_Module__startupDone__S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_knl_Task_Handle__label__S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_knl_Task_Params__init__S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_knl_Task_Object__get__S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_knl_Task_Object__first__S (void)`
- `xdc_Ptr ti_sysbios_knl_Task_Object__next__S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_knl_Task_Object__create__S (xdc_CPtr __aa, const xdc_UChar *__paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_knl_Task_Handle ti_sysbios_knl_Task_create (ti_sysbios_knl_Task_FuncPtr fxn, const ti_sysbios_knl_Task_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Task_construct (ti_sysbios_knl_Task_Struct *__obj, ti_sysbios_knl_Task_FuncPtr fxn, const ti_sysbios_knl_Task_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_knl_Task_destruct (ti_sysbios_knl_Task_Struct *obj)`
- `xdc_Void ti_sysbios_knl_Task_Object__delete__S (xdc_Ptr instp)`
- `void ti_sysbios_knl_Task_delete (ti_sysbios_knl_Task_Handle *instp)`
- `xdc_Bool ti_sysbios_knl_Task_SupportProxy_Proxy__abstract__S (void)`
- `xdc_CPtr ti_sysbios_knl_Task_SupportProxy_Proxy__delegate__S (void)`
- `xdc_Bool ti_sysbios_rts_ti_ThreadLocalStorage_Module__startupDone__S (void)`
- `xdc_Bool ti_sysbios_timers_rti_Timer_Module__startupDone__S (void)`
- `xdc_runtime_Types_Label * ti_sysbios_timers_rti_Timer_Handle__label__S (xdc_Ptr obj, xdc_runtime_Types_Label *lab)`
- `xdc_Void ti_sysbios_timers_rti_Timer_Params__init__S (xdc_Ptr prms, const void *src, xdc_SizeT psz, xdc_SizeT isz)`
- `xdc_Ptr ti_sysbios_timers_rti_Timer_Object__get__S (xdc_Ptr oa, xdc_Int i)`
- `xdc_Ptr ti_sysbios_timers_rti_Timer_Object__first__S (void)`

- `xdc_Ptr ti_sysbios_timers_rti_Timer_Object__next__S (xdc_Ptr obj)`
- `xdc_Ptr ti_sysbios_timers_rti_Timer_Object__create__S (xdc_CPtr __aa, const xdc_UChar *__paramsPtr, xdc_SizeT __psz, xdc_runtime_Error_Block *eb)`
- `ti_sysbios_timers_rti_Timer_Handle ti_sysbios_timers_rti_Timer_create (xdc_Int id, ti_sysbios_interfaces_ITimer_FuncPtr tickFxn, const ti_sysbios_timers_rti_Timer_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_timers_rti_Timer_construct (ti_sysbios_timers_rti_Timer_Struct *__obj, xdc_Int id, ti_sysbios_interfaces_ITimer_FuncPtr tickFxn, const ti_sysbios_timers_rti_Timer_Params *__paramsPtr, xdc_runtime_Error_Block *eb)`
- `void ti_sysbios_timers_rti_Timer_destruct (ti_sysbios_timers_rti_Timer_Struct *obj)`
- `xdc_Void ti_sysbios_timers_rti_Timer_Object__delete__S (xdc_Ptr instp)`
- `void ti_sysbios_timers_rti_Timer_delete (ti_sysbios_timers_rti_Timer_Handle *instp)`
- `xdc_Bool xdc_sysbios_utils_Load_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Assert_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Core_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Defaults_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Diags_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Error_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Gate_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Log_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Main_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Main_Module_GateProxy_Proxy_abstract__S (void)`
- `xdc_CPtr xdc_runtime_Main_Module_GateProxy_Proxy_delegate__S (void)`
- `xdc_Bool xdc_runtime_Memory_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Memory_HeapProxy_Proxy_abstract__S (void)`
- `xdc_CPtr xdc_runtime_Memory_HeapProxy_Proxy_delegate__S (void)`
- `xdc_Bool xdc_runtime_Registry_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Startup_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_SysStd_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_System_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_System_Module_GateProxy_Proxy_abstract__S (void)`
- `xdc_CPtr xdc_runtime_System_Module_GateProxy_Proxy_delegate__S (void)`
- `xdc_Bool xdc_runtime_System_SupportProxy_Proxy_abstract__S (void)`
- `xdc_CPtr xdc_runtime_System_SupportProxy_Proxy_delegate__S (void)`
- `xdc_Bool xdc_runtime_Text_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Timestamp_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_TimestampNull_Module_startupDone__S (void)`
- `xdc_Bool xdc_runtime_Timestamp_SupportProxy_Proxy_abstract__S (void)`
- `xdc_CPtr xdc_runtime_Timestamp_SupportProxy_Proxy_delegate__S (void)`
- `int __xdc_init (void)`

Variables

- `ti_sysbios_BIOS_RtsGateProxy_Module__ ti_sysbios_BIOS_RtsGateProxy_Module_root__V`
- `ti_sysbios_family_c64p_Hwi_Module__ ti_sysbios_family_c64p_Hwi_Module_root__V`
- `ti_sysbios_gates_GateHwi_Module__ ti_sysbios_gates_GateHwi_Module_root__V`
- `ti_sysbios_gates_GateMutex_Module__ ti_sysbios_gates_GateMutex_Module_root__V`
- `ti_sysbios_hal_Hwi_Module__ ti_sysbios_hal_Hwi_Module_root__V`
- `ti_sysbios_hal_Hwi_HwiProxy_Module__ ti_sysbios_hal_Hwi_HwiProxy_Module_root__V`
- `ti_sysbios_heaps_HeapMem_Module__ ti_sysbios_heaps_HeapMem_Module_root__V`
- `ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_root__V`
- `ti_sysbios_knl_Clock_Module__ ti_sysbios_knl_Clock_Module_root__V`
- `ti_sysbios_knl_Clock_TimerProxy_Module__ ti_sysbios_knl_Clock_TimerProxy_Module_root__V`
- `ti_sysbios_knl_Event_Module__ ti_sysbios_knl_Event_Module_root__V`

- `ti_sysbios_knl_Queue_Module__ ti_sysbios_knl_Queue_Module__root__V`
- `ti_sysbios_knl_Semaphore_Module__ ti_sysbios_knl_Semaphore_Module__root__V`
- `ti_sysbios_knl_Task_Module__ ti_sysbios_knl_Task_Module__root__V`
- `ti_sysbios_timers_rti_Timer_Module__ ti_sysbios_timers_rti_Timer_Module__root__V`
- `xdc_runtime_Main_Module_GateProxy_Module__ xdc_runtime_Main_Module_GateProxy_Module__root__V`
- `xdc_runtime_Memory_HeapProxy_Module__ xdc_runtime_Memory_HeapProxy_Module__root__V`
- `xdc_runtime_System_Module_GateProxy_Module__ xdc_runtime_System_Module_GateProxy_Module__root__V`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_IHeap_Interface__BASE__C = {& xdc_runtime__IModule_Interface__BASE__C}`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_ISystemSupport_Interface__BASE__C = {& xdc_runtime__IModule_Interface__BASE__C}`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_ITimestampProvider_Interface__BASE__C = {& xdc_runtime_ITimestampClient_Interface__BASE__C}`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_IGateProvider_Interface__BASE__C = {& xdc_runtime__IModule_Interface__BASE__C}`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_IModule_Interface__BASE__C = {0}`
- `const __FAR__ xdc_runtime_Types_Base xdc_runtime_ITimestampClient_Interface__BASE__C = {& xdc_runtime__IModule_Interface__BASE__C}`
- `const ti_sysbios_gates_GateHwi_Fxns__ ti_sysbios_gates_GateHwi_Module__FXNS__C`
- `const ti_sysbios_gates_GateMutex_Fxns__ ti_sysbios_gates_GateMutex_Module__FXNS__C`
- `const ti_sysbios_heaps_HeapMem_Fxns__ ti_sysbios_heaps_HeapMem_Module__FXNS__C`
- `const xdc_runtime_SysStd_Fxns__ xdc_runtime_SysStd_Module__FXNS__C`
- `const xdc_runtime_Timestamp_Fxns__ xdc_runtime_Timestamp_Module__FXNS__C`
- `const xdc_runtime_TimestampNull_Fxns__ xdc_runtime_TimestampNull_Module__FXNS__C`
- `ti_sysbios_BIOS_Module_State__ ti_sysbios_BIOS_Module__state__V`
- `ti_sysbios_family_c64p_Cache_Module_State__ ti_sysbios_family_c64p_Cache_Module__state__V`
- `ti_sysbios_family_c64p_EventCombiner_Module_State__ ti_sysbios_family_c64p_EventCombiner__Module__state__V`
- `ti_sysbios_family_c64p_Exception_Module_State__ ti_sysbios_family_c64p_Exception_Module__state__V`
- `ti_sysbios_family_c64p_Hwi_Object__ ti_sysbios_family_c64p_Hwi_Object__table__V [5]`
- `void * ti_sysbios_family_c64p_Hwi0`
- `void * __TI_STATIC_BASE`
- `ti_sysbios_family_c64p_Hwi_Module_State__ ti_sysbios_family_c64p_Hwi_Module__state__V`
- `ti_sysbios_gates_GateHwi_Object__ ti_sysbios_gates_GateHwi_Object__table__V [1]`
- `ti_sysbios_gates_GateMutex_Object__ ti_sysbios_gates_GateMutex_Object__table__V [2]`
- `ti_sysbios_hal_Hwi_Object__ ti_sysbios_hal_Hwi_Object__table__V [1]`
- `__T1_ti_sysbios_heaps_HeapMem_Instance_State__buf ti_sysbios_heaps_HeapMem_Instance__State_0_buf__A [16384]`
- `ti_sysbios_heaps_HeapMem_Object__ ti_sysbios_heaps_HeapMem_Object__table__V [1]`
- `ti_sysbios_knl_Clock_Module_State__ ti_sysbios_knl_Clock_Module__state__V`
- `const __T1_ti_sysbios_knl_Idle_funcList ti_sysbios_knl_Idle_funcList__A [3]`
- `const __T1_ti_sysbios_knl_Idle_coreList ti_sysbios_knl_Idle_coreList__A [3]`
- `__T1_ti_sysbios_knl_Task_Instance_State__stack ti_sysbios_knl_Task_Instance_State_0_stack__A [800]`
- `__T1_ti_sysbios_knl_Task_Instance_State__hookEnv ti_sysbios_knl_Task_Instance_State_0_hook__Env__A [1]`
- `ti_sysbios_knl_Task_Object__ ti_sysbios_knl_Task_Object__table__V [1]`
- `__T1_ti_sysbios_knl_Task_Module_State__readyQ ti_sysbios_knl_Task_Module_State_0_readyQ__A [16]`
- `__T1_ti_sysbios_knl_Task_Module_State__idleTask ti_sysbios_knl_Task_Module_State_0_idleTask__A [1]`

- `ti_sysbios_knl_Task_Module_State__ ti_sysbios_knl_Task_Module_state_V`
- `const __T1_ti_sysbios_knl_Task_hooks ti_sysbios_knl_Task_hooks_A [1]`
- `ti_sysbios_rts(ti_ThreadLocalStorage_Module_State__ ti_sysbios_rts(ti_ThreadLocalStorage_Module_state_V`
- `ti_sysbios_timers_rti_Timer_Object__ ti_sysbios_timers_rti_Timer_Object_table_V [1]`
- `__T1_ti_sysbios_timers_rti_Timer_Module_State_device ti_sysbios_timers_rti_Timer_Module_State_0_device_A [2]`
- `__T1_ti_sysbios_timers_rti_Timer_Module_State_intFreqs ti_sysbios_timers_rti_Timer_Module_state_0_intFreqs_A [2]`
- `__T1_ti_sysbios_timers_rti_Timer_Module_State_handles ti_sysbios_timers_rti_Timer_Module_state_0_handles_A [2]`
- `ti_sysbios_timers_rti_Timer_Module_State__ ti_sysbios_timers_rti_Timer_Module_state_V`
- `__T1_ti_sysbios_utils_Load_Module_State_taskStartTime ti_sysbios_utils_Load_Module_State_0_taskStartTime_A [1]`
- `__T1_ti_sysbios_utils_Load_Module_State_runningTask ti_sysbios_utils_Load_Module_State_0_runningTask_A [1]`
- `__T1_ti_sysbios_utils_Load_Module_State_taskEnv ti_sysbios_utils_Load_Module_State_0_taskEnv_A [1]`
- `ti_sysbios_utils_Load_Module_State__ ti_sysbios_utils_Load_Module_state_V`
- `xdc_runtime_Error_Module_State__ xdc_runtime_Error_Module_state_V`
- `xdc_runtime_Memory_Module_State__ xdc_runtime_Memory_Module_state_V`
- `xdc_runtime_Registry_Module_State__ xdc_runtime_Registry_Module_state_V`
- `xdc_runtime_Startup_Module_State__ xdc_runtime_Startup_Module_state_V`
- `const __T1_xdc_runtime_Startup_firstFxns xdc_runtime_Startup_firstFxns_A [2]`
- `const __T1_xdc_runtime_Startup_sfxnTab xdc_runtime_Startup_sfxnTab_A [10]`
- `const __T1_xdc_runtime_Startup_sfxnRts xdc_runtime_Startup_sfxnRts_A [10]`
- `xdcc_runtime_System_Module_State__ xdc_runtime_System_Module_state_V`
- `xdcc_runtime_Text_Module_State__ xdc_runtime_Text_Module_state_V`
- `const __T1_xdc_runtime_Text_charTab xdc_runtime_Text_charTab_A [1]`
- `const __T1_xdc_runtime_Text_nodeTab xdc_runtime_Text_nodeTab_A [1]`
- `const __FAR__ xdc_SizeT ti_sysbios_gates_GateMutex_Instance_State_sem_O = offsetof(ti_sysbios_gates_GateMutex_Object_,Object_field_sem)`
- `const __FAR__ xdc_SizeT ti_sysbios_knl_Clock_Module_State_clockQ_O = offsetof(ti_sysbios_knl_Clock_Module_State_,Object_field_clockQ)`
- `const __FAR__ xdc_SizeT ti_sysbios_knl_Event_Instance_State_pendQ_O = offsetof(ti_sysbios_knl_Event_Object_,Object_field_pendQ)`
- `const __FAR__ xdc_SizeT ti_sysbios_knl_Semaphore_Instance_State_pendQ_O = offsetof(ti_sysbios_knl_Semaphore_Object_,Object_field_pendQ)`
- `const __FAR__ xdc_SizeT ti_sysbios_knl_Task_Module_State_inactiveQ_O = offsetof(ti_sysbios_knl_Task_Module_State_,Object_field_inactiveQ)`
- `const __FAR__ xdc_SizeT ti_sysbios_knl_Task_Module_State_terminatedQ_O = offsetof(ti_sysbios_knl_Task_Module_State_,Object_field_terminatedQ)`
- `const __FAR__ xdc_SizeT ti_sysbios_utils_Load_Module_State_taskList_O = offsetof(ti_sysbios_utils_Load_Module_State_,Object_field_taskList)`
- `xdcc_runtime_Error_Block xdc_runtime_Error_IgnoreBlock`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_diagsEnabled ti_sysbios_BIOS_Module_diagsEnabled_Enabled_C = (xdc_Bits32)0x90`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_diagsIncluded ti_sysbios_BIOS_Module_diagsIncluded_Included_C = (xdc_Bits32)0x90`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_diagsMask ti_sysbios_BIOS_Module_diagsMask_C = ((CT_ti_sysbios_BIOS_Module_diagsMask)0)`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_gateObj ti_sysbios_BIOS_Module_gateObj_C = ((CT_ti_sysbios_BIOS_Module_gateObj)0)`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_gatePrms ti_sysbios_BIOS_Module_gatePrms_C = ((CT_ti_sysbios_BIOS_Module_gatePrms)0)`
- `const __FAR__ CT_ti_sysbios_BIOS_Module_id ti_sysbios_BIOS_Module_id_C = (xdc_Bits16)0x1b`

- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerDefined **ti_sysbios_BIOS_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerObj **ti_sysbios_BIOS_Module_loggerObj_C** = ((CT_ti_sysbios_BIOS_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerFxn0 **ti_sysbios_BIOS_Module_loggerFxn0_C** = ((CT_ti_sysbios_BIOS_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerFxn1 **ti_sysbios_BIOS_Module_loggerFxn1_C** = ((CT_ti_sysbios_BIOS_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerFxn2 **ti_sysbios_BIOS_Module_loggerFxn2_C** = ((CT_ti_sysbios_BIOS_Module_loggerFxn2)0)
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerFxn4 **ti_sysbios_BIOS_Module_loggerFxn4_C** = ((CT_ti_sysbios_BIOS_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_BIOS_Module_loggerFxn8 **ti_sysbios_BIOS_Module_loggerFxn8_C** = ((CT_ti_sysbios_BIOS_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_BIOS_Object_count **ti_sysbios_BIOS_Object_count_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_Object_heap **ti_sysbios_BIOS_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_Object_sizeof **ti_sysbios_BIOS_Object_sizeof_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_Object_table **ti_sysbios_BIOS_Object_table_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_smpEnabled **ti_sysbios_BIOS_smpEnabled_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_mpeEnabled **ti_sysbios_BIOS_mpeEnabled_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_cpuFreq **ti_sysbios_BIOS_cpuFreq_C**
- const __FAR__ CT_ti_sysbios_BIOS_runtimeCreatesEnabled **ti_sysbios_BIOS_runtimeCreatesEnabled_C** = 1
- const __FAR__ CT_ti_sysbios_BIOS_taskEnabled **ti_sysbios_BIOS_taskEnabled_C** = 1
- const __FAR__ CT_ti_sysbios_BIOS_swiEnabled **ti_sysbios_BIOS_swiEnabled_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_clockEnabled **ti_sysbios_BIOS_clockEnabled_C** = 1
- const __FAR__ CT_ti_sysbios_BIOS_defaultKernelHeapInstance **ti_sysbios_BIOS_defaultKernelHeapInstance_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_kernelHeapSize **ti_sysbios_BIOS_kernelHeapSize_C** = (xdc_SizeT)0x1000
- const __FAR__ CT_ti_sysbios_BIOS_kernelHeapSection **ti_sysbios_BIOS_kernelHeapSection_C** = ".kernel_heap"
- const __FAR__ CT_ti_sysbios_BIOS_heapSize **ti_sysbios_BIOS_heapSize_C** = (xdc_SizeT)0x1000
- const __FAR__ CT_ti_sysbios_BIOS_heapSection **ti_sysbios_BIOS_heapSection_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_heapTrackEnabled **ti_sysbios_BIOS_heapTrackEnabled_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_setupSecureContext **ti_sysbios_BIOS_setupSecureContext_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_useSK **ti_sysbios_BIOS_useSK_C** = 0
- const __FAR__ CT_ti_sysbios_BIOS_installedErrorHook **ti_sysbios_BIOS_installedErrorHook_C** = ((CT_ti_sysbios_BIOS_installedErrorHook)((xdc_Fxn)xdc_runtime_Error_print_E))
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsEnabled **ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsIncluded **ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsMask **ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsMask_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsMask)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_gateObj **ti_sysbios_family_c62_IntrinsicsSupport_Module_gateObj_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_gateObj)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_gatePrms **ti_sysbios_family_c62_IntrinsicsSupport_Module_gatePrms_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_id **ti_sysbios_family_c62_IntrinsicsSupport_Module_id_C** = (xdc_Bits16)0x2e

- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerDefined **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerObj **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerObj_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn0 **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn0_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn1 **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn1_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn2 **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn2_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn2)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn4 **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn4_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn8 **ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn8_C** = ((CT_ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Object_count **ti_sysbios_family_c62_IntrinsicsSupport_Object_count_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Object_heap **ti_sysbios_family_c62_IntrinsicsSupport_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Object_sizeof **ti_sysbios_family_c62_IntrinsicsSupport_Object_sizeof_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_IntrinsicsSupport_Object_table **ti_sysbios_family_c62_IntrinsicsSupport_Object_table_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_diagsEnabled **ti_sysbios_family_c62_TaskSupport_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_diagsIncluded **ti_sysbios_family_c62_TaskSupport_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_diagsMask **ti_sysbios_family_c62_TaskSupport_Module_diagsMask_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_diagsMask)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_gateObj **ti_sysbios_family_c62_TaskSupport_Module_gateObj_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_gateObj)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_gatePrms **ti_sysbios_family_c62_TaskSupport_Module_gatePrms_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_id **ti_sysbios_family_c62_TaskSupport_Module_id_C** = (xdc_Bits16)0x2d
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_loggerDefined **ti_sysbios_family_c62_TaskSupport_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_loggerObj **ti_sysbios_family_c62_TaskSupport_Module_loggerObj_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0 **ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn1 **ti_sysbios_family_c62_TaskSupport_Module_loggerFxn1_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn2 **ti_sysbios_family_c62_TaskSupport_Module_loggerFxn2_C** = ((CT_ti_sysbios_family_c62_TaskSupport_Module_loggerFxn2)0)

- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Module_loggerFxn4 **ti_sysbios_family_c62_TaskSupport_Module_loggerFxn4_C** = ((CT__ti_sysbios_family_c62_TaskSupport_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Module_loggerFxn8 **ti_sysbios_family_c62_TaskSupport_Module_loggerFxn8_C** = ((CT__ti_sysbios_family_c62_TaskSupport_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Object_count **ti_sysbios_family_c62_TaskSupport_Object_count_C** = 0
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Object_heap **ti_sysbios_family_c62_TaskSupport_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Object_sizeof **ti_sysbios_family_c62_TaskSupport_Object_sizeof_C** = 0
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_Object_table **ti_sysbios_family_c62_TaskSupport_Object_table_C** = 0
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_defaultStackSize **ti_sysbios_family_c62_TaskSupport_defaultStackSize_C** = (xdc_SizeT)0x800
- const __FAR__ CT__ti_sysbios_family_c62_TaskSupport_stackAlignment **ti_sysbios_family_c62_TaskSupport_stackAlignment_C** = (xdc_UInt)0x8
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_diagsEnabled **ti_sysbios_family_c64p_Cache_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_diagsIncluded **ti_sysbios_family_c64p_Cache_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_diagsMask **ti_sysbios_family_c64p_Cache_Module_diagsMask_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_gateObj **ti_sysbios_family_c64p_Cache_Module_gateObj_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_gatePrms **ti_sysbios_family_c64p_Cache_Module_gatePrms_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_id **ti_sysbios_family_c64p_Cache_Module_id_C** = (xdc_Bits16)0x1a
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerDefined **ti_sysbios_family_c64p_Cache_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerObj **ti_sysbios_family_c64p_Cache_Module_loggerObj_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn0 **ti_sysbios_family_c64p_Cache_Module_loggerFxn0_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn1 **ti_sysbios_family_c64p_Cache_Module_loggerFxn1_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn2 **ti_sysbios_family_c64p_Cache_Module_loggerFxn2_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn4 **ti_sysbios_family_c64p_Cache_Module_loggerFxn4_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn8 **ti_sysbios_family_c64p_Cache_Module_loggerFxn8_C** = ((CT__ti_sysbios_family_c64p_Cache_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Object_count **ti_sysbios_family_c64p_Cache_Object_count_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Object_heap **ti_sysbios_family_c64p_Cache_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Object_sizeof **ti_sysbios_family_c64p_Cache_Object_sizeof_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_Object_table **ti_sysbios_family_c64p_Cache_Object_table_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_initSize **ti_sysbios_family_c64p_Cache_initSize_C**
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFA_CFG **ti_sysbios_family_c64p_Cache_EMIFA_CFG_C** = ((CT__ti_sysbios_family_c64p_Cache_EMIFA_CFG)0)

- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFA_BASE **ti_sysbios_family_c64p_Cache_EMIFA_BASE_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFA_LENGTH **ti_sysbios_family_c64p_Cache_EMIFA_LENGTH_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFB_CFG **ti_sysbios_family_c64p_Cache_EMIFB_CFG_C** = ((CT__ti_sysbios_family_c64p_Cache_EMIFB_CFG)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFB_BASE **ti_sysbios_family_c64p_Cache_EMIFB_BASE_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFB_LENGTH **ti_sysbios_family_c64p_Cache_EMIFB_LENGTH_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFC_CFG **ti_sysbios_family_c64p_Cache_EMIFC_CFG_C** = ((CT__ti_sysbios_family_c64p_Cache_EMIFC_CFG)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFC_BASE **ti_sysbios_family_c64p_Cache_EMIFC_BASE_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_EMIFC_LENGTH **ti_sysbios_family_c64p_Cache_EMIFC_LENGTH_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR0_31 **ti_sysbios_family_c64p_Cache_MAR0_31_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR32_63 **ti_sysbios_family_c64p_Cache_MAR32_63_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR64_95 **ti_sysbios_family_c64p_Cache_MAR64_95_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR96_127 **ti_sysbios_family_c64p_Cache_MAR96_127_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR128_159 **ti_sysbios_family_c64p_Cache_MAR128_159_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR160_191 **ti_sysbios_family_c64p_Cache_MAR160_191_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR192_223 **ti_sysbios_family_c64p_Cache_MAR192_223_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_MAR224_255 **ti_sysbios_family_c64p_Cache_MAR224_255_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_E_invalidL1CacheSize **ti_sysbios_family_c64p_Cache_E_invalidL1CacheSize_C** = (((xdc_runtime_Error_Id)17) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Cache_E_invalidL2CacheSize **ti_sysbios_family_c64p_Cache_E_invalidL2CacheSize_C** = (((xdc_runtime_Error_Id)18) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_diagsEnabled **ti_sysbios_family_c64p_EventCombiner_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_diagsIncluded **ti_sysbios_family_c64p_EventCombiner_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_diagsMask **ti_sysbios_family_c64p_EventCombiner_Module_diagsMask_C** = ((CT__ti_sysbios_family_c64p_EventCombiner_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_gateObj **ti_sysbios_family_c64p_EventCombiner_Module_gateObj_C** = ((CT__ti_sysbios_family_c64p_EventCombiner_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_gatePrms **ti_sysbios_family_c64p_EventCombiner_Module_gatePrms_C** = ((CT__ti_sysbios_family_c64p_EventCombiner_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_id **ti_sysbios_family_c64p_EventCombiner_Module_id_C** = (xdc_Bits16)0x16
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_loggerDefined **ti_sysbios_family_c64p_EventCombiner_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_EventCombiner_Module_loggerObj **ti_sysbios_family_c64p_EventCombiner_Module_loggerObj_C** = ((CT__ti_sysbios_family_c64p_EventCombiner_Module_loggerObj)0)

- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn0) **ti_symbios_family_c64p_EventCombiner_Module_loggerFxn0_C** = ((CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn0)0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn1) **ti_symbios_family_c64p_EventCombiner_Module_loggerFxn1_C** = ((CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn1)0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn2) **ti_symbios_family_c64p_EventCombiner_Module_loggerFxn2_C** = ((CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn2)0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn4) **ti_symbios_family_c64p_EventCombiner_Module_loggerFxn4_C** = ((CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn4)0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn8) **ti_symbios_family_c64p_EventCombiner_Module_loggerFxn8_C** = ((CT(ti_symbios_family_c64p_EventCombiner_Module_loggerFxn8)0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Object_count) **ti_symbios_family_c64p_EventCombiner_Object_count_C** = 0
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Object_heap) **ti_symbios_family_c64p_EventCombiner_Object_heap_C** = 0
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Object_sizeof) **ti_symbios_family_c64p_EventCombiner_Object_sizeof_C** = 0
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_Object_table) **ti_symbios_family_c64p_EventCombiner_Object_table_C** = 0
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_EVTRegs) **ti_symbios_family_c64p_EventCombiner_EVTRegs_C** = ((CT(ti_symbios_family_c64p_EventCombiner_EVTRegs)(0x1800000))
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_A_invalidEventId) **ti_symbios_family_c64p_EventCombiner_A_invalidEventId_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_E_unpluggedEvent) **ti_symbios_family_c64p_EventCombiner_E_unpluggedEvent_C** = (((xdc_runtime_Error_Id)9) << 16 | 0)
- const __FAR__ CT(ti_symbios_family_c64p_EventCombiner_EVTMASK) **ti_symbios_family_c64p_EventCombiner_EVTMASK_C**
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_diagsEnabled) **ti_symbios_family_c64p_Exception_Module_diagsEnabled_C** = (xdc_Bits32)0x190
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_diagsIncluded) **ti_symbios_family_c64p_Exception_Module_diagsIncluded_C** = (xdc_Bits32)0x190
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_diagsMask) **ti_symbios_family_c64p_Exception_Module_diagsMask_C** = ((CT(ti_symbios_family_c64p_Exception_Module_diagsMask)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_gateObj) **ti_symbios_family_c64p_Exception_Module_gateObj_C** = ((CT(ti_symbios_family_c64p_Exception_Module_gateObj)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_gatePrms) **ti_symbios_family_c64p_Exception_Module_gatePrms_C** = ((CT(ti_symbios_family_c64p_Exception_Module_gatePrms)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_id) **ti_symbios_family_c64p_Exception_Module_id_C** = (xdc_Bits16)0x17
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerDefined) **ti_symbios_family_c64p_Exception_Module_loggerDefined_C** = 0
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerObj) **ti_symbios_family_c64p_Exception_Module_loggerObj_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerObj)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerFxn0) **ti_symbios_family_c64p_Exception_Module_loggerFxn0_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerFxn0)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerFxn1) **ti_symbios_family_c64p_Exception_Module_loggerFxn1_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerFxn1)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerFxn2) **ti_symbios_family_c64p_Exception_Module_loggerFxn2_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerFxn2)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerFxn4) **ti_symbios_family_c64p_Exception_Module_loggerFxn4_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerFxn4)0))
- const __FAR__ CT(ti_symbios_family_c64p_Exception_Module_loggerFxn8) **ti_symbios_family_c64p_Exception_Module_loggerFxn8_C** = ((CT(ti_symbios_family_c64p_Exception_Module_loggerFxn8)0))

- const __FAR__ CT_ti_sysbios_family_c64p_Exception_Object_count **Exception_Object_count_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_Object_heap **Exception_Object_heap_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_Object_sizeof **Exception_Object_sizeof_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_Object_table **Exception_Object_table_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_E_exceptionMin **Exception_E_exceptionMin_C** = (((xdc_runtime_Error_Id)10) << 16 | 0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_E_exceptionMax **Exception_E_exceptionMax_C** = (((xdc_runtime_Error_Id)11) << 16 | 0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_useInternalBuffer **Exception_useInternalBuffer_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_enableExternalMPC **Exception_enableExternalMPC_C** = 0 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_enablePrint **Exception_enablePrint_C** = 1 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_exceptionHook **Exception_exceptionHook_C** = ((CT_ti_sysbios_family_c64p_Exception_exceptionHook)0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_internalHook **Exception_internalHook_C** = ((CT_ti_sysbios_family_c64p_Exception_internalHook)0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_externalHook **Exception_externalHook_C** = ((CT_ti_sysbios_family_c64p_Exception_externalHook)0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_nmiHook **ti_sysbios_family_c64p_Exception_nmiHook_C** = ((CT_ti_sysbios_family_c64p_Exception_nmiHook)0) **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Exception_returnHook **ti_sysbios_family_c64p_Exception_returnHook_C** = ((CT_ti_sysbios_family_c64p_Exception_returnHook)0) **ti_sysbios_family_c64p** ↵
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_family_c64p_Hwi_Object_DESC_C**
- const __FAR__ ti_sysbios_family_c64p_Hwi_Params **ti_sysbios_family_c64p_Hwi_Object_PARAMS_C**
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_diagsEnabled **ti_sysbios_family_c64p_Hwi_Module_diagsEnabled_C** = (xdc_Bits32)0x90 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_diagsIncluded **ti_sysbios_family_c64p_Hwi_Module_diagsIncluded_C** = (xdc_Bits32)0x90 **ti_sysbios_family_c64p** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_diagsMask **ti_sysbios_family_c64p_Hwi_Module_diagsMask_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_diagsMask)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_gateObj **ti_sysbios_family_c64p_Hwi_Module_gateObj_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_gateObj)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_gatePrms **ti_sysbios_family_c64p_Hwi_Module_gatePrms_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_gatePrms)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_id **ti_sysbios_family_c64p_Hwi_Module_id_C** = (xdc_Bits16)0x18 **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerDefined **ti_sysbios_family_c64p_Hwi_Module_loggerDefined_C** = 0 **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerObj **ti_sysbios_family_c64p_Hwi_Module_loggerObj_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerObj)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn0 **ti_sysbios_family_c64p_Hwi_Module_loggerFxn0_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn0)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn1 **ti_sysbios_family_c64p_Hwi_Module_loggerFxn1_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn1)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn2 **ti_sysbios_family_c64p_Hwi_Module_loggerFxn2_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn2)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn4 **ti_sysbios_family_c64p_Hwi_Module_loggerFxn4_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn4)0) **ti_sysbios_family_c64p_Hwi** ↵
- const __FAR__ CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn8 **ti_sysbios_family_c64p_Hwi_Module_loggerFxn8_C** = ((CT_ti_sysbios_family_c64p_Hwi_Module_loggerFxn8)0) **ti_sysbios_family_c64p_Hwi** ↵

- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_Object__count **ti_sysbios_family_c64p_Hwi_Object__count_C** = 5
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_Object__heap **ti_sysbios_family_c64p_Hwi_Object__heap_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_Object__sizeof **ti_sysbios_family_c64p_Hwi_Object__sizeof_C** = sizeof(**ti_sysbios_family_c64p_Hwi_Object**)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_Object__table **ti_sysbios_family_c64p_Hwi_Object__table_C** = **ti_sysbios_family_c64p_Hwi_Object__table_V**
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_dispatcherAutoNestingSupport **ti_sysbios_family_c64p_Hwi_dispatcherAutoNestingSupport_C** = 1
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_dispatcherSwiSupport **ti_sysbios_family_c64p_Hwi_dispatcherSwiSupport_C** = 0
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_dispatcherTaskSupport **ti_sysbios_family_c64p_Hwi_dispatcherTaskSupport_C** = 1
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_dispatcherIrpTrackingSupport **ti_sysbios_family_c64p_Hwi_dispatcherIrpTrackingSupport_C** = 1
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_INTRMUX1Address **ti_sysbios_family_c64p_Hwi_INTRMUX1Address_C** = ((CT__ti_sysbios_family_c64p_Hwi_INTRMUX1Address)(0x1800104))
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_E_alreadyDefined **ti_sysbios_family_c64p_Hwi_E_alreadyDefined_C** = (((xdc_runtime_Error_Id)12) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_E_handleNotFound **ti_sysbios_family_c64p_Hwi_E_handleNotFound_C** = (((xdc_runtime_Error_Id)13) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_E_allocSCFailed **ti_sysbios_family_c64p_Hwi_E_allocSCFailed_C** = (((xdc_runtime_Error_Id)14) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_E_registerSCFailed **ti_sysbios_family_c64p_Hwi_E_registerSCFailed_C** = (((xdc_runtime_Error_Id)15) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_E_invalidIntNum **ti_sysbios_family_c64p_Hwi_E_invalidIntNum_C** = (((xdc_runtime_Error_Id)16) << 16 | 0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_LM_begin **ti_sysbios_family_c64p_Hwi_LM_begin_C** = (((xdc_runtime_Log_Event)12) << 16 | 768)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_LD_end **ti_sysbios_family_c64p_Hwi_LD_end_C** = (((xdc_runtime_Log_Event)13) << 16 | 512)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_enableException **ti_sysbios_family_c64p_Hwi_enableException_C** = 1
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_swiDisable **ti_sysbios_family_c64p_Hwi_swiDisable_C** = ((CT__ti_sysbios_family_c64p_Hwi_swiDisable)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_swiRestoreHwi **ti_sysbios_family_c64p_Hwi_swiRestoreHwi_C** = ((CT__ti_sysbios_family_c64p_Hwi_swiRestoreHwi)0)
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_taskDisable **ti_sysbios_family_c64p_Hwi_taskDisable_C** = ((CT__ti_sysbios_family_c64p_Hwi_taskDisable)((xdc_Fxn) **ti_sysbios_knl_Task_disable_E**))
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_taskRestoreHwi **ti_sysbios_family_c64p_Hwi_taskRestoreHwi_C** = ((CT__ti_sysbios_family_c64p_Hwi_taskRestoreHwi)((xdc_Fxn) **ti_sysbios_knl_Task_restoreHwi_E**))
- const __FAR__ CT__ti_sysbios_family_c64p_Hwi_hooks **ti_sysbios_family_c64p_Hwi_hooks_C** = {0, 0}
- const __FAR__ CT__ti_sysbios_family_c64p_TimestampProvider_Module__diagsEnabled **ti_sysbios_family_c64p_TimestampProvider_Module__diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT__ti_sysbios_family_c64p_TimestampProvider_Module__diagsIncluded **ti_sysbios_family_c64p_TimestampProvider_Module__diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT__ti_sysbios_family_c64p_TimestampProvider_Module__diagsMask **ti_sysbios_family_c64p_TimestampProvider_Module__diagsMask_C** = ((CT__ti_sysbios_family_c64p_TimestampProvider_Module__diagsMask)0)
- const __FAR__ CT__ti_sysbios_family_c64p_TimestampProvider_Module__gateObj **ti_sysbios_family_c64p_TimestampProvider_Module__gateObj_C** = ((CT__ti_sysbios_family_c64p_TimestampProvider_Module__gateObj)0)

- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_gatePrms **ti_sysbios_family_c64p_TimestampProvider_Module_gatePrms_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_id **ti_sysbios_family_c64p_TimestampProvider_Module_id_C** = (xdc_Bits16)0x19
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerDefined **ti_sysbios_family_c64p_TimestampProvider_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerObj **ti_sysbios_family_c64p_TimestampProvider_Module_loggerObj_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn0 **ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn0_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn1 **ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn1_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn2 **ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn2_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn2)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn4 **ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn4_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn8 **ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn8_C** = ((CT_ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Object_count **ti_sysbios_family_c64p_TimestampProvider_Object_count_C** = 0
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Object_heap **ti_sysbios_family_c64p_TimestampProvider_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Object_sizeof **ti_sysbios_family_c64p_TimestampProvider_Object_sizeof_C** = 0
- const __FAR__ CT_ti_sysbios_family_c64p_TimestampProvider_Object_table **ti_sysbios_family_c64p_TimestampProvider_Object_table_C** = 0
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_gates_GateHwi_Object_DESC_C**
- const __FAR__ ti_sysbios_gates_GateHwi_Parms **ti_sysbios_gates_GateHwi_Object_PARAMS_C**
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_diagsEnabled **ti_sysbios_gates_GateHwi_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_diagsIncluded **ti_sysbios_gates_GateHwi_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_diagsMask **ti_sysbios_gates_GateHwi_Module_diagsMask_C** = ((CT_ti_sysbios_gates_GateHwi_Module_diagsMask)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_gateObj **ti_sysbios_gates_GateHwi_Module_gateObj_C** = ((CT_ti_sysbios_gates_GateHwi_Module_gateObj)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_gatePrms **ti_sysbios_gates_GateHwi_Module_gatePrms_C** = ((CT_ti_sysbios_gates_GateHwi_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_id **ti_sysbios_gates_GateHwi_Module_id_C** = (xdc_Bits16)0x2f
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerDefined **ti_sysbios_gates_GateHwi_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerObj **ti_sysbios_gates_GateHwi_Module_loggerObj_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerFxn0 **ti_sysbios_gates_GateHwi_Module_loggerFxn0_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerFxn1 **ti_sysbios_gates_GateHwi_Module_loggerFxn1_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerFxn2 **ti_sysbios_gates_GateHwi_Module_loggerFxn2_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerFxn2)0)

- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerFxn4 **ti_sysbios_gates_GateHwi_Module_loggerFxn4_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Module_loggerFxn8 **ti_sysbios_gates_GateHwi_Module_loggerFxn8_C** = ((CT_ti_sysbios_gates_GateHwi_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Object_count **ti_sysbios_gates_GateHwi_Object_count_C** = 1
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Object_heap **ti_sysbios_gates_GateHwi_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Object_sizeof **ti_sysbios_gates_GateHwi_Object_sizeof_C** = sizeof(**ti_sysbios_gates_GateHwi_Object**)
- const __FAR__ CT_ti_sysbios_gates_GateHwi_Object_table **ti_sysbios_gates_GateHwi_Object_table_C** = **ti_sysbios_gates_GateHwi_Object_table_V**
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_gates_GateMutex_Object_DESC_C**
- const __FAR__ ti_sysbios_gates_GateMutex_Params **ti_sysbios_gates_GateMutex_Object_PARAM_S_C**
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_diagsEnabled **ti_sysbios_gates_GateMutex_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_diagsIncluded **ti_sysbios_gates_GateMutex_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_diagsMask **ti_sysbios_gates_GateMutex_Module_diagsMask_C** = ((CT_ti_sysbios_gates_GateMutex_Module_diagsMask)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_gateObj **ti_sysbios_gates_GateMutex_Module_gateObj_C** = ((CT_ti_sysbios_gates_GateMutex_Module_gateObj)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_gatePrms **ti_sysbios_gates_GateMutex_Module_gatePrms_C** = ((CT_ti_sysbios_gates_GateMutex_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_id **ti_sysbios_gates_GateMutex_Module_id_C** = (xdc_Bits16)0x30
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerDefined **ti_sysbios_gates_GateMutex_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerObj **ti_sysbios_gates_GateMutex_Module_loggerObj_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerFxn0 **ti_sysbios_gates_GateMutex_Module_loggerFxn0_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerFxn1 **ti_sysbios_gates_GateMutex_Module_loggerFxn1_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerFxn2 **ti_sysbios_gates_GateMutex_Module_loggerFxn2_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerFxn2)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerFxn4 **ti_sysbios_gates_GateMutex_Module_loggerFxn4_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Module_loggerFxn8 **ti_sysbios_gates_GateMutex_Module_loggerFxn8_C** = ((CT_ti_sysbios_gates_GateMutex_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Object_count **ti_sysbios_gates_GateMutex_Object_count_C** = 2
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Object_heap **ti_sysbios_gates_GateMutex_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Object_sizeof **ti_sysbios_gates_GateMutex_Object_sizeof_C** = sizeof(**ti_sysbios_gates_GateMutex_Object**)
- const __FAR__ CT_ti_sysbios_gates_GateMutex_Object_table **ti_sysbios_gates_GateMutex_Object_table_C** = **ti_sysbios_gates_GateMutex_Object_table_V**
- const __FAR__ CT_ti_sysbios_gates_GateMutex_A_badContext **ti_sysbios_gates_GateMutex_A_badContext_C** = (((xdc_runtime_ASSERT_Id)0) << 16 | 16)
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_hal_Hwi_Object_DESC_C**
- const __FAR__ ti_sysbios_hal_Hwi_Params **ti_sysbios_hal_Hwi_Object_PARAMS_C**
- const __FAR__ CT_ti_sysbios_hal_Hwi_Module_diagsEnabled **ti_sysbios_hal_Hwi_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_hal_Hwi_Module_diagsIncluded **ti_sysbios_hal_Hwi_Module_diagsIncluded_C** = (xdc_Bits32)0x90

- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__diagsMask **ti_sysbios_hal_Hwi_Module_diagsMask_C** = ((CT__ti_sysbios_hal_Hwi_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__gateObj **ti_sysbios_hal_Hwi_Module_gateObj_C** = ((CT__ti_sysbios_hal_Hwi_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__gatePrms **ti_sysbios_hal_Hwi_Module_gatePrms_C** = ((CT__ti_sysbios_hal_Hwi_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__id **ti_sysbios_hal_Hwi_Module_id_C** = (xdc_Bits16)0x2b
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerDefined **ti_sysbios_hal_Hwi_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerObj **ti_sysbios_hal_Hwi_Module_loggerObj_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerFxn0 **ti_sysbios_hal_Hwi_Module_loggerFxn0_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerFxn1 **ti_sysbios_hal_Hwi_Module_loggerFxn1_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerFxn2 **ti_sysbios_hal_Hwi_Module_loggerFxn2_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerFxn4 **ti_sysbios_hal_Hwi_Module_loggerFxn4_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Module__loggerFxn8 **ti_sysbios_hal_Hwi_Module_loggerFxn8_C** = ((CT__ti_sysbios_hal_Hwi_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Object__count **ti_sysbios_hal_Hwi_Object_count_C** = 1
- const __FAR__ CT__ti_sysbios_hal_Hwi_Object__heap **ti_sysbios_hal_Hwi_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_hal_Hwi_Object__sizeof **ti_sysbios_hal_Hwi_Object_sizeof_C** = sizeof(**ti_sysbios_hal_Hwi_Object**)
- const __FAR__ CT__ti_sysbios_hal_Hwi_Object__table **ti_sysbios_hal_Hwi_Object_table_C** = **ti_sysbios_hal_Hwi_Object_table_V**
- const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherAutoNestingSupport **ti_sysbios_hal_Hwi_dispatcherAutoNestingSupport_C** = 1
- const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherSwiSupport **ti_sysbios_hal_Hwi_dispatcherSwiSupport_C** = 0
- const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherTaskSupport **ti_sysbios_hal_Hwi_dispatcherTaskSupport_C** = 1
- const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherIrpTrackingSupport **ti_sysbios_hal_Hwi_dispatcherIrpTrackingSupport_C** = 1
- const __FAR__ CT__ti_sysbios_hal_Hwi_E_stackOverflow **ti_sysbios_hal_Hwi_E_stackOverflow_C** = (((xdc_runtime_Error_Id)29) << 16 | 0)
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_heaps_HeapMem_Object_DESC_C**
- const __FAR__ ti_sysbios_heaps_HeapMem_Params **ti_sysbios_heaps_HeapMem_Object_PARAM_S_C**
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__diagsEnabled **ti_sysbios_heaps_HeapMem_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__diagsIncluded **ti_sysbios_heaps_HeapMem_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__diagsMask **ti_sysbios_heaps_HeapMem_Module_diagsMask_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__gateObj **ti_sysbios_heaps_HeapMem_Module_gateObj_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_gateObj)((const void*)(xdc_runtime_IGateProvider_Handle)&**ti_sysbios_gates_GateMutex_Object_table_V[0]**))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__gatePrms **ti_sysbios_heaps_HeapMem_Module_gatePrms_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__id **ti_sysbios_heaps_HeapMem_Module_id_C** = (xdc_Bits16)0x27
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module__loggerDefined **ti_sysbios_heaps_HeapMem_Module_loggerDefined_C** = 0

- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerObj **ti_sysbios_heaps_HeapMem_Module_loggerObj_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn0 **ti_sysbios_heaps_HeapMem_Module_loggerFxn0_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn1 **ti_sysbios_heaps_HeapMem_Module_loggerFxn1_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn2 **ti_sysbios_heaps_HeapMem_Module_loggerFxn2_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn4 **ti_sysbios_heaps_HeapMem_Module_loggerFxn4_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn8 **ti_sysbios_heaps_HeapMem_Module_loggerFxn8_C** = ((CT__ti_sysbios_heaps_HeapMem_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Object_count **ti_sysbios_heaps_HeapMem_Object_count_C** = 1
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Object_heap **ti_sysbios_heaps_HeapMem_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Object_sizeof **ti_sysbios_heaps_HeapMem_Object_sizeof_C** = sizeof(**ti_sysbios_heaps_HeapMem_Object**)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_Object_table **ti_sysbios_heaps_HeapMem_Object_table_C** = **ti_sysbios_heaps_HeapMem_Object_table_V**
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_A_zeroBlock **ti_sysbios_heaps_HeapMem_A_zeroBlock_C** = (((xdc_runtime_Assert_Id0) << 16 | 16))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_A_heapSize **ti_sysbios_heaps_HeapMem_A_heapSize_C** = (((xdc_runtime_Assert_Id0) << 16 | 16))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_A_align **ti_sysbios_heaps_HeapMem_A_align_C** = (((xdc_runtime_Assert_Id0) << 16 | 16))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_E_memory **ti_sysbios_heaps_HeapMem_E_memory_C** = (((xdc_runtime_Error_Id27) << 16 | 0))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_A_invalidFree **ti_sysbios_heaps_HeapMem_A_invalidFree_C** = (((xdc_runtime_Assert_Id0) << 16 | 16))
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr **ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr_C** = ((CT__ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_primaryHeapEndAddr **ti_sysbios_heaps_HeapMem_primaryHeapEndAddr_C** = ((CT__ti_sysbios_heaps_HeapMem_primaryHeapEndAddr)0)
- const __FAR__ CT__ti_sysbios_heaps_HeapMem_reqAlign **ti_sysbios_heaps_HeapMem_reqAlign_C** = (xdc_SizeT)0x8
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_knl_Clock_Object_DESC_C**
- const __FAR__ ti_sysbios_knl_Clock_Params **ti_sysbios_knl_Clock_Object_PARAMS_C**
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_diagsEnabled **ti_sysbios_knl_Clock_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_diagsIncluded **ti_sysbios_knl_Clock_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_diagsMask **ti_sysbios_knl_Clock_Module_diagsMask_C** = ((CT__ti_sysbios_knl_Clock_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_gateObj **ti_sysbios_knl_Clock_Module_gateObj_C** = ((CT__ti_sysbios_knl_Clock_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_gatePrms **ti_sysbios_knl_Clock_Module_gatePrms_C** = ((CT__ti_sysbios_knl_Clock_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_id **ti_sysbios_knl_Clock_Module_id_C** = (xdc_Bits16)0x1d
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerDefined **ti_sysbios_knl_Clock_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerObj **ti_sysbios_knl_Clock_Module_loggerObj_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerFxn0 **ti_sysbios_knl_Clock_Module_loggerFxn0_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerFxn0)0)

- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerFxn1 **ti_sysbios_knl_Clock_Module_loggerFxn1_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerFxn2 **ti_sysbios_knl_Clock_Module_loggerFxn2_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerFxn4 **ti_sysbios_knl_Clock_Module_loggerFxn4_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Module_loggerFxn8 **ti_sysbios_knl_Clock_Module_loggerFxn8_C** = ((CT__ti_sysbios_knl_Clock_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_knl_Clock_Object_count **ti_sysbios_knl_Clock_Object_count_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Clock_Object_heap **ti_sysbios_knl_Clock_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Clock_Object_sizeof **ti_sysbios_knl_Clock_Object_sizeof_C** = sizeof(**ti_sysbios_knl_Clock_Object**)
- const __FAR__ CT__ti_sysbios_knl_Clock_Object_table **ti_sysbios_knl_Clock_Object_table_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Clock_LW_delayed **ti_sysbios_knl_Clock_LW_delayed_C** = (((xdc_runtime_Log_Event)14) << 16 | 1024)
- const __FAR__ CT__ti_sysbios_knl_Clock_LM_tick **ti_sysbios_knl_Clock_LM_tick_C** = (((xdc_runtime_Log_Event)15) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Clock_LM_begin **ti_sysbios_knl_Clock_LM_begin_C** = (((xdc_runtime_Log_Event)16) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Clock_A_clockDisabled **ti_sysbios_knl_Clock_A_clockDisabled_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Clock_A_badThreadType **ti_sysbios_knl_Clock_A_badThreadType_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Clock_serviceMargin **ti_sysbios_knl_Clock_serviceMargin_C** = (xdc_UInt32)0x0
- const __FAR__ CT__ti_sysbios_knl_Clock_tickSource **ti_sysbios_knl_Clock_tickSource_C** = ti_sysbios_knl_Clock_TickSource_TIMER
- const __FAR__ CT__ti_sysbios_knl_Clock_tickMode **ti_sysbios_knl_Clock_tickMode_C** = ti_sysbios_knl_Clock_TickMode_PERIODIC
- const __FAR__ CT__ti_sysbios_knl_Clock_timerId **ti_sysbios_knl_Clock_timerId_C** = (xdc_UInt)(-0x0 - 1)
- const __FAR__ CT__ti_sysbios_knl_Clock_tickPeriod **ti_sysbios_knl_Clock_tickPeriod_C** = (xdc_UInt32)0x3e8
- const __FAR__ CT__ti_sysbios_knl_Clock_doTickFunc **ti_sysbios_knl_Clock_doTickFunc_C** = ((CT__ti_sysbios_knl_Clock_doTickFunc)(xdc_Fxn) **ti_sysbios_knl_Clock_doTick_!**)
- const __FAR__ CT__ti_sysbios_knl_Clock_triggerClock **ti_sysbios_knl_Clock_triggerClock_C** = 0
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_knl_Event_Object_DESC_C**
- const __FAR__ ti_sysbios_knl_Event_Params **ti_sysbios_knl_Event_Object_PARAMS_C**
- const __FAR__ CT__ti_sysbios_knl_Event_Module_diagsEnabled **ti_sysbios_knl_Event_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Event_Module_diagsIncluded **ti_sysbios_knl_Event_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Event_Module_diagsMask **ti_sysbios_knl_Event_Module_diagsMask_C** = ((CT__ti_sysbios_knl_Event_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_knl_Event_Module_gateObj **ti_sysbios_knl_Event_Module_gateObj_C** = ((CT__ti_sysbios_knl_Event_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_knl_Event_Module_gatePrms **ti_sysbios_knl_Event_Module_gatePrms_C** = ((CT__ti_sysbios_knl_Event_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_knl_Event_Module_id **ti_sysbios_knl_Event_Module_id_C** = (xdc_Bits16)0x20
- const __FAR__ CT__ti_sysbios_knl_Event_Module_loggerDefined **ti_sysbios_knl_Event_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Event_Module_loggerObj **ti_sysbios_knl_Event_Module_loggerObj_C** = ((CT__ti_sysbios_knl_Event_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_knl_Event_Module_loggerFxn0 **ti_sysbios_knl_Event_Module_loggerFxn0_C** = ((CT__ti_sysbios_knl_Event_Module_loggerFxn0)0)

- const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn1) ti_sysbios_knl_Event_Module/loggerFxn1_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn1)0))
- const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn2) ti_sysbios_knl_Event_Module/loggerFxn2_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn2)0))
- const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn4) ti_sysbios_knl_Event_Module/loggerFxn4_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn4)0))
- const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn8) ti_sysbios_knl_Event_Module/loggerFxn8_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn8)0))
- const __FAR__ CT(ti_sysbios_knl_Event_Object_count) ti_sysbios_knl_Event_Object_count_C = 0
- const __FAR__ CT(ti_sysbios_knl_Event_Object_heap) ti_sysbios_knl_Event_Object_heap_C = 0
- const __FAR__ CT(ti_sysbios_knl_Event_Object_sizeof) ti_sysbios_knl_Event_Object_sizeof_C = sizeof(ti_sysbios_knl_Event_Object)
- const __FAR__ CT(ti_sysbios_knl_Event_Object_table) ti_sysbios_knl_Event_Object_table_C = 0
- const __FAR__ CT(ti_sysbios_knl_Event_LM_post) ti_sysbios_knl_Event_LM_post_C = (((xdc(runtime_Log_Event)17) << 16 | 768))
- const __FAR__ CT(ti_sysbios_knl_Event_LM_pend) ti_sysbios_knl_Event_LM_pend_C = (((xdc(runtime_Log_Event)18) << 16 | 768))
- const __FAR__ CT(ti_sysbios_knl_Event_A_nullEventMasks) ti_sysbios_knl_Event_A_nullEventMasks_C = (((xdc_runtime_Assert_Id)0) << 16 | 16))
- const __FAR__ CT(ti_sysbios_knl_Event_A_nullEventId) ti_sysbios_knl_Event_A_nullEventId_C = (((xdc_runtime_Assert_Id)0) << 16 | 16))
- const __FAR__ CT(ti_sysbios_knl_Event_A_eventInUse) ti_sysbios_knl_Event_A_eventInUse_C = (((xdc_runtime_Assert_Id)0) << 16 | 16))
- const __FAR__ CT(ti_sysbios_knl_Event_A_badContext) ti_sysbios_knl_Event_A_badContext_C = (((xdc_runtime_Assert_Id)0) << 16 | 16))
- const __FAR__ CT(ti_sysbios_knl_Event_A_pendTaskDisabled) ti_sysbios_knl_Event_A_pendTaskDisabled_C = (((xdc_runtime_Assert_Id)0) << 16 | 16))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsEnabled) ti_sysbios_knl_Idle_Module_diagsEnabled_C = (xdc_Bits32)0x90
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsIncluded) ti_sysbios_knl_Idle_Module_diagsIncluded_C = (xdc_Bits32)0x90
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsMask) ti_sysbios_knl_Idle_Module_diagsMask_C = ((CT(ti_sysbios_knl_Idle_Module_diagsMask)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_gateObj) ti_sysbios_knl_Idle_Module_gateObj_C = ((CT(ti_sysbios_knl_Idle_Module_gateObj)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_gatePrms) ti_sysbios_knl_Idle_Module_gatePrms_C = ((CT(ti_sysbios_knl_Idle_Module_gatePrms)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_id) ti_sysbios_knl_Idle_Module_id_C = (xdc(Bits16)0xe)
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerDefined) ti_sysbios_knl_Idle_Module_loggerDefined_C = 0
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerObj) ti_sysbios_knl_Idle_Module_loggerObj_C = ((CT(ti_sysbios_knl_Idle_Module_loggerObj)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn0) ti_sysbios_knl_Idle_Module_loggerFxn0_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn0)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn1) ti_sysbios_knl_Idle_Module_loggerFxn1_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn1)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn2) ti_sysbios_knl_Idle_Module_loggerFxn2_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn2)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn4) ti_sysbios_knl_Idle_Module_loggerFxn4_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn4)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn8) ti_sysbios_knl_Idle_Module_loggerFxn8_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn8)0))
- const __FAR__ CT(ti_sysbios_knl_Idle_Object_count) ti_sysbios_knl_Idle_Object_count_C = 0
- const __FAR__ CT(ti_sysbios_knl_Idle_Object_heap) ti_sysbios_knl_Idle_Object_heap_C = 0
- const __FAR__ CT(ti_sysbios_knl_Idle_Object_sizeof) ti_sysbios_knl_Idle_Object_sizeof_C = 0

- const __FAR__ CT__ti_sysbios_knl_Idle_Object_table **ti_sysbios_knl_Idle_Object_table_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Idle_funcList **ti_sysbios_knl_Idle_funcList_C** = {3, ((__T1__ti__sysbios_knl_Idle_funcList *) **ti_sysbios_knl_Idle_funcList_A**)}
- const __FAR__ CT__ti_sysbios_knl_Idle_coreList **ti_sysbios_knl_Idle_coreList_C** = {3, ((__T1__ti__sysbios_knl_Idle_coreList *) **ti_sysbios_knl_Idle_coreList_A**)}
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_diagsEnabled **ti_sysbios_knl_Intrinsics_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_diagsIncluded **ti_sysbios_knl_Intrinsics_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_diagsMask **ti_sysbios_knl_Intrinsics_Module_diagsMask_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_gateObj **ti_sysbios_knl_Intrinsics_Module_gateObj_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_gatePrms **ti_sysbios_knl_Intrinsics_Module_gatePrms_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_id **ti_sysbios_knl_Intrinsics_Module_id_C** = (xdc_Bits16)0x1f
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerDefined **ti_sysbios_knl_Intrinsics_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerObj **ti_sysbios_knl_Intrinsics_Module_loggerObj_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn0 **ti_sysbios_knl_Intrinsics_Module_loggerFxn0_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn1 **ti_sysbios_knl_Intrinsics_Module_loggerFxn1_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn2 **ti_sysbios_knl_Intrinsics_Module_loggerFxn2_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn4 **ti_sysbios_knl_Intrinsics_Module_loggerFxn4_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn8 **ti_sysbios_knl_Intrinsics_Module_loggerFxn8_C** = ((CT__ti_sysbios_knl_Intrinsics_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Object_count **ti_sysbios_knl_Intrinsics_Object_count_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Object_heap **ti_sysbios_knl_Intrinsics_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Object_sizeof **ti_sysbios_knl_Intrinsics_Object_sizeof_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Intrinsics_Object_table **ti_sysbios_knl_Intrinsics_Object_table_C** = 0
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_knl_Queue_Object_DESC_C**
- const __FAR__ ti_sysbios_knl_Queue_Parms **ti_sysbios_knl_Queue_Object_PARAMS_C**
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_diagsEnabled **ti_sysbios_knl_Queue_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_diagsIncluded **ti_sysbios_knl_Queue_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_diagsMask **ti_sysbios_knl_Queue_Module_diagsMask_C** = ((CT__ti_sysbios_knl_Queue_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_gateObj **ti_sysbios_knl_Queue_Module_gateObj_C** = ((CT__ti_sysbios_knl_Queue_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_gatePrms **ti_sysbios_knl_Queue_Module_gatePrms_C** = ((CT__ti_sysbios_knl_Queue_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_id **ti_sysbios_knl_Queue_Module_id_C** = (xdc_Bits16)0x21
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_loggerDefined **ti_sysbios_knl_Queue_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Queue_Module_loggerObj **ti_sysbios_knl_Queue_Module_loggerObj_C** = ((CT__ti_sysbios_knl_Queue_Module_loggerObj)0)

- const __FAR__ CT__ti_sysbios_knl_Queue_Module__loggerFxn0 **ti_sysbios_knl_Queue_Module__loggerFxn0_C** = ((CT__ti_sysbios_knl_Queue_Module__loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module__loggerFxn1 **ti_sysbios_knl_Queue_Module__loggerFxn1_C** = ((CT__ti_sysbios_knl_Queue_Module__loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module__loggerFxn2 **ti_sysbios_knl_Queue_Module__loggerFxn2_C** = ((CT__ti_sysbios_knl_Queue_Module__loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module__loggerFxn4 **ti_sysbios_knl_Queue_Module__loggerFxn4_C** = ((CT__ti_sysbios_knl_Queue_Module__loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Module__loggerFxn8 **ti_sysbios_knl_Queue_Module__loggerFxn8_C** = ((CT__ti_sysbios_knl_Queue_Module__loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_knl_Queue_Object__count **ti_sysbios_knl_Queue_Object__count_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Queue_Object__heap **ti_sysbios_knl_Queue_Object__heap_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Queue_Object__sizeof **ti_sysbios_knl_Queue_Object__sizeof_C** = sizeof(**ti_sysbios_knl_Queue_Object**)
- const __FAR__ CT__ti_sysbios_knl_Queue_Object__table **ti_sysbios_knl_Queue_Object__table_C** = 0
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_knl_Semaphore_Object__DESC_C**
- const __FAR__ ti_sysbios_knl_Semaphore_Parms **ti_sysbios_knl_Semaphore_Object__PARAMS_C**
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__diagsEnabled **ti_sysbios_knl_Semaphore__Module__diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__diagsIncluded **ti_sysbios_knl_Semaphore__Module__diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__diagsMask **ti_sysbios_knl_Semaphore__Module__diagsMask_C** = ((CT__ti_sysbios_knl_Semaphore_Module__diagsMask)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__gateObj **ti_sysbios_knl_Semaphore__Module__gateObj_C** = ((CT__ti_sysbios_knl_Semaphore_Module__gateObj)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__gatePrms **ti_sysbios_knl_Semaphore__Module__gatePrms_C** = ((CT__ti_sysbios_knl_Semaphore_Module__gatePrms)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__id **ti_sysbios_knl_Semaphore_Module__id_C** = (xdc_Bits16)0x22
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerDefined **ti_sysbios_knl_Semaphore__Module__loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerObj **ti_sysbios_knl_Semaphore__Module__loggerObj_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerObj)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerFxn0 **ti_sysbios_knl_Semaphore__Module__loggerFxn0_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerFxn1 **ti_sysbios_knl_Semaphore__Module__loggerFxn1_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerFxn2 **ti_sysbios_knl_Semaphore__Module__loggerFxn2_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerFxn4 **ti_sysbios_knl_Semaphore__Module__loggerFxn4_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Module__loggerFxn8 **ti_sysbios_knl_Semaphore__Module__loggerFxn8_C** = ((CT__ti_sysbios_knl_Semaphore_Module__loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Object__count **ti_sysbios_knl_Semaphore_Object__count_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Object__heap **ti_sysbios_knl_Semaphore_Object__heap_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Object__sizeof **ti_sysbios_knl_Semaphore_Object__sizeof_C** = sizeof(**ti_sysbios_knl_Semaphore_Object**)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_Object__table **ti_sysbios_knl_Semaphore_Object__table_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Semaphore_LM_post **ti_sysbios_knl_Semaphore_LM_post_C** = (((xdc_runtime_Log_Event)19) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Semaphore_LM_pend **ti_sysbios_knl_Semaphore_LM_pend_C** = (((xdc_runtime_Log_Event)20) << 16 | 768)

- const __FAR__ CT_ti_sysbios_knl_Semaphore_A_noEvents **ti_sysbios_knl_Semaphore_A_noEvents_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_A_invTimeout **ti_sysbios_knl_Semaphore_A_invTimeout_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_A_badContext **ti_sysbios_knl_Semaphore_A_badContext_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_A_overflow **ti_sysbios_knl_Semaphore_A_overflow_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_A_pendTaskDisabled **ti_sysbios_knl_Semaphore_A_pendTaskDisabled_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_E_objectNotInKernelSpace **ti_sysbios_knl_Semaphore_E_objectNotInKernelSpace_C** = (((xdc_runtime Error_Id)19) << 16 | 0)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_supportsEvents **ti_sysbios_knl_Semaphore_supportsEvents_C** = 0
- const __FAR__ CT_ti_sysbios_knl_Semaphore_supportsPriority **ti_sysbios_knl_Semaphore_supportsPriority_C** = 1
- const __FAR__ CT_ti_sysbios_knl_Semaphore_eventPost **ti_sysbios_knl_Semaphore_eventPost_C** = ((CT_ti_sysbios_knl_Semaphore_eventPost)0)
- const __FAR__ CT_ti_sysbios_knl_Semaphore_eventSync **ti_sysbios_knl_Semaphore_eventSync_C** = ((CT_ti_sysbios_knl_Semaphore_eventSync)0)
- const __FAR__ xdc_runtime_Core_ObjDesc **ti_sysbios_knl_Task_Object_DESC_C**
- const __FAR__ ti_sysbios_knl_Task_Params **ti_sysbios_knl_Task_Object_PARAMS_C**
- const __FAR__ CT_ti_sysbios_knl_Task_Module_diagsEnabled **ti_sysbios_knl_Task_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_knl_Task_Module_diagsIncluded **ti_sysbios_knl_Task_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_ti_sysbios_knl_Task_Module_diagsMask **ti_sysbios_knl_Task_Module_diagsMask_C** = ((CT_ti_sysbios_knl_Task_Module_diagsMask)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_gateObj **ti_sysbios_knl_Task_Module_gateObj_C** = ((CT_ti_sysbios_knl_Task_Module_gateObj)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_gatePrms **ti_sysbios_knl_Task_Module_gatePrms_C** = ((CT_ti_sysbios_knl_Task_Module_gatePrms)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_id **ti_sysbios_knl_Task_Module_id_C** = (xdc_Bits16)0x23
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerDefined **ti_sysbios_knl_Task_Module_loggerDefined_C** = 0
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerObj **ti_sysbios_knl_Task_Module_loggerObj_C** = ((CT_ti_sysbios_knl_Task_Module_loggerObj)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerFxn0 **ti_sysbios_knl_Task_Module_loggerFxn0_C** = ((CT_ti_sysbios_knl_Task_Module_loggerFxn0)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerFxn1 **ti_sysbios_knl_Task_Module_loggerFxn1_C** = ((CT_ti_sysbios_knl_Task_Module_loggerFxn1)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerFxn2 **ti_sysbios_knl_Task_Module_loggerFxn2_C** = ((CT_ti_sysbios_knl_Task_Module_loggerFxn2)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerFxn4 **ti_sysbios_knl_Task_Module_loggerFxn4_C** = ((CT_ti_sysbios_knl_Task_Module_loggerFxn4)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Module_loggerFxn8 **ti_sysbios_knl_Task_Module_loggerFxn8_C** = ((CT_ti_sysbios_knl_Task_Module_loggerFxn8)0)
- const __FAR__ CT_ti_sysbios_knl_Task_Object_count **ti_sysbios_knl_Task_Object_count_C** = 1
- const __FAR__ CT_ti_sysbios_knl_Task_Object_heap **ti_sysbios_knl_Task_Object_heap_C** = 0
- const __FAR__ CT_ti_sysbios_knl_Task_Object_sizeof **ti_sysbios_knl_Task_Object_sizeof_C** = sizeof(**ti_sysbios_knl_Task_Object**)
- const __FAR__ CT_ti_sysbios_knl_Task_Object_table **ti_sysbios_knl_Task_Object_table_C** = **ti_sysbios_knl_Task_Object_table_V**
- const __FAR__ CT_ti_sysbios_knl_Task_LM_switch **ti_sysbios_knl_Task_LM_switch_C** = (((xdc_runtime Log_Event)21) << 16 | 768)

- const __FAR__ CT__ti_sysbios_knl_Task_LM_sleep **ti_sysbios_knl_Task_LM_sleep__C** = (((xdc_← runtime_Log_Event)22) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Task_LD_ready **ti_sysbios_knl_Task_LD_ready__C** = (((xdc_← runtime_Log_Event)23) << 16 | 512)
- const __FAR__ CT__ti_sysbios_knl_Task_LD_block **ti_sysbios_knl_Task_LD_block__C** = (((xdc_← runtime_Log_Event)24) << 16 | 512)
- const __FAR__ CT__ti_sysbios_knl_Task_LM_yield **ti_sysbios_knl_Task_LM_yield__C** = (((xdc_← runtime_Log_Event)25) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Task_LM_setPri **ti_sysbios_knl_Task_LM_setPri__C** = (((xdc_← runtime_Log_Event)26) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Task_LD_exit **ti_sysbios_knl_Task_LD_exit__C** = (((xdc_runtime_← Log_Event)27) << 16 | 512)
- const __FAR__ CT__ti_sysbios_knl_Task_LM_setAffinity **ti_sysbios_knl_Task_LM_setAffinity__C** = (((xdc_runtime_Log_Event)28) << 16 | 768)
- const __FAR__ CT__ti_sysbios_knl_Task_LM_schedule **ti_sysbios_knl_Task_LM_schedule__C** = (((xdc_runtime_Log_Event)29) << 16 | 1024)
- const __FAR__ CT__ti_sysbios_knl_Task_LM_noWork **ti_sysbios_knl_Task_LM_noWork__C** = (((xdc_← runtime_Log_Event)30) << 16 | 1024)
- const __FAR__ CT__ti_sysbios_knl_Task_E_stackOverflow **ti_sysbios_knl_Task_E_stackOverflow__C** = (((xdc_runtime_Error_Id)20) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_E_spOutOfBounds **ti_sysbios_knl_Task_E_spOutOfBounds__C** = (((xdc_runtime_Error_Id)21) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_E_deleteNotAllowed **ti_sysbios_knl_Task_E_deleteNotAllowed__C** = (((xdc_runtime_Error_Id)22) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_E_moduleStateCheckFailed **ti_sysbios_knl_Task_E_moduleStateCheckFailed__C** = (((xdc_runtime_Error_Id)23) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_E_objectCheckFailed **ti_sysbios_knl_Task_E_objectCheckFailed__C** = (((xdc_runtime_Error_Id)24) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_E_objectNotInKernelSpace **ti_sysbios_knl_Task_E_objectNotInKernelSpace__C** = (((xdc_runtime_Error_Id)25) << 16 | 0)
- const __FAR__ CT__ti_sysbios_knl_Task_A_badThreadType **ti_sysbios_knl_Task_A_badThreadType__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_badTaskState **ti_sysbios_knl_Task_A_badTaskState__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_noPendElem **ti_sysbios_knl_Task_A_noPendElem__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_taskDisabled **ti_sysbios_knl_Task_A_taskDisabled__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_badPriority **ti_sysbios_knl_Task_A_badPriority__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_badTimeout **ti_sysbios_knl_Task_A_badTimeout__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_badAffinity **ti_sysbios_knl_Task_A_badAffinity__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_sleepTaskDisabled **ti_sysbios_knl_Task_A_sleepTaskDisabled__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_A_invalidCoreId **ti_sysbios_knl_Task_A_invalidCoreId__C** = (((xdc_runtime_Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_knl_Task_numPriorities **ti_sysbios_knl_Task_numPriorities__C** = (xdc_← _UInt)0x10
- const __FAR__ CT__ti_sysbios_knl_Task_defaultStackSize **ti_sysbios_knl_Task_defaultStackSize__C** = (xdc_SizeT)0x5dc
- const __FAR__ CT__ti_sysbios_knl_Task_defaultStackHeap **ti_sysbios_knl_Task_defaultStackHeap__C** = 0
- const __FAR__ CT__ti_sysbios_knl_Task_allBlockedFunc **ti_sysbios_knl_Task_allBlockedFunc__C** = ((CT__ti_sysbios_knl_Task_allBlockedFunc)0)

- const __FAR__ CT__ti_sysbios_knl_Task_initStackFlag **ti_sysbios_knl_Task_initStackFlag_C** = 1
- const __FAR__ CT__ti_sysbios_knl_Task_checkStackFlag **ti_sysbios_knl_Task_checkStackFlag_C** = 1
- const __FAR__ CT__ti_sysbios_knl_Task_deleteTerminatedTasks **ti_sysbios_knl_Task_deleteTerminatedTasks_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Task_hooks **ti_sysbios_knl_Task_hooks_C** = {1, ((__T1__ti_sysbios_knl_Task_hooks *) **ti_sysbios_knl_Task_hooks_A**)}
- const __FAR__ CT__ti_sysbios_knl_Task_moduleStateCheckFxn **ti_sysbios_knl_Task_moduleStateCheckFxn_C** = ((CT__ti_sysbios_knl_Task_moduleStateCheckFxn)((xdc_Fxn)ti_sysbios_knl_Task_moduleStateCheck_!))
- const __FAR__ CT__ti_sysbios_knl_Task_moduleStateCheckValueFxn **ti_sysbios_knl_Task_moduleStateCheckValueFxn_C** = ((CT__ti_sysbios_knl_Task_moduleStateCheckValueFxn)((xdc_Fxn)ti_sysbios_knl_Task_getModuleStateCheckValue_!))
- const __FAR__ CT__ti_sysbios_knl_Task_moduleStateCheckFlag **ti_sysbios_knl_Task_moduleStateCheckFlag_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Task_objectCheckFxn **ti_sysbios_knl_Task_objectCheckFxn_C** = ((CT__ti_sysbios_knl_Task_objectCheckFxn)((xdc_Fxn)ti_sysbios_knl_Task_objectCheck_!))
- const __FAR__ CT__ti_sysbios_knl_Task_objectCheckValueFxn **ti_sysbios_knl_Task_objectCheckValueFxn_C** = ((CT__ti_sysbios_knl_Task_objectCheckValueFxn)((xdc_Fxn)ti_sysbios_knl_Task_getObjectCheckValue_!))
- const __FAR__ CT__ti_sysbios_knl_Task_objectCheckFlag **ti_sysbios_knl_Task_objectCheckFlag_C** = 0
- const __FAR__ CT__ti_sysbios_knl_Task_numConstructedTasks **ti_sysbios_knl_Task_numConstructedTasks_C** = (xdc_UInt)0x0
- const __FAR__ CT__ti_sysbios_knl_Task_startupHookFunc **ti_sysbios_knl_Task_startupHookFunc_C** = ((CT__ti_sysbios_knl_Task_startupHookFunc)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsEnabled **ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsIncluded **ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsMask **ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsMask_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_gateObj **ti_sysbios_rts_ti_ThreadLocalStorage_Module_gateObj_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_gatePrms **ti_sysbios_rts_ti_ThreadLocalStorage_Module_gatePrms_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_id **ti_sysbios_rts_ti_ThreadLocalStorage_Module_id_C** = (xdc_Bits16)0x2a
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerDefined **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerObj **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerObj_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn0 **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn0_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn1 **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn1_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn2 **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn2_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn4 **ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn4_C** = ((CT__ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn4)0)

- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module.loggerFxn8) ti_sysbios_rts(ti_ThreadLocalStorage_Module.loggerFxn8_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module.loggerFxn8)0)
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object.count) ti_sysbios_rts(ti_ThreadLocalStorage_Object.count_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object.heap) ti_sysbios_rts(ti_ThreadLocalStorage_Object.heap_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object.sizeof) ti_sysbios_rts(ti_ThreadLocalStorage_Object.sizeof_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object.table) ti_sysbios_rts(ti_ThreadLocalStorage_Object.table_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_heapHandle) ti_sysbios_rts(ti_ThreadLocalStorage_heapHandle_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_enableTLSSupport) ti_sysbios_rts(ti_ThreadLocalStorage_enableTLSSupport_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_TlIlsSectMemory) ti_sysbios_rts(ti_ThreadLocalStorage_TlIlsSectMemory_C = 0
- const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_TlIls_initSectMemory) ti_sysbios_rts(ti_ThreadLocalStorage_TlIls_initSectMemory_C = 0
- const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_timers_rti_Timer_Object.DESC_C
- const __FAR__ ti_sysbios_timers_rti_Timer_Params ti_sysbios_timers_rti_Timer_Object.PARAMS_C
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.diagsEnabled) ti_sysbios_timers_rti_Timer_Module.diagsEnabled_C = (xdc_Bits32)0x90
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.diagsIncluded) ti_sysbios_timers_rti_Timer_Module.diagsIncluded_C = (xdc_Bits32)0x90
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.diagsMask) ti_sysbios_timers_rti_Timer_Module.diagsMask_C = ((CT(ti_sysbios_timers_rti_Timer_Module.diagsMask)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.gateObj) ti_sysbios_timers_rti_Timer_Module.gateObj_C = ((CT(ti_sysbios_timers_rti_Timer_Module.gateObj)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.gatePrms) ti_sysbios_timers_rti_Timer_Module.gatePrms_C = ((CT(ti_sysbios_timers_rti_Timer_Module.gatePrms)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.id) ti_sysbios_timers_rti_Timer_Module.id_C = (xdc_Bits16)0x31
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerDefined) ti_sysbios_timers_rti_Timer_Module.loggerDefined_C = 0
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerObj) ti_sysbios_timers_rti_Timer_Module.loggerObj_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerObj)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn0) ti_sysbios_timers_rti_Timer_Module.loggerFxn0_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn0)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn1) ti_sysbios_timers_rti_Timer_Module.loggerFxn1_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn1)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn2) ti_sysbios_timers_rti_Timer_Module.loggerFxn2_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn2)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn4) ti_sysbios_timers_rti_Timer_Module.loggerFxn4_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn4)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn8) ti_sysbios_timers_rti_Timer_Module.loggerFxn8_C = ((CT(ti_sysbios_timers_rti_Timer_Module.loggerFxn8)0)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object.count) ti_sysbios_timers_rti_Timer_Object.count_C = 1
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object.heap) ti_sysbios_timers_rti_Timer_Object.heap_C = 0
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object.sizeof) ti_sysbios_timers_rti_Timer_Object.sizeof_C = sizeof(ti_sysbios_timers_rti_Timer_Object)
- const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object.table) ti_sysbios_timers_rti_Timer_Object.table_C = ti_sysbios_timers_rti_Timer_Object.table_V

- const __FAR__ CT__ti_sysbios_timers_rti_Timer_A_invalidTimer **ti_sysbios_timers_rti_Timer_A_invalidTimer_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_E_invalidTimer **ti_sysbios_timers_rti_Timer_E_invalidTimer_C** = (((xdc_runtime Error_Id)31) << 16 | 0)
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_E_notAvailable **ti_sysbios_timers_rti_Timer_E_notAvailable_C** = (((xdc_runtime Error_Id)32) << 16 | 0)
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_E_invalidHwiMask **ti_sysbios_timers_rti_Timer_E_invalidHwiMask_C** = (((xdc_runtime Error_Id)33) << 16 | 0)
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_E_cannotSupport **ti_sysbios_timers_rti_Timer_E_cannotSupport_C** = (((xdc_runtime Error_Id)34) << 16 | 0)
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_anyMask **ti_sysbios_timers_rti_Timer_anyMask_C** = (xdc_UInt)0x3
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_continueOnSuspend **ti_sysbios_timers_rti_Timer_continueOnSuspend_C** = 0
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_startupNeeded **ti_sysbios_timers_rti_Timer_startupNeeded_C** = (xdc_UInt)0x1
- const __FAR__ CT__ti_sysbios_timers_rti_Timer_numTimerDevices **ti_sysbios_timers_rti_Timer_numTimerDevices_C** = (xdc_Int)0x2
- const __FAR__ CT__ti_sysbios_utils_Load_Module_diagsEnabled **ti_sysbios_utils_Load_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_utils_Load_Module_diagsIncluded **ti_sysbios_utils_Load_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT__ti_sysbios_utils_Load_Module_diagsMask **ti_sysbios_utils_Load_Module_diagsMask_C** = ((CT__ti_sysbios_utils_Load_Module_diagsMask)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_gateObj **ti_sysbios_utils_Load_Module_gateObj_C** = ((CT__ti_sysbios_utils_Load_Module_gateObj)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_gatePrms **ti_sysbios_utils_Load_Module_gatePrms_C** = ((CT__ti_sysbios_utils_Load_Module_gatePrms)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_id **ti_sysbios_utils_Load_Module_id_C** = (xdc_Bits16)0x29
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerDefined **ti_sysbios_utils_Load_Module_loggerDefined_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerObj **ti_sysbios_utils_Load_Module_loggerObj_C** = ((CT__ti_sysbios_utils_Load_Module_loggerObj)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerFxn0 **ti_sysbios_utils_Load_Module_loggerFxn0_C** = ((CT__ti_sysbios_utils_Load_Module_loggerFxn0)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerFxn1 **ti_sysbios_utils_Load_Module_loggerFxn1_C** = ((CT__ti_sysbios_utils_Load_Module_loggerFxn1)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerFxn2 **ti_sysbios_utils_Load_Module_loggerFxn2_C** = ((CT__ti_sysbios_utils_Load_Module_loggerFxn2)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerFxn4 **ti_sysbios_utils_Load_Module_loggerFxn4_C** = ((CT__ti_sysbios_utils_Load_Module_loggerFxn4)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerFxn8 **ti_sysbios_utils_Load_Module_loggerFxn8_C** = ((CT__ti_sysbios_utils_Load_Module_loggerFxn8)0)
- const __FAR__ CT__ti_sysbios_utils_Load_Object_count **ti_sysbios_utils_Load_Object_count_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_Object_heap **ti_sysbios_utils_Load_Object_heap_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_Object_sizeof **ti_sysbios_utils_Load_Object_sizeof_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_Object_table **ti_sysbios_utils_Load_Object_table_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_LS_cpuLoad **ti_sysbios_utils_Load_LS_cpuLoad_C** = (((xdc_runtime Log_Event)31) << 16 | 2048)
- const __FAR__ CT__ti_sysbios_utils_Load_LS_hwiLoad **ti_sysbios_utils_Load_LS_hwiLoad_C** = (((xdc_runtime Log_Event)32) << 16 | 2048)
- const __FAR__ CT__ti_sysbios_utils_Load_LS_swiload **ti_sysbios_utils_Load_LS_swiload_C** = (((xdc_runtime Log_Event)33) << 16 | 2048)

- const __FAR__ CT__ti_sysbios_utils_Load_LS_taskLoad **ti_sysbios_utils_Load_LS_taskLoad_C** = (((xdc_runtime_Log_Event34) << 16 | 2048)
- const __FAR__ CT__ti_sysbios_utils_Load_postUpdate **ti_sysbios_utils_Load_postUpdate_C** = ((CT__ti_sysbios_utils_Load_postUpdate0)
- const __FAR__ CT__ti_sysbios_utils_Load_updateInIdle **ti_sysbios_utils_Load_updateInIdle_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_windowInMs **ti_sysbios_utils_Load_windowInMs_C** = (xdc_UInt)0x1f4
- const __FAR__ CT__ti_sysbios_utils_Load_hwiEnabled **ti_sysbios_utils_Load_hwiEnabled_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_swiEnabled **ti_sysbios_utils_Load_swiEnabled_C** = 0
- const __FAR__ CT__ti_sysbios_utils_Load_taskEnabled **ti_sysbios_utils_Load_taskEnabled_C** = 1
- const __FAR__ CT__ti_sysbios_utils_Load_autoAddTasks **ti_sysbios_utils_Load_autoAddTasks_C** = 1
- const __FAR__ CT__xdc_runtime_Assert_Module_diagsEnabled **xdc_runtime_Assert_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT__xdc_runtime_Assert_Module_diagsIncluded **xdc_runtime_Assert_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT__xdc_runtime_Assert_Module_diagsMask **xdc_runtime_Assert_Module_diagsMask_C** = ((CT__xdc_runtime_Assert_Module_diagsMask0))
- const __FAR__ CT__xdc_runtime_Assert_Module_gateObj **xdc_runtime_Assert_Module_gateObj_C** = ((CT__xdc_runtime_Assert_Module_gateObj0))
- const __FAR__ CT__xdc_runtime_Assert_Module_gatePrms **xdc_runtime_Assert_Module_gatePrms_C** = ((CT__xdc_runtime_Assert_Module_gatePrms0))
- const __FAR__ CT__xdc_runtime_Assert_Module_id **xdc_runtime_Assert_Module_id_C** = (xdc_Bits16)0x1
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerDefined **xdc_runtime_Assert_Module_loggerDefined_C** = 0
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerObj **xdc_runtime_Assert_Module_loggerObj_C** = ((CT__xdc_runtime_Assert_Module_loggerObj0))
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerFxn0 **xdc_runtime_Assert_Module_loggerFxn0_C** = ((CT__xdc_runtime_Assert_Module_loggerFxn00))
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerFxn1 **xdc_runtime_Assert_Module_loggerFxn1_C** = ((CT__xdc_runtime_Assert_Module_loggerFxn10))
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerFxn2 **xdc_runtime_Assert_Module_loggerFxn2_C** = ((CT__xdc_runtime_Assert_Module_loggerFxn20))
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerFxn4 **xdc_runtime_Assert_Module_loggerFxn4_C** = ((CT__xdc_runtime_Assert_Module_loggerFxn40))
- const __FAR__ CT__xdc_runtime_Assert_Module_loggerFxn8 **xdc_runtime_Assert_Module_loggerFxn8_C** = ((CT__xdc_runtime_Assert_Module_loggerFxn80))
- const __FAR__ CT__xdc_runtime_Assert_Object_count **xdc_runtime_Assert_Object_count_C** = 0
- const __FAR__ CT__xdc_runtime_Assert_Object_heap **xdc_runtime_Assert_Object_heap_C** = 0
- const __FAR__ CT__xdc_runtime_Assert_Object_sizeof **xdc_runtime_Assert_Object_sizeof_C** = 0
- const __FAR__ CT__xdc_runtime_Assert_Object_table **xdc_runtime_Assert_Object_table_C** = 0
- const __FAR__ CT__xdc_runtime_Assert_E_assertFailed **xdc_runtime_Assert_E_assertFailed_C** = (((xdc_runtime_Error_Id1) << 16 | 0))
- const __FAR__ CT__xdc_runtime_Core_Module_diagsEnabled **xdc_runtime_Core_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT__xdc_runtime_Core_Module_diagsIncluded **xdc_runtime_Core_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT__xdc_runtime_Core_Module_diagsMask **xdc_runtime_Core_Module_diagsMask_C** = ((CT__xdc_runtime_Core_Module_diagsMask0))
- const __FAR__ CT__xdc_runtime_Core_Module_gateObj **xdc_runtime_Core_Module_gateObj_C** = ((CT__xdc_runtime_Core_Module_gateObj0))
- const __FAR__ CT__xdc_runtime_Core_Module_gatePrms **xdc_runtime_Core_Module_gatePrms_C** = ((CT__xdc_runtime_Core_Module_gatePrms0))
- const __FAR__ CT__xdc_runtime_Core_Module_id **xdc_runtime_Core_Module_id_C** = (xdc_Bits16)0x2

- const __FAR__ CT_xdc_runtime_Core_Module_loggerDefined **xdc_runtime_Core_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Core_Module_loggerObj **xdc_runtime_Core_Module_loggerObj_C** = ((CT_xdc_runtime_Core_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn0 **xdc_runtime_Core_Module_loggerFxn0_C** = ((CT_xdc_runtime_Core_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn1 **xdc_runtime_Core_Module_loggerFxn1_C** = ((CT_xdc_runtime_Core_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn2 **xdc_runtime_Core_Module_loggerFxn2_C** = ((CT_xdc_runtime_Core_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn4 **xdc_runtime_Core_Module_loggerFxn4_C** = ((CT_xdc_runtime_Core_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn8 **xdc_runtime_Core_Module_loggerFxn8_C** = ((CT_xdc_runtime_Core_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Core_Object_count **xdc_runtime_Core_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Core_Object_heap **xdc_runtime_Core_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Core_Object_sizeof **xdc_runtime_Core_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Core_Object_table **xdc_runtime_Core_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Core_A_initializedParams **xdc_runtime_Core_A_initializedParams_C** = ((xdc_runtime_ASSERT_Id)0) << 16 | 16)
- const __FAR__ CT_xdc_runtime_Defaults_Module_diagsEnabled **xdc_runtime_Defaults_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Defaults_Module_diagsIncluded **xdc_runtime_Defaults_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Defaults_Module_diagsMask **xdc_runtime_Defaults_Module_diagsMask_C** = ((CT_xdc_runtime_Defaults_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_gateObj **xdc_runtime_Defaults_Module_gateObj_C** = ((CT_xdc_runtime_Defaults_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_gatePrms **xdc_runtime_Defaults_Module_gatePrms_C** = ((CT_xdc_runtime_Defaults_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_id **xdc_runtime_Defaults_Module_id_C** = (xdc_Bits16)0x3
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerDefined **xdc_runtime_Defaults_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerObj **xdc_runtime_Defaults_Module_loggerObj_C** = ((CT_xdc_runtime_Defaults_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn0 **xdc_runtime_Defaults_Module_loggerFxn0_C** = ((CT_xdc_runtime_Defaults_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn1 **xdc_runtime_Defaults_Module_loggerFxn1_C** = ((CT_xdc_runtime_Defaults_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn2 **xdc_runtime_Defaults_Module_loggerFxn2_C** = ((CT_xdc_runtime_Defaults_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn4 **xdc_runtime_Defaults_Module_loggerFxn4_C** = ((CT_xdc_runtime_Defaults_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn8 **xdc_runtime_Defaults_Module_loggerFxn8_C** = ((CT_xdc_runtime_Defaults_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Defaults_Object_count **xdc_runtime_Defaults_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Defaults_Object_heap **xdc_runtime_Defaults_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Defaults_Object_sizeof **xdc_runtime_Defaults_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Defaults_Object_table **xdc_runtime_Defaults_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_Module_diagsEnabled **xdc_runtime_Diags_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Diags_Module_diagsIncluded **xdc_runtime_Diags_Module_diagsIncluded_C** = (xdc_Bits32)0x10

- const __FAR__ CT_xdc_runtime_Diags_Module_diagsMask **xdc_runtime_Diags_Module_diagsMask_C** = ((CT_xdc_runtime_Diags_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_gateObj **xdc_runtime_Diags_Module_gateObj_C** = ((CT_xdc_runtime_Diags_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_gatePrms **xdc_runtime_Diags_Module_gatePrms_C** = ((CT_xdc_runtime_Diags_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_id **xdc_runtime_Diags_Module_id_C** = (xdc_Bits16)0x4
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerDefined **xdc_runtime_Diags_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerObj **xdc_runtime_Diags_Module_loggerObj_C** = ((CT_xdc_runtime_Diags_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn0 **xdc_runtime_Diags_Module_loggerFxn0_C** = ((CT_xdc_runtime_Diags_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn1 **xdc_runtime_Diags_Module_loggerFxn1_C** = ((CT_xdc_runtime_Diags_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn2 **xdc_runtime_Diags_Module_loggerFxn2_C** = ((CT_xdc_runtime_Diags_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn4 **xdc_runtime_Diags_Module_loggerFxn4_C** = ((CT_xdc_runtime_Diags_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn8 **xdc_runtime_Diags_Module_loggerFxn8_C** = ((CT_xdc_runtime_Diags_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Diags_Object_count **xdc_runtime_Diags_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_Object_heap **xdc_runtime_Diags_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_Object_sizeof **xdc_runtime_Diags_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_Object_table **xdc_runtime_Diags_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_setMaskEnabled **xdc_runtime_Diags_setMaskEnabled_C** = 0
- const __FAR__ CT_xdc_runtime_Diags_dictBase **xdc_runtime_Diags_dictBase_C** = ((CT_xdc_runtime_Diags_dictBase)0)
- const __FAR__ CT_xdc_runtime_Error_Module_diagsEnabled **xdc_runtime_Error_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Error_Module_diagsIncluded **xdc_runtime_Error_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Error_Module_diagsMask **xdc_runtime_Error_Module_diagsMask_C** = ((CT_xdc_runtime_Error_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Error_Module_gateObj **xdc_runtime_Error_Module_gateObj_C** = ((CT_xdc_runtime_Error_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Error_Module_gatePrms **xdc_runtime_Error_Module_gatePrms_C** = ((CT_xdc_runtime_Error_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Error_Module_id **xdc_runtime_Error_Module_id_C** = (xdc_Bits16)0x5
- const __FAR__ CT_xdc_runtime_Error_Module_loggerDefined **xdc_runtime_Error_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Error_Module_loggerObj **xdc_runtime_Error_Module_loggerObj_C** = ((CT_xdc_runtime_Error_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn0 **xdc_runtime_Error_Module_loggerFxn0_C** = ((CT_xdc_runtime_Error_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn1 **xdc_runtime_Error_Module_loggerFxn1_C** = ((CT_xdc_runtime_Error_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn2 **xdc_runtime_Error_Module_loggerFxn2_C** = ((CT_xdc_runtime_Error_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn4 **xdc_runtime_Error_Module_loggerFxn4_C** = ((CT_xdc_runtime_Error_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn8 **xdc_runtime_Error_Module_loggerFxn8_C** = ((CT_xdc_runtime_Error_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Error_Object_count **xdc_runtime_Error_Object_count_C** = 0

- const __FAR__ CT_xdc_runtime_Error_Object_heap **xdc_runtime_Error_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Error_Object_sizeof **xdc_runtime_Error_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Error_Object_table **xdc_runtime_Error_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Error_policyFxn **xdc_runtime_Error_policyFxn_C** = ((CT_xdc_runtime_Error_policyFxn)((xdc_Fxn)xdc_runtime_Error_policyDefault_E))
- const __FAR__ CT_xdc_runtime_Error_E_generic **xdc_runtime_Error_E_generic_C** = (((xdc_runtime_Error_Id2)<< 16 | 0)
- const __FAR__ CT_xdc_runtime_Error_E_memory **xdc_runtime_Error_E_memory_C** = (((xdc_runtime_Error_Id3)<< 16 | 0)
- const __FAR__ CT_xdc_runtime_Error_E_msgCode **xdc_runtime_Error_E_msgCode_C** = (((xdc_runtime_Error_Id4)<< 16 | 0)
- const __FAR__ CT_xdc_runtime_Error_policy **xdc_runtime_Error_policy_C** = xdc_runtime_Error_U<= NWIND
- const __FAR__ CT_xdc_runtime_Error_raiseHook **xdc_runtime_Error_raiseHook_C** = ((CT_xdc_runtime_Error_raiseHook)((xdc_Fxn)ti_sysbios_BIOS_errorRaiseHook_I))
- const __FAR__ CT_xdc_runtime_Error_maxDepth **xdc_runtime_Error_maxDepth_C** = (xdc_U<= Int16)0x10
- const __FAR__ CT_xdc_runtime_Gate_Module_diagsEnabled **xdc_runtime_Gate_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Gate_Module_diagsIncluded **xdc_runtime_Gate_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Gate_Module_diagsMask **xdc_runtime_Gate_Module_diagsMask_C** = ((CT_xdc_runtime_Gate_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_gateObj **xdc_runtime_Gate_Module_gateObj_C** = ((CT_xdc_runtime_Gate_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_gatePrms **xdc_runtime_Gate_Module_gatePrms_C** = ((CT_xdc_runtime_Gate_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_id **xdc_runtime_Gate_Module_id_C** = (xdc_Bits16)0x6
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerDefined **xdc_runtime_Gate_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerObj **xdc_runtime_Gate_Module_loggerObj_C** = ((CT_xdc_runtime_Gate_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn0 **xdc_runtime_Gate_Module_loggerFxn0_C** = ((CT_xdc_runtime_Gate_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn1 **xdc_runtime_Gate_Module_loggerFxn1_C** = ((CT_xdc_runtime_Gate_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn2 **xdc_runtime_Gate_Module_loggerFxn2_C** = ((CT_xdc_runtime_Gate_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn4 **xdc_runtime_Gate_Module_loggerFxn4_C** = ((CT_xdc_runtime_Gate_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn8 **xdc_runtime_Gate_Module_loggerFxn8_C** = ((CT_xdc_runtime_Gate_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Gate_Object_count **xdc_runtime_Gate_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Gate_Object_heap **xdc_runtime_Gate_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Gate_Object_sizeof **xdc_runtime_Gate_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Gate_Object_table **xdc_runtime_Gate_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Log_Module_diagsEnabled **xdc_runtime_Log_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Log_Module_diagsIncluded **xdc_runtime_Log_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Log_Module_diagsMask **xdc_runtime_Log_Module_diagsMask_C** = ((CT_xdc_runtime_Log_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Log_Module_gateObj **xdc_runtime_Log_Module_gateObj_C** = ((CT_xdc_runtime_Log_Module_gateObj)0)

- const __FAR__ CT_xdc_runtime_Log_Module_gatePrms **xdc_runtime_Log_Module_gatePrms_C** = ((CT_xdc_runtime_Log_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Log_Module_id **xdc_runtime_Log_Module_id_C** = (xdc_Bits16)0x7
- const __FAR__ CT_xdc_runtime_Log_Module_loggerDefined **xdc_runtime_Log_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Log_Module_loggerObj **xdc_runtime_Log_Module_loggerObj_C** = ((CT_xdc_runtime_Log_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn0 **xdc_runtime_Log_Module_loggerFxn0_C** = ((CT_xdc_runtime_Log_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn1 **xdc_runtime_Log_Module_loggerFxn1_C** = ((CT_xdc_runtime_Log_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn2 **xdc_runtime_Log_Module_loggerFxn2_C** = ((CT_xdc_runtime_Log_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn4 **xdc_runtime_Log_Module_loggerFxn4_C** = ((CT_xdc_runtime_Log_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn8 **xdc_runtime_Log_Module_loggerFxn8_C** = ((CT_xdc_runtime_Log_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Log_Object_count **xdc_runtime_Log_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Log_Object_heap **xdc_runtime_Log_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Log_Object_sizeof **xdc_runtime_Log_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Log_Object_table **xdc_runtime_Log_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Log_L_construct **xdc_runtime_Log_L_construct_C** = (((xdc_runtime_Log_Event1) << 16 | 4))
- const __FAR__ CT_xdc_runtime_Log_L_create **xdc_runtime_Log_L_create_C** = (((xdc_runtime_Log_Event2) << 16 | 4))
- const __FAR__ CT_xdc_runtime_Log_L_destruct **xdc_runtime_Log_L_destruct_C** = (((xdc_runtime_Log_Event3) << 16 | 4))
- const __FAR__ CT_xdc_runtime_Log_L_delete **xdc_runtime_Log_L_delete_C** = (((xdc_runtime_Log_Event4) << 16 | 4))
- const __FAR__ CT_xdc_runtime_Log_L_error **xdc_runtime_Log_L_error_C** = (((xdc_runtime_Log_Event5) << 16 | 192))
- const __FAR__ CT_xdc_runtime_Log_L_warning **xdc_runtime_Log_L_warning_C** = (((xdc_runtime_Log_Event6) << 16 | 224))
- const __FAR__ CT_xdc_runtime_Log_L_info **xdc_runtime_Log_L_info_C** = (((xdc_runtime_Log_Event7) << 16 | 16384))
- const __FAR__ CT_xdc_runtime_Log_L_start **xdc_runtime_Log_L_start_C** = (((xdc_runtime_Log_Event8) << 16 | 32768))
- const __FAR__ CT_xdc_runtime_Log_L_stop **xdc_runtime_Log_L_stop_C** = (((xdc_runtime_Log_Event9) << 16 | 32768))
- const __FAR__ CT_xdc_runtime_Log_L_startInstance **xdc_runtime_Log_L_startInstance_C** = (((xdc_runtime_Log_Event10) << 16 | 32768))
- const __FAR__ CT_xdc_runtime_Log_L_stopInstance **xdc_runtime_Log_L_stopInstance_C** = (((xdc_runtime_Log_Event11) << 16 | 32768))
- const __FAR__ CT_xdc_runtime_Main_Module_diagsEnabled **xdc_runtime_Main_Module_diagsEnabled_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Main_Module_diagsIncluded **xdc_runtime_Main_Module_diagsIncluded_C** = (xdc_Bits32)0x90
- const __FAR__ CT_xdc_runtime_Main_Module_diagsMask **xdc_runtime_Main_Module_diagsMask_C** = ((CT_xdc_runtime_Main_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Main_Module_gateObj **xdc_runtime_Main_Module_gateObj_C** = ((CT_xdc_runtime_Main_Module_gateObj)((const void*)(xdc_runtime_IGateProvider_Handle)& ti_sysbios_gates_GateHwi_Object_table_V[0]))
- const __FAR__ CT_xdc_runtime_Main_Module_gatePrms **xdc_runtime_Main_Module_gatePrms_C** = ((CT_xdc_runtime_Main_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Main_Module_id **xdc_runtime_Main_Module_id_C** = (xdc_Bits16)0x8

- const __FAR__ CT_xdc_runtime_Main_Module_loggerDefined **xdc_runtime_Main_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Main_Module_loggerObj **xdc_runtime_Main_Module_loggerObj_C** = ((CT_xdc_runtime_Main_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn0 **xdc_runtime_Main_Module_loggerFxn0_C** = ((CT_xdc_runtime_Main_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn1 **xdc_runtime_Main_Module_loggerFxn1_C** = ((CT_xdc_runtime_Main_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn2 **xdc_runtime_Main_Module_loggerFxn2_C** = ((CT_xdc_runtime_Main_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn4 **xdc_runtime_Main_Module_loggerFxn4_C** = ((CT_xdc_runtime_Main_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn8 **xdc_runtime_Main_Module_loggerFxn8_C** = ((CT_xdc_runtime_Main_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Main_Object_count **xdc_runtime_Main_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Main_Object_heap **xdc_runtime_Main_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Main_Object_sizeof **xdc_runtime_Main_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Main_Object_table **xdc_runtime_Main_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_Module_diagsEnabled **xdc_runtime_Memory_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Memory_Module_diagsIncluded **xdc_runtime_Memory_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Memory_Module_diagsMask **xdc_runtime_Memory_Module_diagsMask_C** = ((CT_xdc_runtime_Memory_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_gateObj **xdc_runtime_Memory_Module_gateObj_C** = ((CT_xdc_runtime_Memory_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_gatePrms **xdc_runtime_Memory_Module_gatePrms_C** = ((CT_xdc_runtime_Memory_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_id **xdc_runtime_Memory_Module_id_C** = (xdc_Bits16)0x9
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerDefined **xdc_runtime_Memory_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerObj **xdc_runtime_Memory_Module_loggerObj_C** = ((CT_xdc_runtime_Memory_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn0 **xdc_runtime_Memory_Module_loggerFxn0_C** = ((CT_xdc_runtime_Memory_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn1 **xdc_runtime_Memory_Module_loggerFxn1_C** = ((CT_xdc_runtime_Memory_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn2 **xdc_runtime_Memory_Module_loggerFxn2_C** = ((CT_xdc_runtime_Memory_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn4 **xdc_runtime_Memory_Module_loggerFxn4_C** = ((CT_xdc_runtime_Memory_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn8 **xdc_runtime_Memory_Module_loggerFxn8_C** = ((CT_xdc_runtime_Memory_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Memory_Object_count **xdc_runtime_Memory_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_Object_heap **xdc_runtime_Memory_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_Object_sizeof **xdc_runtime_Memory_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_Object_table **xdc_runtime_Memory_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Memory_defaultHeapInstance **xdc_runtime_Memory_defaultHeapInstance_C** = (xdc_runtime_IHeap_Handle)& **ti_sysbios_heaps_HeapMem_Object_table_V[0]**
- const __FAR__ CT_xdc_runtime_Registry_Module_diagsEnabled **xdc_runtime_Registry_Module_diagsEnabled_C** = (xdc_Bits32)0x00
- const __FAR__ CT_xdc_runtime_Registry_Module_diagsIncluded **xdc_runtime_Registry_Module_diagsIncluded_C** = (xdc_Bits32)0x90

- const __FAR__ CT_xdc_runtime_Registry_Module_diagsMask **xdc_runtime_Registry_Module_diagsMask_C** = ((CT_xdc_runtime_Registry_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_gateObj **xdc_runtime_Registry_Module_gateObj_C** = ((CT_xdc_runtime_Registry_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_gatePrms **xdc_runtime_Registry_Module_gatePrms_C** = ((CT_xdc_runtime_Registry_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_id **xdc_runtime_Registry_Module_id_C** = (xdc_Bits16)0xa
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerDefined **xdc_runtime_Registry_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerObj **xdc_runtime_Registry_Module_loggerObj_C** = ((CT_xdc_runtime_Registry_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn0 **xdc_runtime_Registry_Module_loggerFxn0_C** = ((CT_xdc_runtime_Registry_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn1 **xdc_runtime_Registry_Module_loggerFxn1_C** = ((CT_xdc_runtime_Registry_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn2 **xdc_runtime_Registry_Module_loggerFxn2_C** = ((CT_xdc_runtime_Registry_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn4 **xdc_runtime_Registry_Module_loggerFxn4_C** = ((CT_xdc_runtime_Registry_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn8 **xdc_runtime_Registry_Module_loggerFxn8_C** = ((CT_xdc_runtime_Registry_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Registry_Object_count **xdc_runtime_Registry_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Registry_Object_heap **xdc_runtime_Registry_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Registry_Object_sizeof **xdc_runtime_Registry_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Registry_Object_table **xdc_runtime_Registry_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Startup_Module_diagsEnabled **xdc_runtime_Startup_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Startup_Module_diagsIncluded **xdc_runtime_Startup_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Startup_Module_diagsMask **xdc_runtime_Startup_Module_diagsMask_C** = ((CT_xdc_runtime_Startup_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_gateObj **xdc_runtime_Startup_Module_gateObj_C** = ((CT_xdc_runtime_Startup_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_gatePrms **xdc_runtime_Startup_Module_gatePrms_C** = ((CT_xdc_runtime_Startup_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_id **xdc_runtime_Startup_Module_id_C** = (xdc_Bits16)0xb
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerDefined **xdc_runtime_Startup_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerObj **xdc_runtime_Startup_Module_loggerObj_C** = ((CT_xdc_runtime_Startup_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn0 **xdc_runtime_Startup_Module_loggerFxn0_C** = ((CT_xdc_runtime_Startup_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn1 **xdc_runtime_Startup_Module_loggerFxn1_C** = ((CT_xdc_runtime_Startup_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn2 **xdc_runtime_Startup_Module_loggerFxn2_C** = ((CT_xdc_runtime_Startup_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn4 **xdc_runtime_Startup_Module_loggerFxn4_C** = ((CT_xdc_runtime_Startup_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn8 **xdc_runtime_Startup_Module_loggerFxn8_C** = ((CT_xdc_runtime_Startup_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_Startup_Object_count **xdc_runtime_Startup_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Startup_Object_heap **xdc_runtime_Startup_Object_heap_C** = 0

- const __FAR__ CT_xdc_runtime_Startup_Object_sizeof **xdc_runtime_Startup_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Startup_Object_table **xdc_runtime_Startup_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Startup_maxPasses **xdc_runtime_Startup_maxPasses_C** = (xdc←Int)0x20
- const __FAR__ CT_xdc_runtime_Startup_firstFxns **xdc_runtime_Startup_firstFxns_C** = {2, ((__T1←xdc_runtime_Startup_firstFxns *) **xdc_runtime_Startup_firstFxns_A**)}
- const __FAR__ CT_xdc_runtime_Startup_lastFxns **xdc_runtime_Startup_lastFxns_C** = {0, 0}
- const __FAR__ CT_xdc_runtime_Startup_startModsFxn **xdc_runtime_Startup_startModsFxn_C** = ((CT_xdc_runtime_Startup_startModsFxn)((xdc_Fxn)xdc_runtime_Startup_startMods_I))
- const __FAR__ CT_xdc_runtime_Startup_execImpl **xdc_runtime_Startup_execImpl_C** = ((CT_xdc←_runtime_Startup_execImpl)((xdc_Fxn) **xdc_runtime_Startup_exec_I**))
- const __FAR__ CT_xdc_runtime_Startup_sfxnTab **xdc_runtime_Startup_sfxnTab_C** = ((CT_xdc←_runtime_Startup_sfxnTab) **xdc_runtime_Startup_sfxnTab_A**)
- const __FAR__ CT_xdc_runtime_Startup_sfxnRts **xdc_runtime_Startup_sfxnRts_C** = ((CT_xdc←_runtime_Startup_sfxnRts) **xdc_runtime_Startup_sfxnRts_A**)
- const __FAR__ CT_xdc_runtime_SysStd_Module_diagsEnabled **xdc_runtime_SysStd_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_SysStd_Module_diagsIncluded **xdc_runtime_SysStd_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_SysStd_Module_diagsMask **xdc_runtime_SysStd_Module_diagsMask_C** = ((CT_xdc_runtime_SysStd_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_gateObj **xdc_runtime_SysStd_Module_gateObj_C** = ((CT_xdc_runtime_SysStd_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_gatePrms **xdc_runtime_SysStd_Module_gatePrms_C** = ((CT_xdc_runtime_SysStd_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_id **xdc_runtime_SysStd_Module_id_C** = (xdc←Bits16)0xd
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerDefined **xdc_runtime_SysStd_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerObj **xdc_runtime_SysStd_Module_loggerObj_C** = ((CT_xdc_runtime_SysStd_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn0 **xdc_runtime_SysStd_Module_loggerFxn0_C** = ((CT_xdc_runtime_SysStd_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn1 **xdc_runtime_SysStd_Module_loggerFxn1_C** = ((CT_xdc_runtime_SysStd_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn2 **xdc_runtime_SysStd_Module_loggerFxn2_C** = ((CT_xdc_runtime_SysStd_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn4 **xdc_runtime_SysStd_Module_loggerFxn4_C** = ((CT_xdc_runtime_SysStd_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn8 **xdc_runtime_SysStd_Module_loggerFxn8_C** = ((CT_xdc_runtime_SysStd_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_SysStd_Object_count **xdc_runtime_SysStd_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_SysStd_Object_heap **xdc_runtime_SysStd_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_SysStd_Object_sizeof **xdc_runtime_SysStd_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_SysStd_Object_table **xdc_runtime_SysStd_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_System_Module_diagsEnabled **xdc_runtime_System_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_System_Module_diagsIncluded **xdc_runtime_System_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_System_Module_diagsMask **xdc_runtime_System_Module_diagsMask_C** = ((CT_xdc_runtime_System_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_System_Module_gateObj **xdc_runtime_System_Module_gateObj_C** = ((CT_xdc_runtime_System_Module_gateObj)((const void*)(xdc_runtime_IGateProvider_Handle)& **ti_sysbios_gates_GateHwi_Object_table_V[0]**))
- const __FAR__ CT_xdc_runtime_System_Module_gatePrms **xdc_runtime_System_Module_gatePrms_C** = ((CT_xdc_runtime_System_Module_gatePrms)0)

- const __FAR__ CT_xdc_runtime_System_Module_id **xdc_runtime_System_Module_id_C** = (xdc_Bits16)0xc
- const __FAR__ CT_xdc_runtime_System_Module_loggerDefined **xdc_runtime_System_Module/loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_System_Module_loggerObj **xdc_runtime_System_Module/loggerObj_C** = ((CT_xdc_runtime_System_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_System_Module_loggerFxn0 **xdc_runtime_System_Module/loggerFxn0_C** = ((CT_xdc_runtime_System_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_System_Module_loggerFxn1 **xdc_runtime_System_Module/loggerFxn1_C** = ((CT_xdc_runtime_System_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_System_Module_loggerFxn2 **xdc_runtime_System_Module/loggerFxn2_C** = ((CT_xdc_runtime_System_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_System_Module_loggerFxn4 **xdc_runtime_System_Module/loggerFxn4_C** = ((CT_xdc_runtime_System_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_System_Module_loggerFxn8 **xdc_runtime_System_Module/loggerFxn8_C** = ((CT_xdc_runtime_System_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_System_Object_count **xdc_runtime_System_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_System_Object_heap **xdc_runtime_System_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_System_Object_sizeof **xdc_runtime_System_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_System_Object_table **xdc_runtime_System_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_System_A_cannotFitIntoArg **xdc_runtime_System_A_cannotFitIntoArg_C** = (((xdc_runtime Assert_Id)0) << 16 | 16)
- const __FAR__ CT_xdc_runtime_System_maxAtexitHandlers **xdc_runtime_System_maxAtexitHandlers_C** = (xdc_Int)0x0
- const __FAR__ CT_xdc_runtime_System_abortFxn **xdc_runtime_System_abortFxn_C** = ((CT_xdc_runtime_System_abortFxn)((xdc_Fxn)xdc_runtime_System_abortStd_E))
- const __FAR__ CT_xdc_runtime_System_exitFxn **xdc_runtime_System_exitFxn_C** = ((CT_xdc_runtime_System_exitFxn)((xdc_Fxn)xdc_runtime_System_exitStd_E))
- const __FAR__ CT_xdc_runtime_System_extendFxn **xdc_runtime_System_extendFxn_C** = ((CT_xdc_runtime_System_extendFxn)((xdc_Fxn)xdc_runtime_System_printfExtend_I))
- const __FAR__ CT_xdc_runtime_Text_Module_diagsEnabled **xdc_runtime_Text_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Text_Module_diagsIncluded **xdc_runtime_Text_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Text_Module_diagsMask **xdc_runtime_Text_Module_diagsMask_C** = ((CT_xdc_runtime_Text_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Text_Module_gateObj **xdc_runtime_Text_Module_gateObj_C** = ((CT_xdc_runtime_Text_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Text_Module_gatePrms **xdc_runtime_Text_Module_gatePrms_C** = ((CT_xdc_runtime_Text_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Text_Module_id **xdc_runtime_Text_Module_id_C** = (xdc_Bits16)0xe
- const __FAR__ CT_xdc_runtime_Text_Module_loggerDefined **xdc_runtime_Text_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Text_Module_loggerObj **xdc_runtime_Text_Module_loggerObj_C** = ((CT_xdc_runtime_Text_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn0 **xdc_runtime_Text_Module_loggerFxn0_C** = ((CT_xdc_runtime_Text_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn1 **xdc_runtime_Text_Module_loggerFxn1_C** = ((CT_xdc_runtime_Text_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn2 **xdc_runtime_Text_Module_loggerFxn2_C** = ((CT_xdc_runtime_Text_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn4 **xdc_runtime_Text_Module_loggerFxn4_C** = ((CT_xdc_runtime_Text_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn8 **xdc_runtime_Text_Module_loggerFxn8_C** = ((CT_xdc_runtime_Text_Module_loggerFxn8)0)

- const __FAR__ CT_xdc_runtime_Text_Object_count **xdc_runtime_Text_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Text_Object_heap **xdc_runtime_Text_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_Text_Object_sizeof **xdc_runtime_Text_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Text_Object_table **xdc_runtime_Text_Object_table_C** = 0
- const __FAR__ CT_xdc_runtime_Text_nameUnknown **xdc_runtime_Text_nameUnknown_C** = "{unknown-instance-name}"
- const __FAR__ CT_xdc_runtime_Text_nameEmpty **xdc_runtime_Text_nameEmpty_C** = "{empty-instance-name}"
- const __FAR__ CT_xdc_runtime_Text_nameStatic **xdc_runtime_Text_nameStatic_C** = "{static-instance-name}"
- const __FAR__ CT_xdc_runtime_Text_isLoaded **xdc_runtime_Text_isLoaded_C** = 0
- const __FAR__ CT_xdc_runtime_Text_charTab **xdc_runtime_Text_charTab_C** = ((CT_xdc_runtime_Text_charTab) **xdc_runtime_Text_charTab_A**)
- const __FAR__ CT_xdc_runtime_Text_nodeTab **xdc_runtime_Text_nodeTab_C** = ((CT_xdc_runtime_Text_nodeTab) **xdc_runtime_Text_nodeTab_A**)
- const __FAR__ CT_xdc_runtime_Text_charCnt **xdc_runtime_Text_charCnt_C** = (xdc_Int16)0x1
- const __FAR__ CT_xdc_runtime_Text_nodeCnt **xdc_runtime_Text_nodeCnt_C** = (xdc_Int16)0x1
- const __FAR__ CT_xdc_runtime_Text_unnamedModsLastId **xdc_runtime_Text_unnamedModsLastId_C** = (xdc_UInt16)0x4000
- const __FAR__ CT_xdc_runtime_Text_registryModsLastId **xdc_runtime_Text_registryModsLastId_C** = (xdc_UInt16)0x7fff
- const __FAR__ CT_xdc_runtime_Text_visitRopeFxN **xdc_runtime_Text_visitRopeFxN_C** = ((CT_xdc_runtime_Text_visitRopeFxN)((xdc_Fxn) **xdc_runtime_Text_visitRope_I**))
- const __FAR__ CT_xdc_runtime_Text_visitRopeFxN2 **xdc_runtime_Text_visitRopeFxN2_C** = ((CT_xdc_runtime_Text_visitRopeFxN2)((xdc_Fxn) xdc_runtime_Text_visitRope2_I))
- const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsEnabled **xdc_runtime_Timestamp_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsIncluded **xdc_runtime_Timestamp_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsMask **xdc_runtime_Timestamp_Module_diagsMask_C** = ((CT_xdc_runtime_Timestamp_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_gateObj **xdc_runtime_Timestamp_Module_gateObj_C** = ((CT_xdc_runtime_Timestamp_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_gatePrms **xdc_runtime_Timestamp_Module_gatePrms_C** = ((CT_xdc_runtime_Timestamp_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_id **xdc_runtime_Timestamp_Module_id_C** = (xdc_Bits16)0xf
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerDefined **xdc_runtime_Timestamp_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerObj **xdc_runtime_Timestamp_Module_loggerObj_C** = ((CT_xdc_runtime_Timestamp_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxN0 **xdc_runtime_Timestamp_Module_loggerFxN0_C** = ((CT_xdc_runtime_Timestamp_Module_loggerFxN0)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxN1 **xdc_runtime_Timestamp_Module_loggerFxN1_C** = ((CT_xdc_runtime_Timestamp_Module_loggerFxN1)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxN2 **xdc_runtime_Timestamp_Module_loggerFxN2_C** = ((CT_xdc_runtime_Timestamp_Module_loggerFxN2)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxN4 **xdc_runtime_Timestamp_Module_loggerFxN4_C** = ((CT_xdc_runtime_Timestamp_Module_loggerFxN4)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxN8 **xdc_runtime_Timestamp_Module_loggerFxN8_C** = ((CT_xdc_runtime_Timestamp_Module_loggerFxN8)0)
- const __FAR__ CT_xdc_runtime_Timestamp_Object_count **xdc_runtime_Timestamp_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_Timestamp_Object_heap **xdc_runtime_Timestamp_Object_heap_C** = 0

- const __FAR__ CT_xdc_runtime_Timestamp_Object_sizeof **xdc_runtime_Timestamp_Object__sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_Timestamp_Object_table **xdc_runtime_Timestamp_Object_table__C** = 0
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsEnabled **xdc_runtime_TimestampNull_Module_diagsEnabled_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsIncluded **xdc_runtime_TimestampNull_Module_diagsIncluded_C** = (xdc_Bits32)0x10
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsMask **xdc_runtime_TimestampNull_Module_diagsMask_C** = ((CT_xdc_runtime_TimestampNull_Module_diagsMask)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_gateObj **xdc_runtime_TimestampNull_Module_gateObj_C** = ((CT_xdc_runtime_TimestampNull_Module_gateObj)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_gatePrms **xdc_runtime_TimestampNull_Module_gatePrms_C** = ((CT_xdc_runtime_TimestampNull_Module_gatePrms)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_id **xdc_runtime_TimestampNull_Module_id_C** = (xdc_Bits16)0x10
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerDefined **xdc_runtime_TimestampNull_Module_loggerDefined_C** = 0
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerObj **xdc_runtime_TimestampNull_Module_loggerObj_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerObj)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn0 **xdc_runtime_TimestampNull_Module_loggerFxn0_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn0)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn1 **xdc_runtime_TimestampNull_Module_loggerFxn1_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn1)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn2 **xdc_runtime_TimestampNull_Module_loggerFxn2_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn2)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn4 **xdc_runtime_TimestampNull_Module_loggerFxn4_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn4)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn8 **xdc_runtime_TimestampNull_Module_loggerFxn8_C** = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn8)0)
- const __FAR__ CT_xdc_runtime_TimestampNull_Object_count **xdc_runtime_TimestampNull_Object_count_C** = 0
- const __FAR__ CT_xdc_runtime_TimestampNull_Object_heap **xdc_runtime_TimestampNull_Object_heap_C** = 0
- const __FAR__ CT_xdc_runtime_TimestampNull_Object_sizeof **xdc_runtime_TimestampNull_Object_sizeof_C** = 0
- const __FAR__ CT_xdc_runtime_TimestampNull_Object_table **xdc_runtime_TimestampNull_Object_table_C** = 0
- __FAR__ int(*volatile **__xdc_init_addr**)(void) = & **__xdc_init**
- const ti_sysbios_heaps_HeapMem_Handle **heap0** = (ti_sysbios_heaps_HeapMem_Handle)((ti_sysbios_heaps_HeapMem_Handle)& **ti_sysbios_heaps_HeapMem_Object_table_V[0]**)

4.16.1 Macro Definition Documentation

4.16.1.1 __config__

```
#define __config__
Definition at line 9 of file dss_mrr_pe674.c.
```

4.16.1.2 __nested__

```
#define __nested__
Definition at line 8 of file dss_mrr_pe674.c.
```

4.16.1.3 ATTRIBUTE

```
#define ATTRIBUTE __attribute__ ((used))  
Definition at line 2764 of file dss_mrr_pe674.c.
```

4.16.1.4 Module_DGSENAB [1/18]

```
#define Module_DGSENAB ti_sysbios_BIOS_RtsGateProxy_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.5 Module_DGSENAB [2/18]

```
#define Module_DGSENAB ti_sysbios_family_c64p_Hwi_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.6 Module_DGSENAB [3/18]

```
#define Module_DGSENAB ti_sysbios_gates_GateHwi_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.7 Module_DGSENAB [4/18]

```
#define Module_DGSENAB ti_sysbios_gates_GateMutex_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.8 Module_DGSENAB [5/18]

```
#define Module_DGSENAB ti_sysbios_hal_Hwi_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.9 Module_DGSENAB [6/18]

```
#define Module_DGSENAB ti_sysbios_hal_Hwi_HwiProxy_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.10 Module_DGSENAB [7/18]

```
#define Module_DGSENAB ti_sysbios_heaps_HeapMem_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.11 Module_DGSENAB [8/18]

```
#define Module_DGSENAB ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.12 Module_DGSENAB [9/18]

```
#define Module_DGSENAB ti_sysbios_knl_Clock_Module_diagsEnabled_C  
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.13 Module__DGSENAB [10/18]

```
#define Module__DGSENAB ti_sysbios_knl_Clock_TimerProxy_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.14 Module__DGSENAB [11/18]

```
#define Module__DGSENAB ti_sysbios_knl_Event_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.15 Module__DGSENAB [12/18]

```
#define Module__DGSENAB ti_sysbios_knl_Queue_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.16 Module__DGSENAB [13/18]

```
#define Module__DGSENAB ti_sysbios_knl_Semaphore_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.17 Module__DGSENAB [14/18]

```
#define Module__DGSENAB ti_sysbios_knl_Task_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.18 Module__DGSENAB [15/18]

```
#define Module__DGSENAB ti_sysbios_timers_rti_Timer_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.19 Module__DGSENAB [16/18]

```
#define Module__DGSENAB xdc_runtime_Main_Module_GateProxy_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.20 Module__DGSENAB [17/18]

```
#define Module__DGSENAB xdc_runtime_Memory_HeapProxy_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.21 Module__DGSENAB [18/18]

```
#define Module__DGSENAB xdc_runtime_System_Module_GateProxy_Module__diagsEnabled__C
Definition at line 13181 of file dss_mrr_pe674.c.
```

4.16.1.22 Module__DGSINCL [1/18]

```
#define Module__DGSINCL ti_sysbios_BIOS_RtsGateProxy_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.23 Module__DGSINCL [2/18]

```
#define Module__DGSINCL ti_sysbios_family_c64p_Hwi_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.24 Module__DGSINCL [3/18]

```
#define Module__DGSINCL ti_sysbios_gates_GateHwi_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.25 Module__DGSINCL [4/18]

```
#define Module__DGSINCL ti_sysbios_gates_GateMutex_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.26 Module__DGSINCL [5/18]

```
#define Module__DGSINCL ti_sysbios_hal_Hwi_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.27 Module__DGSINCL [6/18]

```
#define Module__DGSINCL ti_sysbios_hal_HwiProxy_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.28 Module__DGSINCL [7/18]

```
#define Module__DGSINCL ti_sysbios_heaps_HeapMem_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.29 Module__DGSINCL [8/18]

```
#define Module__DGSINCL ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.30 Module__DGSINCL [9/18]

```
#define Module__DGSINCL ti_sysbios_knl_Clock_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.31 Module__DGSINCL [10/18]

```
#define Module__DGSINCL ti_sysbios_knl_Clock_TimerProxy_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.32 Module__DGSINCL [11/18]

```
#define Module__DGSINCL ti_sysbios_knl_Event_Module__diagsIncluded__C
Definition at line 13174 of file dss_mrr_pe674.c.
```

4.16.1.33 Module__DGSINCL [12/18]

```
#define Module__DGSINCL ti_sysbios_knl_Queue_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.34 Module__DGSINCL [13/18]

```
#define Module__DGSINCL ti_sysbios_knl_Semaphore_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.35 Module__DGSINCL [14/18]

```
#define Module__DGSINCL ti_sysbios_knl_Task_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.36 Module__DGSINCL [15/18]

```
#define Module__DGSINCL ti_sysbios_timers_rti_Timer_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.37 Module__DGSINCL [16/18]

```
#define Module__DGSINCL xdc_runtime_Main_Module_GateProxy_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.38 Module__DGSINCL [17/18]

```
#define Module__DGSINCL xdc_runtime_Memory_HeapProxy_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.39 Module__DGSINCL [18/18]

```
#define Module__DGSINCL xdc_runtime_System_Module_GateProxy_Module__diagsIncluded__C
```

Definition at line 13174 of file dss_mrr_pe674.c.

4.16.1.40 Module__DGSMASK [1/18]

```
#define Module__DGSMASK ti_sysbios_BIOS_RtsGateProxy_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.41 Module__DGSMASK [2/18]

```
#define Module__DGSMASK ti_sysbios_family_c64p_Hwi_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.42 Module__DGSMASK [3/18]

```
#define Module__DGSMASK ti_sysbios_gates_GateHwi_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.43 Module__DGSMASK [4/18]

```
#define Module__DGSMASK ti_sysbios_gates_GateMutex_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.44 Module__DGSMASK [5/18]

```
#define Module__DGSMASK ti_sysbios_hal_Hwi_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.45 Module__DGSMASK [6/18]

```
#define Module__DGSMASK ti_sysbios_hal_HwiProxy_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.46 Module__DGSMASK [7/18]

```
#define Module__DGSMASK ti_sysbios_heaps_HeapMem_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.47 Module__DGSMASK [8/18]

```
#define Module__DGSMASK ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.48 Module__DGSMASK [9/18]

```
#define Module__DGSMASK ti_sysbios_knl_Clock_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.49 Module__DGSMASK [10/18]

```
#define Module__DGSMASK ti_sysbios_knl_Clock_TimerProxy_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.50 Module__DGSMASK [11/18]

```
#define Module__DGSMASK ti_sysbios_knl_Event_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.51 Module__DGSMASK [12/18]

```
#define Module__DGSMASK ti_sysbios_knl_Queue_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.52 Module__DGSMASK [13/18]

```
#define Module__DGSMASK ti_sysbios_knl_Semaphore_Module__diagsMask__C
Definition at line 13188 of file dss_mrr_pe674.c.
```

4.16.1.53 Module__DGSMASK [14/18]

```
#define Module__DGSMASK ti_sysbios_knl_Task_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.54 Module__DGSMASK [15/18]

```
#define Module__DGSMASK ti_sysbios_timers_rti_Timer_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.55 Module__DGSMASK [16/18]

```
#define Module__DGSMASK xdc_runtime_Main_Module_GateProxy_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.56 Module__DGSMASK [17/18]

```
#define Module__DGSMASK xdc_runtime_Memory_HeapProxy_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.57 Module__DGSMASK [18/18]

```
#define Module__DGSMASK xdc_runtime_System_Module_GateProxy_Module__diagsMask__C
```

Definition at line 13188 of file dss_mrr_pe674.c.

4.16.1.58 Module__G_OBJ [1/18]

```
#define Module__G_OBJ ti_sysbios_BIOS_RtsGateProxy_Module__gateObj__C
```

Definition at line 13245 of file dss_mrr_pe674.c.

4.16.1.59 Module__G_OBJ [2/18]

```
#define Module__G_OBJ ti_sysbios_family_c64p_Hwi_Module__gateObj__C
```

Definition at line 13245 of file dss_mrr_pe674.c.

4.16.1.60 Module__G_OBJ [3/18]

```
#define Module__G_OBJ ti_sysbios_gates_GateHwi_Module__gateObj__C
```

Definition at line 13245 of file dss_mrr_pe674.c.

4.16.1.61 Module__G_OBJ [4/18]

```
#define Module__G_OBJ ti_sysbios_gates_GateMutex_Module__gateObj__C
```

Definition at line 13245 of file dss_mrr_pe674.c.

4.16.1.62 Module__G_OBJ [5/18]

```
#define Module__G_OBJ ti_sysbios_hal_Hwi_Module__gateObj__C
```

Definition at line 13245 of file dss_mrr_pe674.c.

4.16.1.63 Module_G_OBJ [6/18]

```
#define Module_G_OBJ ti_sysbios_hal_Hwi_HwiProxy_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.64 Module_G_OBJ [7/18]

```
#define Module_G_OBJ ti_sysbios_heaps_HeapMem_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.65 Module_G_OBJ [8/18]

```
#define Module_G_OBJ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.66 Module_G_OBJ [9/18]

```
#define Module_G_OBJ ti_sysbios_knl_Clock_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.67 Module_G_OBJ [10/18]

```
#define Module_G_OBJ ti_sysbios_knl_Clock_TimerProxy_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.68 Module_G_OBJ [11/18]

```
#define Module_G_OBJ ti_sysbios_knl_Event_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.69 Module_G_OBJ [12/18]

```
#define Module_G_OBJ ti_sysbios_knl_Queue_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.70 Module_G_OBJ [13/18]

```
#define Module_G_OBJ ti_sysbios_knl_Semaphore_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.71 Module_G_OBJ [14/18]

```
#define Module_G_OBJ ti_sysbios_knl_Task_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.72 Module_G_OBJ [15/18]

```
#define Module_G_OBJ ti_sysbios_timers_rti_Timer_Module_gateObj_C
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.73 Module_G_OBJ [16/18]

```
#define Module_G_OBJ xdc_runtime_Main_Module_GateProxy_Module_gateObj__C  
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.74 Module_G_OBJ [17/18]

```
#define Module_G_OBJ xdc_runtime_Memory_HeapProxy_Module_gateObj__C  
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.75 Module_G_OBJ [18/18]

```
#define Module_G_OBJ xdc_runtime_System_Module_GateProxy_Module_gateObj__C  
Definition at line 13245 of file dss_mrr_pe674.c.
```

4.16.1.76 Module_G_PRMS [1/18]

```
#define Module_G_PRMS ti_sysbios_BIOS_RtsGateProxy_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.77 Module_G_PRMS [2/18]

```
#define Module_G_PRMS ti_sysbios_family_c64p_Hwi_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.78 Module_G_PRMS [3/18]

```
#define Module_G_PRMS ti_sysbios_gates_GateHwi_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.79 Module_G_PRMS [4/18]

```
#define Module_G_PRMS ti_sysbios_gates_GateMutex_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.80 Module_G_PRMS [5/18]

```
#define Module_G_PRMS ti_sysbios_hal_Hwi_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.81 Module_G_PRMS [6/18]

```
#define Module_G_PRMS ti_sysbios_hal_Hwi_HwiProxy_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.82 Module_G_PRMS [7/18]

```
#define Module_G_PRMS ti_sysbios_heaps_HeapMem_Module_gatePrms__C  
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.83 Module_G_PRMS [8/18]

```
#define Module_G_PRMS ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.84 Module_G_PRMS [9/18]

```
#define Module_G_PRMS ti_sysbios_knl_Clock_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.85 Module_G_PRMS [10/18]

```
#define Module_G_PRMS ti_sysbios_knl_Clock_TimerProxy_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.86 Module_G_PRMS [11/18]

```
#define Module_G_PRMS ti_sysbios_knl_Event_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.87 Module_G_PRMS [12/18]

```
#define Module_G_PRMS ti_sysbios_knl_Queue_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.88 Module_G_PRMS [13/18]

```
#define Module_G_PRMS ti_sysbios_knl_Semaphore_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.89 Module_G_PRMS [14/18]

```
#define Module_G_PRMS ti_sysbios_knl_Task_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.90 Module_G_PRMS [15/18]

```
#define Module_G_PRMS ti_sysbios_timers_rti_Timer_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.91 Module_G_PRMS [16/18]

```
#define Module_G_PRMS xdc_runtime_Main_Module_GateProxy_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.92 Module_G_PRMS [17/18]

```
#define Module_G_PRMS xdc_runtime_Memory_HeapProxy_Module_gatePrms_C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.93 Module__G_PRMS [18/18]

```
#define Module__G_PRMS xdc_runtime_System_Module_GateProxy_Module__gatePrms__C
Definition at line 13252 of file dss_mrr_pe674.c.
```

4.16.1.94 Module__GP_create [1/18]

```
#define Module__GP_create ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.95 Module__GP_create [2/18]

```
#define Module__GP_create ti_sysbios_family_c64p_Hwi_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.96 Module__GP_create [3/18]

```
#define Module__GP_create ti_sysbios_gates_GateHwi_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.97 Module__GP_create [4/18]

```
#define Module__GP_create ti_sysbios_gates_GateMutex_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.98 Module__GP_create [5/18]

```
#define Module__GP_create ti_sysbios_hal_Hwi_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.99 Module__GP_create [6/18]

```
#define Module__GP_create ti_sysbios_hal_HwiProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.100 Module__GP_create [7/18]

```
#define Module__GP_create ti_sysbios_heaps_HeapMem_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.101 Module__GP_create [8/18]

```
#define Module__GP_create ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.102 Module__GP_create [9/18]

```
#define Module__GP_create ti_sysbios_knl_Clock_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.103 Module__GP_create [10/18]

```
#define Module__GP_create ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.104 Module__GP_create [11/18]

```
#define Module__GP_create ti_sysbios_knl_Event_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.105 Module__GP_create [12/18]

```
#define Module__GP_create ti_sysbios_knl_Queue_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.106 Module__GP_create [13/18]

```
#define Module__GP_create ti_sysbios_knl_Semaphore_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.107 Module__GP_create [14/18]

```
#define Module__GP_create ti_sysbios_knl_Task_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.108 Module__GP_create [15/18]

```
#define Module__GP_create ti_sysbios_timers_rti_Timer_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.109 Module__GP_create [16/18]

```
#define Module__GP_create xdc_runtime_Main_Module_GateProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.110 Module__GP_create [17/18]

```
#define Module__GP_create xdc_runtime_Memory_HeapProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.111 Module__GP_create [18/18]

```
#define Module__GP_create xdc_runtime_System_Module_GateProxy_Module_GateProxy_create
Definition at line 13256 of file dss_mrr_pe674.c.
```

4.16.1.112 Module__GP_delete [1/18]

```
#define Module__GP_delete ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.113 Module__GP_delete [2/18]

```
#define Module__GP_delete ti_sysbios_family_c64p_Hwi_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.114 Module__GP_delete [3/18]

```
#define Module__GP_delete ti_sysbios_gates_GateHwi_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.115 Module__GP_delete [4/18]

```
#define Module__GP_delete ti_sysbios_gates_GateMutex_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.116 Module__GP_delete [5/18]

```
#define Module__GP_delete ti_sysbios_hal_Hwi_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.117 Module__GP_delete [6/18]

```
#define Module__GP_delete ti_sysbios_hal_HwiProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.118 Module__GP_delete [7/18]

```
#define Module__GP_delete ti_sysbios_heaps_HeapMem_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.119 Module__GP_delete [8/18]

```
#define Module__GP_delete ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.120 Module__GP_delete [9/18]

```
#define Module__GP_delete ti_sysbios_knl_Clock_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.121 Module__GP_delete [10/18]

```
#define Module__GP_delete ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.122 Module__GP_delete [11/18]

```
#define Module__GP_delete ti_sysbios_knl_Event_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.123 Module__GP_delete [12/18]

```
#define Module__GP_delete ti_sysbios_knl_Queue_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.124 Module__GP_delete [13/18]

```
#define Module__GP_delete ti_sysbios_knl_Semaphore_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.125 Module__GP_delete [14/18]

```
#define Module__GP_delete ti_sysbios_knl_Task_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.126 Module__GP_delete [15/18]

```
#define Module__GP_delete ti_sysbios_timers_rti_Timer_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.127 Module__GP_delete [16/18]

```
#define Module__GP_delete xdc_runtime_Main_Module_GateProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.128 Module__GP_delete [17/18]

```
#define Module__GP_delete xdc_runtime_Memory_HeapProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.129 Module__GP_delete [18/18]

```
#define Module__GP_delete xdc_runtime_System_Module_GateProxy_Module_GateProxy_delete
Definition at line 13258 of file dss_mrr_pe674.c.
```

4.16.1.130 Module__GP_enter [1/18]

```
#define Module__GP_enter ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.131 Module__GP_enter [2/18]

```
#define Module__GP_enter ti_sysbios_family_c64p_Hwi_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.132 Module__GP_enter [3/18]

```
#define Module__GP_enter ti_sysbios_gates_GateHwi_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.133 Module__GP_enter [4/18]

```
#define Module__GP_enter ti_sysbios_gates_GateMutex_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.134 Module__GP_enter [5/18]

```
#define Module__GP_enter ti_sysbios_hal_Hwi_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.135 Module__GP_enter [6/18]

```
#define Module__GP_enter ti_sysbios_hal_HwiProxy_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.136 Module__GP_enter [7/18]

```
#define Module__GP_enter ti_sysbios_heaps_HeapMem_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.137 Module__GP_enter [8/18]

```
#define Module__GP_enter ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.138 Module__GP_enter [9/18]

```
#define Module__GP_enter ti_sysbios_knl_Clock_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.139 Module__GP_enter [10/18]

```
#define Module__GP_enter ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.140 Module__GP_enter [11/18]

```
#define Module__GP_enter ti_sysbios_knl_Event_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.141 Module__GP_enter [12/18]

```
#define Module__GP_enter ti_sysbios_knl_Queue_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.142 Module__GP_enter [13/18]

```
#define Module__GP_enter ti_sysbios_knl_Semaphore_Module_GateProxy_enter  
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.143 Module__GP_enter [14/18]

```
#define Module__GP_enter ti_sysbios_knl_Task_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.144 Module__GP_enter [15/18]

```
#define Module__GP_enter ti_sysbios_timers_rti_Timer_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.145 Module__GP_enter [16/18]

```
#define Module__GP_enter xdc_runtime_Main_Module_GateProxy_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.146 Module__GP_enter [17/18]

```
#define Module__GP_enter xdc_runtime_Memory_HeapProxy_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.147 Module__GP_enter [18/18]

```
#define Module__GP_enter xdc_runtime_System_Module_GateProxy_Module_GateProxy_enter
Definition at line 13260 of file dss_mrr_pe674.c.
```

4.16.1.148 Module__GP_leave [1/18]

```
#define Module__GP_leave ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.149 Module__GP_leave [2/18]

```
#define Module__GP_leave ti_sysbios_family_c64p_Hwi_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.150 Module__GP_leave [3/18]

```
#define Module__GP_leave ti_sysbios_gates_GateHwi_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.151 Module__GP_leave [4/18]

```
#define Module__GP_leave ti_sysbios_gates_GateMutex_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.152 Module__GP_leave [5/18]

```
#define Module__GP_leave ti_sysbios_hal_Hwi_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.153 Module__GP_leave [6/18]

```
#define Module__GP_leave ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.154 Module__GP_leave [7/18]

```
#define Module__GP_leave ti_sysbios_heaps_HeapMem_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.155 Module__GP_leave [8/18]

```
#define Module__GP_leave ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.156 Module__GP_leave [9/18]

```
#define Module__GP_leave ti_sysbios_knl_Clock_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.157 Module__GP_leave [10/18]

```
#define Module__GP_leave ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.158 Module__GP_leave [11/18]

```
#define Module__GP_leave ti_sysbios_knl_Event_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.159 Module__GP_leave [12/18]

```
#define Module__GP_leave ti_sysbios_knl_Queue_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.160 Module__GP_leave [13/18]

```
#define Module__GP_leave ti_sysbios_knl_Semaphore_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.161 Module__GP_leave [14/18]

```
#define Module__GP_leave ti_sysbios_knl_Task_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.162 Module__GP_leave [15/18]

```
#define Module__GP_leave ti_sysbios_timers_rti_Timer_Module_GateProxy_leave  
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.163 Module__GP_leave [16/18]

```
#define Module__GP_leave xdc_runtime_Main_Module_GateProxy_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.164 Module__GP_leave [17/18]

```
#define Module__GP_leave xdc_runtime_Memory_HeapProxy_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.165 Module__GP_leave [18/18]

```
#define Module__GP_leave xdc_runtime_System_Module_GateProxy_Module_GateProxy_leave
Definition at line 13262 of file dss_mrr_pe674.c.
```

4.16.1.166 Module__GP_query [1/18]

```
#define Module__GP_query ti_sysbios_BIOS_RtsGateProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.167 Module__GP_query [2/18]

```
#define Module__GP_query ti_sysbios_family_c64p_Hwi_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.168 Module__GP_query [3/18]

```
#define Module__GP_query ti_sysbios_gates_GateHwi_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.169 Module__GP_query [4/18]

```
#define Module__GP_query ti_sysbios_gates_GateMutex_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.170 Module__GP_query [5/18]

```
#define Module__GP_query ti_sysbios_hal_Hwi_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.171 Module__GP_query [6/18]

```
#define Module__GP_query ti_sysbios_hal_Hwi_HwiProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.172 Module__GP_query [7/18]

```
#define Module__GP_query ti_sysbios_heaps_HeapMem_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.173 Module__GP_query [8/18]

```
#define Module__GP_query ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.174 Module__GP_query [9/18]

```
#define Module__GP_query ti_sysbios_knl_Clock_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.175 Module__GP_query [10/18]

```
#define Module__GP_query ti_sysbios_knl_Clock_TimerProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.176 Module__GP_query [11/18]

```
#define Module__GP_query ti_sysbios_knl_Event_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.177 Module__GP_query [12/18]

```
#define Module__GP_query ti_sysbios_knl_Queue_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.178 Module__GP_query [13/18]

```
#define Module__GP_query ti_sysbios_knl_Semaphore_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.179 Module__GP_query [14/18]

```
#define Module__GP_query ti_sysbios_knl_Task_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.180 Module__GP_query [15/18]

```
#define Module__GP_query ti_sysbios_timers_rti_Timer_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.181 Module__GP_query [16/18]

```
#define Module__GP_query xdc_runtime_Main_Module_GateProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.182 Module__GP_query [17/18]

```
#define Module__GP_query xdc_runtime_Memory_HeapProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.183 Module__GP_query [18/18]

```
#define Module__GP_query xdc_runtime_System_Module_GateProxy_Module_GateProxy_query
Definition at line 13264 of file dss_mrr_pe674.c.
```

4.16.1.184 Module__LOGDEF [1/18]

```
#define Module__LOGDEF ti_sysbios_BIOS_RtsGateProxy_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.185 Module__LOGDEF [2/18]

```
#define Module__LOGDEF ti_sysbios_family_c64p_Hwi_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.186 Module__LOGDEF [3/18]

```
#define Module__LOGDEF ti_sysbios_gates_GateHwi_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.187 Module__LOGDEF [4/18]

```
#define Module__LOGDEF ti_sysbios_gates_GateMutex_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.188 Module__LOGDEF [5/18]

```
#define Module__LOGDEF ti_sysbios_hal_Hwi_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.189 Module__LOGDEF [6/18]

```
#define Module__LOGDEF ti_sysbios_hal_Hwi_HwiProxy_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.190 Module__LOGDEF [7/18]

```
#define Module__LOGDEF ti_sysbios_heaps_HeapMem_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.191 Module__LOGDEF [8/18]

```
#define Module__LOGDEF ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.192 Module__LOGDEF [9/18]

```
#define Module__LOGDEF ti_sysbios_knl_Clock_Module_loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.193 Module__LOGDEF [10/18]

```
#define Module__LOGDEF ti_sysbios_knl_Clock_TimerProxy_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.194 Module__LOGDEF [11/18]

```
#define Module__LOGDEF ti_sysbios_knl_Event_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.195 Module__LOGDEF [12/18]

```
#define Module__LOGDEF ti_sysbios_knl_Queue_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.196 Module__LOGDEF [13/18]

```
#define Module__LOGDEF ti_sysbios_knl_Semaphore_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.197 Module__LOGDEF [14/18]

```
#define Module__LOGDEF ti_sysbios_knl_Task_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.198 Module__LOGDEF [15/18]

```
#define Module__LOGDEF ti_sysbios_timers_rti_Timer_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.199 Module__LOGDEF [16/18]

```
#define Module__LOGDEF xdc_runtime_Main_Module_GateProxy_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.200 Module__LOGDEF [17/18]

```
#define Module__LOGDEF xdc_runtime_Memory_HeapProxy_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.201 Module__LOGDEF [18/18]

```
#define Module__LOGDEF xdc_runtime_System_Module_GateProxy_Module__loggerDefined__C
Definition at line 13195 of file dss_mrr_pe674.c.
```

4.16.1.202 Module__LOGFXN0 [1/18]

```
#define Module__LOGFXN0 ti_sysbios_BIOS_RtsGateProxy_Module__loggerFxN0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.203 Module__LOGFXN0 [2/18]

```
#define Module__LOGFXN0  ti_sysbios_family_c64p_Hwi_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.204 Module__LOGFXN0 [3/18]

```
#define Module__LOGFXN0  ti_sysbios_gates_GateHwi_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.205 Module__LOGFXN0 [4/18]

```
#define Module__LOGFXN0  ti_sysbios_gates_GateMutex_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.206 Module__LOGFXN0 [5/18]

```
#define Module__LOGFXN0  ti_sysbios_hal_Hwi_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.207 Module__LOGFXN0 [6/18]

```
#define Module__LOGFXN0  ti_sysbios_hal_HwiProxy_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.208 Module__LOGFXN0 [7/18]

```
#define Module__LOGFXN0  ti_sysbios_heaps_HeapMem_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.209 Module__LOGFXN0 [8/18]

```
#define Module__LOGFXN0  ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.210 Module__LOGFXN0 [9/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Clock_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.211 Module__LOGFXN0 [10/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Clock_TimerProxy_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.212 Module__LOGFXN0 [11/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Event_Module__loggerFxn0__C
Definition at line 13210 of file dss_mrr_pe674.c.
```

4.16.1.213 Module__LOGFXN0 [12/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Queue_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.214 Module__LOGFXN0 [13/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Semaphore_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.215 Module__LOGFXN0 [14/18]

```
#define Module__LOGFXN0  ti_sysbios_knl_Task_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.216 Module__LOGFXN0 [15/18]

```
#define Module__LOGFXN0  ti_sysbios_timers_rti_Timer_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.217 Module__LOGFXN0 [16/18]

```
#define Module__LOGFXN0  xdc_runtime_Main_Module_GateProxy_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.218 Module__LOGFXN0 [17/18]

```
#define Module__LOGFXN0  xdc_runtime_Memory_HeapProxy_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.219 Module__LOGFXN0 [18/18]

```
#define Module__LOGFXN0  xdc_runtime_System_Module_GateProxy_Module__loggerFxn0__C
```

Definition at line 13210 of file dss_mrr_pe674.c.

4.16.1.220 Module__LOGFXN1 [1/18]

```
#define Module__LOGFXN1  ti_sysbios_BIOS_RtsGateProxy_Module__loggerFxn1__C
```

Definition at line 13217 of file dss_mrr_pe674.c.

4.16.1.221 Module__LOGFXN1 [2/18]

```
#define Module__LOGFXN1  ti_sysbios_family_c64p_Hwi_Module__loggerFxn1__C
```

Definition at line 13217 of file dss_mrr_pe674.c.

4.16.1.222 Module__LOGFXN1 [3/18]

```
#define Module__LOGFXN1  ti_sysbios_gates_GateHwi_Module__loggerFxn1__C
```

Definition at line 13217 of file dss_mrr_pe674.c.

4.16.1.223 Module__LOGFXN1 [4/18]

```
#define Module__LOGFXN1  ti_sysbios_gates_GateMutex_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.224 Module__LOGFXN1 [5/18]

```
#define Module__LOGFXN1  ti_sysbios_hal_Hwi_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.225 Module__LOGFXN1 [6/18]

```
#define Module__LOGFXN1  ti_sysbios_hal_Hwi_HwiProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.226 Module__LOGFXN1 [7/18]

```
#define Module__LOGFXN1  ti_sysbios_heaps_HeapMem_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.227 Module__LOGFXN1 [8/18]

```
#define Module__LOGFXN1  ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.228 Module__LOGFXN1 [9/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Clock_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.229 Module__LOGFXN1 [10/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Clock_TimerProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.230 Module__LOGFXN1 [11/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Event_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.231 Module__LOGFXN1 [12/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Queue_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.232 Module__LOGFXN1 [13/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Semaphore_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.233 Module__LOGFXN1 [14/18]

```
#define Module__LOGFXN1  ti_sysbios_knl_Task_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.234 Module__LOGFXN1 [15/18]

```
#define Module__LOGFXN1  ti_sysbios_timers_rti_Timer_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.235 Module__LOGFXN1 [16/18]

```
#define Module__LOGFXN1  xdc_runtime_Main_Module_GateProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.236 Module__LOGFXN1 [17/18]

```
#define Module__LOGFXN1  xdc_runtime_Memory_HeapProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.237 Module__LOGFXN1 [18/18]

```
#define Module__LOGFXN1  xdc_runtime_System_Module_GateProxy_Module__loggerFxn1__C
Definition at line 13217 of file dss_mrr_pe674.c.
```

4.16.1.238 Module__LOGFXN2 [1/18]

```
#define Module__LOGFXN2  ti_sysbios_BIOS_RtsGateProxy_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.239 Module__LOGFXN2 [2/18]

```
#define Module__LOGFXN2  ti_sysbios_family_c64p_Hwi_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.240 Module__LOGFXN2 [3/18]

```
#define Module__LOGFXN2  ti_sysbios_gates_GateHwi_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.241 Module__LOGFXN2 [4/18]

```
#define Module__LOGFXN2  ti_sysbios_gates_GateMutex_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.242 Module__LOGFXN2 [5/18]

```
#define Module__LOGFXN2  ti_sysbios_hal_Hwi_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.243 Module__LOGFXN2 [6/18]

```
#define Module__LOGFXN2 ti_sysbios_hal_Hwi_HwiProxy_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.244 Module__LOGFXN2 [7/18]

```
#define Module__LOGFXN2 ti_sysbios_heaps_HeapMem_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.245 Module__LOGFXN2 [8/18]

```
#define Module__LOGFXN2 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.246 Module__LOGFXN2 [9/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Clock_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.247 Module__LOGFXN2 [10/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Clock_TimerProxy_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.248 Module__LOGFXN2 [11/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Event_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.249 Module__LOGFXN2 [12/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Queue_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.250 Module__LOGFXN2 [13/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Semaphore_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.251 Module__LOGFXN2 [14/18]

```
#define Module__LOGFXN2 ti_sysbios_knl_Task_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.252 Module__LOGFXN2 [15/18]

```
#define Module__LOGFXN2 ti_sysbios_timers_rti_Timer_Module__loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.253 Module__LOGFXN2 [16/18]

```
#define Module__LOGFXN2 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.254 Module__LOGFXN2 [17/18]

```
#define Module__LOGFXN2 xdc_runtime_Memory_HeapProxy_Module_loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.255 Module__LOGFXN2 [18/18]

```
#define Module__LOGFXN2 xdc_runtime_System_Module_GateProxy_Module_loggerFxn2__C
Definition at line 13224 of file dss_mrr_pe674.c.
```

4.16.1.256 Module__LOGFXN4 [1/18]

```
#define Module__LOGFXN4 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.257 Module__LOGFXN4 [2/18]

```
#define Module__LOGFXN4 ti_sysbios_family_c64p_Hwi_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.258 Module__LOGFXN4 [3/18]

```
#define Module__LOGFXN4 ti_sysbios_gates_GateHwi_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.259 Module__LOGFXN4 [4/18]

```
#define Module__LOGFXN4 ti_sysbios_gates_GateMutex_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.260 Module__LOGFXN4 [5/18]

```
#define Module__LOGFXN4 ti_sysbios_hal_Hwi_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.261 Module__LOGFXN4 [6/18]

```
#define Module__LOGFXN4 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.262 Module__LOGFXN4 [7/18]

```
#define Module__LOGFXN4 ti_sysbios_heaps_HeapMem_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.263 Module__LOGFXN4 [8/18]

```
#define Module__LOGFXN4 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.264 Module__LOGFXN4 [9/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Clock_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.265 Module__LOGFXN4 [10/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Clock_TimerProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.266 Module__LOGFXN4 [11/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Event_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.267 Module__LOGFXN4 [12/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Queue_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.268 Module__LOGFXN4 [13/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Semaphore_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.269 Module__LOGFXN4 [14/18]

```
#define Module__LOGFXN4 ti_sysbios_knl_Task_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.270 Module__LOGFXN4 [15/18]

```
#define Module__LOGFXN4 ti_sysbios_timers_rti_Timer_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.271 Module__LOGFXN4 [16/18]

```
#define Module__LOGFXN4 xdc_runtime_Main_Module_GateProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.272 Module__LOGFXN4 [17/18]

```
#define Module__LOGFXN4 xdc_runtime_Memory_HeapProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.273 Module__LOGFXN4 [18/18]

```
#define Module__LOGFXN4 xdc_runtime_System_Module_GateProxy_Module_loggerFxn4__C
Definition at line 13231 of file dss_mrr_pe674.c.
```

4.16.1.274 Module__LOGFXN8 [1/18]

```
#define Module__LOGFXN8 ti_sysbios_BIOS_RtsGateProxy_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.275 Module__LOGFXN8 [2/18]

```
#define Module__LOGFXN8 ti_sysbios_family_c64p_Hwi_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.276 Module__LOGFXN8 [3/18]

```
#define Module__LOGFXN8 ti_sysbios_gates_GateHwi_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.277 Module__LOGFXN8 [4/18]

```
#define Module__LOGFXN8 ti_sysbios_gates_GateMutex_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.278 Module__LOGFXN8 [5/18]

```
#define Module__LOGFXN8 ti_sysbios_hal_Hwi_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.279 Module__LOGFXN8 [6/18]

```
#define Module__LOGFXN8 ti_sysbios_hal_Hwi_HwiProxy_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.280 Module__LOGFXN8 [7/18]

```
#define Module__LOGFXN8 ti_sysbios_heaps_HeapMem_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.281 Module__LOGFXN8 [8/18]

```
#define Module__LOGFXN8 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.282 Module__LOGFXN8 [9/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Clock_Module_loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.283 Module__LOGFXN8 [10/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Clock_TimerProxy_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.284 Module__LOGFXN8 [11/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Event_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.285 Module__LOGFXN8 [12/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Queue_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.286 Module__LOGFXN8 [13/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Semaphore_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.287 Module__LOGFXN8 [14/18]

```
#define Module__LOGFXN8 ti_sysbios_knl_Task_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.288 Module__LOGFXN8 [15/18]

```
#define Module__LOGFXN8 ti_sysbios_timers_rti_Timer_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.289 Module__LOGFXN8 [16/18]

```
#define Module__LOGFXN8 xdc_runtime_Main_Module_GateProxy_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.290 Module__LOGFXN8 [17/18]

```
#define Module__LOGFXN8 xdc_runtime_Memory_HeapProxy_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.291 Module__LOGFXN8 [18/18]

```
#define Module__LOGFXN8 xdc_runtime_System_Module_GateProxy_Module__loggerFxn8__C
Definition at line 13238 of file dss_mrr_pe674.c.
```

4.16.1.292 Module__LOGOBJ [1/18]

```
#define Module__LOGOBJ ti_sysbios_BIOS_RtsGateProxy_Module__loggerObj__C
Definition at line 13203 of file dss_mrr_pe674.c.
```

4.16.1.293 Module__LOGOBJ [2/18]

```
#define Module__LOGOBJ  ti_sysbios_family_c64p_Hwi_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.294 Module__LOGOBJ [3/18]

```
#define Module__LOGOBJ  ti_sysbios_gates_GateHwi_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.295 Module__LOGOBJ [4/18]

```
#define Module__LOGOBJ  ti_sysbios_gates_GateMutex_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.296 Module__LOGOBJ [5/18]

```
#define Module__LOGOBJ  ti_sysbios_hal_Hwi_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.297 Module__LOGOBJ [6/18]

```
#define Module__LOGOBJ ti_sysbios_hal_HwiProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.298 Module__LOGOBJ [7/18]

```
#define Module__LOGOBJ  ti_sysbios_heaps_HeapMem_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.299 Module__LOGOBJ [8/18]

```
#define Module__LOGOBJ ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.300 Module__LOGOBJ [9/18]

```
#define Module__LOGOBJ  ti_sysbios_knl_Clock_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.301 Module__LOGOBJ [10/18]

```
#define Module__LOGOBJ ti_sysbios_knl_Clock_TimerProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.302 Module__LOGOBJ [11/18]

```
#define Module__LOGOBJ  ti_sysbios_knl_Event_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.303 Module__LOGOBJ [12/18]

```
#define Module__LOGOBJ  ti_sysbios_knl_Queue_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.304 Module__LOGOBJ [13/18]

```
#define Module__LOGOBJ  ti_sysbios_knl_Semaphore_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.305 Module__LOGOBJ [14/18]

```
#define Module__LOGOBJ  ti_sysbios_knl_Task_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.306 Module__LOGOBJ [15/18]

```
#define Module__LOGOBJ  ti_sysbios_timers_rti_Timer_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.307 Module__LOGOBJ [16/18]

```
#define Module__LOGOBJ  xdc_runtime_Main_Module_GateProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.308 Module__LOGOBJ [17/18]

```
#define Module__LOGOBJ  xdc_runtime_Memory_HeapProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.309 Module__LOGOBJ [18/18]

```
#define Module__LOGOBJ  xdc_runtime_System_Module_GateProxy_Module__loggerObj__C
```

Definition at line 13203 of file dss_mrr_pe674.c.

4.16.1.310 Module__MID [1/18]

```
#define Module__MID  ti_sysbios_BIOS_RtsGateProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.311 Module__MID [2/18]

```
#define Module__MID  ti_sysbios_family_c64p_Hwi_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.312 Module__MID [3/18]

```
#define Module__MID  ti_sysbios_gates_GateHwi_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.313 Module__MID [4/18]

```
#define Module__MID ti_sysbios_gates_GateMutex_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.314 Module__MID [5/18]

```
#define Module__MID ti_sysbios_hal_Hwi_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.315 Module__MID [6/18]

```
#define Module__MID ti_sysbios_hal_HwiProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.316 Module__MID [7/18]

```
#define Module__MID ti_sysbios_heaps_HeapMem_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.317 Module__MID [8/18]

```
#define Module__MID ti_sysbios_heaps_HeapMem_Module_GateProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.318 Module__MID [9/18]

```
#define Module__MID ti_sysbios_knl_Clock_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.319 Module__MID [10/18]

```
#define Module__MID ti_sysbios_knl_Clock_TimerProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.320 Module__MID [11/18]

```
#define Module__MID ti_sysbios_knl_Event_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.321 Module__MID [12/18]

```
#define Module__MID ti_sysbios_knl_Queue_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.322 Module__MID [13/18]

```
#define Module__MID ti_sysbios_knl_Semaphore_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.323 Module__MID [14/18]

```
#define Module__MID ti_sysbios_knl_Task_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.324 Module__MID [15/18]

```
#define Module__MID ti_sysbios_timers_rti_Timer_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.325 Module__MID [16/18]

```
#define Module__MID xdc_runtime_Main_Module_GateProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.326 Module__MID [17/18]

```
#define Module__MID xdc_runtime_Memory_HeapProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.1.327 Module__MID [18/18]

```
#define Module__MID xdc_runtime_System_Module_GateProxy_Module__id__C
```

Definition at line 13167 of file dss_mrr_pe674.c.

4.16.2 Typedef Documentation

4.16.2.1 Header

```
typedef union Header Header
```

4.16.2.2 ti_sysbios_BIOS_Module_State__

```
typedef struct ti_sysbios_BIOS_Module_State__ ti_sysbios_BIOS_Module_State__
```

4.16.2.3 ti_sysbios_BIOS_RtsGateProxy_Module__

```
typedef struct ti_sysbios_BIOS_RtsGateProxy_Module__ ti_sysbios_BIOS_RtsGateProxy_Module__
```

4.16.2.4 ti_sysbios_BIOS_RtsGateProxy_Object__

```
typedef ti_sysbios_gates_GateMutex_Object__ ti_sysbios_BIOS_RtsGateProxy_Object__
```

Definition at line 147 of file dss_mrr_pe674.c.

4.16.2.5 ti_sysbios_family_c64p_Cache_Module_State__

```
typedef struct ti_sysbios_family_c64p_Cache_Module_State__ ti_sysbios_family_c64p_Cache__←
Module_State__
```

4.16.2.6 `ti_sysbios_family_c64p_EventCombiner_Module_State`

```
typedef struct ti_sysbios_family_c64p_EventCombiner_Module_State ti_sysbios_family_c64p_EventCombiner_Module_State
```

4.16.2.7 `ti_sysbios_family_c64p_Exception_Module_State`

```
typedef struct ti_sysbios_family_c64p_Exception_Module_State ti_sysbios_family_c64p_Exception_Module_State
```

4.16.2.8 `ti_sysbios_family_c64p_Hwi_Module`

```
typedef struct ti_sysbios_family_c64p_Hwi_Module ti_sysbios_family_c64p_Hwi_Module
```

4.16.2.9 `ti_sysbios_family_c64p_Hwi_Module_State`

```
typedef struct ti_sysbios_family_c64p_Hwi_Module_State ti_sysbios_family_c64p_Hwi_Module_State
```

4.16.2.10 `ti_sysbios_family_c64p_Hwi_Object`

```
typedef struct ti_sysbios_family_c64p_Hwi_Object ti_sysbios_family_c64p_Hwi_Object
```

4.16.2.11 `ti_sysbios_gates_GateHwi_Module`

```
typedef struct ti_sysbios_gates_GateHwi_Module ti_sysbios_gates_GateHwi_Module
```

4.16.2.12 `ti_sysbios_gates_GateHwi_Object`

```
typedef struct ti_sysbios_gates_GateHwi_Object ti_sysbios_gates_GateHwi_Object
```

4.16.2.13 `ti_sysbios_gates_GateMutex_Module`

```
typedef struct ti_sysbios_gates_GateMutex_Module ti_sysbios_gates_GateMutex_Module
```

4.16.2.14 `ti_sysbios_gates_GateMutex_Object`

```
typedef struct ti_sysbios_gates_GateMutex_Object ti_sysbios_gates_GateMutex_Object
```

4.16.2.15 `ti_sysbios_hal_Hwi_HwiProxy_Module`

```
typedef struct ti_sysbios_hal_Hwi_HwiProxy_Module ti_sysbios_hal_Hwi_HwiProxy_Module
```

4.16.2.16 `ti_sysbios_hal_Hwi_HwiProxy_Object`

```
typedef ti_sysbios_family_c64p_Hwi_Object ti_sysbios_hal_Hwi_HwiProxy_Object
```

Definition at line 295 of file dss_mrr_pe674.c.

4.16.2.17 `ti_sysbios_hal_Hwi_Module`

```
typedef struct ti_sysbios_hal_Hwi_Module ti_sysbios_hal_Hwi_Module
```

4.16.2.18 `ti_sysbios_hal_Hwi_Object`

```
typedef struct ti_sysbios_hal_Hwi_Object ti_sysbios_hal_Hwi_Object
```

4.16.2.19 `ti_sysbios_heaps_HeapMem_Module`

```
typedef struct ti_sysbios_heaps_HeapMem_Module ti_sysbios_heaps_HeapMem_Module
```

4.16.2.20 `ti_sysbios_heaps_HeapMem_Module_GateProxy_Module`

```
typedef struct ti_sysbios_heaps_HeapMem_Module_GateProxy_Module ti_sysbios_heaps_HeapMem_Module_GateProxy_Module
```

4.16.2.21 `ti_sysbios_heaps_HeapMem_Module_GateProxy_Object`

```
typedef ti_sysbios_gates_GateMutex_Object ti_sysbios_heaps_HeapMem_Module_GateProxy_Object
```

Definition at line 347 of file dss_mrr_pe674.c.

4.16.2.22 `ti_sysbios_heaps_HeapMem_Object`

```
typedef struct ti_sysbios_heaps_HeapMem_Object ti_sysbios_heaps_HeapMem_Object
```

4.16.2.23 `ti_sysbios_knl_Clock_Module`

```
typedef struct ti_sysbios_knl_Clock_Module ti_sysbios_knl_Clock_Module
```

4.16.2.24 `ti_sysbios_knl_Clock_Module_State`

```
typedef struct ti_sysbios_knl_Clock_Module_State ti_sysbios_knl_Clock_Module_State
```

4.16.2.25 `ti_sysbios_knl_Clock_Object`

```
typedef struct ti_sysbios_knl_Clock_Object ti_sysbios_knl_Clock_Object
```

4.16.2.26 `ti_sysbios_knl_Clock_TimerProxy_Module`

```
typedef struct ti_sysbios_knl_Clock_TimerProxy_Module ti_sysbios_knl_Clock_TimerProxy_Module
```

4.16.2.27 `ti_sysbios_knl_Clock_TimerProxy_Object`

```
typedef ti_sysbios_timers_rti_Timer_Object ti_sysbios_knl_Clock_TimerProxy_Object
```

Definition at line 425 of file dss_mrr_pe674.c.

4.16.2.28 ti_sysbios_knl_Event_Module_

```
typedef struct  ti_sysbios_knl_Event_Module_  ti_sysbios_knl_Event_Module_
```

4.16.2.29 ti_sysbios_knl_Event_Object_

```
typedef struct  ti_sysbios_knl_Event_Object_  ti_sysbios_knl_Event_Object_
```

4.16.2.30 ti_sysbios_knl_Queue_Module_

```
typedef struct  ti_sysbios_knl_Queue_Module_  ti_sysbios_knl_Queue_Module_
```

4.16.2.31 ti_sysbios_knl_Queue_Object_

```
typedef struct  ti_sysbios_knl_Queue_Object_  ti_sysbios_knl_Queue_Object_
```

4.16.2.32 ti_sysbios_knl_Semaphore_Module_

```
typedef struct  ti_sysbios_knl_Semaphore_Module_  ti_sysbios_knl_Semaphore_Module_
```

4.16.2.33 ti_sysbios_knl_Semaphore_Object_

```
typedef struct  ti_sysbios_knl_Semaphore_Object_  ti_sysbios_knl_Semaphore_Object_
```

4.16.2.34 ti_sysbios_knl_Task_Module_

```
typedef struct  ti_sysbios_knl_Task_Module_  ti_sysbios_knl_Task_Module_
```

4.16.2.35 ti_sysbios_knl_Task_Module_State_

```
typedef struct  ti_sysbios_knl_Task_Module_State_  ti_sysbios_knl_Task_Module_State_
```

4.16.2.36 ti_sysbios_knl_Task_Object_

```
typedef struct  ti_sysbios_knl_Task_Object_  ti_sysbios_knl_Task_Object_
```

4.16.2.37 ti_sysbios_rts_ti_ThreadLocalStorage_Module_State_

```
typedef struct  ti_sysbios_rts_ti_ThreadLocalStorage_Module_State_  ti_sysbios_rts_ti_ThreadLocalStorage_Module_State_
```

4.16.2.38 ti_sysbios_timers_rti_Timer_Module_

```
typedef struct  ti_sysbios_timers_rti_Timer_Module_  ti_sysbios_timers_rti_Timer_Module_
```

4.16.2.39 ti_sysbios_timers_rti_Timer_Module_State_

```
typedef struct  ti_sysbios_timers_rti_Timer_Module_State_  ti_sysbios_timers_rti_Timer_Module_State_
```

4.16.2.40 ti_sysbios_timers_rti_Timer_Object__

```
typedef struct  ti_sysbios_timers_rti_Timer_Object__  ti_sysbios_timers_rti_Timer_Object__
```

4.16.2.41 ti_sysbios_utils_Load_Module_State__

```
typedef struct  ti_sysbios_utils_Load_Module_State__  ti_sysbios_utils_Load_Module_State__
```

4.16.2.42 xdc_runtime_Error_Module_State__

```
typedef struct  xdc_runtime_Error_Module_State__  xdc_runtime_Error_Module_State__
```

4.16.2.43 xdc_runtime_Main_Module_GateProxy_Module__

```
typedef struct  xdc_runtime_Main_Module_GateProxy_Module__  xdc_runtime_Main_Module_GateProxy_Module__
```

4.16.2.44 xdc_runtime_Main_Module_GateProxy_Object__

```
typedef  ti_sysbios_gates_GateHwi_Object__  xdc_runtime_Main_Module_GateProxy_Object__
```

Definition at line 638 of file dss_mrr_pe674.c.

4.16.2.45 xdc_runtime_Memory_HeapProxy_Module__

```
typedef struct  xdc_runtime_Memory_HeapProxy_Module__  xdc_runtime_Memory_HeapProxy_Module__
```

4.16.2.46 xdc_runtime_Memory_HeapProxy_Object__

```
typedef  ti_sysbios_heaps_HeapMem_Object__  xdc_runtime_Memory_HeapProxy_Object__
```

Definition at line 667 of file dss_mrr_pe674.c.

4.16.2.47 xdc_runtime_Memory_Module_State__

```
typedef struct  xdc_runtime_Memory_Module_State__  xdc_runtime_Memory_Module_State__
```

4.16.2.48 xdc_runtime_Registry_Module_State__

```
typedef struct  xdc_runtime_Registry_Module_State__  xdc_runtime_Registry_Module_State__
```

4.16.2.49 xdc_runtime_Startup_Module_State__

```
typedef struct  xdc_runtime_Startup_Module_State__  xdc_runtime_Startup_Module_State__
```

4.16.2.50 xdc_runtime_System_Module_GateProxy_Module__

```
typedef struct  xdc_runtime_System_Module_GateProxy_Module__  xdc_runtime_System_Module_GateProxy_Module__
```

4.16.2.51 `xdc_runtime_System_Module_GateProxy_Object__`

```
typedef ti_sysbios_gates_GateHwi_Object__ xdc_runtime_System_Module_GateProxy_Object__  
Definition at line 711 of file dss_mrr_pe674.c.
```

4.16.2.52 `xdc_runtime_System_Module_State__`

```
typedef struct xdc_runtime_System_Module_State__ xdc_runtime_System_Module_State__
```

4.16.2.53 `xdc_runtime_Text_Module_State__`

```
typedef struct xdc_runtime_Text_Module_State__ xdc_runtime_Text_Module_State__
```

4.16.3 Function Documentation

4.16.3.1 `__xdc_init()`

```
int __xdc_init (void)
```

4.16.3.2 `asm()` [1/49]

```
asm (  
    " .align 0x400 " )
```

4.16.3.3 `asm()` [2/49]

```
asm (  
    " .global _c_int00 " )
```

4.16.3.4 `asm()` [3/49]

```
asm (  
    " .global ti_sysbios_family_c64p_Exception_dispatch__E " )
```

4.16.3.5 `asm()` [4/49]

```
asm (  
    " .global ti_sysbios_family_c64p_Hwi0 " )
```

4.16.3.6 `asm()` [5/49]

```
asm (  
    " .global ti_sysbios_family_c64p_Hwi_dispatchAlways " )
```

4.16.3.7 `asm()` [6/49]

```
asm (  
    " .global ti_sysbios_family_c64p_Hwi_int0 " )
```

4.16.3.8 asm() [7/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int1 " )
```

4.16.3.9 asm() [8/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int10 " )
```

4.16.3.10 asm() [9/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int11 " )
```

4.16.3.11 asm() [10/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int12 " )
```

4.16.3.12 asm() [11/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int13 " )
```

4.16.3.13 asm() [12/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int14 " )
```

4.16.3.14 asm() [13/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int15 " )
```

4.16.3.15 asm() [14/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int2 " )
```

4.16.3.16 asm() [15/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int3 " )
```

4.16.3.17 asm() [16/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int4 " )
```

4.16.3.18 `asm()` [17/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int5 " )
```

4.16.3.19 `asm()` [18/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int6 " )
```

4.16.3.20 `asm()` [19/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int7 " )
```

4.16.3.21 `asm()` [20/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int8 " )
```

4.16.3.22 `asm()` [21/49]

```
asm (
    " .global ti_sysbios_family_c64p_Hwi_int9 " )
```

4.16.3.23 `asm()` [22/49]

```
asm (
    " .nocmp " )
```

4.16.3.24 `asm()` [23/49]

```
asm (
    " .sect \".vecs\" " )
```

4.16.3.25 `asm()` [24/49]

```
asm (
    " b b0 " )
```

4.16.3.26 `asm()` [25/49]

```
asm (
    " ldw *++ b15[2],
    b0 " )
```

4.16.3.27 `asm()` [26/49]

```
asm (
    " mvk 1,
    b0 " )
```

4.16.3.28 asm() [27/49]

```
asm (
    " mvkh _c_int00,
    b0 " )
```

4.16.3.29 asm() [28/49]

```
asm (
    " mvkl _c_int00,
    b0 " )
```

4.16.3.30 asm() [29/49]

```
asm (
    " nop " )
```

4.16.3.31 asm() [30/49]

```
asm (
    " nop 4 " )
```

4.16.3.32 asm() [31/49]

```
asm (
    " stw b0,
    *b15-- " [2] )
```

4.16.3.33 asm() [32/49]

```
asm (
    " stw b0,
    *b15 " [1] )
```

4.16.3.34 asm() [33/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi0: " )
```

4.16.3.35 asm() [34/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int0: " )
```

4.16.3.36 asm() [35/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int10: " )
```

4.16.3.37 asm() [36/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int11:  \"  ")
```

4.16.3.38 asm() [37/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int12:  \"  ")
```

4.16.3.39 asm() [38/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int13:  \"  ")
```

4.16.3.40 asm() [39/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int14:  \"  ")
```

4.16.3.41 asm() [40/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int15:  \"  ")
```

4.16.3.42 asm() [41/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int1:  \"  ")
```

4.16.3.43 asm() [42/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int2:  \"  ")
```

4.16.3.44 asm() [43/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int3:  \"  ")
```

4.16.3.45 asm() [44/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int4:  \"  ")
```

4.16.3.46 asm() [45/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int5:  \"  ")
```

4.16.3.47 asm() [46/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int6:    " )
```

4.16.3.48 asm() [47/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int7:    " )
```

4.16.3.49 asm() [48/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int8:    " )
```

4.16.3.50 asm() [49/49]

```
asm (
    "ti_sysbios_family_c64p_Hwi_int9:    " )
```

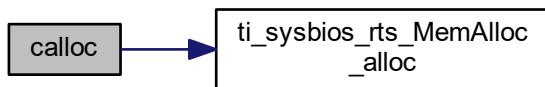
4.16.3.51 calloc()

```
Void ATTRIBUTE* calloc (
    SizeT nmemb,
    SizeT size )
```

Definition at line 2878 of file dss_mrr_pe674.c.

References ti_sysbios_rts_MemAlloc_alloc().

Here is the call graph for this function:

**4.16.3.52 DSP_sleep()**

```
xdc_Void DSP_sleep (
    xdc_Void )
```

4.16.3.53 free()

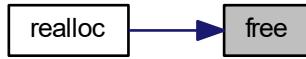
```
Void ATTRIBUTE free (
    Void * ptr )
```

Definition at line 2901 of file dss_mrr_pe674.c.

References Header::actualBuf, Header::header, and Header::size.

Referenced by realloc().

Here is the caller graph for this function:



4.16.3.54 malloc()

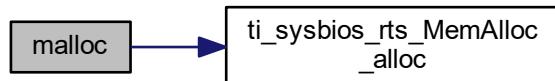
```
Void ATTRIBUTE* malloc (
    SizeT size )
```

Definition at line 2829 of file dss_mrr_pe674.c.

References ti_sysbios_rts_MemAlloc_alloc().

Referenced by realloc().

Here is the call graph for this function:



Here is the caller graph for this function:



4.16.3.55 memalign()

```
Void ATTRIBUTE* memalign (
    SizeT alignment,
    SizeT size )
```

Definition at line 2838 of file dss_mrr_pe674.c.

References Header::actualBuf, Header::header, and Header::size.

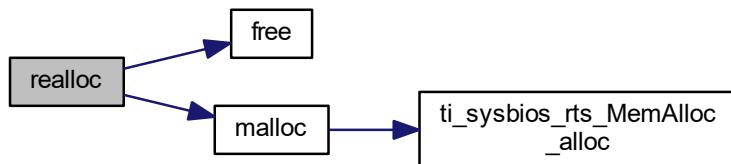
4.16.3.56 realloc()

```
Void ATTRIBUTE* realloc (
    Void * ptr,
    SizeT size )
```

Definition at line 2917 of file dss_mrr_pe674.c.

References free(), Header::header, malloc(), and Header::size.

Here is the call graph for this function:



4.16.3.57 ti_sysbios_BIOS_atExitFunc_I()

```
Void ti_sysbios_BIOS_atExitFunc_I (
    Int notused )
```

Definition at line 2590 of file dss_mrr_pe674.c.

References `ti_sysbios_BIOS_Module_state_V`.

Referenced by `ti_sysbios_BIOS_startFunc_I()`.

Here is the caller graph for this function:



4.16.3.58 ti_sysbios_BIOS_errorRaiseHook()

```
Void ti_sysbios_BIOS_errorRaiseHook (
    xdc_runtime_Error_Block * eb )
```

Definition at line 2679 of file dss_mrr_pe674.c.

References `ti_sysbios_BIOS_removeRTSLock()`.

Here is the call graph for this function:



4.16.3.59 ti_sysbios_BIOS_exitFunc() [1/2]

```
Void ti_sysbios_BIOS_exitFunc (
    Int stat )
```

Definition at line 2665 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_removeRTSLock().

Here is the call graph for this function:



4.16.3.60 ti_sysbios_BIOS_exitFunc() [2/2]

```
xdc_Void ti_sysbios_BIOS_exitFunc (
    xdc_Int )
```

4.16.3.61 ti_sysbios_BIOS_Module__startupDone__S()

```
xdc_Bool ti_sysbios_BIOS_Module__startupDone__S (
    void )
```

Definition at line 9428 of file dss_mrr_pe674.c.

4.16.3.62 ti_sysbios_BIOS_nullFunc__I()

```
Void ti_sysbios_BIOS_nullFunc__I ( )
```

Definition at line 2636 of file dss_mrr_pe674.c.

4.16.3.63 ti_sysbios_BIOS_registerRTSLock() [1/2]

```
Void ti_sysbios_BIOS_registerRTSLock ( )
```

Referenced by ti_sysbios_BIOS_startFunc__I().

Here is the caller graph for this function:

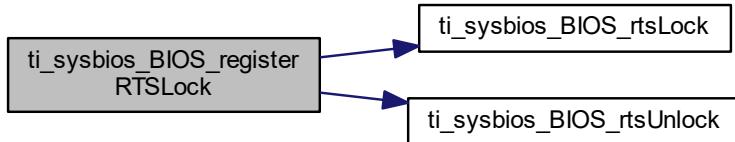


4.16.3.64 ti_sysbios_BIOS_registerRTSLock() [2/2]

```
Void ti_sysbios_BIOS_registerRTSLock (
    Void )
```

Definition at line 2643 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_Module_state_V, ti_sysbios_BIOS_rtsLock(), and ti_sysbios_BIOS_rtsUnlock().
Here is the call graph for this function:



4.16.3.65 ti_sysbios_BIOS_removeRTSLock()

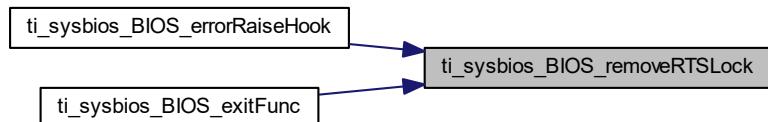
```
Void ti_sysbios_BIOS_removeRTSLock (
    Void )
```

Definition at line 2654 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_Module_state_V.

Referenced by ti_sysbios_BIOS_errorRaiseHook(), and ti_sysbios_BIOS_exitFunc().

Here is the caller graph for this function:



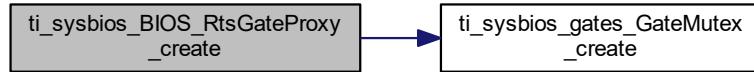
4.16.3.66 ti_sysbios_BIOS_RtsGateProxy_create()

```
ti_sysbios_BIOS_RtsGateProxy_Handle ti_sysbios_BIOS_RtsGateProxy_create (
    const ti_sysbios_BIOS_RtsGateProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8480 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_create().

Here is the call graph for this function:



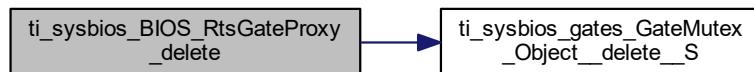
4.16.3.67 ti_sysbios_BIOS_RtsGateProxy_delete()

```
void ti_sysbios_BIOS_RtsGateProxy_delete (
    ti_sysbios_BIOS_RtsGateProxy_Handle * instp )
```

Definition at line 8486 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object_delete_S().

Here is the call graph for this function:



4.16.3.68 ti_sysbios_BIOS_RtsGateProxy_enter_E()

```
xdc_IArg ti_sysbios_BIOS_RtsGateProxy_enter_E (
    ti_sysbios_BIOS_RtsGateProxy_Handle __inst )
```

Definition at line 8510 of file dss_mrr_pe674.c.

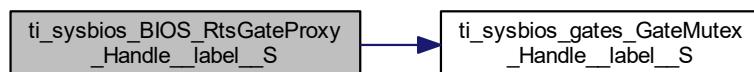
4.16.3.69 ti_sysbios_BIOS_RtsGateProxy_Handle_label_S()

```
xdc_runtime_Types_Label* ti_sysbios_BIOS_RtsGateProxy_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 8498 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Handle_label_S().

Here is the call graph for this function:



4.16.3.70 ti_sysbios_BIOS_RtsGateProxy_leave_E()

```
xdc_Void ti_sysbios_BIOS_RtsGateProxy_leave_E (
    ti_sysbios_BIOS_RtsGateProxy_Handle __inst,
    xdc_IArg key )
```

Definition at line 8516 of file dss_mrr_pe674.c.

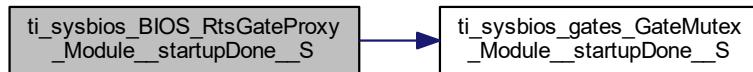
4.16.3.71 ti_sysbios_BIOS_RtsGateProxy_Module_startupDone_S()

```
xdc_Bool ti_sysbios_BIOS_RtsGateProxy_Module_startupDone_S (
    void )
```

Definition at line 8474 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Module_startupDone_S().

Here is the call graph for this function:



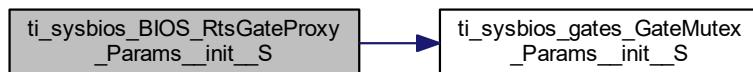
4.16.3.72 ti_sysbios_BIOS_RtsGateProxy_Params_init_S()

```
void ti_sysbios_BIOS_RtsGateProxy_Params_init_S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 8492 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Params_init_S().

Here is the call graph for this function:



4.16.3.73 ti_sysbios_BIOS_RtsGateProxy_Proxy_abstract_S()

```
xdc_Bool ti_sysbios_BIOS_RtsGateProxy_Proxy_abstract_S (
    void )
```

Definition at line 9543 of file dss_mrr_pe674.c.

4.16.3.74 ti_sysbios_BIOS_RtsGateProxy_Proxy__delegate__S()

```
xdc_CPtr ti_sysbios_BIOS_RtsGateProxy_Proxy__delegate__S (
    void )
```

Definition at line 9547 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Module__FXNS__C.

4.16.3.75 ti_sysbios_BIOS_RtsGateProxy_query__E()

```
xdc_Bool ti_sysbios_BIOS_RtsGateProxy_query__E (
    xdc_Int qual )
```

Definition at line 8504 of file dss_mrr_pe674.c.

4.16.3.76 ti_sysbios_BIOS_rtsLock()

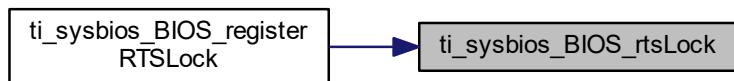
Void ti_sysbios_BIOS_rtsLock ()

Definition at line 2606 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_Module__state__V.

Referenced by ti_sysbios_BIOS_registerRTSLock().

Here is the caller graph for this function:



4.16.3.77 ti_sysbios_BIOS_rtsUnlock()

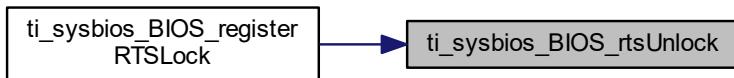
Void ti_sysbios_BIOS_rtsUnlock ()

Definition at line 2622 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_Module__state__V.

Referenced by ti_sysbios_BIOS_registerRTSLock().

Here is the caller graph for this function:



4.16.3.78 ti_sysbios_BIOS_startFunc()

```
xdc_Void ti_sysbios_BIOS_startFunc (
    xdc_Void )
```

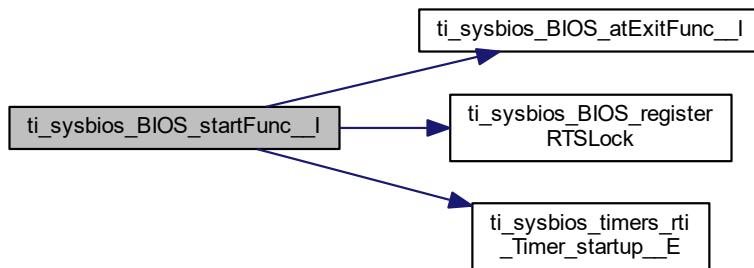
4.16.3.79 ti_sysbios_BIOS_startFunc_I()

```
Void ti_sysbios_BIOS_startFunc_I ( )
```

Definition at line 2568 of file dss_mrr_pe674.c.

References ti_sysbios_BIOS_atExitFunc_I(), ti_sysbios_BIOS_registerRTSLock(), and ti_sysbios_timers_rti_Timer_startup_E().

Here is the call graph for this function:



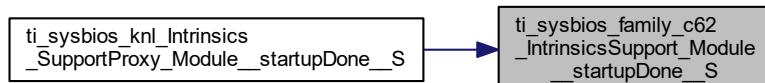
4.16.3.80 ti_sysbios_family_c62_IntrinsicsSupport_Module_startupDone_S()

```
xdc_Bool ti_sysbios_family_c62_IntrinsicsSupport_Module_startupDone_S (
    void )
```

Definition at line 9559 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Intrinsics_SupportProxy_Module_startupDone_S().

Here is the caller graph for this function:



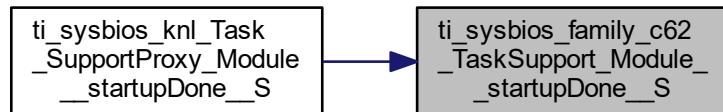
4.16.3.81 ti_sysbios_family_c62_TaskSupport_Module_startupDone_S()

```
xdc_Bool ti_sysbios_family_c62_TaskSupport_Module_startupDone_S (
    void )
```

Definition at line 9571 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Task_SupportProxy_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.82 ti_sysbios_family_c64p_Cache_Module_startupDone_F()

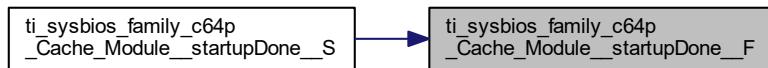
```
xdc_Bool ti_sysbios_family_c64p_Cache_Module_startupDone_F (
    void )
```

Definition at line 2076 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_family_c64p_Cache_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.83 ti_sysbios_family_c64p_Cache_Module_startupDone_S()

```
xdc_Bool ti_sysbios_family_c64p_Cache_Module_startupDone_S (
    void )
```

Definition at line 9583 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Cache_Module_startupDone_F().

Here is the call graph for this function:



4.16.3.84 ti_sysbios_family_c64p_Cache_Module_startup_E()

```
xdc_Int ti_sysbios_family_c64p_Cache_Module_startup_E (
    xdc_Int )
```

4.16.3.85 ti_sysbios_family_c64p_EventCombiner_dispatch()

```
xdc_Void ti_sysbios_family_c64p_EventCombiner_dispatch (
    xdc_UArg    )
```

4.16.3.86 ti_sysbios_family_c64p_EventCombiner_Module_startupDone_F()

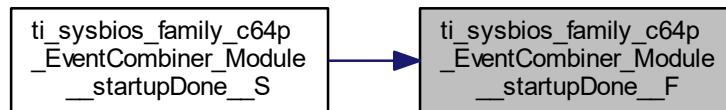
```
xdc_Bool ti_sysbios_family_c64p_EventCombiner_Module_startupDone_F (
    void    )
```

Definition at line 2060 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_family_c64p_EventCombiner_Module_startupDone_S().

Here is the caller graph for this function:



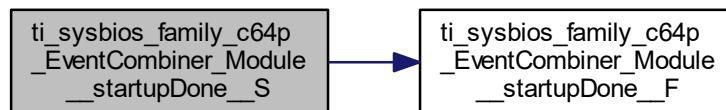
4.16.3.87 ti_sysbios_family_c64p_EventCombiner_Module_startupDone_S()

```
xdc_Bool ti_sysbios_family_c64p_EventCombiner_Module_startupDone_S (
    void    )
```

Definition at line 9595 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_EventCombiner_Module_startupDone_F().

Here is the call graph for this function:



4.16.3.88 ti_sysbios_family_c64p_EventCombiner_Module_startup_E()

```
xdc_Int ti_sysbios_family_c64p_EventCombiner_Module_startup_E (
    xdc_Int    )
```

4.16.3.89 ti_sysbios_family_c64p_EventCombiner_unused()

```
xdc_Void ti_sysbios_family_c64p_EventCombiner_unused (
    xdc_UArg    )
```

4.16.3.90 ti_sysbios_family_c64p_Exception_Module_startupDone_F()

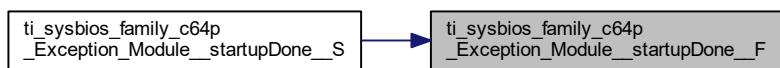
```
xdc_Bool ti_sysbios_family_c64p_Exception_Module_startupDone_F (
    void    )
```

Definition at line 2064 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_family_c64p_Exception_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.91 ti_sysbios_family_c64p_Exception_Module_startupDone_S()

```
xdc_Bool ti_sysbios_family_c64p_Exception_Module_startupDone_S (
    void    )
```

Definition at line 9607 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Exception_Module_startupDone_F().

Here is the call graph for this function:



4.16.3.92 ti_sysbios_family_c64p_Exception_Module_startup_E()

```
xdc_Int ti_sysbios_family_c64p_Exception_Module_startup_E (
    xdc_Int    )
```

4.16.3.93 ti_sysbios_family_c64p_Hwi_construct()

```
void ti_sysbios_family_c64p_Hwi_construct (
    ti_sysbios_family_c64p_Hwi_Struct * __obj,
    xdc_Int intNum,
    ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn,
    const ti_sysbios_family_c64p_Hwi_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9835 of file dss_mrr_pe674.c.

References `ti_sysbios_family_c64p_Hwi_Object__DESC__C`.

4.16.3.94 `ti_sysbios_family_c64p_Hwi_create()`

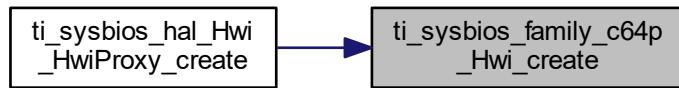
```
ti_sysbios_family_c64p_Hwi_Handle ti_sysbios_family_c64p_Hwi_create (
    xdc_Int intNum,
    ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn,
    const ti_sysbios_family_c64p_Hwi_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9811 of file `dss_mrr_pe674.c`.

References `ti_sysbios_family_c64p_Hwi_Object__DESC__C`.

Referenced by `ti_sysbios_hal_Hwi_HwiProxy_create()`.

Here is the caller graph for this function:



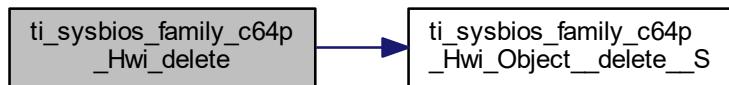
4.16.3.95 `ti_sysbios_family_c64p_Hwi_delete()`

```
void ti_sysbios_family_c64p_Hwi_delete (
    ti_sysbios_family_c64p_Hwi_Handle * instp )
```

Definition at line 9865 of file `dss_mrr_pe674.c`.

References `ti_sysbios_family_c64p_Hwi_Object__delete__S()`.

Here is the call graph for this function:



4.16.3.96 `ti_sysbios_family_c64p_Hwi_destruct()`

```
void ti_sysbios_family_c64p_Hwi_destruct (
    ti_sysbios_family_c64p_Hwi_Struct * obj )
```

Definition at line 9852 of file `dss_mrr_pe674.c`.

References `ti_sysbios_family_c64p_Hwi_Object__DESC__C`.

4.16.3.97 ti_sysbios_family_c64p_Hwi_Handle_label_S()

```
xdc_runtime_Types_Label* ti_sysbios_family_c64p_Hwi_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 9729 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_hal_Hwi_HwiProxy_Handle_label_S().

Here is the caller graph for this function:



4.16.3.98 ti_sysbios_family_c64p_Hwi_Module_startupDone_F()

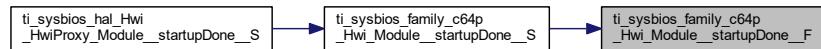
```
xdc_Bool ti_sysbios_family_c64p_Hwi_Module_startupDone_F (
    void )
```

Definition at line 2068 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_family_c64p_Hwi_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.99 ti_sysbios_family_c64p_Hwi_Module_startupDone_S()

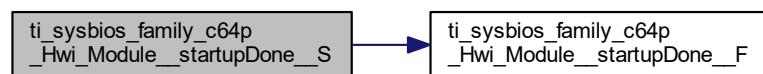
```
xdc_Bool ti_sysbios_family_c64p_Hwi_Module_startupDone_S (
    void )
```

Definition at line 9723 of file dss_mrr_pe674.c.

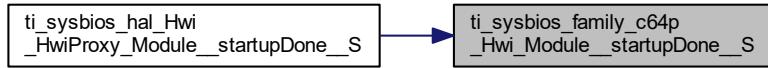
References ti_sysbios_family_c64p_Hwi_Module_startupDone_F().

Referenced by ti_sysbios_hal_Hwi_HwiProxy_Module_startupDone_S().

Here is the call graph for this function:



Here is the caller graph for this function:



4.16.3.100 ti_sysbios_family_c64p_Hwi_Module_startup_E()

```
xdc_Int ti_sysbios_family_c64p_Hwi_Module_startup_E (
    xdc_Int )
```

4.16.3.101 ti_sysbios_family_c64p_Hwi_Object_create_S()

```
xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9782 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Object_DESC_C.

4.16.3.102 ti_sysbios_family_c64p_Hwi_Object_delete_S()

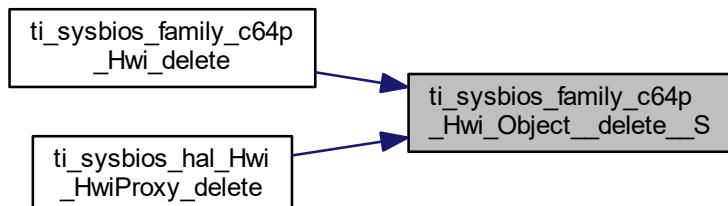
```
xdc_Void ti_sysbios_family_c64p_Hwi_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 9858 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Object_DESC_C.

Referenced by ti_sysbios_family_c64p_Hwi_delete(), and ti_sysbios_hal_Hwi_HwiProxy_delete().

Here is the caller graph for this function:



4.16.3.103 ti_sysbios_family_c64p_Hwi_Object_first_S()

```
xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_first_S (
```

```
    void )
```

Definition at line 9756 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Module__::link, and ti_sysbios_family_c64p_Hwi_Module_root_V.

4.16.3.104 ti_sysbios_family_c64p_Hwi_Object_get_S()

```
xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 9745 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Object_table_C.

4.16.3.105 ti_sysbios_family_c64p_Hwi_Object_next_S()

```
xdc_Ptr ti_sysbios_family_c64p_Hwi_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 9769 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Module__::link, and ti_sysbios_family_c64p_Hwi_Module_root_V.

4.16.3.106 ti_sysbios_family_c64p_Hwi_Params_init_S()

```
xdc_Void ti_sysbios_family_c64p_Hwi_Params_init_S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 9739 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Object_PARAMS_C.

Referenced by ti_sysbios_hal_Hwi_HwiProxy_Params_init_S().

Here is the caller graph for this function:



4.16.3.107 ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_F()

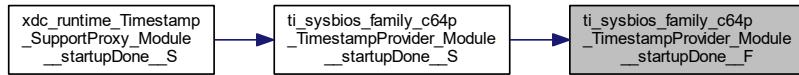
```
xdc_Bool ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_F (
    void )
```

Definition at line 2072 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.108 ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_S()

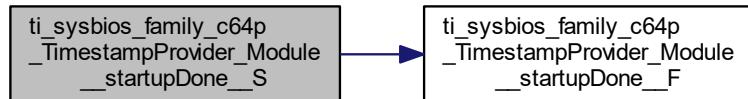
```
xdc_Bool ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_S (
    void )
```

Definition at line 9876 of file dss_mrr_pe674.c.

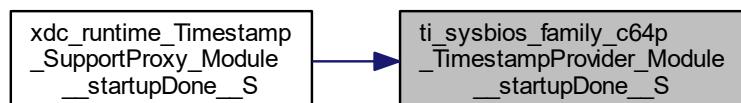
References `ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_F()`.

Referenced by `xdc_runtime_Timestamp_SupportProxy_Module_startupDone_S()`.

Here is the call graph for this function:



Here is the caller graph for this function:



4.16.3.109 ti_sysbios_family_c64p_TimestampProvider_Module_startup_E()

```
xdc_Int ti_sysbios_family_c64p_TimestampProvider_Module_startup_E (
    xdc_Int )
```

4.16.3.110 ti_sysbios_gates_GateHwi_construct()

```
void ti_sysbios_gates_GateHwi_construct (
    ti_sysbios_gates_GateHwi_Struct * __obj,
    const ti_sysbios_gates_GateHwi_Params * __paramsPtr )
```

Definition at line 10091 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object__DESC__C.

4.16.3.111 ti_sysbios_gates_GateHwi_create()

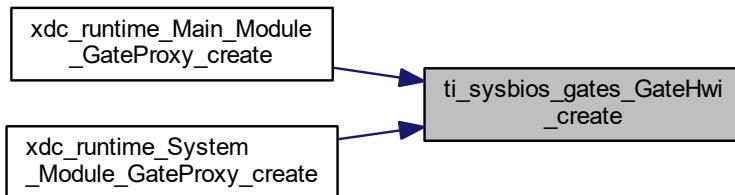
```
ti_sysbios_gates_GateHwi_Handle ti_sysbios_gates_GateHwi_create (
    const ti_sysbios_gates_GateHwi_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10073 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object__DESC__C.

Referenced by xdc_runtime_Main_Module_GateProxy_create(), and xdc_runtime_System_Module_GateProxy_create().

Here is the caller graph for this function:



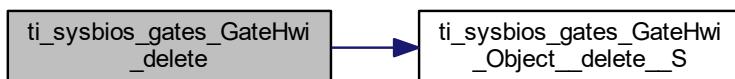
4.16.3.112 ti_sysbios_gates_GateHwi_delete()

```
void ti_sysbios_gates_GateHwi_delete (
    ti_sysbios_gates_GateHwi_Handle * instp )
```

Definition at line 10116 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object__delete__S().

Here is the call graph for this function:



4.16.3.113 ti_sysbios_gates_GateHwi_destruct()

```
void ti_sysbios_gates_GateHwi_destruct (
    ti_sysbios_gates_GateHwi_Struct * obj )
```

Definition at line 10103 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object__DESC__C.

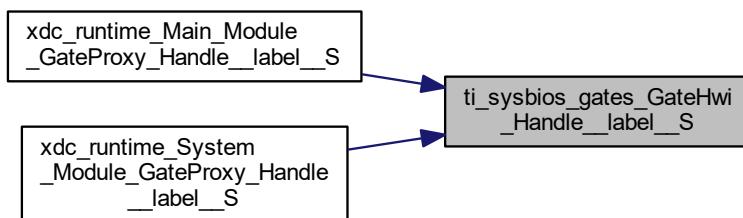
4.16.3.114 ti_sysbios_gates_GateHwi_Handle__label__S()

```
xdc_runtime_Types_Label* ti_sysbios_gates_GateHwi_Handle__label__S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 9998 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_Main_Module_GateProxy_Handle__label__S(), and xdc_runtime_System_Module_GateProxy_Handle__label__S().

Here is the caller graph for this function:



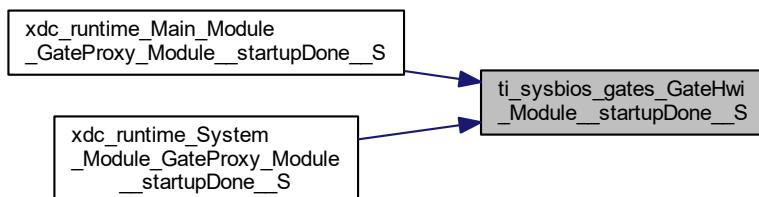
4.16.3.115 ti_sysbios_gates_GateHwi_Module_startupDone__S()

```
xdc_Bool ti_sysbios_gates_GateHwi_Module_startupDone__S (
    void )
```

Definition at line 9992 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_Main_Module_GateProxy_Module_startupDone__S(), and xdc_runtime_System_Module_GateProxy_Module_startupDone__S().

Here is the caller graph for this function:



4.16.3.116 ti_sysbios_gates_GateHwi_Object_create__S()

```
xdc_Ptr ti_sysbios_gates_GateHwi_Object_create__S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10051 of file dss_mrr_pe674.c.

References `ti_sysbios_gates_GateHwi_Object__DESC__C`.

4.16.3.117 `ti_sysbios_gates_GateHwi_Object__delete__S()`

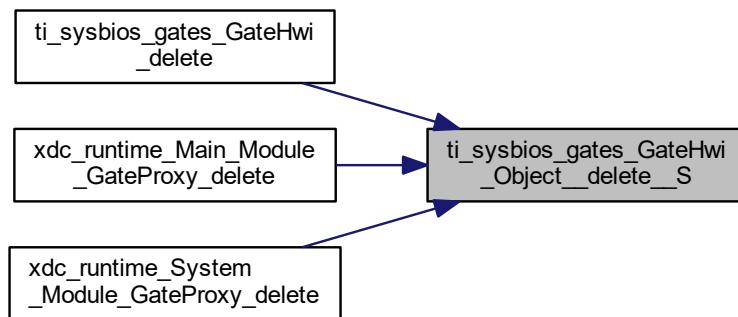
```
xdc_Void ti_sysbios_gates_GateHwi_Object__delete__S (
    xdc_Ptr instp )
```

Definition at line 10109 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateHwi_Object__DESC__C`.

Referenced by `ti_sysbios_gates_GateHwi_delete()`, `xdc_runtime_Main_Module_GateProxy_delete()`, and `xdc_runtime_System_Module_GateProxy_delete()`.

Here is the caller graph for this function:



4.16.3.118 `ti_sysbios_gates_GateHwi_Object__first__S()`

```
xdc_Ptr ti_sysbios_gates_GateHwi_Object__first__S (
    void )
```

Definition at line 10025 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateHwi_Module__link`, and `ti_sysbios_gates_GateHwi_Module__root__V`.

4.16.3.119 `ti_sysbios_gates_GateHwi_Object__get__S()`

```
xdc_Ptr ti_sysbios_gates_GateHwi_Object__get__S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 10014 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateHwi_Object__table__C`.

4.16.3.120 `ti_sysbios_gates_GateHwi_Object__next__S()`

```
xdc_Ptr ti_sysbios_gates_GateHwi_Object__next__S (
    xdc_Ptr obj )
```

Definition at line 10038 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateHwi_Module__link`, and `ti_sysbios_gates_GateHwi_Module__root__V`.

4.16.3.121 ti_sysbios_gates_GateHwi_Params__init__S()

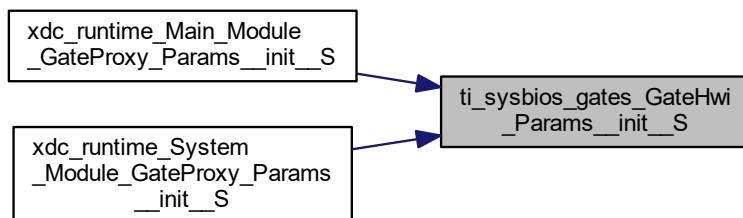
```
xdc_Void ti_sysbios_gates_GateHwi_Params__init__S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 10008 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object__PARAMS__C.

Referenced by xdc_runtime_Main_Module_GateProxy_Params__init__S(), and xdc_runtime_System_Module_GateProxy_Params__init__S().

Here is the caller graph for this function:



4.16.3.122 ti_sysbios_gates_GateMutex_construct()

```
void ti_sysbios_gates_GateMutex_construct (
    ti_sysbios_gates_GateMutex_Struct * __obj,
    const ti_sysbios_gates_GateMutex_Params * __paramsPtr )
```

Definition at line 10330 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object__DESC__C.

4.16.3.123 ti_sysbios_gates_GateMutex_create()

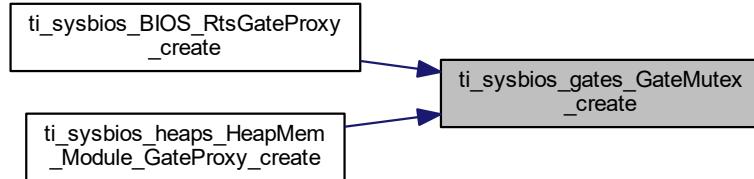
```
ti_sysbios_gates_GateMutex_Handle ti_sysbios_gates_GateMutex_create (
    const ti_sysbios_gates_GateMutex_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10312 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object__DESC__C.

Referenced by ti_sysbios_BIOS_RtsGateProxy_create(), and ti_sysbios_heaps_HeapMem_Module_GateProxy_create().

Here is the caller graph for this function:



4.16.3.124 ti_sysbios_gates_GateMutex_delete()

```
void ti_sysbios_gates_GateMutex_delete (
    ti_sysbios_gates_GateMutex_Handle * instp )
```

Definition at line 10355 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object_delete_S().

Here is the call graph for this function:



4.16.3.125 ti_sysbios_gates_GateMutex_destruct()

```
void ti_sysbios_gates_GateMutex_destruct (
    ti_sysbios_gates_GateMutex_Struct * obj )
```

Definition at line 10342 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object_DESC_C().

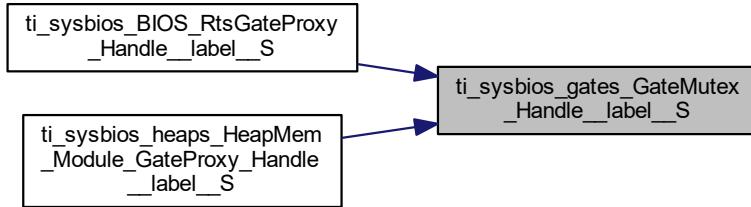
4.16.3.126 ti_sysbios_gates_GateMutex_Handle_label_S()

```
xdc_runtime_Label* ti_sysbios_gates_GateMutex_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 10237 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_BIOS_RtsGateProxy_Handle_label_S(), and ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle_label_S().

Here is the caller graph for this function:



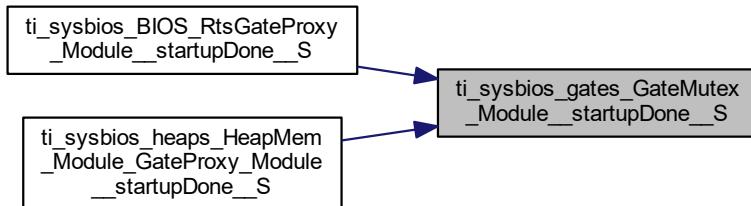
4.16.3.127 ti_sysbios_gates_GateMutex_Module_startupDone_S()

```
xdc_Bool ti_sysbios_gates_GateMutex_Module_startupDone_S (
    void )
```

Definition at line 10231 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_BIOS_RtsGateProxy_Module_startupDone_S(), and ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.128 ti_sysbios_gates_GateMutex_Object_create_S()

```
xdc_Ptr ti_sysbios_gates_GateMutex_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10290 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object_DESC_C.

4.16.3.129 ti_sysbios_gates_GateMutex_Object_delete_S()

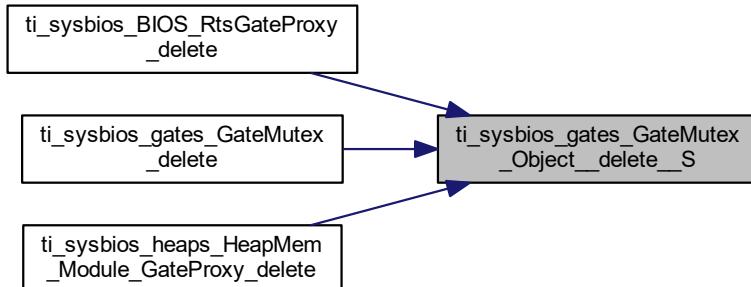
```
xdc_Void ti_sysbios_gates_GateMutex_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 10348 of file dss_mrr_pe674.c.

References `ti_sysbios_gates_GateMutex_Object__DESC__C`.

Referenced by `ti_sysbios_BIOS_RtsGateProxy_delete()`, `ti_sysbios_gates_GateMutex_delete()`, and `ti_sysbios_heaps_HeapMem_Module_GateProxy_delete()`.

Here is the caller graph for this function:



4.16.3.130 `ti_sysbios_gates_GateMutex_Object__first__S()`

```
xdc_Ptr ti_sysbios_gates_GateMutex_Object__first__S (
    void )
```

Definition at line 10264 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateMutex_Module__::link`, and `ti_sysbios_gates_GateMutex_Module__root__V`.

4.16.3.131 `ti_sysbios_gates_GateMutex_Object__get__S()`

```
xdc_Ptr ti_sysbios_gates_GateMutex_Object__get__S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 10253 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateMutex_Object__table__C`.

4.16.3.132 `ti_sysbios_gates_GateMutex_Object__next__S()`

```
xdc_Ptr ti_sysbios_gates_GateMutex_Object__next__S (
    xdc_Ptr obj )
```

Definition at line 10277 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateMutex_Module__::link`, and `ti_sysbios_gates_GateMutex_Module__root__V`.

4.16.3.133 `ti_sysbios_gates_GateMutex_Params__init__S()`

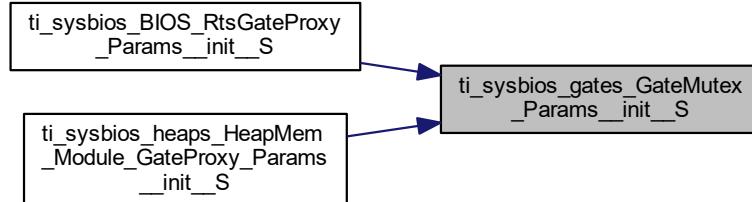
```
xdc_Void ti_sysbios_gates_GateMutex_Params__init__S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 10247 of file `dss_mrr_pe674.c`.

References `ti_sysbios_gates_GateMutex_Object__PARAMS__C`.

Referenced by `ti_sysbios_BIOS_RtsGateProxy_Params_init_S()`, and `ti_sysbios_heaps_HeapMem_Module_GateProxy_Params_init_S()`.

Here is the caller graph for this function:



4.16.3.134 `ti_sysbios_hal_Hwi_checkStack()`

```
xdc_Void ti_sysbios_hal_Hwi_checkStack (
    xdc_Void )
```

4.16.3.135 `ti_sysbios_hal_Hwi_construct()`

```
void ti_sysbios_hal_Hwi_construct (
    ti_sysbios_hal_Hwi_Struct * __obj,
    xdc_Int intNum,
    ti_sysbios_hal_Hwi_FuncPtr hwiFxn,
    const ti_sysbios_hal_Hwi_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10582 of file `dss_mrr_pe674.c`.

References `ti_sysbios_hal_Hwi_Object_DESC_C`.

4.16.3.136 `ti_sysbios_hal_Hwi_create()`

```
ti_sysbios_hal_Hwi_Handle ti_sysbios_hal_Hwi_create (
    xdc_Int intNum,
    ti_sysbios_hal_Hwi_FuncPtr hwiFxn,
    const ti_sysbios_hal_Hwi_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10558 of file `dss_mrr_pe674.c`.

References `ti_sysbios_hal_Hwi_Object_DESC_C`.

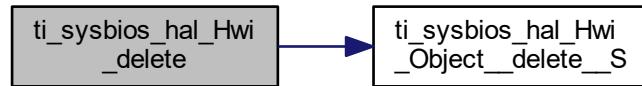
4.16.3.137 `ti_sysbios_hal_Hwi_delete()`

```
void ti_sysbios_hal_Hwi_delete (
    ti_sysbios_hal_Hwi_Handle * instp )
```

Definition at line 10612 of file `dss_mrr_pe674.c`.

References `ti_sysbios_hal_Hwi_Object_delete_S()`.

Here is the call graph for this function:



4.16.3.138 ti_sysbios_hal_Hwi_destruct()

```
void ti_sysbios_hal_Hwi_destruct (
    ti_sysbios_hal_Hwi_Struct * obj )
```

Definition at line 10599 of file dss_mrr_pe674.c.
References ti_sysbios_hal_Hwi_Object_DESC_C.

4.16.3.139 ti_sysbios_hal_Hwi_Handle_label_S()

```
xdc_runtime_Label* ti_sysbios_hal_Hwi_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 10476 of file dss_mrr_pe674.c.

4.16.3.140 ti_sysbios_hal_HwiProxy_clearInterrupt_E()

```
xdc_Void ti_sysbios_hal_HwiProxy_clearInterrupt_E (
    xdc_UInt intNum )
```

Definition at line 8631 of file dss_mrr_pe674.c.

4.16.3.141 ti_sysbios_hal_HwiProxy_create()

```
ti_sysbios_hal_HwiProxy_Handle ti_sysbios_hal_HwiProxy_create (
    xdc_Int intNum,
    ti_sysbios_interfaces_IHwi_FuncPtr hwiFxn,
    const ti_sysbios_hal_HwiProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8535 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_create().

Here is the call graph for this function:



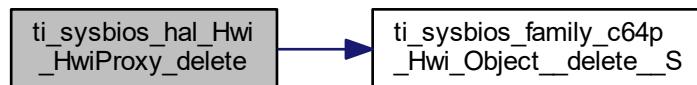
4.16.3.142 ti_sysbios_hal_Hwi_HwiProxy_delete()

```
void ti_sysbios_hal_Hwi_HwiProxy_delete (
    ti_sysbios_hal_Hwi_HwiProxy_Handle * instp )
```

Definition at line 8541 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Object_delete_S().

Here is the call graph for this function:



4.16.3.143 ti_sysbios_hal_Hwi_HwiProxy_disable_E()

```
xdc_UInt ti_sysbios_hal_Hwi_HwiProxy_disable_E (
    void )
```

Definition at line 8577 of file dss_mrr_pe674.c.

4.16.3.144 ti_sysbios_hal_Hwi_HwiProxy_disableInterrupt_E()

```
xdc_UInt ti_sysbios_hal_Hwi_HwiProxy_disableInterrupt_E (
    xdc_UInt intNum )
```

Definition at line 8613 of file dss_mrr_pe674.c.

4.16.3.145 ti_sysbios_hal_Hwi_HwiProxy_enable_E()

```
xdc_UInt ti_sysbios_hal_Hwi_HwiProxy_enable_E (
    void )
```

Definition at line 8583 of file dss_mrr_pe674.c.

4.16.3.146 ti_sysbios_hal_Hwi_HwiProxy_enableInterrupt_E()

```
xdc_UInt ti_sysbios_hal_Hwi_HwiProxy_enableInterrupt_E (
    xdc_UInt intNum )
```

Definition at line 8619 of file dss_mrr_pe674.c.

4.16.3.147 ti_sysbios_hal_Hwi_HwiProxy_getCoreStackInfo_E()

```
xdc_Bool ti_sysbios_hal_Hwi_HwiProxy_getCoreStackInfo_E (
    ti_sysbios_interfaces_IHwi_StackInfo * stkInfo,
    xdc_Bool computeStackDepth,
    xdc_UInt coreId )
```

Definition at line 8565 of file dss_mrr_pe674.c.

4.16.3.148 `ti_sysbios_hal_Hwi_HwiProxy_getFunc__E()`

```
ti_sysbios_interfaces_IHwi_FuncPtr ti_sysbios_hal_Hwi_HwiProxy_getFunc__E (
    ti_sysbios_hal_Hwi_HwiProxy_Handle __inst,
    xdc_UArg * arg )
```

Definition at line 8637 of file dss_mrr_pe674.c.

4.16.3.149 `ti_sysbios_hal_Hwi_HwiProxy_getHookContext__E()`

```
xdc_Ptr ti_sysbios_hal_Hwi_HwiProxy_getHookContext__E (
    ti_sysbios_hal_Hwi_HwiProxy_Handle __inst,
    xdc_Int id )
```

Definition at line 8649 of file dss_mrr_pe674.c.

4.16.3.150 `ti_sysbios_hal_Hwi_HwiProxy_getIrp__E()`

```
ti_sysbios_interfaces_IHwi_Irp ti_sysbios_hal_Hwi_HwiProxy_getIrp__E (
    ti_sysbios_hal_Hwi_HwiProxy_Handle __inst )
```

Definition at line 8661 of file dss_mrr_pe674.c.

4.16.3.151 `ti_sysbios_hal_Hwi_HwiProxy_getStackInfo__E()`

```
xdc_Bool ti_sysbios_hal_Hwi_HwiProxy_getStackInfo__E (
    ti_sysbios_interfaces_IHwi_StackInfo * stkInfo,
    xdc_Bool computeStackDepth )
```

Definition at line 8559 of file dss_mrr_pe674.c.

4.16.3.152 `ti_sysbios_hal_Hwi_HwiProxy_getTaskSP__E()`

```
xdc_Char* ti_sysbios_hal_Hwi_HwiProxy_getTaskSP__E (
    void )
```

Definition at line 8607 of file dss_mrr_pe674.c.

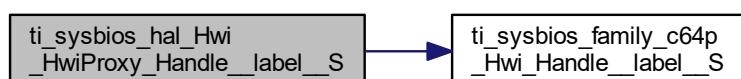
4.16.3.153 `ti_sysbios_hal_Hwi_HwiProxy_Handle_label_S()`

```
xdc_runtime_Types_Label* ti_sysbios_hal_Hwi_HwiProxy_Handle_label__S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 8553 of file dss_mrr_pe674.c.

References `ti_sysbios_family_c64p_Hwi_Handle_label_S()`.

Here is the call graph for this function:



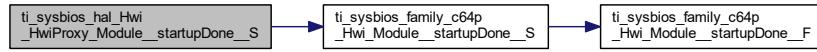
4.16.3.154 ti_sysbios_hal_Hwi_HwiProxy_Module_startupDone_S()

```
xdc_Bool ti_sysbios_hal_Hwi_HwiProxy_Module_startupDone_S (
    void )
```

Definition at line 8529 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Module_startupDone_S().

Here is the call graph for this function:

**4.16.3.155 ti_sysbios_hal_Hwi_HwiProxy_Params_init_S()**

```
void ti_sysbios_hal_Hwi_HwiProxy_Params_init_S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 8547 of file dss_mrr_pe674.c.

References ti_sysbios_family_c64p_Hwi_Params_init_S().

Here is the call graph for this function:

**4.16.3.156 ti_sysbios_hal_Hwi_HwiProxy_post_E()**

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_post_E (
    xdc_UInt intNum )
```

Definition at line 8601 of file dss_mrr_pe674.c.

4.16.3.157 ti_sysbios_hal_Hwi_HwiProxy_Proxy_abstract_S()

```
xdc_Bool ti_sysbios_hal_Hwi_HwiProxy_Proxy_abstract_S (
    void )
```

Definition at line 10726 of file dss_mrr_pe674.c.

4.16.3.158 ti_sysbios_hal_Hwi_HwiProxy_Proxy_delegate_S()

```
xdc_CPtr ti_sysbios_hal_Hwi_HwiProxy_Proxy_delegate_S (
    void )
```

Definition at line 10730 of file dss_mrr_pe674.c.

4.16.3.159 ti_sysbios_hal_Hwi_HwiProxy_restore__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_restore__E (
    xdc_UInt key )
```

Definition at line 8589 of file dss_mrr_pe674.c.

4.16.3.160 ti_sysbios_hal_Hwi_HwiProxy_restoreInterrupt__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_restoreInterrupt__E (
    xdc_UInt intNum,
    xdc_UInt key )
```

Definition at line 8625 of file dss_mrr_pe674.c.

4.16.3.161 ti_sysbios_hal_Hwi_HwiProxy_setFunc__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_setFunc__E (
    ti_sysbios_hal_Hwi_HwiProxy_Handle __inst,
    ti_sysbios_interfaces_IHwi_FuncPtr fxn,
    xdc_UArg arg )
```

Definition at line 8643 of file dss_mrr_pe674.c.

4.16.3.162 ti_sysbios_hal_Hwi_HwiProxy_setHookContext__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_setHookContext__E (
    ti_sysbios_hal_Hwi_HwiProxy_Handle __inst,
    xdc_Int id,
    xdc_Ptr hookContext )
```

Definition at line 8655 of file dss_mrr_pe674.c.

4.16.3.163 ti_sysbios_hal_Hwi_HwiProxy_startup__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_startup__E (
    void )
```

Definition at line 8571 of file dss_mrr_pe674.c.

4.16.3.164 ti_sysbios_hal_Hwi_HwiProxy_switchFromBootStack__E()

```
xdc_Void ti_sysbios_hal_Hwi_HwiProxy_switchFromBootStack__E (
    void )
```

Definition at line 8595 of file dss_mrr_pe674.c.

4.16.3.165 ti_sysbios_hal_Hwi_initStack()

```
xdc_Void ti_sysbios_hal_Hwi_initStack (
    xdc_Void )
```

4.16.3.166 ti_sysbios_hal_Hwi_Module_startupDone__F()

```
xdc_Bool ti_sysbios_hal_Hwi_Module_startupDone__F (
    void )
```

Definition at line 2088 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_hal_Hwi_Module_startupDone_S().

Here is the caller graph for this function:



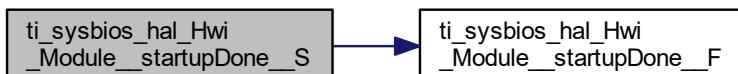
4.16.3.167 ti_sysbios_hal_Hwi_Module_startupDone_S()

```
xdc_Bool ti_sysbios_hal_Hwi_Module_startupDone_S (
    void )
```

Definition at line 10470 of file dss_mrr_pe674.c.

References ti_sysbios_hal_Hwi_Module_startupDone_F().

Here is the call graph for this function:



4.16.3.168 ti_sysbios_hal_Hwi_Module_startup_E()

```
xdc_Int ti_sysbios_hal_Hwi_Module_startup_E (
    xdc_Int )
```

4.16.3.169 ti_sysbios_hal_Hwi_Object_create_S()

```
xdc_Ptr ti_sysbios_hal_Hwi_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

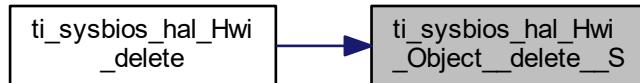
Definition at line 10529 of file dss_mrr_pe674.c.

References ti_sysbios_hal_Hwi_Object_DESC_C.

4.16.3.170 ti_sysbios_hal_Hwi_Object_delete_S()

```
xdc_Void ti_sysbios_hal_Hwi_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 10605 of file dss_mrr_pe674.c.
 References ti_sysbios_hal_Hwi_Object__DESC__C.
 Referenced by ti_sysbios_hal_Hwi_delete().
 Here is the caller graph for this function:



4.16.3.171 ti_sysbios_hal_Hwi_Object_first_S()

```
xdc_Ptr ti_sysbios_hal_Hwi_Object_first_S (
    void )
```

Definition at line 10503 of file dss_mrr_pe674.c.
 References ti_sysbios_hal_Hwi_Module__::link, and ti_sysbios_hal_Hwi_Module__root__V.

4.16.3.172 ti_sysbios_hal_Hwi_Object_get_S()

```
xdc_Ptr ti_sysbios_hal_Hwi_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 10492 of file dss_mrr_pe674.c.
 References ti_sysbios_hal_Hwi_Object_table_C.

4.16.3.173 ti_sysbios_hal_Hwi_Object_next_S()

```
xdc_Ptr ti_sysbios_hal_Hwi_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 10516 of file dss_mrr_pe674.c.
 References ti_sysbios_hal_Hwi_Module__::link, and ti_sysbios_hal_Hwi_Module__root__V.

4.16.3.174 ti_sysbios_hal_Hwi_Params_init_S()

```
xdc_Void ti_sysbios_hal_Hwi_Params_init_S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 10486 of file dss_mrr_pe674.c.
 References ti_sysbios_hal_Hwi_Object_PARAMS_C.

4.16.3.175 ti_sysbios_heaps_HeapMem_construct()

```
void ti_sysbios_heaps_HeapMem_construct (
    ti_sysbios_heaps_HeapMem_Struct * __obj,
    const ti_sysbios_heaps_HeapMem_Params * __paramsPtr )
```

Definition at line 10945 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object__DESC__C.

4.16.3.176 ti_sysbios_heaps_HeapMem_create()

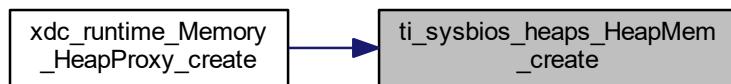
```
ti_sysbios_heaps_HeapMem_Handle ti_sysbios_heaps_HeapMem_create (
    const ti_sysbios_heaps_HeapMem_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10927 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object__DESC__C.

Referenced by xdc_runtime_Memory_HeapProxy_create().

Here is the caller graph for this function:



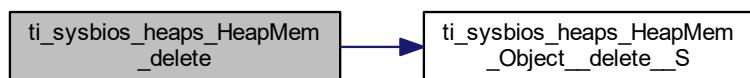
4.16.3.177 ti_sysbios_heaps_HeapMem_delete()

```
void ti_sysbios_heaps_HeapMem_delete (
    ti_sysbios_heaps_HeapMem_Handle * instp )
```

Definition at line 10970 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object__delete__S().

Here is the call graph for this function:



4.16.3.178 ti_sysbios_heaps_HeapMem_destruct()

```
void ti_sysbios_heaps_HeapMem_destruct (
    ti_sysbios_heaps_HeapMem_Struct * obj )
```

Definition at line 10957 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object__DESC__C.

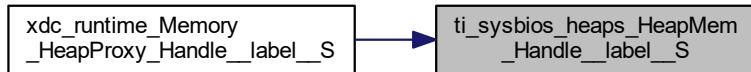
4.16.3.179 ti_sysbios_heaps_HeapMem_Handle_label_S()

```
xdc_runtime_Types_Label* ti_sysbios_heaps_HeapMem_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 10852 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_Memory_HeapProxy_Handle_label_S().

Here is the caller graph for this function:



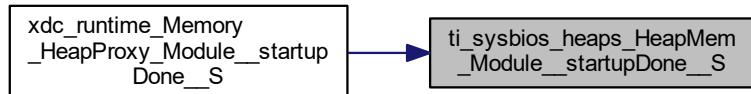
4.16.3.180 ti_sysbios_heaps_HeapMem_Module_startupDone_S()

```
xdc_Bool ti_sysbios_heaps_HeapMem_Module_startupDone_S (
    void )
```

Definition at line 10846 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_Memory_HeapProxy_Module_startupDone_S().

Here is the caller graph for this function:



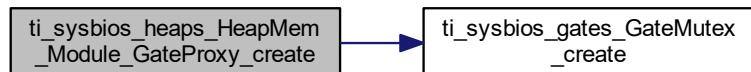
4.16.3.181 ti_sysbios_heaps_HeapMem_Module_GateProxy_create()

```
ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle ti_sysbios_heaps_HeapMem_Module_GateProxy_create (
    const ti_sysbios_heaps_HeapMem_Module_GateProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8680 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_create().

Here is the call graph for this function:



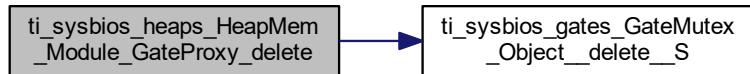
4.16.3.182 ti_sysbios_heaps_HeapMem_Module_GateProxy_delete()

```
void ti_sysbios_heaps_HeapMem_Module_GateProxy_delete (
    ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle * instp )
```

Definition at line 8686 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Object_delete_S().

Here is the call graph for this function:



4.16.3.183 ti_sysbios_heaps_HeapMem_Module_GateProxy_enter_E()

```
xdc_IArg ti_sysbios_heaps_HeapMem_Module_GateProxy_enter_E (
    ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle __inst )
```

Definition at line 8710 of file dss_mrr_pe674.c.

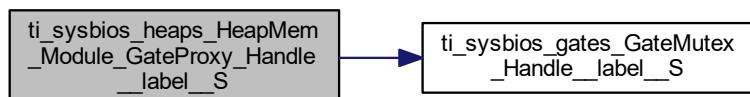
4.16.3.184 ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle_label_S()

```
xdc_runtime_Types_Label* ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 8698 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Handle_label_S().

Here is the call graph for this function:



4.16.3.185 ti_sysbios_heaps_HeapMem_Module_GateProxy_leave_E()

```
xdc_Void ti_sysbios_heaps_HeapMem_Module_GateProxy_leave_E (
    ti_sysbios_heaps_HeapMem_Module_GateProxy_Handle __inst,
    xdc_IArg key )
```

Definition at line 8716 of file dss_mrr_pe674.c.

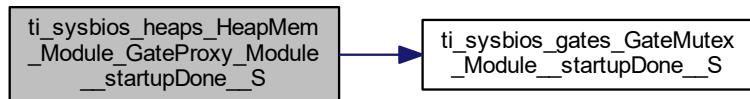
4.16.3.186 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_startupDone__S()

```
xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_startupDone__S (
    void )
```

Definition at line 8674 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Module_startupDone__S().

Here is the call graph for this function:



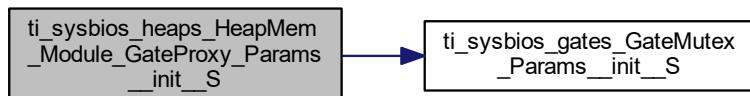
4.16.3.187 ti_sysbios_heaps_HeapMem_Module_GateProxy_Params_init__S()

```
void ti_sysbios_heaps_HeapMem_Module_GateProxy_Params_init__S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 8692 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Params_init__S().

Here is the call graph for this function:



4.16.3.188 ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_abstract__S()

```
xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_abstract__S (
    void )
```

Definition at line 11084 of file dss_mrr_pe674.c.

4.16.3.189 ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_delegate__S()

```
xdc_CPtr ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_delegate__S (
    void )
```

Definition at line 11088 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateMutex_Module_FXNS__C.

4.16.3.190 ti_sysbios_heaps_HeapMem_Module_GateProxy_query_E()

```
xdc_Bool ti_sysbios_heaps_HeapMem_Module_GateProxy_query_E (
    xdc_Int qual )
```

Definition at line 8704 of file dss_mrr_pe674.c.

4.16.3.191 ti_sysbios_heaps_HeapMem_Object_create_S()

```
xdc_Ptr ti_sysbios_heaps_HeapMem_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

Definition at line 10905 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object_DESC_C.

4.16.3.192 ti_sysbios_heaps_HeapMem_Object_delete_S()

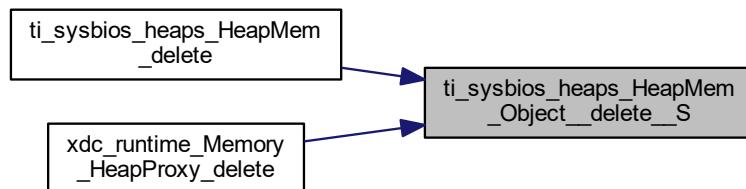
```
xdc_Void ti_sysbios_heaps_HeapMem_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 10963 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object_DESC_C.

Referenced by ti_sysbios_heaps_HeapMem_delete(), and xdc_runtime_Memory_HeapProxy_delete().

Here is the caller graph for this function:

**4.16.3.193 ti_sysbios_heaps_HeapMem_Object_first_S()**

```
xdc_Ptr ti_sysbios_heaps_HeapMem_Object_first_S (
    void )
```

Definition at line 10879 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Module__link, and ti_sysbios_heaps_HeapMem_Module_root_V.

4.16.3.194 ti_sysbios_heaps_HeapMem_Object_get_S()

```
xdc_Ptr ti_sysbios_heaps_HeapMem_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 10868 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object_table_C.

4.16.3.195 `ti_sysbios_heaps_HeapMem_Object__next__S()`

```
xdc_Ptr ti_sysbios_heaps_HeapMem_Object__next__S (
    xdc_Ptr obj )
```

Definition at line 10892 of file dss_mrr_pe674.c.

References `ti_sysbios_heaps_HeapMem_Module__::link`, and `ti_sysbios_heaps_HeapMem_Module__root__V`.

4.16.3.196 `ti_sysbios_heaps_HeapMem_Params__init__S()`

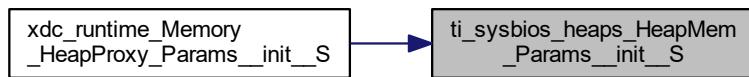
```
xdc_Void ti_sysbios_heaps_HeapMem_Params__init__S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 10862 of file dss_mrr_pe674.c.

References `ti_sysbios_heaps_HeapMem_Object__PARAMS__C`.

Referenced by `xdc_runtime_Memory_HeapProxy_Params__init__S()`.

Here is the caller graph for this function:



4.16.3.197 `ti_sysbios_knl_Clock_construct()`

```
void ti_sysbios_knl_Clock_construct (
    ti_sysbios_knl_Clock_Struct * __obj,
    ti_sysbios_knl_Clock_FuncPtr clockFxn,
    xdc_UInt timeout,
    const ti_sysbios_knl_Clock_Params * __paramsPtr )
```

Definition at line 11304 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Clock_Object__DESC__C`.

4.16.3.198 `ti_sysbios_knl_Clock_create()`

```
ti_sysbios_knl_Clock_Handle ti_sysbios_knl_Clock_create (
    ti_sysbios_knl_Clock_FuncPtr clockFxn,
    xdc_UInt timeout,
    const ti_sysbios_knl_Clock_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 11286 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Clock_Object__DESC__C`.

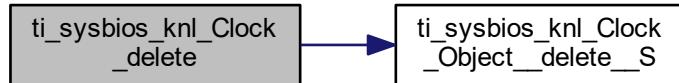
4.16.3.199 `ti_sysbios_knl_Clock_delete()`

```
void ti_sysbios_knl_Clock_delete (
    ti_sysbios_knl_Clock_Handle * instp )
```

Definition at line 11329 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Clock_Object__delete__S()`.

Here is the call graph for this function:



4.16.3.200 `ti_sysbios_knl_Clock_destruct()`

```
void ti_sysbios_knl_Clock_destruct (
    ti_sysbios_knl_Clock_Struct * obj )
```

Definition at line 11316 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Clock_Object__DESC__C`.

4.16.3.201 `ti_sysbios_knl_Clock_doTick_I()` [1/2]

```
Void ti_sysbios_knl_Clock_doTick_I (
    UArg arg )
```

Definition at line 2704 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Clock_Module__state__V`.

4.16.3.202 `ti_sysbios_knl_Clock_doTick_I()` [2/2]

```
xdc_Void ti_sysbios_knl_Clock_doTick_I (
    xdc_UArg )
```

4.16.3.203 `ti_sysbios_knl_Clock_Handle_label_S()`

```
xdc_runtime_Label* ti_sysbios_knl_Clock_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 11210 of file `dss_mrr_pe674.c`.

4.16.3.204 `ti_sysbios_knl_Clock_Module_startupDone_F()`

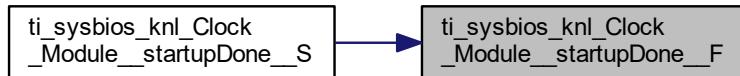
```
xdc_Bool ti_sysbios_knl_Clock_Module_startupDone_F (
    void )
```

Definition at line 2080 of file `dss_mrr_pe674.c`.

References `xdc_runtime_Startup_Module__state__V`.

Referenced by `ti_sysbios_knl_Clock_Module_startupDone_S()`.

Here is the caller graph for this function:



4.16.3.205 ti_sysbios_knl_Clock_Module_startupDone_S()

```
xdc_Bool ti_sysbios_knl_Clock_Module_startupDone_S (
    void )
```

Definition at line 11204 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Module_startupDone_F().

Here is the call graph for this function:



4.16.3.206 ti_sysbios_knl_Clock_Module_startup_E()

```
xdc_Int ti_sysbios_knl_Clock_Module_startup_E (
    xdc_Int )
```

4.16.3.207 ti_sysbios_knl_Clock_Object_create_S()

```
xdc_Ptr ti_sysbios_knl_Clock_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * __eb )
```

Definition at line 11263 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Object_DESC_C.

4.16.3.208 ti_sysbios_knl_Clock_Object_delete_S()

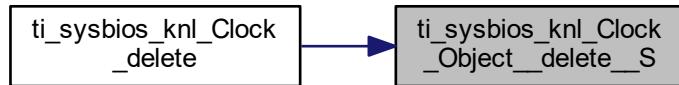
```
xdc_Void ti_sysbios_knl_Clock_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 11322 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Object_DESC_C.

Referenced by ti_sysbios_knl_Clock_delete().

Here is the caller graph for this function:



4.16.3.209 ti_sysbios_knl_Clock_Object_first_S()

```
xdc_Ptr ti_sysbios_knl_Clock_Object_first_S (
    void )
```

Definition at line 11237 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Module__::link, and ti_sysbios_knl_Clock_Module_root_V.

4.16.3.210 ti_sysbios_knl_Clock_Object_get_S()

```
xdc_Ptr ti_sysbios_knl_Clock_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 11226 of file dss_mrr_pe674.c.

4.16.3.211 ti_sysbios_knl_Clock_Object_next_S()

```
xdc_Ptr ti_sysbios_knl_Clock_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 11250 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Module__::link, and ti_sysbios_knl_Clock_Module_root_V.

4.16.3.212 ti_sysbios_knl_Clock_Params_init_S()

```
xdc_Void ti_sysbios_knl_Clock_Params_init_S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 11220 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Clock_Object_PARAMS_C.

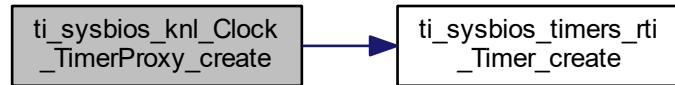
4.16.3.213 ti_sysbios_knl_Clock_TimerProxy_create()

```
ti_sysbios_knl_Clock_TimerProxy_Handle ti_sysbios_knl_Clock_TimerProxy_create (
    xdc_Int id,
    ti_sysbios_interfaces_ITimer_FuncPtr tickFxn,
    const ti_sysbios_knl_Clock_TimerProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8735 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_create().

Here is the call graph for this function:



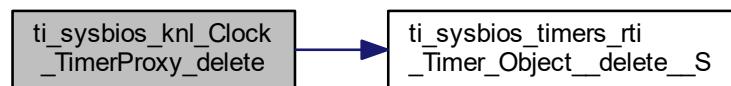
4.16.3.214 ti_sysbios_knl_Clock_TimerProxy_delete()

```
void ti_sysbios_knl_Clock_TimerProxy_delete (
    ti_sysbios_knl_Clock_TimerProxy_Handle * instp )
```

Definition at line 8741 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_delete__S().

Here is the call graph for this function:



4.16.3.215 ti_sysbios_knl_Clock_TimerProxy_getCount__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getCount__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8819 of file dss_mrr_pe674.c.

4.16.3.216 ti_sysbios_knl_Clock_TimerProxy_getCurrentTick__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getCurrentTick__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_Bool save )
```

Definition at line 8861 of file dss_mrr_pe674.c.

4.16.3.217 ti_sysbios_knl_Clock_TimerProxy_getExpiredCounts__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getExpiredCounts__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8849 of file dss_mrr_pe674.c.

4.16.3.218 ti_sysbios_knl_Clock_TimerProxy_getExpiredTicks__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getExpiredTicks__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UInt32 tickPeriod )
```

Definition at line 8855 of file dss_mrr_pe674.c.

4.16.3.219 ti_sysbios_knl_Clock_TimerProxy_getFreq__E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_getFreq__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_runtime_Types_FreqHz * freq )
```

Definition at line 8825 of file dss_mrr_pe674.c.

4.16.3.220 ti_sysbios_knl_Clock_TimerProxy_getFunc__E()

```
ti_sysbios_interfaces_ITimer_FuncPtr ti_sysbios_knl_Clock_TimerProxy_getFunc__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UArg * arg )
```

Definition at line 8831 of file dss_mrr_pe674.c.

4.16.3.221 ti_sysbios_knl_Clock_TimerProxy_getMaxTicks__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getMaxTicks__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8777 of file dss_mrr_pe674.c.

4.16.3.222 ti_sysbios_knl_Clock_TimerProxy_getNumTimers__E()

```
xdc_UInt ti_sysbios_knl_Clock_TimerProxy_getNumTimers__E (
    void )
```

Definition at line 8759 of file dss_mrr_pe674.c.

4.16.3.223 ti_sysbios_knl_Clock_TimerProxy_getPeriod__E()

```
xdc_UInt32 ti_sysbios_knl_Clock_TimerProxy_getPeriod__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8813 of file dss_mrr_pe674.c.

4.16.3.224 ti_sysbios_knl_Clock_TimerProxy_getStatus__E()

```
ti_sysbios_interfaces_ITimer_Status ti_sysbios_knl_Clock_TimerProxy_getStatus__E (
    xdc_UInt id )
```

Definition at line 8765 of file dss_mrr_pe674.c.

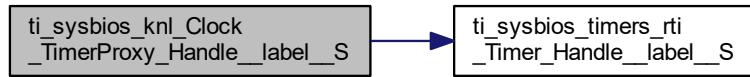
4.16.3.225 ti_sysbios_knl_Clock_TimerProxy_Handle_label__S()

```
xdc_runtime_Types_Label* ti_sysbios_knl_Clock_TimerProxy_Handle_label__S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 8753 of file dss_mrr_pe674.c.

References [ti_sysbios_timers_rti_Timer_Handle_label__S\(\)](#).

Here is the call graph for this function:



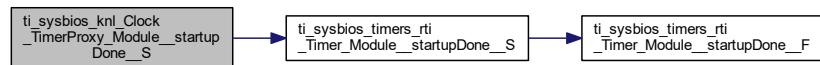
4.16.3.226 ti_sysbios_knl_Clock_TimerProxy_Module_startupDone_S()

```
xdc_Bool ti_sysbios_knl_Clock_TimerProxy_Module_startupDone_S (
    void )
```

Definition at line 8729 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Module_startupDone_S().

Here is the call graph for this function:



4.16.3.227 ti_sysbios_knl_Clock_TimerProxy_Params_init_S()

```
void ti_sysbios_knl_Clock_TimerProxy_Params_init_S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 8747 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Params_init_S().

Here is the call graph for this function:



4.16.3.228 ti_sysbios_knl_Clock_TimerProxy_Proxy_abstract_S()

```
xdc_Bool ti_sysbios_knl_Clock_TimerProxy_Proxy_abstract_S (
    void )
```

Definition at line 11443 of file dss_mrr_pe674.c.

4.16.3.229 ti_sysbios_knl_Clock_TimerProxy_Proxy_delegate_S()

```
xdc_CPtr ti_sysbios_knl_Clock_TimerProxy_Proxy_delegate_S (
    void )
```

Definition at line 11447 of file dss_mrr_pe674.c.

4.16.3.230 ti_sysbios_knl_Clock_TimerProxy_setFunc_E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_setFunc_E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    ti_sysbios_interfaces_ITimer_FuncPtr fxn,
    xdc_UArg arg )
```

Definition at line 8837 of file dss_mrr_pe674.c.

4.16.3.231 ti_sysbios_knl_Clock_TimerProxy_setNextTick_E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_setNextTick_E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UInt32 ticks )
```

Definition at line 8783 of file dss_mrr_pe674.c.

4.16.3.232 ti_sysbios_knl_Clock_TimerProxy_setPeriod_E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_setPeriod_E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UInt32 period )
```

Definition at line 8801 of file dss_mrr_pe674.c.

4.16.3.233 ti_sysbios_knl_Clock_TimerProxy_setPeriodMicroSecs_E()

```
xdc_Bool ti_sysbios_knl_Clock_TimerProxy_setPeriodMicroSecs_E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UInt32 microsecs )
```

Definition at line 8807 of file dss_mrr_pe674.c.

4.16.3.234 ti_sysbios_knl_Clock_TimerProxy_start_E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_start_E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8789 of file dss_mrr_pe674.c.

4.16.3.235 ti_sysbios_knl_Clock_TimerProxy_startup_E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_startup_E (
    void )
```

Definition at line 8771 of file dss_mrr_pe674.c.

4.16.3.236 ti_sysbios_knl_Clock_TimerProxy_stop__E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_stop__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst )
```

Definition at line 8795 of file dss_mrr_pe674.c.

4.16.3.237 ti_sysbios_knl_Clock_TimerProxy_trigger__E()

```
xdc_Void ti_sysbios_knl_Clock_TimerProxy_trigger__E (
    ti_sysbios_knl_Clock_TimerProxy_Handle __inst,
    xdc_UInt32 cycles )
```

Definition at line 8843 of file dss_mrr_pe674.c.

4.16.3.238 ti_sysbios_knl_Event_construct()

```
void ti_sysbios_knl_Event_construct (
    ti_sysbios_knl_Event_Struct * __obj,
    const ti_sysbios_knl_Event_Params * __paramsPtr )
```

Definition at line 11662 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Event_Object__DESC__C.

4.16.3.239 ti_sysbios_knl_Event_create()

```
ti_sysbios_knl_Event_Handle ti_sysbios_knl_Event_create (
    const ti_sysbios_knl_Event_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 11644 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Event_Object__DESC__C.

4.16.3.240 ti_sysbios_knl_Event_delete()

```
void ti_sysbios_knl_Event_delete (
    ti_sysbios_knl_Event_Handle * instp )
```

Definition at line 11687 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Event_Object__delete__S().

Here is the call graph for this function:



4.16.3.241 ti_sysbios_knl_Event_destruct()

```
void ti_sysbios_knl_Event_destruct (
    ti_sysbios_knl_Event_Struct * obj )
```

Definition at line 11674 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Event_Object__DESC__C`.

4.16.3.242 `ti_sysbios_knl_Event_Handle_label_S()`

```
xdc_runtime_Types_Label* ti_sysbios_knl_Event_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 11569 of file `dss_mrr_pe674.c`.

4.16.3.243 `ti_sysbios_knl_Event_Module_startupDone_S()`

```
xdc_Bool ti_sysbios_knl_Event_Module_startupDone_S (
    void )
```

Definition at line 11563 of file `dss_mrr_pe674.c`.

4.16.3.244 `ti_sysbios_knl_Event_Object_create_S()`

```
xdc_Ptr ti_sysbios_knl_Event_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

Definition at line 11622 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Event_Object__DESC__C`.

4.16.3.245 `ti_sysbios_knl_Event_Object_delete_S()`

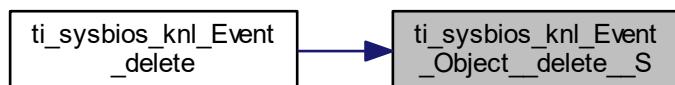
```
xdc_Void ti_sysbios_knl_Event_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 11680 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Event_Object__DESC__C`.

Referenced by `ti_sysbios_knl_Event_delete()`.

Here is the caller graph for this function:



4.16.3.246 `ti_sysbios_knl_Event_Object_first_S()`

```
xdc_Ptr ti_sysbios_knl_Event_Object_first_S (
    void )
```

Definition at line 11596 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Event_Module__link`, and `ti_sysbios_knl_Event_Module_root_V`.

4.16.3.247 ti_sysbios_knl_Event_Object__get__S()

```
xdc_Ptr ti_sysbios_knl_Event_Object__get__S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 11585 of file dss_mrr_pe674.c.

4.16.3.248 ti_sysbios_knl_Event_Object__next__S()

```
xdc_Ptr ti_sysbios_knl_Event_Object__next__S (
    xdc_Ptr obj )
```

Definition at line 11609 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Event_Module__::link`, and `ti_sysbios_knl_Event_Module__root__V`.

4.16.3.249 ti_sysbios_knl_Event_Params__init__S()

```
xdc_Void ti_sysbios_knl_Event_Params__init__S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 11579 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Event_Object__PARAMS__C`.

4.16.3.250 ti_sysbios_knl_Idle_Module__startupDone__S()

```
xdc_Bool ti_sysbios_knl_Idle_Module__startupDone__S (
    void )
```

Definition at line 11698 of file dss_mrr_pe674.c.

4.16.3.251 ti_sysbios_knl_Intrinsics_Module__startupDone__S()

```
xdc_Bool ti_sysbios_knl_Intrinsics_Module__startupDone__S (
    void )
```

Definition at line 11710 of file dss_mrr_pe674.c.

4.16.3.252 ti_sysbios_knl_Intrinsics_SupportProxy_maxbit__E()

```
xdc_UInt ti_sysbios_knl_Intrinsics_SupportProxy_maxbit__E (
    xdc_UInt bits )
```

Definition at line 8880 of file dss_mrr_pe674.c.

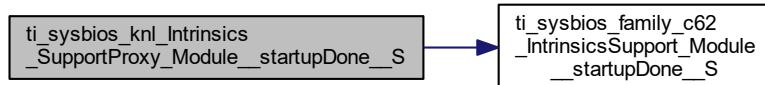
4.16.3.253 ti_sysbios_knl_Intrinsics_SupportProxy_Module__startupDone__S()

```
xdc_Bool ti_sysbios_knl_Intrinsics_SupportProxy_Module__startupDone__S (
    void )
```

Definition at line 8874 of file dss_mrr_pe674.c.

References `ti_sysbios_family_c62_IntrinsicsSupport_Module__startupDone__S()`.

Here is the call graph for this function:



4.16.3.254 ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_abstract_S()

```
xdc_Bool ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_abstract_S (
    void )
```

Definition at line 11722 of file dss_mrr_pe674.c.

4.16.3.255 ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_delegate_S()

```
xdc_CPtr ti_sysbios_knl_Intrinsics_SupportProxy_Proxy_delegate_S (
    void )
```

Definition at line 11726 of file dss_mrr_pe674.c.

4.16.3.256 ti_sysbios_knl_Queue_construct()

```
void ti_sysbios_knl_Queue_construct (
    ti_sysbios_knl_Queue_Struct * __obj,
    const ti_sysbios_knl_Queue_Parms * __paramsPtr )
```

Definition at line 11940 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Object_DESC_C.

4.16.3.257 ti_sysbios_knl_Queue_create()

```
ti_sysbios_knl_Queue_Handle ti_sysbios_knl_Queue_create (
    const ti_sysbios_knl_Queue_Parms * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 11922 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Object_DESC_C.

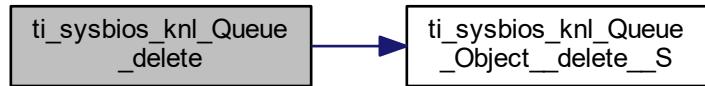
4.16.3.258 ti_sysbios_knl_Queue_delete()

```
void ti_sysbios_knl_Queue_delete (
    ti_sysbios_knl_Queue_Handle * instp )
```

Definition at line 11965 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Object_delete_S().

Here is the call graph for this function:



4.16.3.259 `ti_sysbios_knl_Queue_destruct()`

```
void ti_sysbios_knl_Queue_destruct (
    ti_sysbios_knl_Queue_Struct * obj )
```

Definition at line 11952 of file dss_mrr_pe674.c.
References `ti_sysbios_knl_Queue_Object_DESC_C`.

4.16.3.260 `ti_sysbios_knl_Queue_Handle_label_S()`

```
xdc_runtime_Types_Label* ti_sysbios_knl_Queue_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 11847 of file dss_mrr_pe674.c.

4.16.3.261 `ti_sysbios_knl_Queue_Module_startupDone_S()`

```
xdc_Bool ti_sysbios_knl_Queue_Module_startupDone_S (
    void )
```

Definition at line 11841 of file dss_mrr_pe674.c.

4.16.3.262 `ti_sysbios_knl_Queue_Object_create_S()`

```
xdc_Ptr ti_sysbios_knl_Queue_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
```

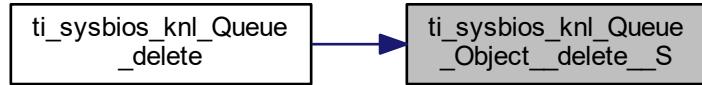
Definition at line 11900 of file dss_mrr_pe674.c.
References `ti_sysbios_knl_Queue_Object_DESC_C`.

4.16.3.263 `ti_sysbios_knl_Queue_Object_delete_S()`

```
xdc_Void ti_sysbios_knl_Queue_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 11958 of file dss_mrr_pe674.c.
References `ti_sysbios_knl_Queue_Object_DESC_C`.
Referenced by `ti_sysbios_knl_Queue_delete()`.

Here is the caller graph for this function:



4.16.3.264 ti_sysbios_knl_Queue_Object_first_S()

```
xdc_Ptr ti_sysbios_knl_Queue_Object_first_S (
    void )
```

Definition at line 11874 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Module::link, and ti_sysbios_knl_Queue_Module_root_V.

4.16.3.265 ti_sysbios_knl_Queue_Object_get_S()

```
xdc_Ptr ti_sysbios_knl_Queue_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 11863 of file dss_mrr_pe674.c.

4.16.3.266 ti_sysbios_knl_Queue_Object_next_S()

```
xdc_Ptr ti_sysbios_knl_Queue_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 11887 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Module::link, and ti_sysbios_knl_Queue_Module_root_V.

4.16.3.267 ti_sysbios_knl_Queue_Params_init_S()

```
xdc_Void ti_sysbios_knl_Queue_Params_init_S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 11857 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Queue_Object_PARAMS_C.

4.16.3.268 ti_sysbios_knl_Semaphore_construct()

```
void ti_sysbios_knl_Semaphore_construct (
    ti_sysbios_knl_Semaphore_Struct * __obj,
    xdc_Int count,
    const ti_sysbios_knl_Semaphore_Params * __paramsPtr )
```

Definition at line 12180 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Semaphore_Object_DESC_C.

4.16.3.269 ti_sysbios_knl_Semaphore_create()

```
ti_sysbios_knl_Semaphore_Handle ti_sysbios_knl_Semaphore_create (
    xdc_Int count,
    const ti_sysbios_knl_Semaphore_Params * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 12162 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Semaphore_Object__DESC__C.

4.16.3.270 ti_sysbios_knl_Semaphore_delete()

```
void ti_sysbios_knl_Semaphore_delete (
    ti_sysbios_knl_Semaphore_Handle * instp )
```

Definition at line 12205 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Semaphore_Object__delete__S().

Here is the call graph for this function:



4.16.3.271 ti_sysbios_knl_Semaphore_destruct()

```
void ti_sysbios_knl_Semaphore_destruct (
    ti_sysbios_knl_Semaphore_Struct * obj )
```

Definition at line 12192 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Semaphore_Object__DESC__C.

4.16.3.272 ti_sysbios_knl_Semaphore_Handle__label__S()

```
xdc_runtime_Label* ti_sysbios_knl_Semaphore_Handle__label__S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 12086 of file dss_mrr_pe674.c.

4.16.3.273 ti_sysbios_knl_Semaphore_Module__startupDone__S()

```
xdc_Bool ti_sysbios_knl_Semaphore_Module__startupDone__S (
    void )
```

Definition at line 12080 of file dss_mrr_pe674.c.

4.16.3.274 ti_sysbios_knl_Semaphore_Object__create__S()

```
xdc_Ptr ti_sysbios_knl_Semaphore_Object__create__S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
```

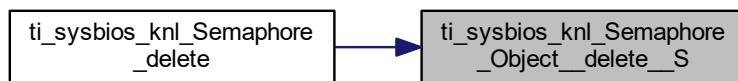
```

    xdc_SizeT __psz,
    xdc_runtime_Error_Block * eb )
Definition at line 12139 of file dss_mrr_pe674.c.
References ti_sysbios_knl_Semaphore_Object__DESC__C.
```

4.16.3.275 ti_sysbios_knl_Semaphore_Object__delete__S()

```
xdc_Void ti_sysbios_knl_Semaphore_Object__delete__S (
    xdc_Ptr instp )
```

Definition at line 12198 of file dss_mrr_pe674.c.
 References ti_sysbios_knl_Semaphore_Object__DESC__C.
 Referenced by ti_sysbios_knl_Semaphore_delete().
 Here is the caller graph for this function:



4.16.3.276 ti_sysbios_knl_Semaphore_Object__first__S()

```
xdc_Ptr ti_sysbios_knl_Semaphore_Object__first__S (
    void )
```

Definition at line 12113 of file dss_mrr_pe674.c.
 References ti_sysbios_knl_Semaphore_Module__::link, and ti_sysbios_knl_Semaphore_Module__root__V.

4.16.3.277 ti_sysbios_knl_Semaphore_Object__get__S()

```
xdc_Ptr ti_sysbios_knl_Semaphore_Object__get__S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 12102 of file dss_mrr_pe674.c.

4.16.3.278 ti_sysbios_knl_Semaphore_Object__next__S()

```
xdc_Ptr ti_sysbios_knl_Semaphore_Object__next__S (
    xdc_Ptr obj )
```

Definition at line 12126 of file dss_mrr_pe674.c.
 References ti_sysbios_knl_Semaphore_Module__::link, and ti_sysbios_knl_Semaphore_Module__root__V.

4.16.3.279 ti_sysbios_knl_Semaphore_Params__init__S()

```

xdc_Void ti_sysbios_knl_Semaphore_Params__init__S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 12096 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Semaphore_Object__PARAMS__C`.

4.16.3.280 `ti_sysbios_knl_Task_construct()`

```
void ti_sysbios_knl_Task_construct (
    ti_sysbios_knl_Task_Struct * __obj,
    ti_sysbios_knl_Task_FuncPtr fxn,
    const ti_sysbios_knl_Task_Parms * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 12432 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Task_Object__DESC__C`.

4.16.3.281 `ti_sysbios_knl_Task_create()`

```
ti_sysbios_knl_Task_Handle ti_sysbios_knl_Task_create (
    ti_sysbios_knl_Task_FuncPtr fxn,
    const ti_sysbios_knl_Task_Parms * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 12408 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Task_Object__DESC__C`.

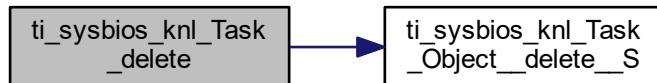
4.16.3.282 `ti_sysbios_knl_Task_delete()`

```
void ti_sysbios_knl_Task_delete (
    ti_sysbios_knl_Task_Handle * instp )
```

Definition at line 12462 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Task_Object__delete__S()`.

Here is the call graph for this function:



4.16.3.283 `ti_sysbios_knl_Task_destruct()`

```
void ti_sysbios_knl_Task_destruct (
    ti_sysbios_knl_Task_Struct * obj )
```

Definition at line 12449 of file `dss_mrr_pe674.c`.

References `ti_sysbios_knl_Task_Object__DESC__C`.

4.16.3.284 `ti_sysbios_knl_Task_disable__E()`

```
xdc_UInt ti_sysbios_knl_Task_disable__E (
    xdc_Void )
```

4.16.3.285 ti_sysbios_knl_Task_Handle__label__S()

```
xdc_runtime_Types_Label* ti_sysbios_knl_Task_Handle__label__S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 12326 of file dss_mrr_pe674.c.

4.16.3.286 ti_sysbios_knl_Task_Module__startupDone__F()

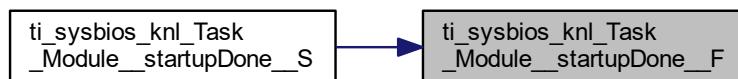
```
xdc_Bool ti_sysbios_knl_Task_Module__startupDone__F (
    void )
```

Definition at line 2084 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_knl_Task_Module_startupDone_S().

Here is the caller graph for this function:

**4.16.3.287 ti_sysbios_knl_Task_Module__startupDone__S()**

```
xdc_Bool ti_sysbios_knl_Task_Module__startupDone__S (
    void )
```

Definition at line 12320 of file dss_mrr_pe674.c.

References ti_sysbios_knl_Task_Module_startupDone_F().

Here is the call graph for this function:

**4.16.3.288 ti_sysbios_knl_Task_Module_startup__E()**

```
xdc_Int ti_sysbios_knl_Task_Module_startup__E (
    xdc_Int )
```

4.16.3.289 ti_sysbios_knl_Task_Object__create__S()

```
xdc_Ptr ti_sysbios_knl_Task_Object__create__S (
    xdc_CPtr __aa,
```

```
const xdc_UChar * __paramsPtr,
xdc_SizeT __psz,
xdc_runtime_Error_Block * eb )
```

Definition at line 12379 of file dss_mrr_pe674.c.

References [ti_sysbios_knl_Task_Object__DESC__C](#).

4.16.3.290 ti_sysbios_knl_Task_Object_delete_S()

```
xdc_Void ti_sysbios_knl_Task_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 12455 of file dss_mrr_pe674.c.

References [ti_sysbios_knl_Task_Object__DESC__C](#).

Referenced by [ti_sysbios_knl_Task_delete\(\)](#).

Here is the caller graph for this function:



4.16.3.291 ti_sysbios_knl_Task_Object_first_S()

```
xdc_Ptr ti_sysbios_knl_Task_Object_first_S (
    void )
```

Definition at line 12353 of file dss_mrr_pe674.c.

References [ti_sysbios_knl_Task_Module__link](#), and [ti_sysbios_knl_Task_Module_root_V](#).

4.16.3.292 ti_sysbios_knl_Task_Object_get_S()

```
xdc_Ptr ti_sysbios_knl_Task_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 12342 of file dss_mrr_pe674.c.

References [ti_sysbios_knl_Task_Object_table_C](#).

4.16.3.293 ti_sysbios_knl_Task_Object_next_S()

```
xdc_Ptr ti_sysbios_knl_Task_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 12366 of file dss_mrr_pe674.c.

References [ti_sysbios_knl_Task_Module__link](#), and [ti_sysbios_knl_Task_Module_root_V](#).

4.16.3.294 ti_sysbios_knl_Task_Params_init_S()

```
xdc_Void ti_sysbios_knl_Task_Params_init_S (
    xdc_Ptr prms,
    const void * src,
```

```
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 12336 of file dss_mrr_pe674.c.

References `ti_sysbios_knl_Task_Object__PARAMS__C`.

4.16.3.295 `ti_sysbios_knl_Task_restoreHwi__E()`

```
xdc_Void ti_sysbios_knl_Task_restoreHwi__E (
    xdc_UInt )
```

4.16.3.296 `ti_sysbios_knl_Task_SupportProxy_checkStack__E()`

```
xdc_Bool ti_sysbios_knl_Task_SupportProxy_checkStack__E (
    xdc_Char * stack,
    xdc_SizeT size )
```

Definition at line 8911 of file dss_mrr_pe674.c.

4.16.3.297 `ti_sysbios_knl_Task_SupportProxy_getDefaultStackSize__E()`

```
xdc_SizeT ti_sysbios_knl_Task_SupportProxy_getDefaultStackSize__E (
    void )
```

Definition at line 8929 of file dss_mrr_pe674.c.

4.16.3.298 `ti_sysbios_knl_Task_SupportProxy_getStackAlignment__E()`

```
xdc_UInt ti_sysbios_knl_Task_SupportProxy_getStackAlignment__E (
    void )
```

Definition at line 8923 of file dss_mrr_pe674.c.

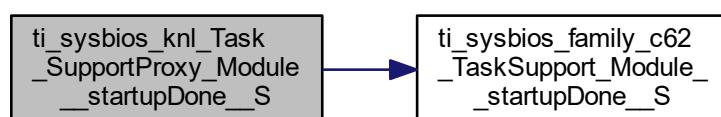
4.16.3.299 `ti_sysbios_knl_Task_SupportProxy_Module_startupDone__S()`

```
xdc_Bool ti_sysbios_knl_Task_SupportProxy_Module_startupDone__S (
    void )
```

Definition at line 8893 of file dss_mrr_pe674.c.

References `ti_sysbios_family_c62_TaskSupport_Module_startupDone__S()`.

Here is the call graph for this function:



4.16.3.300 `ti_sysbios_knl_Task_SupportProxy_Proxy_abstract__S()`

```
xdc_Bool ti_sysbios_knl_Task_SupportProxy_Proxy_abstract__S (
    void )
```

Definition at line 12473 of file dss_mrr_pe674.c.

4.16.3.301 ti_sysbios_knl_Task_SupportProxy_Proxy__delegate__S()

```
xdc_CPtr ti_sysbios_knl_Task_SupportProxy_Proxy__delegate__S (
    void )
```

Definition at line 12477 of file dss_mrr_pe674.c.

4.16.3.302 ti_sysbios_knl_Task_SupportProxy_stackUsed__E()

```
xdc_SizeT ti_sysbios_knl_Task_SupportProxy_stackUsed__E (
    xdc_Char * stack,
    xdc_SizeT size )
```

Definition at line 8917 of file dss_mrr_pe674.c.

4.16.3.303 ti_sysbios_knl_Task_SupportProxy_start__E()

```
xdc_Ptr ti_sysbios_knl_Task_SupportProxy_start__E (
    xdc_Ptr curTask,
    ti_sysbios_interfaces_ITaskSupport_FuncPtr enterFxn,
    ti_sysbios_interfaces_ITaskSupport_FuncPtr exitFxn,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8899 of file dss_mrr_pe674.c.

4.16.3.304 ti_sysbios_knl_Task_SupportProxy_swap__E()

```
xdc_Void ti_sysbios_knl_Task_SupportProxy_swap__E (
    xdc_Ptr * oldtskContext,
    xdc_Ptr * newtskContext )
```

Definition at line 8905 of file dss_mrr_pe674.c.

4.16.3.305 ti_sysbios_rts_MemAlloc_alloc()

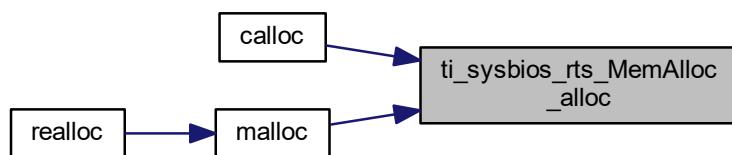
```
static Void* ti_sysbios_rts_MemAlloc_alloc (
    SizeT size ) [static]
```

Definition at line 2802 of file dss_mrr_pe674.c.

References Header::actualBuf, Header::header, and Header::size.

Referenced by calloc(), and malloc().

Here is the caller graph for this function:



4.16.3.306 ti_sysbios_rts_ti_ThreadLocalStorage_Module_startupDone_S()

```
xdc_Bool ti_sysbios_rts_ti_ThreadLocalStorage_Module_startupDone_S (
    void )
```

Definition at line 12488 of file dss_mrr_pe674.c.

4.16.3.307 ti_sysbios_timers_rti_Timer_construct()

```
void ti_sysbios_timers_rti_Timer_construct (
    ti_sysbios_timers_rti_Timer_Struct * __obj,
    xdc_Int id,
    ti_sysbios_interfaces_ITimer_FuncPtr tickFxn,
    const ti_sysbios_timers_rti_Timer_Parms * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 12716 of file dss_mrr_pe674.c.

References `ti_sysbios_timers_rti_Timer_Object_DESC_C`.

4.16.3.308 ti_sysbios_timers_rti_Timer_create()

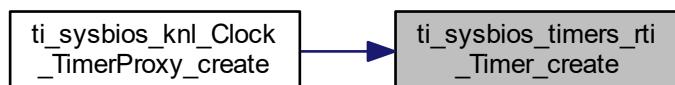
```
ti_sysbios_timers_rti_Timer_Handle ti_sysbios_timers_rti_Timer_create (
    xdc_Int id,
    ti_sysbios_interfaces_ITimer_FuncPtr tickFxn,
    const ti_sysbios_timers_rti_Timer_Parms * __paramsPtr,
    xdc_runtime_Error_Block * eb )
```

Definition at line 12692 of file dss_mrr_pe674.c.

References `ti_sysbios_timers_rti_Timer_Object_DESC_C`.

Referenced by `ti_sysbios_knl_Clock_TimerProxy_create()`.

Here is the caller graph for this function:

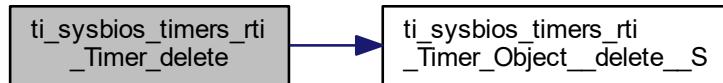
**4.16.3.309 ti_sysbios_timers_rti_Timer_delete()**

```
void ti_sysbios_timers_rti_Timer_delete (
    ti_sysbios_timers_rti_Timer_Handle * instp )
```

Definition at line 12746 of file dss_mrr_pe674.c.

References `ti_sysbios_timers_rti_Timer_Object_delete_S()`.

Here is the call graph for this function:



4.16.3.310 ti_sysbios_timers_rti_Timer_destruct()

```
void ti_sysbios_timers_rti_Timer_destruct (
    ti_sysbios_timers_rti_Timer_Struct * obj )
```

Definition at line 12733 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_DESC_C.

4.16.3.311 ti_sysbios_timers_rti_Timer_Handle_label_S()

```
xdc_runtime_Label* ti_sysbios_timers_rti_Timer_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 12610 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Clock_TimerProxy_Handle_label_S().

Here is the caller graph for this function:



4.16.3.312 ti_sysbios_timers_rti_Timer_Module_startupDone_F()

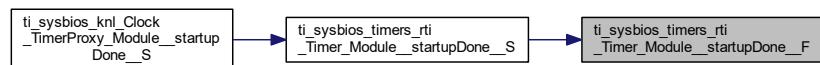
```
xdc_Bool ti_sysbios_timers_rti_Timer_Module_startupDone_F (
    void )
```

Definition at line 2092 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by ti_sysbios_timers_rti_Timer_Module_startupDone_S().

Here is the caller graph for this function:



4.16.3.313 ti_sysbios_timers_rti_Timer_Module_startupDone_S()

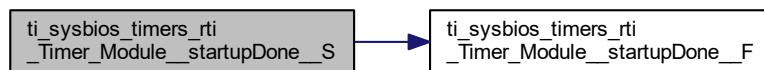
```
xdc_Bool ti_sysbios_timers_rti_Timer_Module_startupDone_S (
    void )
```

Definition at line 12604 of file dss_mrr_pe674.c.

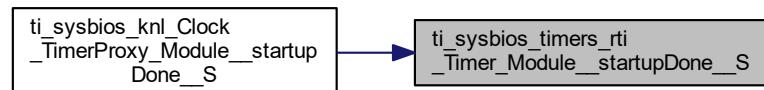
References ti_sysbios_timers_rti_Timer_Module_startupDone_F().

Referenced by ti_sysbios_knl_Clock_TimerProxy_Module_startupDone_S().

Here is the call graph for this function:



Here is the caller graph for this function:



4.16.3.314 ti_sysbios_timers_rti_Timer_Module_startup_E()

```
xdc_Int ti_sysbios_timers_rti_Timer_Module_startup_E (
    xdc_Int )
```

4.16.3.315 ti_sysbios_timers_rti_Timer_Object_create_S()

```
xdc_Ptr ti_sysbios_timers_rti_Timer_Object_create_S (
    xdc_CPtr __aa,
    const xdc_UChar * __paramsPtr,
    xdc_SizeT __psz,
    xdc_runtime_Error_Block * __eb )
```

Definition at line 12663 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_DESC_C.

4.16.3.316 ti_sysbios_timers_rti_Timer_Object_delete_S()

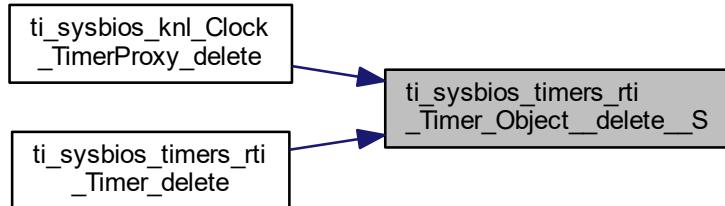
```
xdc_Void ti_sysbios_timers_rti_Timer_Object_delete_S (
    xdc_Ptr instp )
```

Definition at line 12739 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_DESC_C.

Referenced by ti_sysbios_knl_Clock_TimerProxy_delete(), and ti_sysbios_timers_rti_Timer_delete().

Here is the caller graph for this function:



4.16.3.317 ti_sysbios_timers_rti_Timer_Object_first_S()

```
xdc_Ptr ti_sysbios_timers_rti_Timer_Object_first_S (
    void )
```

Definition at line 12637 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Module__::link, and ti_sysbios_timers_rti_Timer_Module__root__V.

4.16.3.318 ti_sysbios_timers_rti_Timer_Object_get_S()

```
xdc_Ptr ti_sysbios_timers_rti_Timer_Object_get_S (
    xdc_Ptr oa,
    xdc_Int i )
```

Definition at line 12626 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_table__C.

4.16.3.319 ti_sysbios_timers_rti_Timer_Object_next_S()

```
xdc_Ptr ti_sysbios_timers_rti_Timer_Object_next_S (
    xdc_Ptr obj )
```

Definition at line 12650 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Module__::link, and ti_sysbios_timers_rti_Timer_Module__root__V.

4.16.3.320 ti_sysbios_timers_rti_Timer_Params_init_S()

```
xdc_Void ti_sysbios_timers_rti_Timer_Params_init_S (
    xdc_Ptr prms,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 12620 of file dss_mrr_pe674.c.

References ti_sysbios_timers_rti_Timer_Object_PARAMS__C.

Referenced by ti_sysbios_knl_Clock_TimerProxy_Params_init_S().

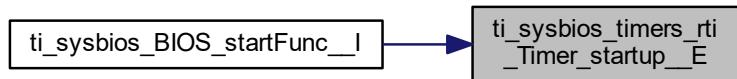
Here is the caller graph for this function:



4.16.3.321 ti_sysbios_timers_rti_Timer_startup_E()

Void ti_sysbios_timers_rti_Timer_startup_E ()
Referenced by ti_sysbios_BIOS_startFunc_I().

Here is the caller graph for this function:



4.16.3.322 ti_sysbios_utils_Load_Module_startupDone_S()

xdc_Bool ti_sysbios_utils_Load_Module_startupDone_S (void)

Definition at line 12757 of file dss_mrr_pe674.c.

4.16.3.323 ti_sysbios_utils_Load_taskRegHook_E()

xdc_Void ti_sysbios_utils_Load_taskRegHook_E (xdc_Int)

4.16.3.324 ti_sysbios_utils_Load_update_E()

Void ti_sysbios_utils_Load_update_E ()
Definition at line 2735 of file dss_mrr_pe674.c.

4.16.3.325 xdc_META() [1/5]

xdc_META (__ASM__)

4.16.3.326 xdc__META() [2/5]

```
xdc__META (
    __ISA__ )
```

4.16.3.327 xdc__META() [3/5]

```
xdc__META (
    __PLAT__ )
```

4.16.3.328 xdc__META() [4/5]

```
xdc__META (
    __TARG__ )
```

4.16.3.329 xdc__META() [5/5]

```
xdc__META (
    __TRDR__ )
```

4.16.3.330 xdc_runtime Assert_Module_startupDone_S()

```
xdc_Bool xdc_runtime Assert_Module_startupDone_S (
    void )
```

Definition at line 12769 of file dss_mrr_pe674.c.

4.16.3.331 xdc_runtime Core_Module_startupDone_S()

```
xdc_Bool xdc_runtime Core_Module_startupDone_S (
    void )
```

Definition at line 12781 of file dss_mrr_pe674.c.

4.16.3.332 xdc_runtime Defaults_Module_startupDone_S()

```
xdc_Bool xdc_runtime Defaults_Module_startupDone_S (
    void )
```

Definition at line 12793 of file dss_mrr_pe674.c.

4.16.3.333 xdc_runtime Diags_Module_startupDone_S()

```
xdc_Bool xdc_runtime Diags_Module_startupDone_S (
    void )
```

Definition at line 12805 of file dss_mrr_pe674.c.

4.16.3.334 xdc_runtime Error_Module_startupDone_S()

```
xdc_Bool xdc_runtime Error_Module_startupDone_S (
    void )
```

Definition at line 12817 of file dss_mrr_pe674.c.

4.16.3.335 xdc_runtime_Gate_Module_startupDone_S()

```
xdc_Bool xdc_runtime_Gate_Module_startupDone_S (
    void )
```

Definition at line 12829 of file dss_mrr_pe674.c.

4.16.3.336 xdc_runtime_IGateProvider_create()

```
xdc_runtime_IGateProvider_Handle xdc_runtime_IGateProvider_create (
    xdc_runtime_IGateProvider_Module mod,
    const xdc_runtime_IGateProvider_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9411 of file dss_mrr_pe674.c.

4.16.3.337 xdc_runtime_IGateProvider_delete()

```
void xdc_runtime_IGateProvider_delete (
    xdc_runtime_IGateProvider_Handle * instp )
```

Definition at line 9417 of file dss_mrr_pe674.c.

4.16.3.338 xdc_runtime_IHeap_create()

```
xdc_runtime_IHeap_Handle xdc_runtime_IHeap_create (
    xdc_runtime_IHeap_Module mod,
    const xdc_runtime_IHeap_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9394 of file dss_mrr_pe674.c.

4.16.3.339 xdc_runtime_IHeap_delete()

```
void xdc_runtime_IHeap_delete (
    xdc_runtime_IHeap_Handle * instp )
```

Definition at line 9400 of file dss_mrr_pe674.c.

4.16.3.340 xdc_runtime_Log_Module_startupDone_S()

```
xdc_Bool xdc_runtime_Log_Module_startupDone_S (
    void )
```

Definition at line 12841 of file dss_mrr_pe674.c.

4.16.3.341 xdc_runtime_Main_Module_startupDone_S()

```
xdc_Bool xdc_runtime_Main_Module_startupDone_S (
    void )
```

Definition at line 12853 of file dss_mrr_pe674.c.

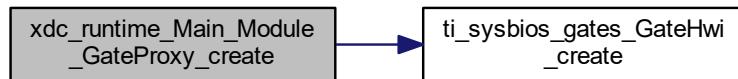
4.16.3.342 xdc_runtime_Main_Module_GateProxy_create()

```
xdc_runtime_Main_Module_GateProxy_Handle xdc_runtime_Main_Module_GateProxy_create (
    const xdc_runtime_Main_Module_GateProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 8948 of file dss_mrr_pe674.c.

References `ti_sysbios_gates_GateHwi_create()`.

Here is the call graph for this function:



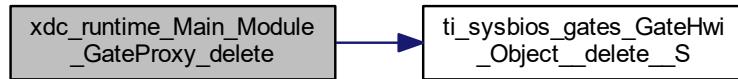
4.16.3.343 xdc_runtime_Main_Module_GateProxy_delete()

```
void xdc_runtime_Main_Module_GateProxy_delete (
    xdc_runtime_Main_Module_GateProxy_Handle * instp )
```

Definition at line 8954 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object_delete_S().

Here is the call graph for this function:



4.16.3.344 xdc_runtime_Main_Module_GateProxy_enter__E()

```
xdc_IArg xdc_runtime_Main_Module_GateProxy_enter__E (
    xdc_runtime_Main_Module_GateProxy_Handle __inst )
```

Definition at line 8978 of file dss_mrr_pe674.c.

4.16.3.345 xdc_runtime_Main_Module_GateProxy_Handle_label_S()

```
xdc_runtime_Types_Label* xdc_runtime_Main_Module_GateProxy_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 8966 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Handle_label_S().

Here is the call graph for this function:



4.16.3.346 xdc_runtime_Main_Module_GateProxy_Leave__E()

```
xdc_Void xdc_runtime_Main_Module_GateProxy_Leave__E (
    xdc_runtime_Main_Module_GateProxy_Handle __inst,
    xdc_IArg key )
```

Definition at line 8984 of file dss_mrr_pe674.c.

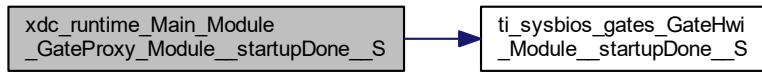
4.16.3.347 xdc_runtime_Main_Module_GateProxy_Module_startupDone__S()

```
xdc_Bool xdc_runtime_Main_Module_GateProxy_Module_startupDone__S (
    void )
```

Definition at line 8942 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Module_startupDone__S().

Here is the call graph for this function:



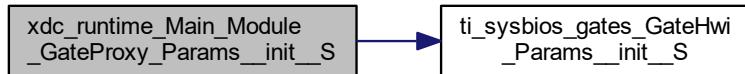
4.16.3.348 xdc_runtime_Main_Module_GateProxy_Params_init__S()

```
void xdc_runtime_Main_Module_GateProxy_Params_init__S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 8960 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Params_init__S().

Here is the call graph for this function:



4.16.3.349 `xdc_runtime_Main_Module_GateProxy_Proxy_abstract_S()`

```
xdc_Bool xdc_runtime_Main_Module_GateProxy_Proxy_abstract_S (
    void )
```

Definition at line 12968 of file dss_mrr_pe674.c.

4.16.3.350 `xdc_runtime_Main_Module_GateProxy_Proxy_delegate_S()`

```
xdc_CPtr xdc_runtime_Main_Module_GateProxy_Proxy_delegate_S (
    void )
```

Definition at line 12972 of file dss_mrr_pe674.c.

References `ti_sysbios_gates_GateHwi_Module_FXNS_C`.

4.16.3.351 `xdc_runtime_Main_Module_GateProxy_query_E()`

```
xdc_Bool xdc_runtime_Main_Module_GateProxy_query_E (
    xdc_Int qual )
```

Definition at line 8972 of file dss_mrr_pe674.c.

4.16.3.352 `xdc_runtime_Memory_HeapProxy_alloc_E()`

```
xdc_Ptr xdc_runtime_Memory_HeapProxy_alloc_E (
    xdc_runtime_Memory_HeapProxy_Handle __inst,
    xdc_SizeT size,
    xdc_SizeT align,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9027 of file dss_mrr_pe674.c.

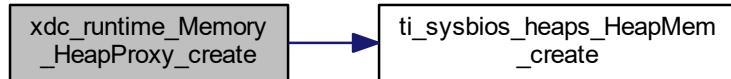
4.16.3.353 `xdc_runtime_Memory_HeapProxy_create()`

```
xdc_runtime_Memory_HeapProxy_Handle xdc_runtime_Memory_HeapProxy_create (
    const xdc_runtime_Memory_HeapProxy_Params * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9003 of file dss_mrr_pe674.c.

References `ti_sysbios_heaps_HeapMem_create()`.

Here is the call graph for this function:



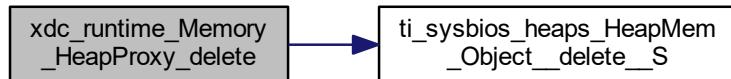
4.16.3.354 xdc_runtime_Memory_HeapProxy_delete()

```
void xdc_runtime_Memory_HeapProxy_delete (
    xdc_runtime_Memory_HeapProxy_Handle * instp )
```

Definition at line 9009 of file dss_mrr_pe674.c.

References ti_sysbios_heaps_HeapMem_Object__delete__S().

Here is the call graph for this function:



4.16.3.355 xdc_runtime_Memory_HeapProxy_free__E()

```
xdc_Void xdc_runtime_Memory_HeapProxy_free__E (
    xdc_runtime_Memory_HeapProxy_Handle __inst,
    xdc_Ptr block,
    xdc_SizeT size )
```

Definition at line 9033 of file dss_mrr_pe674.c.

4.16.3.356 xdc_runtime_Memory_HeapProxy_getStats__E()

```
xdc_Void xdc_runtime_Memory_HeapProxy_getStats__E (
    xdc_runtime_Memory_HeapProxy_Handle __inst,
    xdc_runtime_Memory_Stats * stats )
```

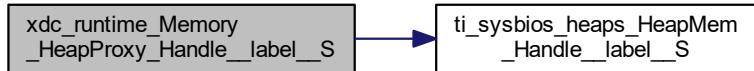
Definition at line 9045 of file dss_mrr_pe674.c.

4.16.3.357 xdc_runtime_Memory_HeapProxy_Handle__label__S()

```
xdc_runtime_Label* xdc_runtime_Memory_HeapProxy_Handle__label__S (
    xdc_Ptr obj,
    xdc_runtime_Label * lab )
```

Definition at line 9021 of file dss_mrr_pe674.c.

References `ti_sysbios_heaps_HeapMem_Handle__label__S()`.
 Here is the call graph for this function:



4.16.3.358 `xdc_runtime_Memory_HeapProxy_isBlocking__E()`

```
xdc_Bool xdc_runtime_Memory_HeapProxy_isBlocking__E (
    xdc_runtime_Memory_HeapProxy_Handle __inst )
```

Definition at line 9039 of file `dss_mrr_pe674.c`.

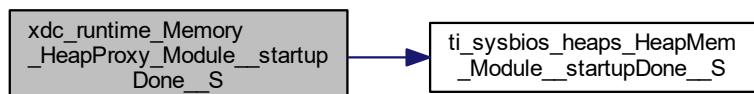
4.16.3.359 `xdc_runtime_Memory_HeapProxy_Module_startupDone__S()`

```
xdc_Bool xdc_runtime_Memory_HeapProxy_Module_startupDone__S (
    void )
```

Definition at line 8997 of file `dss_mrr_pe674.c`.

References `ti_sysbios_heaps_HeapMem_Module_startupDone__S()`.

Here is the call graph for this function:



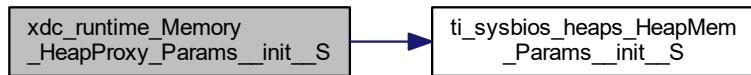
4.16.3.360 `xdc_runtime_Memory_HeapProxy_Params_init__S()`

```
void xdc_runtime_Memory_HeapProxy_Params_init__S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 9015 of file `dss_mrr_pe674.c`.

References `ti_sysbios_heaps_HeapMem_Params_init__S()`.

Here is the call graph for this function:



4.16.3.361 `xdc_runtime_Memory_HeapProxy_Proxy_abstract_S()`

```
xdc_Bool xdc_runtime_Memory_HeapProxy_Proxy_abstract_S (
    void )
```

Definition at line 13099 of file dss_mrr_pe674.c.

4.16.3.362 `xdc_runtime_Memory_HeapProxy_Proxy_delegate_S()`

```
xdc_CPtr xdc_runtime_Memory_HeapProxy_Proxy_delegate_S (
    void )
```

Definition at line 13103 of file dss_mrr_pe674.c.

References `ti_sysbios_heaps_HeapMem_Module_FXNS_C`.

4.16.3.363 `xdc_runtime_Memory_Module_startupDone_S()`

```
xdc_Bool xdc_runtime_Memory_Module_startupDone_S (
    void )
```

Definition at line 12984 of file dss_mrr_pe674.c.

4.16.3.364 `xdc_runtime_Registry_Module_startupDone_S()`

```
xdc_Bool xdc_runtime_Registry_Module_startupDone_S (
    void )
```

Definition at line 13115 of file dss_mrr_pe674.c.

4.16.3.365 `xdc_runtime_Startup_exec_I()`

```
xdc_Void xdc_runtime_Startup_exec_I (
    void )
```

Definition at line 2121 of file dss_mrr_pe674.c.

References `xdc_runtime_Startup_startModsFxn_C`.

4.16.3.366 `xdc_runtime_Startup_Module_startupDone_S()`

```
xdc_Bool xdc_runtime_Startup_Module_startupDone_S (
    void )
```

Definition at line 13127 of file dss_mrr_pe674.c.

4.16.3.367 `xdc_runtime_Startup_reset__I()`

```
xdc_Void xdc_runtime_Startup_reset__I (
    void )
```

Definition at line 2158 of file dss_mrr_pe674.c.

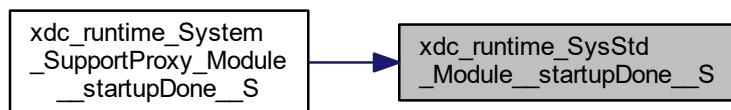
4.16.3.368 `xdc_runtime_SysStd_Module_startupDone__S()`

```
xdc_Bool xdc_runtime_SysStd_Module_startupDone__S (
    void )
```

Definition at line 13139 of file dss_mrr_pe674.c.

Referenced by `xdc_runtime_System_SupportProxy_Module_startupDone__S()`.

Here is the caller graph for this function:



4.16.3.369 `xdc_runtime_System_aprintf__E()`

```
xdc_Int xdc_runtime_System_aprintf__E (
    xdc_CString fmt,
    ... )
```

Definition at line 8401 of file dss_mrr_pe674.c.

4.16.3.370 `xdc_runtime_System_aprintf_va__E()`

```
xdc_Int xdc_runtime_System_aprintf_va__E (
    xdc_CString fmt,
    va_list __va )
```

Definition at line 8395 of file dss_mrr_pe674.c.

4.16.3.371 `xdc_runtime_System_asprintf__E()`

```
xdc_Int xdc_runtime_System_asprintf__E (
    xdc_Char buf[],
    xdc_CString fmt,
    ... )
```

Definition at line 8437 of file dss_mrr_pe674.c.

4.16.3.372 `xdc_runtime_System_asprintf_va__E()`

```
xdc_Int xdc_runtime_System_asprintf_va__E (
    xdc_Char buf[],
    xdc_CString fmt,
    va_list __va )
```

Definition at line 8431 of file dss_mrr_pe674.c.

4.16.3.373 xdc_runtime_System_Module_startupDone_F()

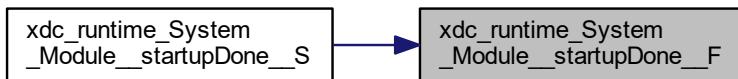
```
xdc_Bool xdc_runtime_System_Module_startupDone_F (
    void )
```

Definition at line 2056 of file dss_mrr_pe674.c.

References xdc_runtime_Startup_Module_state_V.

Referenced by xdc_runtime_System_Module_startupDone_S().

Here is the caller graph for this function:



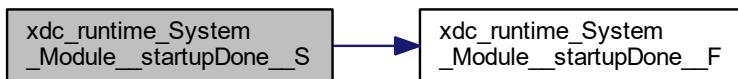
4.16.3.374 xdc_runtime_System_Module_startupDone_S()

```
xdc_Bool xdc_runtime_System_Module_startupDone_S (
    void )
```

Definition at line 13151 of file dss_mrr_pe674.c.

References xdc_runtime_System_Module_startupDone_F().

Here is the call graph for this function:



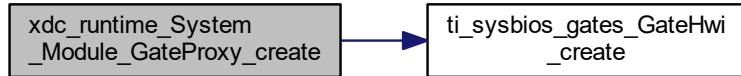
4.16.3.375 xdc_runtime_System_Module_GateProxy_create()

```
xdc_runtime_System_Module_GateProxy_Handle xdc_runtime_System_Module_GateProxy_create (
    const xdc_runtime_System_Module_GateProxy_Parms * prms,
    xdc_runtime_Error_Block * eb )
```

Definition at line 9064 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_create().

Here is the call graph for this function:



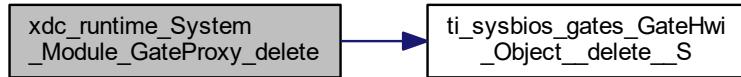
4.16.3.376 xdc_runtime_System_Module_GateProxy_delete()

```
void xdc_runtime_System_Module_GateProxy_delete (
    xdc_runtime_System_Module_GateProxy_Handle * instp )
```

Definition at line 9070 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Object_delete_S().

Here is the call graph for this function:



4.16.3.377 xdc_runtime_System_Module_GateProxy_enter__E()

```
xdc_IArg xdc_runtime_System_Module_GateProxy_enter__E (
    xdc_runtime_System_Module_GateProxy_Handle __inst )
```

Definition at line 9094 of file dss_mrr_pe674.c.

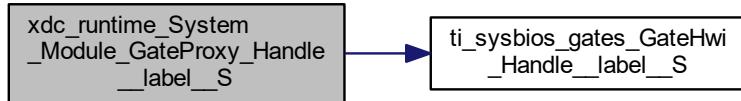
4.16.3.378 xdc_runtime_System_Module_GateProxy_Handle_label_S()

```
xdc_runtime_Types_Label* xdc_runtime_System_Module_GateProxy_Handle_label_S (
    xdc_Ptr obj,
    xdc_runtime_Types_Label * lab )
```

Definition at line 9082 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Handle_label_S().

Here is the call graph for this function:



4.16.3.379 xdc_runtime_System_Module_GateProxy_leave_E()

```
xdc_Void xdc_runtime_System_Module_GateProxy_leave_E (
    xdc_runtime_System_Module_GateProxy_Handle __inst,
    xdc_IArg key )
```

Definition at line 9100 of file dss_mrr_pe674.c.

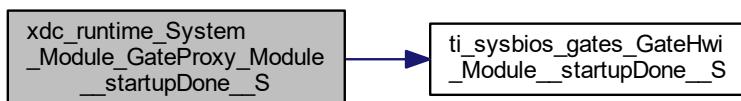
4.16.3.380 xdc_runtime_System_Module_GateProxy_Module_startupDone_S()

```
xdc_Bool xdc_runtime_System_Module_GateProxy_Module_startupDone_S (
    void )
```

Definition at line 9058 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Module_startupDone_S().

Here is the call graph for this function:



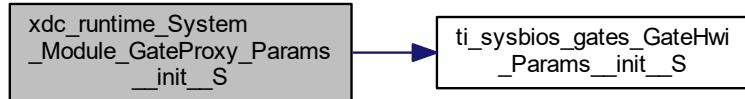
4.16.3.381 xdc_runtime_System_Module_GateProxy_Params_init_S()

```
void xdc_runtime_System_Module_GateProxy_Params_init_S (
    xdc_Ptr dst,
    const void * src,
    xdc_SizeT psz,
    xdc_SizeT isz )
```

Definition at line 9076 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Params_init_S().

Here is the call graph for this function:



4.16.3.382 xdc_runtime_System_Module_GateProxy_Proxy_abstract_S()

```
xdc_Bool xdc_runtime_System_Module_GateProxy_Proxy_abstract_S (
    void )
```

Definition at line 13266 of file dss_mrr_pe674.c.

4.16.3.383 xdc_runtime_System_Module_GateProxy_Proxy_delegate_S()

```
xdc_CPtr xdc_runtime_System_Module_GateProxy_Proxy_delegate_S (
    void )
```

Definition at line 13270 of file dss_mrr_pe674.c.

References ti_sysbios_gates_GateHwi_Module_FXNS_C.

4.16.3.384 xdc_runtime_System_Module_GateProxy_query_E()

```
xdc_Bool xdc_runtime_System_Module_GateProxy_query_E (
    xdc_Int qual )
```

Definition at line 9088 of file dss_mrr_pe674.c.

4.16.3.385 xdc_runtime_System_Module_startup_E()

```
xdc_Int xdc_runtime_System_Module_startup_E (
    xdc_Int )
```

4.16.3.386 xdc_runtime_System_printf_E()

```
xdc_Int xdc_runtime_System_printf_E (
    xdc_CString fmt,
    ... )
```

Definition at line 8383 of file dss_mrr_pe674.c.

4.16.3.387 xdc_runtime_System_printf_va_E()

```
xdc_Int xdc_runtime_System_printf_va_E (
    xdc_CString fmt,
    va_list __va )
```

Definition at line 8377 of file dss_mrr_pe674.c.

4.16.3.388 xdc_runtime_System_printfExtend__I()

```
xdc_Int xdc_runtime_System_printfExtend__I (
    xdc_Char ** pbuf,
    xdc_CString * pfmt,
    xdc_VaList * pva,
    xdc_runtime_System_ParseData * parse )
```

Definition at line 2187 of file dss_mrr_pe674.c.

4.16.3.389 xdc_runtime_System_snprintf__E()

```
xdc_Int xdc_runtime_System_snprintf__E (
    xdc_Char buf[],
    xdc_SizeT n,
    xdc_CString fmt,
    ... )
```

Definition at line 8455 of file dss_mrr_pe674.c.

4.16.3.390 xdc_runtime_System_snprintf_va__E()

```
xdc_Int xdc_runtime_System_snprintf_va__E (
    xdc_Char buf[],
    xdc_SizeT n,
    xdc_CString fmt,
    va_list __va )
```

Definition at line 8449 of file dss_mrr_pe674.c.

4.16.3.391 xdc_runtime_System_sprintf__E()

```
xdc_Int xdc_runtime_System_sprintf__E (
    xdc_Char buf[],
    xdc_CString fmt,
    ... )
```

Definition at line 8419 of file dss_mrr_pe674.c.

4.16.3.392 xdc_runtime_System_sprintf_va__E()

```
xdc_Int xdc_runtime_System_sprintf_va__E (
    xdc_Char buf[],
    xdc_CString fmt,
    va_list __va )
```

Definition at line 8413 of file dss_mrr_pe674.c.

4.16.3.393 xdc_runtime_System_SupportProxy_abort__E()

```
xdc_Void xdc_runtime_System_SupportProxy_abort__E (
    xdc_CString str )
```

Definition at line 9119 of file dss_mrr_pe674.c.

4.16.3.394 xdc_runtime_System_SupportProxy_exit__E()

```
xdc_Void xdc_runtime_System_SupportProxy_exit__E (
    xdc_Int stat )
```

Definition at line 9125 of file dss_mrr_pe674.c.

4.16.3.395 `xdc_runtime_System_SupportProxy_flush__E()`

```
xdc_Void xdc_runtime_System_SupportProxy_flush__E (
    void )
```

Definition at line 9131 of file dss_mrr_pe674.c.

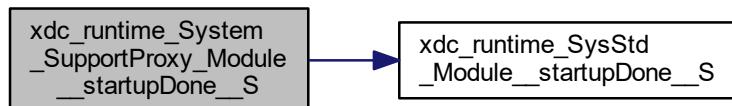
4.16.3.396 `xdc_runtime_System_SupportProxy_Module__startupDone__S()`

```
xdc_Bool xdc_runtime_System_SupportProxy_Module__startupDone__S (
    void )
```

Definition at line 9113 of file dss_mrr_pe674.c.

References `xdc_runtime_SysStd_Module_startupDone__S()`.

Here is the call graph for this function:



4.16.3.397 `xdc_runtime_System_SupportProxy_Proxy__abstract__S()`

```
xdc_Bool xdc_runtime_System_SupportProxy_Proxy__abstract__S (
    void )
```

Definition at line 13282 of file dss_mrr_pe674.c.

4.16.3.398 `xdc_runtime_System_SupportProxy_Proxy__delegate__S()`

```
xdc_CPtr xdc_runtime_System_SupportProxy_Proxy__delegate__S (
    void )
```

Definition at line 13286 of file dss_mrr_pe674.c.

References `xdc_runtime_SysStd_Module_FXNS__C`.

4.16.3.399 `xdc_runtime_System_SupportProxy_putch__E()`

```
xdc_Void xdc_runtime_System_SupportProxy_putch__E (
    xdc_Char ch )
```

Definition at line 9137 of file dss_mrr_pe674.c.

4.16.3.400 `xdc_runtime_System_SupportProxy_ready__E()`

```
xdc_Bool xdc_runtime_System_SupportProxy_ready__E (
    void )
```

Definition at line 9143 of file dss_mrr_pe674.c.

4.16.3.401 xdc_runtime_Text_Module__startupDone__S()

```
xdc_Bool xdc_runtime_Text_Module__startupDone__S (
    void )
```

Definition at line 13297 of file dss_mrr_pe674.c.

4.16.3.402 xdc_runtime_Text_visitRope__I()

```
void xdc_runtime_Text_visitRope__I (
    xdc_runtime_Text_RopeId rope,
    xdc_Fxn visFxn,
    xdc_Ptr visState )
```

Definition at line 2347 of file dss_mrr_pe674.c.

4.16.3.403 xdc_runtime_Timestamp_Module__startupDone__S()

```
xdc_Bool xdc_runtime_Timestamp_Module__startupDone__S (
    void )
```

Definition at line 13309 of file dss_mrr_pe674.c.

4.16.3.404 xdc_runtime_Timestamp_SupportProxy_get32__E()

```
xdc_Bits32 xdc_runtime_Timestamp_SupportProxy_get32__E (
    void )
```

Definition at line 9162 of file dss_mrr_pe674.c.

4.16.3.405 xdc_runtime_Timestamp_SupportProxy_get64__E()

```
xdc_Void xdc_runtime_Timestamp_SupportProxy_get64__E (
    xdc_runtime_Types_Timestamp64 * result )
```

Definition at line 9168 of file dss_mrr_pe674.c.

4.16.3.406 xdc_runtime_Timestamp_SupportProxy_getFreq__E()

```
xdc_Void xdc_runtime_Timestamp_SupportProxy_getFreq__E (
    xdc_runtime_Types_FreqHz * freq )
```

Definition at line 9174 of file dss_mrr_pe674.c.

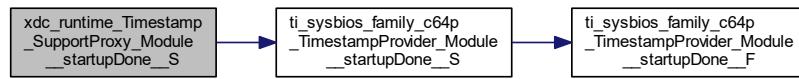
4.16.3.407 xdc_runtime_Timestamp_SupportProxy_Module__startupDone__S()

```
xdc_Bool xdc_runtime_Timestamp_SupportProxy_Module__startupDone__S (
    void )
```

Definition at line 9156 of file dss_mrr_pe674.c.

References `ti_sysbios_family_c64p_TimestampProvider_Module__startupDone__S()`.

Here is the call graph for this function:



4.16.3.408 xdc_runtime_Timestamp_SupportProxy_Proxy_abstract_S()

```
xdc_Bool xdc_runtime_Timestamp_SupportProxy_Proxy_abstract_S (
    void )
```

Definition at line 13333 of file dss_mrr_pe674.c.

4.16.3.409 xdc_runtime_Timestamp_SupportProxy_Proxy_delegate_S()

```
xdc_CPtr xdc_runtime_Timestamp_SupportProxy_Proxy_delegate_S (
    void )
```

Definition at line 13337 of file dss_mrr_pe674.c.

4.16.3.410 xdc_runtime_TimestampNull_Module_startupDone_S()

```
xdc_Bool xdc_runtime_TimestampNull_Module_startupDone_S (
    void )
```

Definition at line 13321 of file dss_mrr_pe674.c.

4.16.4 Variable Documentation**4.16.4.1 __TI_STATIC_BASE**

```
void* __TI_STATIC_BASE
```

4.16.4.2 __xdc_init_addr

```
__FAR__ int (* volatile __xdc_init_addr) (void) = & __xdc_init
Definition at line 13357 of file dss_mrr_pe674.c.
```

4.16.4.3 heap0

```
const ti_sysbios_heaps_HeapMem_Handle heap0 = (ti_sysbios_heaps_HeapMem_Handle)((ti_sysbios_
heaps_HeapMem_Handle)& ti_sysbios_heaps_HeapMem_Object_table_V[0])
Definition at line 13373 of file dss_mrr_pe674.c.
```

4.16.4.4 ti_sysbios_BIOS_clockEnabled_C

```
const __FAR__ CT	ti_sysbios_BIOS_clockEnabled ti_sysbios_BIOS_clockEnabled_C = 1
Definition at line 3106 of file dss_mrr_pe674.c.
```

4.16.4.5 ti_sysbios_BIOS_cpuFreq_C

```
const __FAR__ CT	ti_sysbios_BIOS_cpuFreq ti_sysbios_BIOS_cpuFreq_C
Initial value:
= {
    (xdc_Bits32) 0x0,
    (xdc_Bits32) 0x23c34600,
}
```

Definition at line 3087 of file dss_mrr_pe674.c.

4.16.4.6 **ti_sysbios_BIOS_defaultKernelHeapInstance__C**

```
const __FAR__ CT(ti_sysbios_BIOS_defaultKernelHeapInstance ti_sysbios_BIOS_defaultKernelHeapInstance__C = 0
```

Definition at line 3110 of file dss_mrr_pe674.c.

4.16.4.7 **ti_sysbios_BIOS_heapSection__C**

```
const __FAR__ CT(ti_sysbios_BIOS_heapSection ti_sysbios_BIOS_heapSection__C = 0
```

Definition at line 3126 of file dss_mrr_pe674.c.

4.16.4.8 **ti_sysbios_BIOS_heapSize__C**

```
const __FAR__ CT(ti_sysbios_BIOS_heapSize ti_sysbios_BIOS_heapSize__C = (xdc_SizeT)0x1000
```

Definition at line 3122 of file dss_mrr_pe674.c.

4.16.4.9 **ti_sysbios_BIOS_heapTrackEnabled__C**

```
const __FAR__ CT(ti_sysbios_BIOS_heapTrackEnabled ti_sysbios_BIOS_heapTrackEnabled__C = 0
```

Definition at line 3130 of file dss_mrr_pe674.c.

4.16.4.10 **ti_sysbios_BIOS_installedErrorHook__C**

```
const __FAR__ CT(ti_sysbios_BIOS_installedErrorHook ti_sysbios_BIOS_installedErrorHook__C = ((CT(ti_sysbios_BIOS_installedErrorHook)((xdc_Fxn)xdc_runtime_Error_print__E))
```

Definition at line 3142 of file dss_mrr_pe674.c.

4.16.4.11 **ti_sysbios_BIOS_kernelHeapSection__C**

```
const __FAR__ CT(ti_sysbios_BIOS_kernelHeapSection ti_sysbios_BIOS_kernelHeapSection__C = ".kernel_heap"
```

Definition at line 3118 of file dss_mrr_pe674.c.

4.16.4.12 **ti_sysbios_BIOS_kernelHeapSize__C**

```
const __FAR__ CT(ti_sysbios_BIOS_kernelHeapSize ti_sysbios_BIOS_kernelHeapSize__C = (xdc_SizeT)0x1000
```

Definition at line 3114 of file dss_mrr_pe674.c.

4.16.4.13 **ti_sysbios_BIOS_Module_diagsEnabled__C**

```
const __FAR__ CT(ti_sysbios_BIOS_Module_diagsEnabled ti_sysbios_BIOS_Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 3011 of file dss_mrr_pe674.c.

4.16.4.14 **ti_sysbios_BIOS_Module_diagsIncluded__C**

```
const __FAR__ CT(ti_sysbios_BIOS_Module_diagsIncluded ti_sysbios_BIOS_Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 3015 of file dss_mrr_pe674.c.

4.16.4.15 `ti_sysbios_BIOS_Module_diagsMask__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_diagsMask) ti_sysbios_BIOS_Module_diagsMask__C =
((CT(ti_sysbios_BIOS_Module_diagsMask) 0))
Definition at line 3019 of file dss_mrr_pe674.c.
```

4.16.4.16 `ti_sysbios_BIOS_Module_gateObj__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_gateObj) ti_sysbios_BIOS_Module_gateObj__C = ((CT(ti_sysbios_BIOS_Module_gateObj) 0))
Definition at line 3023 of file dss_mrr_pe674.c.
```

4.16.4.17 `ti_sysbios_BIOS_Module_gatePrms__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_gatePrms) ti_sysbios_BIOS_Module_gatePrms__C = ((CT(ti_sysbios_BIOS_Module_gatePrms) 0))
Definition at line 3027 of file dss_mrr_pe674.c.
```

4.16.4.18 `ti_sysbios_BIOS_Module_id__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_id) ti_sysbios_BIOS_Module_id__C = (xdc_Bits16) 0x1b
Definition at line 3031 of file dss_mrr_pe674.c.
```

4.16.4.19 `ti_sysbios_BIOS_Module_loggerDefined__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_loggerDefined) ti_sysbios_BIOS_Module_loggerDefined__C = 0
Definition at line 3035 of file dss_mrr_pe674.c.
```

4.16.4.20 `ti_sysbios_BIOS_Module_loggerFxn0__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_loggerFxn0) ti_sysbios_BIOS_Module_loggerFxn0__C =
((CT(ti_sysbios_BIOS_Module_loggerFxn0) 0))
Definition at line 3043 of file dss_mrr_pe674.c.
```

4.16.4.21 `ti_sysbios_BIOS_Module_loggerFxn1__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_loggerFxn1) ti_sysbios_BIOS_Module_loggerFxn1__C =
((CT(ti_sysbios_BIOS_Module_loggerFxn1) 0))
Definition at line 3047 of file dss_mrr_pe674.c.
```

4.16.4.22 `ti_sysbios_BIOS_Module_loggerFxn2__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_loggerFxn2) ti_sysbios_BIOS_Module_loggerFxn2__C =
((CT(ti_sysbios_BIOS_Module_loggerFxn2) 0))
Definition at line 3051 of file dss_mrr_pe674.c.
```

4.16.4.23 `ti_sysbios_BIOS_Module_loggerFxn4__C`

```
const __FAR__ CT(ti_sysbios_BIOS_Module_loggerFxn4) ti_sysbios_BIOS_Module_loggerFxn4__C =
((CT(ti_sysbios_BIOS_Module_loggerFxn4) 0))
Definition at line 3055 of file dss_mrr_pe674.c.
```

4.16.4.24 ti_sysbios_BIOS_Module_loggerFxn8_C

```
const __FAR__ CT__ti_sysbios_BIOS_Module_loggerFxn8 ti_sysbios_BIOS_Module_loggerFxn8__C =
((CT__ti_sysbios_BIOS_Module_loggerFxn8)0)
Definition at line 3059 of file dss_mrr_pe674.c.
```

4.16.4.25 ti_sysbios_BIOS_Module_loggerObj_C

```
const __FAR__ CT__ti_sysbios_BIOS_Module_loggerObj ti_sysbios_BIOS_Module_loggerObj__C =
((CT__ti_sysbios_BIOS_Module_loggerObj)0)
Definition at line 3039 of file dss_mrr_pe674.c.
```

4.16.4.26 ti_sysbios_BIOS_Module_state_V

ti_sysbios_BIOS_Module_State ti_sysbios_BIOS_Module_state_V

Initial value:

```
= {
    {
        (xdc_Bits32)0x0,
        (xdc_Bits32)0x23c34600,
    },
    (xdc_UInt)0x0,
    ((xdc_IArg)(0x0)),
    (ti_sysbios_BIOS_RtsGateProxy_Handle)&ti_sysbios_gates_GateMutex_Object_table_V[1],
    ti_sysbios_BIOS_ThreadType_Main,
    ((void*)0),
    ((xdc_Void*)(xdc_Void))((xdc_Fxn)ti_sysbios_BIOS_startFunc),
    ((xdc_Void*)(xdc_Int))((xdc_Fxn)ti_sysbios_BIOS_exitFunc),
}
```

Definition at line 920 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_BIOS_atExitFunc_I()`, `ti_sysbios_BIOS_registerRTSLock()`, `ti_sysbios_BIOS_removeRTSLock()`, `ti_sysbios_BIOS_rtsLock()`, and `ti_sysbios_BIOS_rtsUnlock()`.

4.16.4.27 ti_sysbios_BIOS_mpeEnabled_C

```
const __FAR__ CT__ti_sysbios_BIOS_mpeEnabled ti_sysbios_BIOS_mpeEnabled__C = 0
Definition at line 3083 of file dss_mrr_pe674.c.
```

4.16.4.28 ti_sysbios_BIOS_Object_count_C

```
const __FAR__ CT__ti_sysbios_BIOS_Object_count ti_sysbios_BIOS_Object_count__C = 0
Definition at line 3063 of file dss_mrr_pe674.c.
```

4.16.4.29 ti_sysbios_BIOS_Object_heap_C

```
const __FAR__ CT__ti_sysbios_BIOS_Object_heap ti_sysbios_BIOS_Object_heap__C = 0
Definition at line 3067 of file dss_mrr_pe674.c.
```

4.16.4.30 ti_sysbios_BIOS_Object_sizeof_C

```
const __FAR__ CT__ti_sysbios_BIOS_Object_sizeof ti_sysbios_BIOS_Object_sizeof__C = 0
Definition at line 3071 of file dss_mrr_pe674.c.
```

4.16.4.31 ti_sysbios_BIOS_Object_table_C

```
const __FAR__ CT(ti_sysbios_BIOS_Object_table) ti_sysbios_BIOS_Object_table_C = 0
Definition at line 3075 of file dss_mrr_pe674.c.
```

4.16.4.32 ti_sysbios_BIOS_RtsGateProxy_Module_root_V

```
ti_sysbios_BIOS_RtsGateProxy_Module ti_sysbios_BIOS_RtsGateProxy_Module_root_V
```

4.16.4.33 ti_sysbios_BIOS_runtimeCreatesEnabled_C

```
const __FAR__ CT(ti_sysbios_BIOS_runtimeCreatesEnabled) ti_sysbios_BIOS_runtimeCreatesEnabled_C = 1
Definition at line 3094 of file dss_mrr_pe674.c.
```

4.16.4.34 ti_sysbios_BIOS_setupSecureContext_C

```
const __FAR__ CT(ti_sysbios_BIOS_setupSecureContext) ti_sysbios_BIOS_setupSecureContext_C = 0
Definition at line 3134 of file dss_mrr_pe674.c.
```

4.16.4.35 ti_sysbios_BIOS_smpEnabled_C

```
const __FAR__ CT(ti_sysbios_BIOS_smpEnabled) ti_sysbios_BIOS_smpEnabled_C = 0
Definition at line 3079 of file dss_mrr_pe674.c.
```

4.16.4.36 ti_sysbios_BIOS_swiEnabled_C

```
const __FAR__ CT(ti_sysbios_BIOS_swiEnabled) ti_sysbios_BIOS_swiEnabled_C = 0
Definition at line 3102 of file dss_mrr_pe674.c.
```

4.16.4.37 ti_sysbios_BIOS_taskEnabled_C

```
const __FAR__ CT(ti_sysbios_BIOS_taskEnabled) ti_sysbios_BIOS_taskEnabled_C = 1
Definition at line 3098 of file dss_mrr_pe674.c.
```

4.16.4.38 ti_sysbios_BIOS_useSK_C

```
const __FAR__ CT(ti_sysbios_BIOS_useSK) ti_sysbios_BIOS_useSK_C = 0
Definition at line 3138 of file dss_mrr_pe674.c.
```

4.16.4.39 ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsEnabled) ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsEnabled_C = (xdc_Bits32)0x90
Definition at line 3156 of file dss_mrr_pe674.c.
```

4.16.4.40 ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsIncluded) ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsIncluded_C = (xdc_Bits32)0x90
Definition at line 3160 of file dss_mrr_pe674.c.
```

4.16.4.41 ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_diagsMask) ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_diagsMask_C = ((CT(ti_sysbios_family_c62_Intrinsics_<-
Support_Module_diagsMask) 0)
```

Definition at line 3164 of file dss_mrr_pe674.c.

4.16.4.42 ti_sysbios_family_c62_IntrinsicsSupport_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_gateObj) ti_sysbios_family_<-
_c62_IntrinsicsSupport_Module_gateObj_C = ((CT(ti_sysbios_family_c62_IntrinsicsSupport_<-
Module_gateObj) 0)
```

Definition at line 3168 of file dss_mrr_pe674.c.

4.16.4.43 ti_sysbios_family_c62_IntrinsicsSupport_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_gatePrms) ti_sysbios_family_<-
_c62_IntrinsicsSupport_Module_gatePrms_C = ((CT(ti_sysbios_family_c62_IntrinsicsSupport_<-
Module_gatePrms) 0)
```

Definition at line 3172 of file dss_mrr_pe674.c.

4.16.4.44 ti_sysbios_family_c62_IntrinsicsSupport_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_id) ti_sysbios_family_c62_<-
IntrinsicsSupport_Module_id_C = (xdc_Bits16) 0x2e
```

Definition at line 3176 of file dss_mrr_pe674.c.

4.16.4.45 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerDefined) ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerDefined_C = 0
```

Definition at line 3180 of file dss_mrr_pe674.c.

4.16.4.46 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn0) ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c62_Intrinsics_<-
Support_Module_loggerFxn0) 0)
```

Definition at line 3188 of file dss_mrr_pe674.c.

4.16.4.47 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn1) ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c62_Intrinsics_<-
Support_Module_loggerFxn1) 0)
```

Definition at line 3192 of file dss_mrr_pe674.c.

4.16.4.48 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn2) ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c62_Intrinsics_<-
Support_Module_loggerFxn2) 0)
```

Definition at line 3196 of file dss_mrr_pe674.c.

4.16.4.49 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn4 ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c62_Intrinsics<-
Support_Module_loggerFxn4) 0)
```

Definition at line 3200 of file dss_mrr_pe674.c.

4.16.4.50 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerFxn8 ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerFxn8_C = ((CT(ti_sysbios_family_c62_Intrinsics<-
Support_Module_loggerFxn8) 0)
```

Definition at line 3204 of file dss_mrr_pe674.c.

4.16.4.51 ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Module_loggerObj ti_sysbios_<-
family_c62_IntrinsicsSupport_Module_loggerObj_C = ((CT(ti_sysbios_family_c62_Intrinsics<-
Support_Module_loggerObj) 0)
```

Definition at line 3184 of file dss_mrr_pe674.c.

4.16.4.52 ti_sysbios_family_c62_IntrinsicsSupport_Object_count_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Object_count ti_sysbios_family_<-
c62_IntrinsicsSupport_Object_count_C = 0
```

Definition at line 3208 of file dss_mrr_pe674.c.

4.16.4.53 ti_sysbios_family_c62_IntrinsicsSupport_Object_heap_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Object_heap ti_sysbios_family_c62_<-
_IntrinsicsSupport_Object_heap_C = 0
```

Definition at line 3212 of file dss_mrr_pe674.c.

4.16.4.54 ti_sysbios_family_c62_IntrinsicsSupport_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Object_sizeof ti_sysbios_family_<-
c62_IntrinsicsSupport_Object_sizeof_C = 0
```

Definition at line 3216 of file dss_mrr_pe674.c.

4.16.4.55 ti_sysbios_family_c62_IntrinsicsSupport_Object_table_C

```
const __FAR__ CT(ti_sysbios_family_c62_IntrinsicsSupport_Object_table ti_sysbios_family_<-
c62_IntrinsicsSupport_Object_table_C = 0
```

Definition at line 3220 of file dss_mrr_pe674.c.

4.16.4.56 ti_sysbios_family_c62_TaskSupport_defaultStackSize_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_defaultStackSize ti_sysbios_family_c62_<-
TaskSupport_defaultStackSize_C = (xdc_SizeT)0x800
```

Definition at line 3297 of file dss_mrr_pe674.c.

4.16.4.57 ti_sysbios_family_c62_TaskSupport_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_diagsEnabled) ti_sysbios_family_c62_TaskSupport_Module_diagsEnabled_C = (xdc_Bits32)0x90
```

Definition at line 3229 of file dss_mrr_pe674.c.

4.16.4.58 ti_sysbios_family_c62_TaskSupport_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_diagsIncluded) ti_sysbios_family_c62_TaskSupport_Module_diagsIncluded_C = (xdc_Bits32)0x90
```

Definition at line 3233 of file dss_mrr_pe674.c.

4.16.4.59 ti_sysbios_family_c62_TaskSupport_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_diagsMask) ti_sysbios_family_c62_TaskSupport_Module_diagsMask_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_diagsMask) 0)
```

Definition at line 3237 of file dss_mrr_pe674.c.

4.16.4.60 ti_sysbios_family_c62_TaskSupport_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_gateObj) ti_sysbios_family_c62_TaskSupport_Module_gateObj_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_gateObj) 0)
```

Definition at line 3241 of file dss_mrr_pe674.c.

4.16.4.61 ti_sysbios_family_c62_TaskSupport_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_gatePrms) ti_sysbios_family_c62_TaskSupport_Module_gatePrms_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_gatePrms) 0)
```

Definition at line 3245 of file dss_mrr_pe674.c.

4.16.4.62 ti_sysbios_family_c62_TaskSupport_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_id) ti_sysbios_family_c62_TaskSupport_Module_id_C = (xdc_Bits16)0x2d
```

Definition at line 3249 of file dss_mrr_pe674.c.

4.16.4.63 ti_sysbios_family_c62_TaskSupport_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerDefined) ti_sysbios_family_c62_TaskSupport_Module_loggerDefined_C = 0
```

Definition at line 3253 of file dss_mrr_pe674.c.

4.16.4.64 ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0) ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn0) 0)
```

Definition at line 3261 of file dss_mrr_pe674.c.

4.16.4.65 ti_sysbios_family_c62_TaskSupport_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn1 ti_sysbios_family_c62←
_TaskSupport_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_logger←
Fxn1) 0)
```

Definition at line 3265 of file dss_mrr_pe674.c.

4.16.4.66 ti_sysbios_family_c62_TaskSupport_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn2 ti_sysbios_family_c62←
_TaskSupport_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_logger←
Fxn2) 0)
```

Definition at line 3269 of file dss_mrr_pe674.c.

4.16.4.67 ti_sysbios_family_c62_TaskSupport_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn4 ti_sysbios_family_c62←
_TaskSupport_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_logger←
Fxn4) 0)
```

Definition at line 3273 of file dss_mrr_pe674.c.

4.16.4.68 ti_sysbios_family_c62_TaskSupport_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerFxn8 ti_sysbios_family_c62←
_TaskSupport_Module_loggerFxn8_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_logger←
Fxn8) 0)
```

Definition at line 3277 of file dss_mrr_pe674.c.

4.16.4.69 ti_sysbios_family_c62_TaskSupport_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Module_loggerObj ti_sysbios_family_c62←
_TaskSupport_Module_loggerObj_C = ((CT(ti_sysbios_family_c62_TaskSupport_Module_logger←
Obj) 0)
```

Definition at line 3257 of file dss_mrr_pe674.c.

4.16.4.70 ti_sysbios_family_c62_TaskSupport_Object_count_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Object_count ti_sysbios_family_c62_Task←
Support_Object_count_C = 0
```

Definition at line 3281 of file dss_mrr_pe674.c.

4.16.4.71 ti_sysbios_family_c62_TaskSupport_Object_heap_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Object_heap ti_sysbios_family_c62_Task←
Support_Object_heap_C = 0
```

Definition at line 3285 of file dss_mrr_pe674.c.

4.16.4.72 ti_sysbios_family_c62_TaskSupport_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Object_sizeof ti_sysbios_family_c62←
TaskSupport_Object_sizeof_C = 0
```

Definition at line 3289 of file dss_mrr_pe674.c.

4.16.4.73 ti_sysbios_family_c62_TaskSupport_Object_table_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_Object_table) ti_sysbios_family_c62_TaskSupport_Object_table_C = 0
Definition at line 3293 of file dss_mrr_pe674.c.
```

4.16.4.74 ti_sysbios_family_c62_TaskSupport_stackAlignment_C

```
const __FAR__ CT(ti_sysbios_family_c62_TaskSupport_stackAlignment) ti_sysbios_family_c62_TaskSupport_stackAlignment_C = (xdc_UInt)0x8
Definition at line 3301 of file dss_mrr_pe674.c.
```

4.16.4.75 ti_sysbios_family_c64p_Cache_E_invalidL1CacheSize_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_E_invalidL1CacheSize) ti_sysbios_family_c64p_Cache_E_invalidL1CacheSize_C = (((xdc_runtime_Error_Id)17) << 16 | 0)
Definition at line 3469 of file dss_mrr_pe674.c.
```

4.16.4.76 ti_sysbios_family_c64p_Cache_E_invalidL2CacheSize_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_E_invalidL2CacheSize) ti_sysbios_family_c64p_Cache_E_invalidL2CacheSize_C = (((xdc_runtime_Error_Id)18) << 16 | 0)
Definition at line 3473 of file dss_mrr_pe674.c.
```

4.16.4.77 ti_sysbios_family_c64p_Cache_EMIFA_BASE_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFA_BASE) ti_sysbios_family_c64p_Cache_EMIFA_BASE_C = (xdc_UInt)0x0
Definition at line 3405 of file dss_mrr_pe674.c.
```

4.16.4.78 ti_sysbios_family_c64p_Cache_EMIFA_CFG_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFA_CFG) ti_sysbios_family_c64p_Cache_EMIFA_CFG_C = ((CT(ti_sysbios_family_c64p_Cache_EMIFA_CFG)0))
Definition at line 3401 of file dss_mrr_pe674.c.
```

4.16.4.79 ti_sysbios_family_c64p_Cache_EMIFA_LENGTH_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFA_LENGTH) ti_sysbios_family_c64p_Cache_EMIFA_LENGTH_C = (xdc_UInt)0x0
Definition at line 3409 of file dss_mrr_pe674.c.
```

4.16.4.80 ti_sysbios_family_c64p_Cache_EMIFB_BASE_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFB_BASE) ti_sysbios_family_c64p_Cache_EMIFB_BASE_C = (xdc_UInt)0x0
Definition at line 3417 of file dss_mrr_pe674.c.
```

4.16.4.81 ti_sysbios_family_c64p_Cache_EMIFB_CFG_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFB_CFG) ti_sysbios_family_c64p_Cache_EMIFB_CFG_C = ((CT(ti_sysbios_family_c64p_Cache_EMIFB_CFG)0))
```

Definition at line 3413 of file dss_mrr_pe674.c.

4.16.4.82 ti_sysbios_family_c64p_Cache_EMIFB_LENGTH__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFB_LENGTH) ti_sysbios_family_c64p_Cache_EMIFB_LENGTH__C = (xdc_UInt)0x0
```

Definition at line 3421 of file dss_mrr_pe674.c.

4.16.4.83 ti_sysbios_family_c64p_Cache_EMIFC_BASE__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFC_BASE) ti_sysbios_family_c64p_Cache_EMIFC_BASE__C = (xdc_UInt)0x0
```

Definition at line 3429 of file dss_mrr_pe674.c.

4.16.4.84 ti_sysbios_family_c64p_Cache_EMIFC_CFG__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFC_CFG) ti_sysbios_family_c64p_Cache_EMIFC_CFG__C = ((CT(ti_sysbios_family_c64p_Cache_EMIFC_CFG))0)
```

Definition at line 3425 of file dss_mrr_pe674.c.

4.16.4.85 ti_sysbios_family_c64p_Cache_EMIFC_LENGTH__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_EMIFC_LENGTH) ti_sysbios_family_c64p_Cache_EMIFC_LENGTH__C = (xdc_UInt)0x0
```

Definition at line 3433 of file dss_mrr_pe674.c.

4.16.4.86 ti_sysbios_family_c64p_Cache_initSize__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_initSize) ti_sysbios_family_c64p_Cache_initSize__C
```

Initial value:

```
= {  
    ti_sysbios_family_c64p_Cache_L1Size_32K,  
    ti_sysbios_family_c64p_Cache_L1Size_32K,  
    ti_sysbios_family_c64p_Cache_L2Size_0K,  
}
```

Definition at line 3393 of file dss_mrr_pe674.c.

4.16.4.87 ti_sysbios_family_c64p_Cache_MAR0_31__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR0_31) ti_sysbios_family_c64p_Cache_MAR0_31__C = (xdc_UInt32)0x0
```

Definition at line 3437 of file dss_mrr_pe674.c.

4.16.4.88 ti_sysbios_family_c64p_Cache_MAR128_159__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR128_159) ti_sysbios_family_c64p_Cache_MAR128_159__C = (xdc_UInt32)0x0
```

Definition at line 3453 of file dss_mrr_pe674.c.

4.16.4.89 ti_sysbios_family_c64p_Cache_MAR160_191_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR160_191 ti_sysbios_family_c64p_Cache_MA←
R160_191_C = (xdc_UInt32) 0x0
```

Definition at line 3457 of file dss_mrr_pe674.c.

4.16.4.90 ti_sysbios_family_c64p_Cache_MAR192_223_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR192_223 ti_sysbios_family_c64p_Cache_MA←
R192_223_C = (xdc_UInt32) 0x0
```

Definition at line 3461 of file dss_mrr_pe674.c.

4.16.4.91 ti_sysbios_family_c64p_Cache_MAR224_255_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR224_255 ti_sysbios_family_c64p_Cache_MA←
R224_255_C = (xdc_UInt32) 0x0
```

Definition at line 3465 of file dss_mrr_pe674.c.

4.16.4.92 ti_sysbios_family_c64p_Cache_MAR32_63_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR32_63 ti_sysbios_family_c64p_Cache_MAR32←
63_C = (xdc_UInt32) 0x0
```

Definition at line 3441 of file dss_mrr_pe674.c.

4.16.4.93 ti_sysbios_family_c64p_Cache_MAR64_95_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR64_95 ti_sysbios_family_c64p_Cache_MAR64←
95_C = (xdc_UInt32) 0x0
```

Definition at line 3445 of file dss_mrr_pe674.c.

4.16.4.94 ti_sysbios_family_c64p_Cache_MAR96_127_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_MAR96_127 ti_sysbios_family_c64p_Cache_MAR96←
127_C = (xdc_UInt32) 0x0
```

Definition at line 3449 of file dss_mrr_pe674.c.

4.16.4.95 ti_sysbios_family_c64p_Cache_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_diagsEnabled ti_sysbios_family_c64p←
Cache_Module_diagsEnabled_C = (xdc_Bits32) 0x90
```

Definition at line 3325 of file dss_mrr_pe674.c.

4.16.4.96 ti_sysbios_family_c64p_Cache_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_diagsIncluded ti_sysbios_family_c64p←
Cache_Module_diagsIncluded_C = (xdc_Bits32) 0x90
```

Definition at line 3329 of file dss_mrr_pe674.c.

4.16.4.97 ti_sysbios_family_c64p_Cache_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_diagsMask ti_sysbios_family_c64p←
Cache_Module_diagsMask_C = ((CT(ti_sysbios_family_c64p_Cache_Module_diagsMask) 0)
```

Definition at line 3333 of file dss_mrr_pe674.c.

4.16.4.98 ti_sysbios_family_c64p_Cache_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_gateObj) ti_sysbios_family_c64p_Cache->
Module_gateObj_C = ((CT(ti_sysbios_family_c64p_Cache_Module_gateObj)) 0)
```

Definition at line 3337 of file dss_mrr_pe674.c.

4.16.4.99 ti_sysbios_family_c64p_Cache_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_gatePrms) ti_sysbios_family_c64p_Cache->
Module_gatePrms_C = ((CT(ti_sysbios_family_c64p_Cache_Module_gatePrms)) 0)
```

Definition at line 3341 of file dss_mrr_pe674.c.

4.16.4.100 ti_sysbios_family_c64p_Cache_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_id) ti_sysbios_family_c64p_Cache->
Module_id_C = (xdc_Bits16) 0x1a
```

Definition at line 3345 of file dss_mrr_pe674.c.

4.16.4.101 ti_sysbios_family_c64p_Cache_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerDefined) ti_sysbios_family_c64p->
Cache_Module_loggerDefined_C = 0
```

Definition at line 3349 of file dss_mrr_pe674.c.

4.16.4.102 ti_sysbios_family_c64p_Cache_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn0) ti_sysbios_family_c64p->
Cache_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn0)) 0)
```

Definition at line 3357 of file dss_mrr_pe674.c.

4.16.4.103 ti_sysbios_family_c64p_Cache_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn1) ti_sysbios_family_c64p->
Cache_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn1)) 0)
```

Definition at line 3361 of file dss_mrr_pe674.c.

4.16.4.104 ti_sysbios_family_c64p_Cache_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn2) ti_sysbios_family_c64p->
Cache_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn2)) 0)
```

Definition at line 3365 of file dss_mrr_pe674.c.

4.16.4.105 ti_sysbios_family_c64p_Cache_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn4) ti_sysbios_family_c64p->
Cache_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn4)) 0)
```

Definition at line 3369 of file dss_mrr_pe674.c.

4.16.4.106 ti_sysbios_family_c64p_Cache_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn8 ti_sysbios_family_c64p_Cache_Module_loggerFxn8_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerFxn8) 0)
Definition at line 3373 of file dss_mrr_pe674.c.
```

4.16.4.107 ti_sysbios_family_c64p_Cache_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Module_loggerObj ti_sysbios_family_c64p_Cache_Module_loggerObj_C = ((CT(ti_sysbios_family_c64p_Cache_Module_loggerObj) 0)
Definition at line 3353 of file dss_mrr_pe674.c.
```

4.16.4.108 ti_sysbios_family_c64p_Cache_Module_state_V

ti_sysbios_family_c64p_Cache_Module_State ti_sysbios_family_c64p_Cache_Module_state_V
Initial value:
= {
 ((xdc_UInt32*)0),
}

Definition at line 948 of file dss_mrr_pe674.c.

4.16.4.109 ti_sysbios_family_c64p_Cache_Object_count_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Object_count ti_sysbios_family_c64p_Cache_Object_count_C = 0
Definition at line 3377 of file dss_mrr_pe674.c.
```

4.16.4.110 ti_sysbios_family_c64p_Cache_Object_heap_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Object_heap ti_sysbios_family_c64p_Cache_Object_heap_C = 0
Definition at line 3381 of file dss_mrr_pe674.c.
```

4.16.4.111 ti_sysbios_family_c64p_Cache_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Object_sizeof ti_sysbios_family_c64p_Cache_Object_sizeof_C = 0
Definition at line 3385 of file dss_mrr_pe674.c.
```

4.16.4.112 ti_sysbios_family_c64p_Cache_Object_table_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Cache_Object_table ti_sysbios_family_c64p_Cache_Object_table_C = 0
Definition at line 3389 of file dss_mrr_pe674.c.
```

4.16.4.113 ti_sysbios_family_c64p_EventCombiner_A_invalidEventId_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_A_invalidEventId ti_sysbios_family_c64p_EventCombiner_A_invalidEventId_C = (((xdc_runtime_ASSERT_Id) 0) << 16 | 16)
Definition at line 4082 of file dss_mrr_pe674.c.
```

4.16.4.114 ti_sysbios_family_c64p_EventCombiner_E_unpluggedEvent_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_E_unpluggedEvent ti_sysbios_family_c64p_EventCombiner_E_unpluggedEvent_C = (((xdc_runtime_Error_Id)9) << 16 | 0)
Definition at line 4086 of file dss_mrr_pe674.c.
```

4.16.4.115 ti_sysbios_family_c64p_EventCombiner_EVTMASK_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_EVTMASK ti_sysbios_family_c64p_EventCombiner_EVTMASK_C
Initial value:
= {
    (xdc_Bits32) (-0x0 - 1),
    (xdc_Bits32) (-0x0 - 1),
    (xdc_Bits32) (-0x0 - 1),
    (xdc_Bits32) (-0x0 - 1),
}
Definition at line 4090 of file dss_mrr_pe674.c.
```

4.16.4.116 ti_sysbios_family_c64p_EventCombiner_EVTRegs_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_EVTRegs ti_sysbios_family_c64p_EventCombiner_EVTRegs_C = ((CT(ti_sysbios_family_c64p_EventCombiner_EVTRegs) (0x1800000))
Definition at line 4078 of file dss_mrr_pe674.c.
```

4.16.4.117 ti_sysbios_family_c64p_EventCombiner_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_diagsEnabled ti_sysbios_family_c64p_EventCombiner_Module_diagsEnabled_C = (xdc_Bits32)0x90
Definition at line 4010 of file dss_mrr_pe674.c.
```

4.16.4.118 ti_sysbios_family_c64p_EventCombiner_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_diagsIncluded ti_sysbios_family_c64p_EventCombiner_Module_diagsIncluded_C = (xdc_Bits32)0x90
Definition at line 4014 of file dss_mrr_pe674.c.
```

4.16.4.119 ti_sysbios_family_c64p_EventCombiner_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_diagsMask ti_sysbios_family_c64p_EventCombiner_Module_diagsMask_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_diagsMask) 0)
Definition at line 4018 of file dss_mrr_pe674.c.
```

4.16.4.120 ti_sysbios_family_c64p_EventCombiner_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_gateObj ti_sysbios_family_c64p_EventCombiner_Module_gateObj_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_gateObj) 0)
Definition at line 4022 of file dss_mrr_pe674.c.
```

4.16.4.121 ti_sysbios_family_c64p_EventCombiner_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_gatePrms ti_sysbios_family_c64p_EventCombiner_Module_gatePrms_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_gatePrms) 0)
```

Definition at line 4026 of file dss_mrr_pe674.c.

4.16.4.122 ti_sysbios_family_c64p_EventCombiner_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_id ti_sysbios_family_c64p_EventCombiner_Module_id_C = (xdc_Bits16) 0x16
```

Definition at line 4030 of file dss_mrr_pe674.c.

4.16.4.123 ti_sysbios_family_c64p_EventCombiner_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerDefined ti_sysbios_family_c64p_EventCombiner_Module_loggerDefined_C = 0
```

Definition at line 4034 of file dss_mrr_pe674.c.

4.16.4.124 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn0 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn0) 0)
```

Definition at line 4042 of file dss_mrr_pe674.c.

4.16.4.125 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn1 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn1) 0)
```

Definition at line 4046 of file dss_mrr_pe674.c.

4.16.4.126 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn2 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn2) 0)
```

Definition at line 4050 of file dss_mrr_pe674.c.

4.16.4.127 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn4 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn4) 0)
```

Definition at line 4054 of file dss_mrr_pe674.c.

4.16.4.128 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn8 ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn8_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerFxn8) 0)
```

Definition at line 4058 of file dss_mrr_pe674.c.

4.16.4.129 ti_sysbios_family_c64p_EventCombiner_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerObj) ti_sysbios_family_c64p_EventCombiner_Module_loggerObj_C = ((CT(ti_sysbios_family_c64p_EventCombiner_Module_loggerObj) 0)
```

Definition at line 4038 of file dss_mrr_pe674.c.

4.16.4.130 ti_sysbios_family_c64p_EventCombiner_Module_state_V

```
ti_sysbios_family_c64p_EventCombiner_Module_State ti_sysbios_family_c64p_EventCombiner_Module_state_V
```

Definition at line 1345 of file dss_mrr_pe674.c.

4.16.4.131 ti_sysbios_family_c64p_EventCombiner_Object_count_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Object_count) ti_sysbios_family_c64p_EventCombiner_Object_count_C = 0
```

Definition at line 4062 of file dss_mrr_pe674.c.

4.16.4.132 ti_sysbios_family_c64p_EventCombiner_Object_heap_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Object_heap) ti_sysbios_family_c64p_EventCombiner_Object_heap_C = 0
```

Definition at line 4066 of file dss_mrr_pe674.c.

4.16.4.133 ti_sysbios_family_c64p_EventCombiner_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Object_sizeof) ti_sysbios_family_c64p_EventCombiner_Object_sizeof_C = 0
```

Definition at line 4070 of file dss_mrr_pe674.c.

4.16.4.134 ti_sysbios_family_c64p_EventCombiner_Object_table_C

```
const __FAR__ CT(ti_sysbios_family_c64p_EventCombiner_Object_table) ti_sysbios_family_c64p_EventCombiner_Object_table_C = 0
```

Definition at line 4074 of file dss_mrr_pe674.c.

4.16.4.135 ti_sysbios_family_c64p_Exception_E_exceptionMax_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_E_exceptionMax) ti_sysbios_family_c64p_Exception_E_exceptionMax_C = (((xdc_runtime_Error_Id)11) << 16 | 0)
```

Definition at line 4216 of file dss_mrr_pe674.c.

4.16.4.136 ti_sysbios_family_c64p_Exception_E_exceptionMin_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_E_exceptionMin) ti_sysbios_family_c64p_Exception_E_exceptionMin_C = (((xdc_runtime_Error_Id)10) << 16 | 0)
```

Definition at line 4212 of file dss_mrr_pe674.c.

4.16.4.137 ti_sysbios_family_c64p_Exception_enableExternalMPC__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_enableExternalMPC) ti_sysbios_family_c64p->
Exception_enableExternalMPC__C = 0
Definition at line 4224 of file dss_mrr_pe674.c.
```

4.16.4.138 ti_sysbios_family_c64p_Exception_enablePrint__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_enablePrint) ti_sysbios_family_c64p_Exception->
_enablePrint__C = 1
Definition at line 4228 of file dss_mrr_pe674.c.
```

4.16.4.139 ti_sysbios_family_c64p_Exception_exceptionHook__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_exceptionHook) ti_sysbios_family_c64p->
Exception_exceptionHook__C = ((CT(ti_sysbios_family_c64p_Exception_exceptionHook)) 0)
Definition at line 4232 of file dss_mrr_pe674.c.
```

4.16.4.140 ti_sysbios_family_c64p_Exception_externalHook__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_externalHook) ti_sysbios_family_c64p->
Exception_externalHook__C = ((CT(ti_sysbios_family_c64p_Exception_externalHook)) 0)
Definition at line 4240 of file dss_mrr_pe674.c.
```

4.16.4.141 ti_sysbios_family_c64p_Exception_internalHook__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_internalHook) ti_sysbios_family_c64p->
Exception_internalHook__C = ((CT(ti_sysbios_family_c64p_Exception_internalHook)) 0)
Definition at line 4236 of file dss_mrr_pe674.c.
```

4.16.4.142 ti_sysbios_family_c64p_Exception_Module_diagsEnabled__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_diagsEnabled) ti_sysbios_family_c64p->
c64p_Exception_Module_diagsEnabled__C = (xdc_Bits32) 0x190
Definition at line 4144 of file dss_mrr_pe674.c.
```

4.16.4.143 ti_sysbios_family_c64p_Exception_Module_diagsIncluded__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_diagsIncluded) ti_sysbios_family_c64p->
c64p_Exception_Module_diagsIncluded__C = (xdc_Bits32) 0x190
Definition at line 4148 of file dss_mrr_pe674.c.
```

4.16.4.144 ti_sysbios_family_c64p_Exception_Module_diagsMask__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_diagsMask) ti_sysbios_family_c64p->
Exception_Module_diagsMask__C = ((CT(ti_sysbios_family_c64p_Exception_Module_diagsMask)) 0)
Definition at line 4152 of file dss_mrr_pe674.c.
```

4.16.4.145 ti_sysbios_family_c64p_Exception_Module_gateObj__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_gateObj) ti_sysbios_family_c64p->
Exception_Module_gateObj__C = ((CT(ti_sysbios_family_c64p_Exception_Module_gateObj)) 0)
```

Definition at line 4156 of file dss_mrr_pe674.c.

4.16.4.146 ti_sysbios_family_c64p_Exception_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_gatePrms ti_sysbios_family_c64p_<-
Exception_Module_gatePrms_C = ((CT(ti_sysbios_family_c64p_Exception_Module_gatePrms)0)
```

Definition at line 4160 of file dss_mrr_pe674.c.

4.16.4.147 ti_sysbios_family_c64p_Exception_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_id ti_sysbios_family_c64p_Exception_<-
_Module_id_C = (xdc_Bits16)0x17
```

Definition at line 4164 of file dss_mrr_pe674.c.

4.16.4.148 ti_sysbios_family_c64p_Exception_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_loggerDefined ti_sysbios_family_c64p_<-
Exception_Module_loggerDefined_C = 0
```

Definition at line 4168 of file dss_mrr_pe674.c.

4.16.4.149 ti_sysbios_family_c64p_Exception_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn0 ti_sysbios_family_c64p_<-
Exception_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn0)0)
```

Definition at line 4176 of file dss_mrr_pe674.c.

4.16.4.150 ti_sysbios_family_c64p_Exception_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn1 ti_sysbios_family_c64p_<-
Exception_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn1)0))
```

Definition at line 4180 of file dss_mrr_pe674.c.

4.16.4.151 ti_sysbios_family_c64p_Exception_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn2 ti_sysbios_family_c64p_<-
Exception_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn2)0))
```

Definition at line 4184 of file dss_mrr_pe674.c.

4.16.4.152 ti_sysbios_family_c64p_Exception_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn4 ti_sysbios_family_c64p_<-
Exception_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c64p_Exception_Module_loggerFxn4)0))
```

Definition at line 4188 of file dss_mrr_pe674.c.

4.16.4.153 ti_sysbios_family_c64p_Exception_Module_loggerFxn8_C

```
const __FAR__ CT_ti_sysbios_family_c64p_Exception_Module_loggerFxn8 ti_sysbios_family_c64p_Exception_Module_loggerFxn8_C = ((CT_ti_sysbios_family_c64p_Exception_Module_loggerFxn8)0)
```

Definition at line 4192 of file dss_mrr_pe674.c.

4.16.4.154 ti_sysbios_family_c64p_Exception_Module_loggerObj_C

```
const __FAR__ CT_ti_sysbios_family_c64p_Exception_Module_loggerObj ti_sysbios_family_c64p->Exception_Module_loggerObj__C = ((CT_ti_sysbios_family_c64p_Exception_Module_loggerObj)0)
```

4.16.4.155 ti_sysbios_family_c64p_Exception_Module__state__V

```
ti_sysbios_family_c64p_Exception_Module_State ti_sysbios_family_c64p_Exception_Module_state_V
```

Initial value:

Definition at line 1366 of file dss_mrr_ne674.c.

4.16.4.156 ti sysbios family c64p Exception pmiHook C

```
const __FAR__ CT_tisysbios_family_c64p_Exception_nmiHook ti_sysbios_family_c64p_Exception_nmiHook C = ((CT_tisysbios_family_c64p_Exception_nmiHook)0);
```

Definition at line 4244 of file dss_mrr_ne674.c

4.16.4.157 ti sysbjos family c64p Exception Object count C

```
const __FAR__ CT_tli_sysbios_family_c64p_Exception_Object__count ti_sysbios_family_c64p_<--  
Exception Object count C = 0
```

Definition at line 4196 of file dss_mrr_pe674.c.

4.16.4.158 ti sysbios family c64p Exception Object heap C

```
const __FAR__ CT_ti_sysbios_family_c64p_Exception_Object__heap ti_sysbios_family_c64p__Exception_Object_heap_C = 0
```

Definition at line 4200 of file dss_mrr_pe674.c.

4.16.4.159 **ti_sysbios_family_c64p_Exception_Object_sizeof_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Object_sizeof ti_sysbios_family_c64p->
Exception_Object_sizeof_C = 0
```

Definition at line 4204 of file dss_mrr_pe674.c.

4.16.4.160 **ti_sysbios_family_c64p_Exception_Object_table_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_Object_table ti_sysbios_family_c64p->
Exception_Object_table_C = 0
```

Definition at line 4208 of file dss_mrr_pe674.c.

4.16.4.161 **ti_sysbios_family_c64p_Exception_returnHook_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_returnHook ti_sysbios_family_c64p_Exception->
_returnHook_C = ((CT(ti_sysbios_family_c64p_Exception_returnHook)0)
```

Definition at line 4248 of file dss_mrr_pe674.c.

4.16.4.162 **ti_sysbios_family_c64p_Exception_useInternalBuffer_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Exception_useInternalBuffer ti_sysbios_family_c64p->
Exception_useInternalBuffer_C = 0
```

Definition at line 4220 of file dss_mrr_pe674.c.

4.16.4.163 **ti_sysbios_family_c64p_Hwi0**

```
void* ti_sysbios_family_c64p_Hwi0
```

4.16.4.164 **ti_sysbios_family_c64p_Hwi_dispatcherAutoNestingSupport_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_dispatcherAutoNestingSupport ti_sysbios_family->
c64p_Hwi_dispatcherAutoNestingSupport_C = 1
```

Definition at line 4460 of file dss_mrr_pe674.c.

4.16.4.165 **ti_sysbios_family_c64p_Hwi_dispatcherIrpTrackingSupport_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_dispatcherIrpTrackingSupport ti_sysbios_family->
c64p_Hwi_dispatcherIrpTrackingSupport_C = 1
```

Definition at line 4472 of file dss_mrr_pe674.c.

4.16.4.166 **ti_sysbios_family_c64p_Hwi_dispatcherSwiSupport_C**

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_dispatcherSwiSupport ti_sysbios_family_c64p_Hwi->
_dispatcherSwiSupport_C = 0
```

Definition at line 4464 of file dss_mrr_pe674.c.

4.16.4.167 ti_sysbios_family_c64p_Hwi_dispatcherTaskSupport__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_dispatcherTaskSupport ti_sysbios_family_c64p_Hwi_dispatcherTaskSupport__C = 1
Definition at line 4468 of file dss_mrr_pe674.c.
```

4.16.4.168 ti_sysbios_family_c64p_Hwi_E_allocSCFailed__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_E_allocSCFailed ti_sysbios_family_c64p_Hwi_E_allocSCFailed__C = (((xdc_runtime_Error_Id)14) << 16 | 0)
Definition at line 4488 of file dss_mrr_pe674.c.
```

4.16.4.169 ti_sysbios_family_c64p_Hwi_E_alreadyDefined__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_E_alreadyDefined ti_sysbios_family_c64p_Hwi_E_alreadyDefined__C = (((xdc_runtime_Error_Id)12) << 16 | 0)
Definition at line 4480 of file dss_mrr_pe674.c.
```

4.16.4.170 ti_sysbios_family_c64p_Hwi_E_handleNotFound__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_E_handleNotFound ti_sysbios_family_c64p_Hwi_E_handleNotFound__C = (((xdc_runtime_Error_Id)13) << 16 | 0)
Definition at line 4484 of file dss_mrr_pe674.c.
```

4.16.4.171 ti_sysbios_family_c64p_Hwi_E_invalidIntNum__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_E_invalidIntNum ti_sysbios_family_c64p_Hwi_E_invalidIntNum__C = (((xdc_runtime_Error_Id)16) << 16 | 0)
Definition at line 4496 of file dss_mrr_pe674.c.
```

4.16.4.172 ti_sysbios_family_c64p_Hwi_E_registerSCFailed__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_E_registerSCFailed ti_sysbios_family_c64p_Hwi_E_registerSCFailed__C = (((xdc_runtime_Error_Id)15) << 16 | 0)
Definition at line 4492 of file dss_mrr_pe674.c.
```

4.16.4.173 ti_sysbios_family_c64p_Hwi_enableException__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_enableException ti_sysbios_family_c64p_Hwi_enableException__C = 1
Definition at line 4508 of file dss_mrr_pe674.c.
```

4.16.4.174 ti_sysbios_family_c64p_Hwi_hooks__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_hooks ti_sysbios_family_c64p_Hwi_hooks__C = {0,
0}
Definition at line 4528 of file dss_mrr_pe674.c.
```

4.16.4.175 ti_sysbios_family_c64p_Hwi_INTRMUX1Address__C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_INTRMUX1Address ti_sysbios_family_c64p_Hwi_INTRMUX1Address__C = ((CT(ti_sysbios_family_c64p_Hwi_INTRMUX1Address) (0x1800104)))
```

Definition at line 4476 of file dss_mrr_pe674.c.

4.16.4.176 `ti_sysbios_family_c64p_Hwi_LD_end__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_LD_end ti_sysbios_family_c64p_Hwi_LD_end__C =
(((xdc_runtime_Log_Event)13) << 16 | 512)
```

Definition at line 4504 of file dss_mrr_pe674.c.

4.16.4.177 `ti_sysbios_family_c64p_Hwi_LM_begin__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_LM_begin ti_sysbios_family_c64p_Hwi_LM_begin__C =
(((xdc_runtime_Log_Event)12) << 16 | 768)
```

Definition at line 4500 of file dss_mrr_pe674.c.

4.16.4.178 `ti_sysbios_family_c64p_Hwi_Module_diagsEnabled__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_diagsEnabled ti_sysbios_family_c64p_Hwi←
_Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 4392 of file dss_mrr_pe674.c.

4.16.4.179 `ti_sysbios_family_c64p_Hwi_Module_diagsIncluded__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_diagsIncluded ti_sysbios_family_c64p←
Hwi_Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 4396 of file dss_mrr_pe674.c.

4.16.4.180 `ti_sysbios_family_c64p_Hwi_Module_diagsMask__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_diagsMask ti_sysbios_family_c64p_Hwi←
Module_diagsMask__C = ((CT(ti_sysbios_family_c64p_Hwi_Module_diagsMask) 0))
```

Definition at line 4400 of file dss_mrr_pe674.c.

4.16.4.181 `ti_sysbios_family_c64p_Hwi_Module_gateObj__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_gateObj ti_sysbios_family_c64p_Hwi←
Module_gateObj__C = ((CT(ti_sysbios_family_c64p_Hwi_Module_gateObj) 0))
```

Definition at line 4404 of file dss_mrr_pe674.c.

4.16.4.182 `ti_sysbios_family_c64p_Hwi_Module_gatePrms__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_gatePrms ti_sysbios_family_c64p_Hwi←
Module_gatePrms__C = ((CT(ti_sysbios_family_c64p_Hwi_Module_gatePrms) 0))
```

Definition at line 4408 of file dss_mrr_pe674.c.

4.16.4.183 `ti_sysbios_family_c64p_Hwi_Module_id__C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_id ti_sysbios_family_c64p_Hwi_Module←
id__C = (xdc_Bits16)0x18
```

Definition at line 4412 of file dss_mrr_pe674.c.

4.16.4.184 ti_sysbios_family_c64p_Hwi_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerDefined ti_sysbios_family_c64p_Hwi_Module_loggerDefined_C) = 0
Definition at line 4416 of file dss_mrr_pe674.c.
```

4.16.4.185 ti_sysbios_family_c64p_Hwi_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn0 ti_sysbios_family_c64p_Hwi_Module_loggerFxn0_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn0) 0)
Definition at line 4424 of file dss_mrr_pe674.c.
```

4.16.4.186 ti_sysbios_family_c64p_Hwi_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn1 ti_sysbios_family_c64p_Hwi_Module_loggerFxn1_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn1) 0)
Definition at line 4428 of file dss_mrr_pe674.c.
```

4.16.4.187 ti_sysbios_family_c64p_Hwi_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn2 ti_sysbios_family_c64p_Hwi_Module_loggerFxn2_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn2) 0)
Definition at line 4432 of file dss_mrr_pe674.c.
```

4.16.4.188 ti_sysbios_family_c64p_Hwi_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn4 ti_sysbios_family_c64p_Hwi_Module_loggerFxn4_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn4) 0)
Definition at line 4436 of file dss_mrr_pe674.c.
```

4.16.4.189 ti_sysbios_family_c64p_Hwi_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn8 ti_sysbios_family_c64p_Hwi_Module_loggerFxn8_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerFxn8) 0)
Definition at line 4440 of file dss_mrr_pe674.c.
```

4.16.4.190 ti_sysbios_family_c64p_Hwi_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Module_loggerObj ti_sysbios_family_c64p_Hwi_Module_loggerObj_C) = ((CT(ti_sysbios_family_c64p_Hwi_Module_loggerObj) 0)
Definition at line 4420 of file dss_mrr_pe674.c.
```

4.16.4.191 ti_sysbios_family_c64p_Hwi_Module_root_V

ti_sysbios_family_c64p_Hwi_Module ti_sysbios_family_c64p_Hwi_Module_root_V

Initial value:

```
= {
    {&ti_sysbios_family_c64p_Hwi_Module_root_V.link,
     &ti_sysbios_family_c64p_Hwi_Module_root_V.link},
}
```

Definition at line 4279 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_family_c64p_Hwi_Object_first_S()`, and `ti_sysbios_family_c64p_Hwi_Object_next__S()`.

4.16.4.192 `ti_sysbios_family_c64p_Hwi_Module_state_V`

`ti_sysbios_family_c64p_Hwi_Module_State ti_sysbios_family_c64p_Hwi_Module_state_V`
 Definition at line 1408 of file dss_mrr_pe674.c.

4.16.4.193 `ti_sysbios_family_c64p_Hwi_Object_count_C`

```
const __FAR__ CT	ti_sysbios_family_c64p_Hwi_Object_count ti_sysbios_family_c64p_Hwi_Object->
__count_C = 5
Definition at line 4444 of file dss_mrr_pe674.c.
```

4.16.4.194 `ti_sysbios_family_c64p_Hwi_Object_DESC_C`

`const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_family_c64p_Hwi_Object_DESC_C`

Initial value:

```
= {
    (xdc_CPtr)0,
    &ti_sysbios_family_c64p_Hwi_Module_root_V.link,
    sizeof(ti_sysbios_family_c64p_Hwi_S1) - sizeof(ti_sysbios_family_c64p_Hwi_Object2),
    0,
    0,
    sizeof(ti_sysbios_family_c64p_Hwi_Object2),
    (xdc_CPtr)&ti_sysbios_family_c64p_Hwi_Object_PARAMS_C,
    sizeof(ti_sysbios_family_c64p_Hwi_Params),
}
Definition at line 4256 of file dss_mrr_pe674.c.
```

Referenced by `ti_sysbios_family_c64p_Hwi_construct()`, `ti_sysbios_family_c64p_Hwi_create()`, `ti_sysbios_family_c64p_Hwi_destruct()`, `ti_sysbios_family_c64p_Hwi_Object_create_S()`, and `ti_sysbios_family_c64p_Hwi_Object_delete_S()`.

4.16.4.195 `ti_sysbios_family_c64p_Hwi_Object_heap_C`

```
const __FAR__ CT	ti_sysbios_family_c64p_Hwi_Object_heap ti_sysbios_family_c64p_Hwi_Object->
_heap_C = 0
Definition at line 4448 of file dss_mrr_pe674.c.
```

4.16.4.196 `ti_sysbios_family_c64p_Hwi_Object_PARAMS_C`

`const __FAR__ ti_sysbios_family_c64p_Hwi_Params ti_sysbios_family_c64p_Hwi_Object_PARAMS_C`

Initial value:

```
= {
    sizeof(ti_sysbios_family_c64p_Hwi_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_family_c64p_Hwi_Object_PARAMS_C.__iprms,
    ti_sysbios_interfaces_IHwi_MaskingOption_SELF,
    ((xdc_UArg)(0x0)),
    1,
    (xdc_Int)(-0x0 - 1),
    (xdc_Int)0x0,
    (xdc_Bits16)0x0,
    (xdc_Bits16)0x0,
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
Definition at line 4260 of file dss_mrr_pe674.c.
```

Referenced by `ti_sysbios_family_c64p_Hwi_Params_init_S()`.

4.16.4.197 `ti_sysbios_family_c64p_Hwi_Object_sizeof_C`

```
const __FAR__ CT	ti_sysbios_family_c64p_Hwi_Object_sizeof ti_sysbios_family_c64p_Hwi->
Object_sizeof_C = sizeof( ti_sysbios_family_c64p_Hwi_Object )
```

Definition at line 4452 of file dss_mrr_pe674.c.

4.16.4.198 `ti_sysbios_family_c64p_Hwi_Object_table_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_Object_table ti_sysbios_family_c64p_Hwi_Object->
__table_C = ti_sysbios_family_c64p_Hwi_Object_table_V
```

Definition at line 4456 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_family_c64p_Hwi_Object_get_S()`.

4.16.4.199 `ti_sysbios_family_c64p_Hwi_Object_table_V`

```
ti_sysbios_family_c64p_Hwi_Object ti_sysbios_family_c64p_Hwi_Object_table_V
```

Definition at line 1386 of file dss_mrr_pe674.c.

4.16.4.200 `ti_sysbios_family_c64p_Hwi_swiDisable_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_swiDisable ti_sysbios_family_c64p_Hwi_swiDisable->
__C = ((CT(ti_sysbios_family_c64p_Hwi_swiDisable) 0))
```

Definition at line 4512 of file dss_mrr_pe674.c.

4.16.4.201 `ti_sysbios_family_c64p_Hwi_swiRestoreHwi_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_swiRestoreHwi ti_sysbios_family_c64p_Hwi_swi->
RestoreHwi_C = ((CT(ti_sysbios_family_c64p_Hwi_swiRestoreHwi) 0))
```

Definition at line 4516 of file dss_mrr_pe674.c.

4.16.4.202 `ti_sysbios_family_c64p_Hwi_taskDisable_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_taskDisable ti_sysbios_family_c64p_Hwi_task->
Disable_C = ((CT(ti_sysbios_family_c64p_Hwi_taskDisable) ((xdc_Fxn) ti_sysbios_knl_Task->
disable_E)))
```

Definition at line 4520 of file dss_mrr_pe674.c.

4.16.4.203 `ti_sysbios_family_c64p_Hwi_taskRestoreHwi_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_Hwi_taskRestoreHwi ti_sysbios_family_c64p_Hwi_task->
RestoreHwi_C = ((CT(ti_sysbios_family_c64p_Hwi_taskRestoreHwi) ((xdc_Fxn) ti_sysbios_knl->
Task_restoreHwi_E)))
```

Definition at line 4524 of file dss_mrr_pe674.c.

4.16.4.204 `ti_sysbios_family_c64p_TimestampProvider_Module_diagsEnabled_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_diagsEnabled ti_sysbios->
family_c64p_TimestampProvider_Module_diagsEnabled_C = (xdc_Bits32) 0x10
```

Definition at line 4537 of file dss_mrr_pe674.c.

4.16.4.205 `ti_sysbios_family_c64p_TimestampProvider_Module_diagsIncluded_C`

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_diagsIncluded ti_sysbios->
family_c64p_TimestampProvider_Module_diagsIncluded_C = (xdc_Bits32) 0x10
```

Definition at line 4541 of file dss_mrr_pe674.c.

4.16.4.206 ti_sysbios_family_c64p_TimestampProvider_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_diagsMask) ti_sysbios_<-
family_c64p_TimestampProvider_Module_diagsMask_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_diagsMask) 0)
```

Definition at line 4545 of file dss_mrr_pe674.c.

4.16.4.207 ti_sysbios_family_c64p_TimestampProvider_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_gateObj) ti_sysbios_<-
family_c64p_TimestampProvider_Module_gateObj_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_gateObj) 0)
```

Definition at line 4549 of file dss_mrr_pe674.c.

4.16.4.208 ti_sysbios_family_c64p_TimestampProvider_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_gatePrms) ti_sysbios_<-
family_c64p_TimestampProvider_Module_gatePrms_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_gatePrms) 0)
```

Definition at line 4553 of file dss_mrr_pe674.c.

4.16.4.209 ti_sysbios_family_c64p_TimestampProvider_Module_id_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_id) ti_sysbios_family_c64p_<-
TimestampProvider_Module_id_C = (xdc_Bits16)0x19
```

Definition at line 4557 of file dss_mrr_pe674.c.

4.16.4.210 ti_sysbios_family_c64p_TimestampProvider_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerDefined) ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerDefined_C = 0
```

Definition at line 4561 of file dss_mrr_pe674.c.

4.16.4.211 ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn0) ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerFxn0_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_loggerFxn0) 0)
```

Definition at line 4569 of file dss_mrr_pe674.c.

4.16.4.212 ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn1) ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerFxn1_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_loggerFxn1) 0)
```

Definition at line 4573 of file dss_mrr_pe674.c.

4.16.4.213 ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn2) ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerFxn2_C = ((CT(ti_sysbios_family_c64p_TimestampProvider_<-
Module_loggerFxn2) 0)
```

Definition at line 4577 of file dss_mrr_pe674.c.

4.16.4.214 ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn4 ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerFxn4_C = ((CT(ti_sysbios_family_c64p_Timestamp<-
Provider_Module_loggerFxn4) 0)
```

Definition at line 4581 of file dss_mrr_pe674.c.

4.16.4.215 ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerFxn8 ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerFxn8_C = ((CT(ti_sysbios_family_c64p_Timestamp<-
Provider_Module_loggerFxn8) 0)
```

Definition at line 4585 of file dss_mrr_pe674.c.

4.16.4.216 ti_sysbios_family_c64p_TimestampProvider_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Module_loggerObj ti_sysbios_<-
family_c64p_TimestampProvider_Module_loggerObj_C = ((CT(ti_sysbios_family_c64p_Timestamp<-
Provider_Module_loggerObj) 0)
```

Definition at line 4565 of file dss_mrr_pe674.c.

4.16.4.217 ti_sysbios_family_c64p_TimestampProvider_Object_count_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Object_count ti_sysbios_family_<-
c64p_TimestampProvider_Object_count_C = 0
```

Definition at line 4589 of file dss_mrr_pe674.c.

4.16.4.218 ti_sysbios_family_c64p_TimestampProvider_Object_heap_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Object_heap ti_sysbios_family_<-
c64p_TimestampProvider_Object_heap_C = 0
```

Definition at line 4593 of file dss_mrr_pe674.c.

4.16.4.219 ti_sysbios_family_c64p_TimestampProvider_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Object_sizeof ti_sysbios_family_<-
c64p_TimestampProvider_Object_sizeof_C = 0
```

Definition at line 4597 of file dss_mrr_pe674.c.

4.16.4.220 ti_sysbios_family_c64p_TimestampProvider_Object_table_C

```
const __FAR__ CT(ti_sysbios_family_c64p_TimestampProvider_Object_table ti_sysbios_family_<-
c64p_TimestampProvider_Object_table_C = 0
```

Definition at line 4601 of file dss_mrr_pe674.c.

4.16.4.221 ti_sysbios_gates_GateHwi_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_diagsEnabled ti_sysbios_gates_GateHwi_<-
Module_diagsEnabled_C = (xdc_Bits32) 0x90
```

Definition at line 4639 of file dss_mrr_pe674.c.

4.16.4.222 `ti_sysbios_gates_GateHwi_Module_diagsIncluded_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_diagsIncluded ti_sysbios_gates_GateHwi_←
Module_diagsIncluded_C = (xdc_Bits32) 0x90
Definition at line 4643 of file dss_mrr_pe674.c.
```

4.16.4.223 `ti_sysbios_gates_GateHwi_Module_diagsMask_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_diagsMask ti_sysbios_gates_GateHwi_Module_←
__diagsMask_C = ((CT__ti_sysbios_gates_GateHwi_Module_diagsMask) 0)
Definition at line 4647 of file dss_mrr_pe674.c.
```

4.16.4.224 `ti_sysbios_gates_GateHwi_Module_FXNS_C`

```
const ti_sysbios_gates_GateHwi_Fxns__ ti_sysbios_gates_GateHwi_Module_FXNS_C
```

Initial value:

```
= {
    &xdc_runtime_IGateProvider_Interface__BASE__C,
    &ti_sysbios_gates_GateHwi_Module_FXNS_C.__sfxns,
    ti_sysbios_gates_GateHwi_query__E,
    ti_sysbios_gates_GateHwi_enter__E,
    ti_sysbios_gates_GateHwi_leave__E,
    {
        ti_sysbios_gates_GateHwi_Object__create__S,
        ti_sysbios_gates_GateHwi_Object__delete__S,
        ti_sysbios_gates_GateHwi_Handle__label__S,
        0x2f,
    }
}
```

Definition at line 774 of file dss_mrr_pe674.c.

Referenced by `xdc_runtime_Main_Module_GateProxy_Proxy_delegate__S()`, and `xdc_runtime_System_←
Module_GateProxy_Proxy_delegate__S()`.

4.16.4.225 `ti_sysbios_gates_GateHwi_Module_gateObj_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_gateObj ti_sysbios_gates_GateHwi_Module_←
gateObj_C = ((CT__ti_sysbios_gates_GateHwi_Module_gateObj) 0)
```

Definition at line 4651 of file dss_mrr_pe674.c.

4.16.4.226 `ti_sysbios_gates_GateHwi_Module_gatePrms_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_gatePrms ti_sysbios_gates_GateHwi_Module_←
_gatePrms_C = ((CT__ti_sysbios_gates_GateHwi_Module_gatePrms) 0)
```

Definition at line 4655 of file dss_mrr_pe674.c.

4.16.4.227 `ti_sysbios_gates_GateHwi_Module_id_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_id ti_sysbios_gates_GateHwi_Module_id_C =
(xdc_Bits16) 0x2f
```

Definition at line 4659 of file dss_mrr_pe674.c.

4.16.4.228 `ti_sysbios_gates_GateHwi_Module_loggerDefined_C`

```
const __FAR__ CT__ti_sysbios_gates_GateHwi_Module_loggerDefined ti_sysbios_gates_GateHwi_←
Module_loggerDefined_C = 0
```

Definition at line 4663 of file dss_mrr_pe674.c.

4.16.4.229 ti_sysbios_gates_GateHwi_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerFxn0 ti_sysbios_gates_GateHwi_←
Module_loggerFxn0_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerFxn0) 0)
Definition at line 4671 of file dss_mrr_pe674.c.
```

4.16.4.230 ti_sysbios_gates_GateHwi_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerFxn1 ti_sysbios_gates_GateHwi_←
Module_loggerFxn1_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerFxn1) 0)
Definition at line 4675 of file dss_mrr_pe674.c.
```

4.16.4.231 ti_sysbios_gates_GateHwi_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerFxn2 ti_sysbios_gates_GateHwi_←
Module_loggerFxn2_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerFxn2) 0)
Definition at line 4679 of file dss_mrr_pe674.c.
```

4.16.4.232 ti_sysbios_gates_GateHwi_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerFxn4 ti_sysbios_gates_GateHwi_←
Module_loggerFxn4_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerFxn4) 0)
Definition at line 4683 of file dss_mrr_pe674.c.
```

4.16.4.233 ti_sysbios_gates_GateHwi_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerFxn8 ti_sysbios_gates_GateHwi_←
Module_loggerFxn8_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerFxn8) 0)
Definition at line 4687 of file dss_mrr_pe674.c.
```

4.16.4.234 ti_sysbios_gates_GateHwi_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Module_loggerObj ti_sysbios_gates_GateHwi_Module_←
__loggerObj_C = ((CT(ti_sysbios_gates_GateHwi_Module_loggerObj) 0)
Definition at line 4667 of file dss_mrr_pe674.c.
```

4.16.4.235 ti_sysbios_gates_GateHwi_Module_root_V

ti_sysbios_gates_GateHwi_Module ti_sysbios_gates_GateHwi_Module_root_V

Initial value:

```
= {
    {&ti_sysbios_gates_GateHwi_Module_root_V.link,
     &ti_sysbios_gates_GateHwi_Module_root_V.link},
}
```

Definition at line 4625 of file dss_mrr_pe674.c.

Referenced by **ti_sysbios_gates_GateHwi_Object_first_S()**, and **ti_sysbios_gates_GateHwi_Object_next_S()**.

4.16.4.236 ti_sysbios_gates_GateHwi_Object_count_C

```
const __FAR__ CT(ti_sysbios_gates_GateHwi_Object_count ti_sysbios_gates_GateHwi_Object_←
count_C = 1
```

Definition at line 4691 of file dss_mrr_pe674.c.

4.16.4.237 `ti_sysbios_gates_GateHwi_Object__DESC__C`

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_gates_GateHwi_Object__DESC__C
Initial value:
= {
    (xdc_CPtr)&ti_sysbios_gates_GateHwi_Module__FXNS__C,
    &ti_sysbios_gates_GateHwi_Module__root__V.link,
    sizeof(ti_sysbios_gates_GateHwi__S1) - sizeof(ti_sysbios_gates_GateHwi_Object2__),
    0,
    0,
    sizeof(ti_sysbios_gates_GateHwi_Object2__),
    (xdc_CPtr)&ti_sysbios_gates_GateHwi_Object__PARAMS__C,
    sizeof(ti_sysbios_gates_GateHwi_Params),
}
```

Definition at line 4609 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateHwi_construct()`, `ti_sysbios_gates_GateHwi_create()`, `ti_sysbios_gates_GateHwi_destruct()`, `ti_sysbios_gates_GateHwi_Object__create__S()`, and `ti_sysbios_gates_GateHwi_Object__delete__S()`.

4.16.4.238 `ti_sysbios_gates_GateHwi_Object__heap__C`

```
const __FAR__ CT_ti_sysbios_gates_GateHwi_Object__heap ti_sysbios_gates_GateHwi_Object__←
heap__C = 0
```

Definition at line 4695 of file dss_mrr_pe674.c.

4.16.4.239 `ti_sysbios_gates_GateHwi_Object__PARAMS__C`

```
const __FAR__ ti_sysbios_gates_GateHwi_Params ti_sysbios_gates_GateHwi_Object__PARAMS__C
Initial value:
= {
    sizeof(ti_sysbios_gates_GateHwi_Params),
    0,
    0,
    (xdc_runtime_IInstance_Parms*)&ti_sysbios_gates_GateHwi_Object__PARAMS__C.__iprms,
    {
        sizeof(xdc_runtime_IInstance_Parms),
        0,
    },
}
```

Definition at line 4613 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateHwi_Params__init__S()`.

4.16.4.240 `ti_sysbios_gates_GateHwi_Object__sizeof__C`

```
const __FAR__ CT_ti_sysbios_gates_GateHwi_Object__sizeof ti_sysbios_gates_GateHwi_Object__←
sizeof__C = sizeof(ti_sysbios_gates_GateHwi_Object__)
```

Definition at line 4699 of file dss_mrr_pe674.c.

4.16.4.241 `ti_sysbios_gates_GateHwi_Object__table__C`

```
const __FAR__ CT_ti_sysbios_gates_GateHwi_Object__table ti_sysbios_gates_GateHwi_Object__←
table__C = ti_sysbios_gates_GateHwi_Object__table__V
```

Definition at line 4703 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateHwi_Object__get__S()`.

4.16.4.242 `ti_sysbios_gates_GateHwi_Object__table__V`

```
ti_sysbios_gates_GateHwi_Object__ ti_sysbios_gates_GateHwi_Object__table__V
Initial value:
= {
    {
        &ti_sysbios_gates_GateHwi_Module__FXNS__C,
```

```

},
}

Definition at line 1427 of file dss_mrr_pe674.c.
```

4.16.4.243 ti_sysbios_gates_GateMutex_A_badContext_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_A_badContext ti_sysbios_gates_GateMutex_A_badContext_C = (((xdc_runtime_Assert_Id)0) << 16 | 16)
Definition at line 4838 of file dss_mrr_pe674.c.
```

4.16.4.244 ti_sysbios_gates_GateMutex_Instance_State_sem_O

```
const __FAR__ xdc_SizeT ti_sysbios_gates_GateMutex_Instance_State_sem_O = offsetof( ti_sysbios_gates_GateMutex_Object, Object_field_sem)
Definition at line 1935 of file dss_mrr_pe674.c.
```

4.16.4.245 ti_sysbios_gates_GateMutex_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_diagsEnabled ti_sysbios_gates_GateMutex_Module_diagsEnabled_C = (xdc_Bits32)0x90
Definition at line 4770 of file dss_mrr_pe674.c.
```

4.16.4.246 ti_sysbios_gates_GateMutex_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_diagsIncluded ti_sysbios_gates_GateMutex_Module_diagsIncluded_C = (xdc_Bits32)0x90
Definition at line 4774 of file dss_mrr_pe674.c.
```

4.16.4.247 ti_sysbios_gates_GateMutex_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_diagsMask ti_sysbios_gates_GateMutex_Module_diagsMask_C = ((CT(ti_sysbios_gates_GateMutex_Module_diagsMask) 0)
Definition at line 4778 of file dss_mrr_pe674.c.
```

4.16.4.248 ti_sysbios_gates_GateMutex_Module_FXNS_C

```
const ti_sysbios_gates_GateMutex_Fxns ti_sysbios_gates_GateMutex_Module_FXNS_C
Initial value:
= {
    &xdc_runtime_IGateProvider_Interface_BASE_C,
    &ti_sysbios_gates_GateMutex_Module_FXNS_C.sfxns,
    ti_sysbios_gates_GateMutex_query_E,
    ti_sysbios_gates_GateMutex_enter_E,
    ti_sysbios_gates_GateMutex_leave_E,
    {
        ti_sysbios_gates_GateMutex_Object_create_S,
        ti_sysbios_gates_GateMutex_Object_delete_S,
        ti_sysbios_gates_GateMutex_Handle_label_S,
        0x30,
    }
}
Definition at line 795 of file dss_mrr_pe674.c.
```

Referenced by ti_sysbios_BIOS_RtsGateProxy_Proxy_delegate_S(), and ti_sysbios_heaps_HeapMem_Module_GateProxy_Proxy_delegate_S().

4.16.4.249 ti_sysbios_gates_GateMutex_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_gateObj) ti_sysbios_gates_GateMutex_<-
Module_gateObj_C = ((CT(ti_sysbios_gates_GateMutex_Module_gateObj))0)
Definition at line 4782 of file dss_mrr_pe674.c.
```

4.16.4.250 ti_sysbios_gates_GateMutex_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_gatePrms) ti_sysbios_gates_GateMutex_<-
Module_gatePrms_C = ((CT(ti_sysbios_gates_GateMutex_Module_gatePrms))0)
Definition at line 4786 of file dss_mrr_pe674.c.
```

4.16.4.251 ti_sysbios_gates_GateMutex_Module_id_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_id) ti_sysbios_gates_GateMutex_Module_<-
id_C = (xdc_Bits16)0x30
Definition at line 4790 of file dss_mrr_pe674.c.
```

4.16.4.252 ti_sysbios_gates_GateMutex_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerDefined) ti_sysbios_gates_Gate<-
Mutex_Module_loggerDefined_C = 0
Definition at line 4794 of file dss_mrr_pe674.c.
```

4.16.4.253 ti_sysbios_gates_GateMutex_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerFxn0) ti_sysbios_gates_GateMutex_<-
Module_loggerFxn0_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerFxn0))0)
Definition at line 4802 of file dss_mrr_pe674.c.
```

4.16.4.254 ti_sysbios_gates_GateMutex_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerFxn1) ti_sysbios_gates_GateMutex_<-
Module_loggerFxn1_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerFxn1))0)
Definition at line 4806 of file dss_mrr_pe674.c.
```

4.16.4.255 ti_sysbios_gates_GateMutex_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerFxn2) ti_sysbios_gates_GateMutex_<-
Module_loggerFxn2_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerFxn2))0)
Definition at line 4810 of file dss_mrr_pe674.c.
```

4.16.4.256 ti_sysbios_gates_GateMutex_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerFxn4) ti_sysbios_gates_GateMutex_<-
Module_loggerFxn4_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerFxn4))0)
Definition at line 4814 of file dss_mrr_pe674.c.
```

4.16.4.257 ti_sysbios_gates_GateMutex_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerFxn8) ti_sysbios_gates_GateMutex_<-
Module_loggerFxn8_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerFxn8))0)
```

Definition at line 4818 of file dss_mrr_pe674.c.

4.16.4.258 ti_sysbios_gates_GateMutex_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Module_loggerObj) ti_sysbios_gates_GateMutex_<-
Module_loggerObj_C = ((CT(ti_sysbios_gates_GateMutex_Module_loggerObj) 0)
```

Definition at line 4798 of file dss_mrr_pe674.c.

4.16.4.259 ti_sysbios_gates_GateMutex_Module_root_V

```
ti_sysbios_gates_GateMutex_Module ti_sysbios_gates_GateMutex_Module_root_V
```

Initial value:

```
= {
    &ti_sysbios_gates_GateMutex_Module_root_V.link,
    &ti_sysbios_gates_GateMutex_Module_root_V.link},
}
```

Definition at line 4727 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateMutex_Object_first_S()`, and `ti_sysbios_gates_GateMutex_Object_next_S()`.

4.16.4.260 ti_sysbios_gates_GateMutex_Object_count_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Object_count) ti_sysbios_gates_GateMutex_Object->
__count_C = 2
```

Definition at line 4822 of file dss_mrr_pe674.c.

4.16.4.261 ti_sysbios_gates_GateMutex_Object_DESC_C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_gates_GateMutex_Object_DESC_C
```

Initial value:

```
= {
    (xdc_CPtr)&ti_sysbios_gates_GateMutex_Module_FXNS_C,
    &ti_sysbios_gates_GateMutex_Module_root_V.link,
    sizeof(ti_sysbios_gates_GateMutex_S1) - sizeof(ti_sysbios_gates_GateMutex_Object2),
    0,
    0,
    sizeof(ti_sysbios_gates_GateMutex_Object2),
    (xdc_CPtr)&ti_sysbios_gates_GateMutex_Object_PARAMS_C,
    sizeof(ti_sysbios_gates_GateMutex_Params),
}
```

Definition at line 4711 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateMutex_construct()`, `ti_sysbios_gates_GateMutex_create()`, `ti_sysbios_gates_GateMutex_destruct()`, `ti_sysbios_gates_GateMutex_Object_create_S()`, and `ti_sysbios_gates_GateMutex_Object_delete_S()`.

4.16.4.262 ti_sysbios_gates_GateMutex_Object_heap_C

```
const __FAR__ CT(ti_sysbios_gates_GateMutex_Object_heap) ti_sysbios_gates_GateMutex_Object->
__heap_C = 0
```

Definition at line 4826 of file dss_mrr_pe674.c.

4.16.4.263 ti_sysbios_gates_GateMutex_Object_PARAMS_C

```
const __FAR__ ti_sysbios_gates_GateMutex_Params ti_sysbios_gates_GateMutex_Object_PARAMS_C
```

Initial value:

```
= {
    sizeof(ti_sysbios_gates_GateMutex_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_gates_GateMutex_Object_PARAMS_C.__iprms,
```

```

    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}

```

Definition at line 4715 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateMutex_Params_init_S()`.

4.16.4.264 `ti_sysbios_gates_GateMutex_Object_sizeof_C`

```
const __FAR__ CT__ti_sysbios_gates_GateMutex_Object_sizeof ti_sysbios_gates_GateMutex_Object_sizeof_C = sizeof( ti_sysbios_gates_GateMutex_Object )
```

Definition at line 4830 of file dss_mrr_pe674.c.

4.16.4.265 `ti_sysbios_gates_GateMutex_Object_table_C`

```
const __FAR__ CT__ti_sysbios_gates_GateMutex_Object_table ti_sysbios_gates_GateMutex_Object_table_C = ti_sysbios_gates_GateMutex_Object_table_V
```

Definition at line 4834 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_gates_GateMutex_Object_get_S()`.

4.16.4.266 `ti_sysbios_gates_GateMutex_Object_table_V`

```
ti_sysbios_gates_GateMutex_Object ti_sysbios_gates_GateMutex_Object_table_V
```

Definition at line 1435 of file dss_mrr_pe674.c.

4.16.4.267 `ti_sysbios_hal_Hwi_dispatcherAutoNestingSupport_C`

```
const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherAutoNestingSupport ti_sysbios_hal_Hwi_dispatcherAutoNestingSupport_C = 1
```

Definition at line 4950 of file dss_mrr_pe674.c.

4.16.4.268 `ti_sysbios_hal_Hwi_dispatcherIrpTrackingSupport_C`

```
const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherIrpTrackingSupport ti_sysbios_hal_Hwi_dispatcherIrpTrackingSupport_C = 1
```

Definition at line 4962 of file dss_mrr_pe674.c.

4.16.4.269 `ti_sysbios_hal_Hwi_dispatcherSwiSupport_C`

```
const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherSwiSupport ti_sysbios_hal_Hwi_dispatcherSwiSupport_C = 0
```

Definition at line 4954 of file dss_mrr_pe674.c.

4.16.4.270 `ti_sysbios_hal_Hwi_dispatcherTaskSupport_C`

```
const __FAR__ CT__ti_sysbios_hal_Hwi_dispatcherTaskSupport ti_sysbios_hal_Hwi_dispatcherTaskSupport_C = 1
```

Definition at line 4958 of file dss_mrr_pe674.c.

4.16.4.271 ti_sysbios_hal_Hwi_E_stackOverflow__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_E_stackOverflow ti_sysbios_hal_Hwi_E_stackOverflow__C =
(((xdc_runtime_Error_Id)29) << 16 | 0)
Definition at line 4966 of file dss_mrr_pe674.c.
```

4.16.4.272 ti_sysbios_hal_Hwi_HwiProxy_Module_root__V

```
ti_sysbios_hal_Hwi_HwiProxy_Module ti_sysbios_hal_Hwi_HwiProxy_Module_root__V
```

4.16.4.273 ti_sysbios_hal_Hwi_Module_diagsEnabled__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_diagsEnabled ti_sysbios_hal_Hwi_Module_diagsEnabled__C =
Enabled__C = (xdc_Bits32)0x90
Definition at line 4882 of file dss_mrr_pe674.c.
```

4.16.4.274 ti_sysbios_hal_Hwi_Module_diagsIncluded__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_diagsIncluded ti_sysbios_hal_Hwi_Module_diagsIncluded__C =
Included__C = (xdc_Bits32)0x90
Definition at line 4886 of file dss_mrr_pe674.c.
```

4.16.4.275 ti_sysbios_hal_Hwi_Module_diagsMask__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_diagsMask ti_sysbios_hal_Hwi_Module_diagsMask__C =
_C = ((CT(ti_sysbios_hal_Hwi_Module_diagsMask) 0)
Definition at line 4890 of file dss_mrr_pe674.c.
```

4.16.4.276 ti_sysbios_hal_Hwi_Module_gateObj__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_gateObj ti_sysbios_hal_Hwi_Module_gateObj__C =
((CT(ti_sysbios_hal_Hwi_Module_gateObj) 0)
Definition at line 4894 of file dss_mrr_pe674.c.
```

4.16.4.277 ti_sysbios_hal_Hwi_Module_gatePrms__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_gatePrms ti_sysbios_hal_Hwi_Module_gatePrms__C =
((CT(ti_sysbios_hal_Hwi_Module_gatePrms) 0)
Definition at line 4898 of file dss_mrr_pe674.c.
```

4.16.4.278 ti_sysbios_hal_Hwi_Module_id__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_id ti_sysbios_hal_Hwi_Module_id__C = (xdc_Bits16)0x2b
Definition at line 4902 of file dss_mrr_pe674.c.
```

4.16.4.279 ti_sysbios_hal_Hwi_Module_loggerDefined__C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerDefined ti_sysbios_hal_Hwi_Module_loggerDefined__C =
Defined__C = 0
Definition at line 4906 of file dss_mrr_pe674.c.
```

4.16.4.280 ti_sysbios_hal_Hwi_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerFxn0) ti_sysbios_hal_Hwi_Module_logger←
Fxn0_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerFxn0) 0))
```

Definition at line 4914 of file dss_mrr_pe674.c.

4.16.4.281 ti_sysbios_hal_Hwi_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerFxn1) ti_sysbios_hal_Hwi_Module_logger←
Fxn1_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerFxn1) 0))
```

Definition at line 4918 of file dss_mrr_pe674.c.

4.16.4.282 ti_sysbios_hal_Hwi_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerFxn2) ti_sysbios_hal_Hwi_Module_logger←
Fxn2_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerFxn2) 0))
```

Definition at line 4922 of file dss_mrr_pe674.c.

4.16.4.283 ti_sysbios_hal_Hwi_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerFxn4) ti_sysbios_hal_Hwi_Module_logger←
Fxn4_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerFxn4) 0))
```

Definition at line 4926 of file dss_mrr_pe674.c.

4.16.4.284 ti_sysbios_hal_Hwi_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerFxn8) ti_sysbios_hal_Hwi_Module_logger←
Fxn8_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerFxn8) 0))
```

Definition at line 4930 of file dss_mrr_pe674.c.

4.16.4.285 ti_sysbios_hal_Hwi_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Module_loggerObj) ti_sysbios_hal_Hwi_Module_loggerObj←
_C = ((CT(ti_sysbios_hal_Hwi_Module_loggerObj) 0))
```

Definition at line 4910 of file dss_mrr_pe674.c.

4.16.4.286 ti_sysbios_hal_Hwi_Module_root_V

ti_sysbios_hal_Hwi_Module ti_sysbios_hal_Hwi_Module_root_V

Initial value:

```
= {
    {&ti_sysbios_hal_Hwi_Module_root_V.link,
     &ti_sysbios_hal_Hwi_Module_root_V.link},
}
```

Definition at line 4867 of file dss_mrr_pe674.c.

Referenced by **ti_sysbios_hal_Hwi_Object_first_S()**, and **ti_sysbios_hal_Hwi_Object_next_S()**.

4.16.4.287 ti_sysbios_hal_Hwi_Object_count_C

```
const __FAR__ CT(ti_sysbios_hal_Hwi_Object_count) ti_sysbios_hal_Hwi_Object_count_C = 1
```

Definition at line 4934 of file dss_mrr_pe674.c.

4.16.4.288 ti_sysbios_hal_Hwi_Object__DESC__C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_hal_Hwi_Object__DESC__C
Initial value:
= {
    (xdc_CPtr)0,
    &ti_sysbios_hal_Hwi_Module_root_V.link,
    sizeof(ti_sysbios_hal_Hwi__S1) - sizeof(ti_sysbios_hal_Hwi_Object2__),
    0,
    0,
    sizeof(ti_sysbios_hal_Hwi_Object2__),
    (xdc_CPtr)&ti_sysbios_hal_Hwi_Object__PARAMS__C,
    sizeof(ti_sysbios_hal_Hwi_Params),
}
```

Definition at line 4846 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_hal_Hwi_construct(), ti_sysbios_hal_Hwi_create(), ti_sysbios_hal_Hwi_destruct(), ti_sysbios_hal_Hwi_Object__create__S(), and ti_sysbios_hal_Hwi_Object__delete__S().

4.16.4.289 ti_sysbios_hal_Hwi_Object__heap__C

```
const __FAR__ CT_ti_sysbios_hal_Hwi_Object__heap ti_sysbios_hal_Hwi_Object__heap__C = 0
Definition at line 4938 of file dss_mrr_pe674.c.
```

4.16.4.290 ti_sysbios_hal_Hwi_Object__PARAMS__C

```
const __FAR__ ti_sysbios_hal_Hwi_Params ti_sysbios_hal_Hwi_Object__PARAMS__C
Initial value:
= {
    sizeof(ti_sysbios_hal_Hwi_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_hal_Hwi_Object__PARAMS__C.__iprms,
    ti_sysbios_interfaces_IHwi_MaskingOption_SELF,
    ((xdc_UArg)(0x0)),
    1,
    (xdc_Int)(-0x0 - 1),
    (xdc_Int)(-0x0 - 1),
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 4850 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_hal_Hwi_Params__init__S().

4.16.4.291 ti_sysbios_hal_Hwi_Object__sizeof__C

```
const __FAR__ CT_ti_sysbios_hal_Hwi_Object__sizeof ti_sysbios_hal_Hwi_Object__sizeof__C =
sizeof(ti_sysbios_hal_Hwi_Object__)
Definition at line 4942 of file dss_mrr_pe674.c.
```

4.16.4.292 ti_sysbios_hal_Hwi_Object__table__C

```
const __FAR__ CT_ti_sysbios_hal_Hwi_Object__table ti_sysbios_hal_Hwi_Object__table__C = ti↔
_sysbios_hal_Hwi_Object_table_V
Definition at line 4946 of file dss_mrr_pe674.c.
Referenced by ti_sysbios_hal_Hwi_Object__get__S().
```

4.16.4.293 ti_sysbios_hal_Hwi_Object__table__V

```
ti_sysbios_hal_Hwi_Object__ ti_sysbios_hal_Hwi_Object__table__V
Initial value:
= {
```

```
{
    0,
    (ti_sysbios_hal_Hwi_HwiProxy_Handle)&ti_sysbios_family_c64p_Hwi_Object__table__V[4],
},
}
```

Definition at line 1443 of file dss_mrr_pe674.c.

4.16.4.294 ti_sysbios_heaps_HeapMem_A_align_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_A_align ti_sysbios_heaps_HeapMem_A_align__C = (((xdc←
(runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5098 of file dss_mrr_pe674.c.

4.16.4.295 ti_sysbios_heaps_HeapMem_A_heapSize_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_A_heapSize ti_sysbios_heaps_HeapMem_A_heapSize__C = (
((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5094 of file dss_mrr_pe674.c.

4.16.4.296 ti_sysbios_heaps_HeapMem_A_invalidFree_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_A_invalidFree ti_sysbios_heaps_HeapMem_A_invalid←
Free__C = ((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5106 of file dss_mrr_pe674.c.

4.16.4.297 ti_sysbios_heaps_HeapMem_A_zeroBlock_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_A_zeroBlock ti_sysbios_heaps_HeapMem_A_zeroBlock←
__C = ((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5090 of file dss_mrr_pe674.c.

4.16.4.298 ti_sysbios_heaps_HeapMem_E_memory_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_E_memory ti_sysbios_heaps_HeapMem_E_memory__C =
(((xdc_runtime Error_Id)27) << 16 | 0)
```

Definition at line 5102 of file dss_mrr_pe674.c.

4.16.4.299 ti_sysbios_heaps_HeapMem_Instance_State_0_buf_A

```
__T1(ti_sysbios_heaps_HeapMem_Instance_State_buf ti_sysbios_heaps_HeapMem_Instance_State_0←
buf__A
```

Definition at line 1459 of file dss_mrr_pe674.c.

4.16.4.300 ti_sysbios_heaps_HeapMem_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_diagsEnabled ti_sysbios_heaps_HeapMem←
Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 5022 of file dss_mrr_pe674.c.

4.16.4.301 ti_sysbios_heaps_HeapMem_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_diagsIncluded ti_sysbios_heaps_HeapMem←
Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 5026 of file dss_mrr_pe674.c.

4.16.4.302 ti_sysbios_heaps_HeapMem_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_diagsMask ti_sysbios_heaps_HeapMem_Module->diagsMask_C = ((CT(ti_sysbios_heaps_HeapMem_Module_diagsMask) 0)
```

Definition at line 5030 of file dss_mrr_pe674.c.

4.16.4.303 ti_sysbios_heaps_HeapMem_Module_FXNS_C

```
const ti_sysbios_heaps_HeapMem_Fxns ti_sysbios_heaps_HeapMem_Module_FXNS_C
```

Initial value:

```
= {
    &xdc_runtime_IHeap_Interface__BASE_C,
    &ti_sysbios_heaps_HeapMem_Module_FXNS_C._sfxns,
    ti_sysbios_heaps_HeapMem_alloc_E,
    ti_sysbios_heaps_HeapMem_free_E,
    ti_sysbios_heaps_HeapMem_isBlocking_E,
    ti_sysbios_heaps_HeapMem_getStats_E,
    {
        ti_sysbios_heaps_HeapMem_Object_create_S,
        ti_sysbios_heaps_HeapMem_Object_delete_S,
        ti_sysbios_heaps_HeapMem_Handle_label_S,
        0x27,
    }
}
```

Definition at line 816 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_Memory_HeapProxy_Proxy_delegate_S().

4.16.4.304 ti_sysbios_heaps_HeapMem_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_gateObj ti_sysbios_heaps_HeapMem_Module->gateObj_C = ((CT(ti_sysbios_heaps_HeapMem_Module_gateObj) ((const void*) (xdc_runtime_IGate->Provider_Handle) & ti_sysbios_gates_GateMutex_Object_table_V[0])))
```

Definition at line 5034 of file dss_mrr_pe674.c.

4.16.4.305 ti_sysbios_heaps_HeapMem_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_gatePrms ti_sysbios_heaps_HeapMem_Module->gatePrms_C = ((CT(ti_sysbios_heaps_HeapMem_Module_gatePrms) 0))
```

Definition at line 5038 of file dss_mrr_pe674.c.

4.16.4.306 ti_sysbios_heaps_HeapMem_Module_id_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_id ti_sysbios_heaps_HeapMem_Module_id_C = (xdc_Bits16) 0x27)
```

Definition at line 5042 of file dss_mrr_pe674.c.

4.16.4.307 ti_sysbios_heaps_HeapMem_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerDefined ti_sysbios_heaps_HeapMem->Module_loggerDefined_C = 0
```

Definition at line 5046 of file dss_mrr_pe674.c.

4.16.4.308 ti_sysbios_heaps_HeapMem_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn0 ti_sysbios_heaps_HeapMem_Module_loggerFxn0_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn0) 0))
Definition at line 5054 of file dss_mrr_pe674.c.
```

4.16.4.309 ti_sysbios_heaps_HeapMem_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn1 ti_sysbios_heaps_HeapMem_Module_loggerFxn1_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn1) 0))
Definition at line 5058 of file dss_mrr_pe674.c.
```

4.16.4.310 ti_sysbios_heaps_HeapMem_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn2 ti_sysbios_heaps_HeapMem_Module_loggerFxn2_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn2) 0))
Definition at line 5062 of file dss_mrr_pe674.c.
```

4.16.4.311 ti_sysbios_heaps_HeapMem_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn4 ti_sysbios_heaps_HeapMem_Module_loggerFxn4_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn4) 0))
Definition at line 5066 of file dss_mrr_pe674.c.
```

4.16.4.312 ti_sysbios_heaps_HeapMem_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn8 ti_sysbios_heaps_HeapMem_Module_loggerFxn8_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerFxn8) 0))
Definition at line 5070 of file dss_mrr_pe674.c.
```

4.16.4.313 ti_sysbios_heaps_HeapMem_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Module_loggerObj ti_sysbios_heaps_HeapMem_Module_loggerObj_C = ((CT(ti_sysbios_heaps_HeapMem_Module_loggerObj) 0))
Definition at line 5050 of file dss_mrr_pe674.c.
```

4.16.4.314 ti_sysbios_heaps_HeapMem_Module_root_V

ti_sysbios_heaps_HeapMem_Module ti_sysbios_heaps_HeapMem_Module_root_V

Initial value:

```
= {
    {&ti_sysbios_heaps_HeapMem_Module_root_V.link,
     &ti_sysbios_heaps_HeapMem_Module_root_V.link},
}
```

Definition at line 5001 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_heaps_HeapMem_Object_first_S()`, and `ti_sysbios_heaps_HeapMem_Object_next_S()`.

4.16.4.315 ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_root_V

```
ti_sysbios_heaps_HeapMem_Module_GateProxy_Module ti_sysbios_heaps_HeapMem_Module_GateProxy_Module_root_V
```

4.16.4.316 ti_sysbios_heaps_HeapMem_Object_count_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Object_count ti_sysbios_heaps_HeapMem_Object__C)
count_C = 1
```

Definition at line 5074 of file dss_mrr_pe674.c.

4.16.4.317 ti_sysbios_heaps_HeapMem_Object_DESC_C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_heaps_HeapMem_Object_DESC_C
```

Initial value:

```
= {
    (xdc_CPtr)&ti_sysbios_heaps_HeapMem_Module_FXNS_C,
    &ti_sysbios_heaps_HeapMem_Module_root_V.link,
    sizeof(ti_sysbios_heaps_HeapMem_S1) - sizeof(ti_sysbios_heaps_HeapMem_Object2),
    0,
    0,
    sizeof(ti_sysbios_heaps_HeapMem_Object2),
    (xdc_CPtr)&ti_sysbios_heaps_HeapMem_Object_PARAMS_C,
    sizeof(ti_sysbios_heaps_HeapMem_Params),
}
```

Definition at line 4979 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_heaps_HeapMem_construct(), ti_sysbios_heaps_HeapMem_create(), ti_sysbios_heaps_HeapMem_destruct(), ti_sysbios_heaps_HeapMem_Object_create_S(), and ti_sysbios_heaps_HeapMem_Object_delete_S().

4.16.4.318 ti_sysbios_heaps_HeapMem_Object_heap_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Object_heap ti_sysbios_heaps_HeapMem_Object__C)
heap_C = 0
```

Definition at line 5078 of file dss_mrr_pe674.c.

4.16.4.319 ti_sysbios_heaps_HeapMem_Object_PARAMS_C

```
const __FAR__ ti_sysbios_heaps_HeapMem_Params ti_sysbios_heaps_HeapMem_Object_PARAMS_C
```

Initial value:

```
= {
    sizeof(ti_sysbios_heaps_HeapMem_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_heaps_HeapMem_Object_PARAMS_C.iprms,
    (xdc_SizedT)0x0,
    ((xdc_Ptr)(0x0)),
    ((xdc_UArg)(0x0)),
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 4983 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_heaps_HeapMem_Params_init_S().

4.16.4.320 ti_sysbios_heaps_HeapMem_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Object_sizeof ti_sysbios_heaps_HeapMem_Object__C)
sizeof_C = sizeof(ti_sysbios_heaps_HeapMem_Object__)
```

Definition at line 5082 of file dss_mrr_pe674.c.

4.16.4.321 ti_sysbios_heaps_HeapMem_Object_table_C

```
const __FAR__ CT(ti_sysbios_heaps_HeapMem_Object_table ti_sysbios_heaps_HeapMem_Object__C)
table_C = ti_sysbios_heaps_HeapMem_Object_table_V
```

Definition at line 5086 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_heaps_HeapMem_Object__get__S()`.

4.16.4.322 `ti_sysbios_heaps_HeapMem_Object__table__V`

`ti_sysbios_heaps_HeapMem_Object__ ti_sysbios_heaps_HeapMem_Object__table__V`

Initial value:

```
= {
    {
        &ti_sysbios_heaps_HeapMem_Module__FXNS__C,
        ((xdc_UArg) (0x8)),
        ((void*)ti_sysbios_heaps_HeapMem_Instance_State_0_buf__A),
        {
            ((ti_sysbios_heaps_HeapMem_Header*) 0),
            ((xdc_UArg) (0x4000)),
        },
        (xdc_SizeT) 0x8,
    },
}
```

Definition at line 1478 of file `dss_mrr_pe674.c`.

4.16.4.323 `ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr__C`

```
const __FAR__ CT__ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr ti_sysbios_heaps_HeapMem__-
primaryHeapBaseAddr__C = ((CT__ti_sysbios_heaps_HeapMem_primaryHeapBaseAddr) 0)
```

Definition at line 5110 of file `dss_mrr_pe674.c`.

4.16.4.324 `ti_sysbios_heaps_HeapMem_primaryHeapEndAddr__C`

```
const __FAR__ CT__ti_sysbios_heaps_HeapMem_primaryHeapEndAddr ti_sysbios_heaps_HeapMem__-
primaryHeapEndAddr__C = ((CT__ti_sysbios_heaps_HeapMem_primaryHeapEndAddr) 0)
```

Definition at line 5114 of file `dss_mrr_pe674.c`.

4.16.4.325 `ti_sysbios_heaps_HeapMem_reqAlign__C`

```
const __FAR__ CT__ti_sysbios_heaps_HeapMem_reqAlign ti_sysbios_heaps_HeapMem_reqAlign__C =
(xdc_SizeT) 0x8
```

Definition at line 5118 of file `dss_mrr_pe674.c`.

4.16.4.326 `ti_sysbios_knl_Clock_A_badThreadType__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_A_badThreadType ti_sysbios_knl_Clock_A_badThreadType__-
__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 5271 of file `dss_mrr_pe674.c`.

4.16.4.327 `ti_sysbios_knl_Clock_A_clockDisabled__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_A_clockDisabled ti_sysbios_knl_Clock_A_clockDisabled__-
__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 5267 of file `dss_mrr_pe674.c`.

4.16.4.328 `ti_sysbios_knl_Clock_doTickFunc__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_doTickFunc ti_sysbios_knl_Clock_doTickFunc__C = ((CT__-
ti_sysbios_knl_Clock_doTickFunc) ((xdc_Fxn) ti_sysbios_knl_Clock_doTick__I))
```

Definition at line 5295 of file `dss_mrr_pe674.c`.

4.16.4.329 ti_sysbios_knl_Clock_LM_begin__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_LM_begin) ti_sysbios_knl_Clock_LM_begin__C = (((xdc←
runtime_Log_Event)16) << 16 | 768)
```

Definition at line 5263 of file dss_mrr_pe674.c.

4.16.4.330 ti_sysbios_knl_Clock_LM_tick__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_LM_tick) ti_sysbios_knl_Clock_LM_tick__C = (((xdc←
runtime_Log_Event)15) << 16 | 768)
```

Definition at line 5259 of file dss_mrr_pe674.c.

4.16.4.331 ti_sysbios_knl_Clock_LW_delayed__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_LW_delayed) ti_sysbios_knl_Clock_LW_delayed__C = (((xdc←
(runtime_Log_Event)14) << 16 | 1024)
```

Definition at line 5255 of file dss_mrr_pe674.c.

4.16.4.332 ti_sysbios_knl_Clock_Module_diagsEnabled__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_diagsEnabled) ti_sysbios_knl_Clock_Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 5187 of file dss_mrr_pe674.c.

4.16.4.333 ti_sysbios_knl_Clock_Module_diagsIncluded__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_diagsIncluded) ti_sysbios_knl_Clock_Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 5191 of file dss_mrr_pe674.c.

4.16.4.334 ti_sysbios_knl_Clock_Module_diagsMask__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_diagsMask) ti_sysbios_knl_Clock_Module_diagsMask__C = ((CT(ti_sysbios_knl_Clock_Module_diagsMask))0)
```

Definition at line 5195 of file dss_mrr_pe674.c.

4.16.4.335 ti_sysbios_knl_Clock_Module_gateObj__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_gateObj) ti_sysbios_knl_Clock_Module_gateObj__C = ((CT(ti_sysbios_knl_Clock_Module_gateObj))0)
```

Definition at line 5199 of file dss_mrr_pe674.c.

4.16.4.336 ti_sysbios_knl_Clock_Module_gatePrms__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_gatePrms) ti_sysbios_knl_Clock_Module_gatePrms__C = ((CT(ti_sysbios_knl_Clock_Module_gatePrms))0)
```

Definition at line 5203 of file dss_mrr_pe674.c.

4.16.4.337 ti_sysbios_knl_Clock_Module_id__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_Module_id) ti_sysbios_knl_Clock_Module_id__C = (xdc←
Bits16)0x1d
```

Definition at line 5207 of file dss_mrr_pe674.c.

4.16.4.338 `ti_sysbios_knl_Clock_Module__loggerDefined__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerDefined ti_sysbios_knl_Clock_Module__←
loggerDefined__C = 0
```

Definition at line 5211 of file dss_mrr_pe674.c.

4.16.4.339 `ti_sysbios_knl_Clock_Module__loggerFxn0__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerFxn0 ti_sysbios_knl_Clock_Module__←
loggerFxn0__C = ((CT__ti_sysbios_knl_Clock_Module__loggerFxn0) 0)
```

Definition at line 5219 of file dss_mrr_pe674.c.

4.16.4.340 `ti_sysbios_knl_Clock_Module__loggerFxn1__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerFxn1 ti_sysbios_knl_Clock_Module__←
loggerFxn1__C = ((CT__ti_sysbios_knl_Clock_Module__loggerFxn1) 0)
```

Definition at line 5223 of file dss_mrr_pe674.c.

4.16.4.341 `ti_sysbios_knl_Clock_Module__loggerFxn2__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerFxn2 ti_sysbios_knl_Clock_Module__←
loggerFxn2__C = ((CT__ti_sysbios_knl_Clock_Module__loggerFxn2) 0)
```

Definition at line 5227 of file dss_mrr_pe674.c.

4.16.4.342 `ti_sysbios_knl_Clock_Module__loggerFxn4__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerFxn4 ti_sysbios_knl_Clock_Module__←
loggerFxn4__C = ((CT__ti_sysbios_knl_Clock_Module__loggerFxn4) 0)
```

Definition at line 5231 of file dss_mrr_pe674.c.

4.16.4.343 `ti_sysbios_knl_Clock_Module__loggerFxn8__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerFxn8 ti_sysbios_knl_Clock_Module__←
loggerFxn8__C = ((CT__ti_sysbios_knl_Clock_Module__loggerFxn8) 0)
```

Definition at line 5235 of file dss_mrr_pe674.c.

4.16.4.344 `ti_sysbios_knl_Clock_Module__loggerObj__C`

```
const __FAR__ CT__ti_sysbios_knl_Clock_Module__loggerObj ti_sysbios_knl_Clock_Module__logger←
Obj__C = ((CT__ti_sysbios_knl_Clock_Module__loggerObj) 0)
```

Definition at line 5215 of file dss_mrr_pe674.c.

4.16.4.345 `ti_sysbios_knl_Clock_Module__root__V`

`ti_sysbios_knl_Clock_Module__ ti_sysbios_knl_Clock_Module__root__V`

Initial value:

```
= {
    &ti_sysbios_knl_Clock_Module__root__V.link,
    &ti_sysbios_knl_Clock_Module__root__V.link,
}
```

Definition at line 5150 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Clock_Object__first__S()`, and `ti_sysbios_knl_Clock_Object__next__S()`.

4.16.4.346 `ti_sysbios_knl_Clock_Module__state__V`

```
ti_sysbios_knl_Clock_Module_State ti_sysbios_knl_Clock_Module__state__V
Initial value:
= {
    (xdc_UInt32) 0x0,
    (xdc_UInt) 0x0,
    (ti_sysbios_knl_Clock_TimerProxy_Handle)&ti_sysbios_timers_rti_Timer_Object__table__V[0],
    0,
    (xdc_UInt) 0x1,
    (xdc_UInt32) 0x1,
    (xdc_UInt32) 0x0,
    0,
    0,
    0,
    {
        {
            ((ti_sysbios_knl_Queue_Elem*)((void*)&ti_sysbios_knl_Clock_Module__state__V.Object_field_clockQ.elem)),
            ((ti_sysbios_knl_Queue_Elem*)((void*)&ti_sysbios_knl_Clock_Module__state__V.Object_field_clockQ.elem)),
        },
    },
}
```

Definition at line 1506 of file `dss_mrr_pe674.c`.

Referenced by `ti_sysbios_knl_Clock_doTick__I()`.

4.16.4.347 `ti_sysbios_knl_Clock_Module_State_clockQ__O`

```
const __FAR__ xdc_SizeT ti_sysbios_knl_Clock_Module_State_clockQ__O = offsetof( ti_sysbios_knl_Clock_Module_State, Object_field_clockQ)
Definition at line 1943 of file dss_mrr_pe674.c.
```

4.16.4.348 `ti_sysbios_knl_Clock_Object__count__C`

```
const __FAR__ CT_ti_sysbios_knl_Clock_Object__count ti_sysbios_knl_Clock_Object__count__C = 0
Definition at line 5239 of file dss_mrr_pe674.c.
```

4.16.4.349 `ti_sysbios_knl_Clock_Object__DESC__C`

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_knl_Clock_Object__DESC__C
Initial value:
= {
    (xdc_CPtr)-1,
    &ti_sysbios_knl_Clock_Module__root__V.link,
    sizeof(ti_sysbios_knl_Clock__S1) - sizeof(ti_sysbios_knl_Clock_Object2__),
    0,
    0,
    sizeof(ti_sysbios_knl_Clock_Object2__),
    (xdc_CPtr)&ti_sysbios_knl_Clock_Object__PARAMS__C,
    sizeof(ti_sysbios_knl_Clock_Params),
}
```

Definition at line 5131 of file `dss_mrr_pe674.c`.

Referenced by `ti_sysbios_knl_Clock_construct()`, `ti_sysbios_knl_Clock_create()`, `ti_sysbios_knl_Clock_destruct()`, `ti_sysbios_knl_Clock_Object__create__S()`, and `ti_sysbios_knl_Clock_Object__delete__S()`.

4.16.4.350 `ti_sysbios_knl_Clock_Object__heap__C`

```
const __FAR__ CT_ti_sysbios_knl_Clock_Object__heap ti_sysbios_knl_Clock_Object__heap__C = 0
Definition at line 5243 of file dss_mrr_pe674.c.
```

4.16.4.351 ti_sysbios_knl_Clock_Object__PARAMS__C

```
const __FAR__ ti_sysbios_knl_Clock_Params ti_sysbios_knl_Clock_Object__PARAMS__C
Initial value:
= {
    sizeof(ti_sysbios_knl_Clock_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_knl_Clock_Object__PARAMS__C.__iprms,
    0,
    (xdc_UInt32)0x0,
    ((xdc_UArg)0),
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 5135 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Clock_Params__init__S().

4.16.4.352 ti_sysbios_knl_Clock_Object__sizeof__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_Object__sizeof ti_sysbios_knl_Clock_Object__sizeof__C =
sizeof(ti_sysbios_knl_Clock_Object__)
```

Definition at line 5247 of file dss_mrr_pe674.c.

4.16.4.353 ti_sysbios_knl_Clock_Object__table__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_Object__table ti_sysbios_knl_Clock_Object__table__C = 0
Definition at line 5251 of file dss_mrr_pe674.c.
```

4.16.4.354 ti_sysbios_knl_Clock_serviceMargin__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_serviceMargin ti_sysbios_knl_Clock_serviceMargin__C =
(xdc_UInt32)0x0
```

Definition at line 5275 of file dss_mrr_pe674.c.

4.16.4.355 ti_sysbios_knl_Clock_tickMode__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_tickMode ti_sysbios_knl_Clock_tickMode__C = ti_sysbios_knl_Clock_TickMode_PERIODIC
```

Definition at line 5283 of file dss_mrr_pe674.c.

4.16.4.356 ti_sysbios_knl_Clock_tickPeriod__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_tickPeriod ti_sysbios_knl_Clock_tickPeriod__C = (xdc_UInt32)0x3e8
```

Definition at line 5291 of file dss_mrr_pe674.c.

4.16.4.357 ti_sysbios_knl_Clock_tickSource__C

```
const __FAR__ CT	ti_sysbios_knl_Clock_tickSource ti_sysbios_knl_Clock_tickSource__C = ti_sysbios_knl_Clock_TickSource_TIMER
```

Definition at line 5279 of file dss_mrr_pe674.c.

4.16.4.358 ti_sysbios_knl_Clock_timerId__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_timerId) ti_sysbios_knl_Clock_timerId__C = ((xdc_U←
Int)(-0x0 - 1)
```

Definition at line 5287 of file dss_mrr_pe674.c.

4.16.4.359 ti_sysbios_knl_Clock_TimerProxy_Module_root__V

```
ti_sysbios_knl_Clock_TimerProxy_Module ti_sysbios_knl_Clock_TimerProxy_Module_root__V
```

4.16.4.360 ti_sysbios_knl_Clock_triggerClock__C

```
const __FAR__ CT(ti_sysbios_knl_Clock_triggerClock) ti_sysbios_knl_Clock_triggerClock__C = 0
Definition at line 5299 of file dss_mrr_pe674.c.
```

4.16.4.361 ti_sysbios_knl_Event_A_badContext__C

```
const __FAR__ CT(ti_sysbios_knl_Event_A_badContext) ti_sysbios_knl_Event_A_badContext__C =
(((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5423 of file dss_mrr_pe674.c.

4.16.4.362 ti_sysbios_knl_Event_A_eventInUse__C

```
const __FAR__ CT(ti_sysbios_knl_Event_A_eventInUse) ti_sysbios_knl_Event_A_eventInUse__C =
(((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5419 of file dss_mrr_pe674.c.

4.16.4.363 ti_sysbios_knl_Event_A_nullEventId__C

```
const __FAR__ CT(ti_sysbios_knl_Event_A_nullEventId) ti_sysbios_knl_Event_A_nullEventId__C =
(((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5415 of file dss_mrr_pe674.c.

4.16.4.364 ti_sysbios_knl_Event_A_nullEventMasks__C

```
const __FAR__ CT(ti_sysbios_knl_Event_A_nullEventMasks) ti_sysbios_knl_Event_A_nullEventMasks__C =
(((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5411 of file dss_mrr_pe674.c.

4.16.4.365 ti_sysbios_knl_Event_A_pendTaskDisabled__C

```
const __FAR__ CT(ti_sysbios_knl_Event_A_pendTaskDisabled) ti_sysbios_knl_Event_A_pendTaskDisabled__C =
(((xdc_runtime Assert_Id)0) << 16 | 16)
```

Definition at line 5427 of file dss_mrr_pe674.c.

4.16.4.366 ti_sysbios_knl_Event_Instance_State_pendQ__O

```
const __FAR__ xdc_SizeT ti_sysbios_knl_Event_Instance_State_pendQ__O = offsetof( ti_sysbios_knl_Event_Object__, Object_field_pendQ)
```

Definition at line 1951 of file dss_mrr_pe674.c.

4.16.4.367 ti_sysbios_knl_Event_LM_pend__C

```
const __FAR__ CT(ti_sysbios_knl_Event_LM_pend) ti_sysbios_knl_Event_LM_pend__C = (((xdc←
runtime_Log_Event)18) << 16 | 768)
```

Definition at line 5407 of file dss_mrr_pe674.c.

4.16.4.368 ti_sysbios_knl_Event_LM_post__C

```
const __FAR__ CT(ti_sysbios_knl_Event_LM_post) ti_sysbios_knl_Event_LM_post__C = (((xdc←
runtime_Log_Event)17) << 16 | 768)
```

Definition at line 5403 of file dss_mrr_pe674.c.

4.16.4.369 ti_sysbios_knl_Event_Module_diagsEnabled__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_diagsEnabled) ti_sysbios_knl_Event_Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 5335 of file dss_mrr_pe674.c.

4.16.4.370 ti_sysbios_knl_Event_Module_diagsIncluded__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_diagsIncluded) ti_sysbios_knl_Event_Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 5339 of file dss_mrr_pe674.c.

4.16.4.371 ti_sysbios_knl_Event_Module_diagsMask__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_diagsMask) ti_sysbios_knl_Event_Module_diagsMask__C = ((CT(ti_sysbios_knl_Event_Module_diagsMask))0)
```

Definition at line 5343 of file dss_mrr_pe674.c.

4.16.4.372 ti_sysbios_knl_Event_Module_gateObj__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_gateObj) ti_sysbios_knl_Event_Module_gateObj__C = ((CT(ti_sysbios_knl_Event_Module_gateObj))0)
```

Definition at line 5347 of file dss_mrr_pe674.c.

4.16.4.373 ti_sysbios_knl_Event_Module_gatePrms__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_gatePrms) ti_sysbios_knl_Event_Module_gatePrms__C = ((CT(ti_sysbios_knl_Event_Module_gatePrms))0)
```

Definition at line 5351 of file dss_mrr_pe674.c.

4.16.4.374 ti_sysbios_knl_Event_Module_id__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_id) ti_sysbios_knl_Event_Module_id__C = (xdc←
Bits16)0x20
```

Definition at line 5355 of file dss_mrr_pe674.c.

4.16.4.375 ti_sysbios_knl_Event_Module_loggerDefined__C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerDefined) ti_sysbios_knl_Event_Module_loggerDefined__C = 0
```

Definition at line 5359 of file dss_mrr_pe674.c.

4.16.4.376 ti_sysbios_knl_Event_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn0 ti_sysbios_knl_Event_Module_loggerFxn0_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn0) 0)
```

Definition at line 5367 of file dss_mrr_pe674.c.

4.16.4.377 ti_sysbios_knl_Event_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn1 ti_sysbios_knl_Event_Module_loggerFxn1_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn1) 0)
```

Definition at line 5371 of file dss_mrr_pe674.c.

4.16.4.378 ti_sysbios_knl_Event_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn2 ti_sysbios_knl_Event_Module_loggerFxn2_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn2) 0)
```

Definition at line 5375 of file dss_mrr_pe674.c.

4.16.4.379 ti_sysbios_knl_Event_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn4 ti_sysbios_knl_Event_Module_loggerFxn4_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn4) 0)
```

Definition at line 5379 of file dss_mrr_pe674.c.

4.16.4.380 ti_sysbios_knl_Event_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerFxn8 ti_sysbios_knl_Event_Module_loggerFxn8_C = ((CT(ti_sysbios_knl_Event_Module_loggerFxn8) 0)
```

Definition at line 5383 of file dss_mrr_pe674.c.

4.16.4.381 ti_sysbios_knl_Event_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Module_loggerObj ti_sysbios_knl_Event_Module_loggerObj_C = ((CT(ti_sysbios_knl_Event_Module_loggerObj) 0)
```

Definition at line 5363 of file dss_mrr_pe674.c.

4.16.4.382 ti_sysbios_knl_Event_Module_root_V

ti_sysbios_knl_Event_Module ti_sysbios_knl_Event_Module_root_V

Initial value:

```
= {  
    &ti_sysbios_knl_Event_Module_root_V.link,  
    &ti_sysbios_knl_Event_Module_root_V.link,  
}
```

Definition at line 5328 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Event_Object_first_S(), and ti_sysbios_knl_Event_Object_next_S().

4.16.4.383 ti_sysbios_knl_Event_Object_count_C

```
const __FAR__ CT(ti_sysbios_knl_Event_Object_count ti_sysbios_knl_Event_Object_count_C = 0
```

Definition at line 5387 of file dss_mrr_pe674.c.

4.16.4.384 ti_sysbios_knl_Event_Object__DESC__C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_knl_Event_Object__DESC__C
```

Initial value:

```
= {
    (xdc_CPtr)-1,
    &ti_sysbios_knl_Event_Module__root__V.link,
    sizeof(ti_sysbios_knl_Event__S1) - sizeof(ti_sysbios_knl_Event_Object2__),
    0,
    0,
    sizeof(ti_sysbios_knl_Event_Object2__),
    (xdc_CPtr)&ti_sysbios_knl_Event_Object__PARAMS__C,
    sizeof(ti_sysbios_knl_Event_Params),
}
```

Definition at line 5312 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Event_construct(), ti_sysbios_knl_Event_create(), ti_sysbios_knl_Event_destruct(), ti_sysbios_knl_Event_Object__create__S(), and ti_sysbios_knl_Event_Object__delete__S().

4.16.4.385 ti_sysbios_knl_Event_Object__heap__C

```
const __FAR__ CT	ti_sysbios_knl_Event_Object__heap ti_sysbios_knl_Event_Object__heap__C = 0
```

Definition at line 5391 of file dss_mrr_pe674.c.

4.16.4.386 ti_sysbios_knl_Event_Object__PARAMS__C

```
const __FAR__ ti_sysbios_knl_Event_Params ti_sysbios_knl_Event_Object__PARAMS__C
```

Initial value:

```
= {
    sizeof(ti_sysbios_knl_Event_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_knl_Event_Object__PARAMS__C.__iprms,
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 5316 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Event_Params__init__S().

4.16.4.387 ti_sysbios_knl_Event_Object__sizeof__C

```
const __FAR__ CT	ti_sysbios_knl_Event_Object__sizeof ti_sysbios_knl_Event_Object__sizeof__C = sizeof( ti_sysbios_knl_Event_Object__ )
```

Definition at line 5395 of file dss_mrr_pe674.c.

4.16.4.388 ti_sysbios_knl_Event_Object__table__C

```
const __FAR__ CT	ti_sysbios_knl_Event_Object__table ti_sysbios_knl_Event_Object__table__C = 0
```

Definition at line 5399 of file dss_mrr_pe674.c.

4.16.4.389 ti_sysbios_knl_Idle_coreList__A

```
const __T1_ti_sysbios_knl_Idle_coreList ti_sysbios_knl_Idle_coreList__A
```

Initial value:

```
= {
    (xdc_UInt)0x0,
    (xdc_UInt)0x0,
    (xdc_UInt)0x0,
```

Definition at line 1536 of file dss_mrr_pe674.c.

4.16.4.390 ti_sysbios_knl_Idle_coreList_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_coreList) ti_sysbios_knl_Idle_coreList_C = {3, ((T1←
ti_sysbios_knl_Idle_coreList *) ti_sysbios_knl_Idle_coreList_A)}
```

Definition at line 5524 of file dss_mrr_pe674.c.

4.16.4.391 ti_sysbios_knl_Idle_funcList_A

```
const __T1(ti_sysbios_knl_Idle_funcList) ti_sysbios_knl_Idle_funcList_A
```

Initial value:

```
= {
    ((xdc_Void(*)(xdc_Void))((xdc_Fxn)DSP_sleep)),
    ((xdc_Void(*)(xdc_Void))((xdc_Fxn)ti_sysbios_utils_Load_idleFxn_E)),
    ((xdc_Void(*)(xdc_Void))((xdc_Fxn)ti_sysbios_hal_Hwi_checkStack)),
}
```

Definition at line 1533 of file dss_mrr_pe674.c.

4.16.4.392 ti_sysbios_knl_Idle_funcList_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_funcList) ti_sysbios_knl_Idle_funcList_C = {3, ((T1←
ti_sysbios_knl_Idle_funcList *) ti_sysbios_knl_Idle_funcList_A)}
```

Definition at line 5520 of file dss_mrr_pe674.c.

4.16.4.393 ti_sysbios_knl_Idle_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsEnabled) ti_sysbios_knl_Idle_Module_diagsEnabled_C = (xdc_Bits32)0x90
```

Definition at line 5452 of file dss_mrr_pe674.c.

4.16.4.394 ti_sysbios_knl_Idle_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsIncluded) ti_sysbios_knl_Idle_Module_diagsIncluded_C = (xdc_Bits32)0x90
```

Definition at line 5456 of file dss_mrr_pe674.c.

4.16.4.395 ti_sysbios_knl_Idle_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_diagsMask) ti_sysbios_knl_Idle_Module_diagsMask_C = ((CT(ti_sysbios_knl_Idle_Module_diagsMask))0)
```

Definition at line 5460 of file dss_mrr_pe674.c.

4.16.4.396 ti_sysbios_knl_Idle_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_gateObj) ti_sysbios_knl_Idle_Module_gateObj_C = ((CT(ti_sysbios_knl_Idle_Module_gateObj))0)
```

Definition at line 5464 of file dss_mrr_pe674.c.

4.16.4.397 ti_sysbios_knl_Idle_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_gatePrms) ti_sysbios_knl_Idle_Module_gatePrms_C = ((CT(ti_sysbios_knl_Idle_Module_gatePrms))0)
```

Definition at line 5468 of file dss_mrr_pe674.c.

4.16.4.398 ti_sysbios_knl_Idle_Module_id_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_id) ti_sysbios_knl_Idle_Module_id_C = (xdc←
Bits16) 0x1e
```

Definition at line 5472 of file dss_mrr_pe674.c.

4.16.4.399 ti_sysbios_knl_Idle_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerDefined) ti_sysbios_knl_Idle_Module←
loggerDefined_C = 0
```

Definition at line 5476 of file dss_mrr_pe674.c.

4.16.4.400 ti_sysbios_knl_Idle_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn0) ti_sysbios_knl_Idle_Module_logger←
Fxn0_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn0)) 0)
```

Definition at line 5484 of file dss_mrr_pe674.c.

4.16.4.401 ti_sysbios_knl_Idle_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn1) ti_sysbios_knl_Idle_Module_logger←
Fxn1_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn1)) 0)
```

Definition at line 5488 of file dss_mrr_pe674.c.

4.16.4.402 ti_sysbios_knl_Idle_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn2) ti_sysbios_knl_Idle_Module_logger←
Fxn2_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn2)) 0)
```

Definition at line 5492 of file dss_mrr_pe674.c.

4.16.4.403 ti_sysbios_knl_Idle_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn4) ti_sysbios_knl_Idle_Module_logger←
Fxn4_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn4)) 0)
```

Definition at line 5496 of file dss_mrr_pe674.c.

4.16.4.404 ti_sysbios_knl_Idle_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerFxn8) ti_sysbios_knl_Idle_Module_logger←
Fxn8_C = ((CT(ti_sysbios_knl_Idle_Module_loggerFxn8)) 0)
```

Definition at line 5500 of file dss_mrr_pe674.c.

4.16.4.405 ti_sysbios_knl_Idle_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Module_loggerObj) ti_sysbios_knl_Idle_Module_logger←
Obj_C = ((CT(ti_sysbios_knl_Idle_Module_loggerObj)) 0)
```

Definition at line 5480 of file dss_mrr_pe674.c.

4.16.4.406 ti_sysbios_knl_Idle_Object_count_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Object_count) ti_sysbios_knl_Idle_Object_count_C = 0
```

Definition at line 5504 of file dss_mrr_pe674.c.

4.16.4.407 ti_sysbios_knl_Idle_Object_heap_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Object_heap ti_sysbios_knl_Idle_Object_heap_C) = 0
Definition at line 5508 of file dss_mrr_pe674.c.
```

4.16.4.408 ti_sysbios_knl_Idle_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Object_sizeof ti_sysbios_knl_Idle_Object_sizeof_C) = 0
Definition at line 5512 of file dss_mrr_pe674.c.
```

4.16.4.409 ti_sysbios_knl_Idle_Object_table_C

```
const __FAR__ CT(ti_sysbios_knl_Idle_Object_table ti_sysbios_knl_Idle_Object_table_C) = 0
Definition at line 5516 of file dss_mrr_pe674.c.
```

4.16.4.410 ti_sysbios_knl_Intrinsics_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_diagsEnabled ti_sysbios_knl_Intrinsics_Module_diagsEnabled_C) = (xdc_Bits32)0x90
Definition at line 5533 of file dss_mrr_pe674.c.
```

4.16.4.411 ti_sysbios_knl_Intrinsics_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_diagsIncluded ti_sysbios_knl_Intrinsics_Module_diagsIncluded_C) = (xdc_Bits32)0x90
Definition at line 5537 of file dss_mrr_pe674.c.
```

4.16.4.412 ti_sysbios_knl_Intrinsics_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_diagsMask ti_sysbios_knl_Intrinsics_Module_diagsMask_C) = ((CT(ti_sysbios_knl_Intrinsics_Module_diagsMask)0)
Definition at line 5541 of file dss_mrr_pe674.c.
```

4.16.4.413 ti_sysbios_knl_Intrinsics_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_gateObj ti_sysbios_knl_Intrinsics_Module_gateObj_C) = ((CT(ti_sysbios_knl_Intrinsics_Module_gateObj)0)
Definition at line 5545 of file dss_mrr_pe674.c.
```

4.16.4.414 ti_sysbios_knl_Intrinsics_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_gatePrms ti_sysbios_knl_Intrinsics_Module_gatePrms_C) = ((CT(ti_sysbios_knl_Intrinsics_Module_gatePrms)0)
Definition at line 5549 of file dss_mrr_pe674.c.
```

4.16.4.415 ti_sysbios_knl_Intrinsics_Module_id_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_id ti_sysbios_knl_Intrinsics_Module_id_C) = (xdc_Bits16)0x1f
Definition at line 5553 of file dss_mrr_pe674.c.
```

4.16.4.416 `ti_sysbios_knl_Intrinsics_Module_loggerDefined_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerDefined ti_sysbios_knl_Intrinsics_<-
Module_loggerDefined_C = 0
Definition at line 5557 of file dss_mrr_pe674.c.
```

4.16.4.417 `ti_sysbios_knl_Intrinsics_Module_loggerFxn0_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn0 ti_sysbios_knl_Intrinsics_<-
Module_loggerFxn0_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn0) 0)
Definition at line 5565 of file dss_mrr_pe674.c.
```

4.16.4.418 `ti_sysbios_knl_Intrinsics_Module_loggerFxn1_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn1 ti_sysbios_knl_Intrinsics_<-
Module_loggerFxn1_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn1) 0)
Definition at line 5569 of file dss_mrr_pe674.c.
```

4.16.4.419 `ti_sysbios_knl_Intrinsics_Module_loggerFxn2_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn2 ti_sysbios_knl_Intrinsics_<-
Module_loggerFxn2_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn2) 0)
Definition at line 5573 of file dss_mrr_pe674.c.
```

4.16.4.420 `ti_sysbios_knl_Intrinsics_Module_loggerFxn4_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn4 ti_sysbios_knl_Intrinsics_<-
Module_loggerFxn4_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn4) 0)
Definition at line 5577 of file dss_mrr_pe674.c.
```

4.16.4.421 `ti_sysbios_knl_Intrinsics_Module_loggerFxn8_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn8 ti_sysbios_knl_Intrinsics_<-
Module_loggerFxn8_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerFxn8) 0)
Definition at line 5581 of file dss_mrr_pe674.c.
```

4.16.4.422 `ti_sysbios_knl_Intrinsics_Module_loggerObj_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Module_loggerObj ti_sysbios_knl_Intrinsics_<-
Module_loggerObj_C = ((CT(ti_sysbios_knl_Intrinsics_Module_loggerObj) 0)
Definition at line 5561 of file dss_mrr_pe674.c.
```

4.16.4.423 `ti_sysbios_knl_Intrinsics_Object_count_C`

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Object_count ti_sysbios_knl_Intrinsics_Object_<-
count_C = 0
Definition at line 5585 of file dss_mrr_pe674.c.
```

4.16.4.424 ti_sysbios_knl_Intrinsics_Object_heap_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Object_heap) ti_sysbios_knl_Intrinsics_Object_heap_C = 0
```

Definition at line 5589 of file dss_mrr_pe674.c.

4.16.4.425 ti_sysbios_knl_Intrinsics_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Object_sizeof) ti_sysbios_knl_Intrinsics_Object_sizeof_C = 0
```

Definition at line 5593 of file dss_mrr_pe674.c.

4.16.4.426 ti_sysbios_knl_Intrinsics_Object_table_C

```
const __FAR__ CT(ti_sysbios_knl_Intrinsics_Object_table) ti_sysbios_knl_Intrinsics_Object_table_C = 0
```

Definition at line 5597 of file dss_mrr_pe674.c.

4.16.4.427 ti_sysbios_knl_Queue_Module_diagsEnabled_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_diagsEnabled) ti_sysbios_knl_Queue_Module_diagsEnabled_C = (xdc_Bits32) 0x90
```

Definition at line 5633 of file dss_mrr_pe674.c.

4.16.4.428 ti_sysbios_knl_Queue_Module_diagsIncluded_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_diagsIncluded) ti_sysbios_knl_Queue_Module_diagsIncluded_C = (xdc_Bits32) 0x90
```

Definition at line 5637 of file dss_mrr_pe674.c.

4.16.4.429 ti_sysbios_knl_Queue_Module_diagsMask_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_diagsMask) ti_sysbios_knl_Queue_Module_diagsMask_C = ((CT(ti_sysbios_knl_Queue_Module_diagsMask)) 0)
```

Definition at line 5641 of file dss_mrr_pe674.c.

4.16.4.430 ti_sysbios_knl_Queue_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_gateObj) ti_sysbios_knl_Queue_Module_gateObj_C = ((CT(ti_sysbios_knl_Queue_Module_gateObj)) 0)
```

Definition at line 5645 of file dss_mrr_pe674.c.

4.16.4.431 ti_sysbios_knl_Queue_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_gatePrms) ti_sysbios_knl_Queue_Module_gatePrms_C = ((CT(ti_sysbios_knl_Queue_Module_gatePrms)) 0)
```

Definition at line 5649 of file dss_mrr_pe674.c.

4.16.4.432 ti_sysbios_knl_Queue_Module_id_C

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_id) ti_sysbios_knl_Queue_Module_id_C = (xdc_Bits16) 0x21
```

Definition at line 5653 of file dss_mrr_pe674.c.

4.16.4.433 `ti_sysbios_knl_Queue_Module_loggerDefined_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerDefined ti_sysbios_knl_Queue_Module_←
loggerDefined_C = 0
```

Definition at line 5657 of file dss_mrr_pe674.c.

4.16.4.434 `ti_sysbios_knl_Queue_Module_loggerFxn0_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerFxn0 ti_sysbios_knl_Queue_Module_←
loggerFxn0_C = ((CT(ti_sysbios_knl_Queue_Module_loggerFxn0) 0)
```

Definition at line 5665 of file dss_mrr_pe674.c.

4.16.4.435 `ti_sysbios_knl_Queue_Module_loggerFxn1_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerFxn1 ti_sysbios_knl_Queue_Module_←
loggerFxn1_C = ((CT(ti_sysbios_knl_Queue_Module_loggerFxn1) 0)
```

Definition at line 5669 of file dss_mrr_pe674.c.

4.16.4.436 `ti_sysbios_knl_Queue_Module_loggerFxn2_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerFxn2 ti_sysbios_knl_Queue_Module_←
loggerFxn2_C = ((CT(ti_sysbios_knl_Queue_Module_loggerFxn2) 0)
```

Definition at line 5673 of file dss_mrr_pe674.c.

4.16.4.437 `ti_sysbios_knl_Queue_Module_loggerFxn4_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerFxn4 ti_sysbios_knl_Queue_Module_←
loggerFxn4_C = ((CT(ti_sysbios_knl_Queue_Module_loggerFxn4) 0)
```

Definition at line 5677 of file dss_mrr_pe674.c.

4.16.4.438 `ti_sysbios_knl_Queue_Module_loggerFxn8_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerFxn8 ti_sysbios_knl_Queue_Module_←
loggerFxn8_C = ((CT(ti_sysbios_knl_Queue_Module_loggerFxn8) 0)
```

Definition at line 5681 of file dss_mrr_pe674.c.

4.16.4.439 `ti_sysbios_knl_Queue_Module_loggerObj_C`

```
const __FAR__ CT(ti_sysbios_knl_Queue_Module_loggerObj ti_sysbios_knl_Queue_Module_logger←
Obj_C = ((CT(ti_sysbios_knl_Queue_Module_loggerObj) 0)
```

Definition at line 5661 of file dss_mrr_pe674.c.

4.16.4.440 `ti_sysbios_knl_Queue_Module_root_V`

`ti_sysbios_knl_Queue_Module ti_sysbios_knl_Queue_Module_root_V`

Initial value:

```
= {
    &ti_sysbios_knl_Queue_Module_root_V.link,
    &ti_sysbios_knl_Queue_Module_root_V.link,
}
```

Definition at line 5626 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Queue_Object__first__S()`, and `ti_sysbios_knl_Queue_Object__next__S()`.

4.16.4.441 `ti_sysbios_knl_Queue_Object__count__C`

```
const __FAR__ CT__ti_sysbios_knl_Queue_Object__count ti_sysbios_knl_Queue_Object__count__C = 0
Definition at line 5685 of file dss_mrr_pe674.c.
```

4.16.4.442 `ti_sysbios_knl_Queue_Object__DESC__C`

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_knl_Queue_Object__DESC__C
```

Initial value:

```
= {
    (xdc_CPtr)-1,
    &ti_sysbios_knl_Queue_Module__root__V.link,
    sizeof(ti_sysbios_knl_Queue__S1) - sizeof(ti_sysbios_knl_QueueObject2),
    0,
    0,
    sizeof(ti_sysbios_knl_QueueObject2),
    (xdc_CPtr)&ti_sysbios_knl_Queue__Object__PARAMS__C,
    sizeof(ti_sysbios_knl_Queue_Params),
}
```

Definition at line 5610 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Queue_construct()`, `ti_sysbios_knl_Queue_create()`, `ti_sysbios_knl_Queue__destruct()`, `ti_sysbios_knl_Queue_Object__create__S()`, and `ti_sysbios_knl_Queue_Object__delete__S()`.

4.16.4.443 `ti_sysbios_knl_Queue_Object__heap__C`

```
const __FAR__ CT__ti_sysbios_knl_Queue_Object__heap ti_sysbios_knl_Queue_Object__heap__C = 0
```

Definition at line 5689 of file dss_mrr_pe674.c.

4.16.4.444 `ti_sysbios_knl_Queue_Object__PARAMS__C`

```
const __FAR__ ti_sysbios_knl_Queue_Params ti_sysbios_knl_Queue_Object__PARAMS__C
```

Initial value:

```
= {
    sizeof(ti_sysbios_knl_Queue_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_knl_Queue_Object__PARAMS__C.__iprms,
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 5614 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Queue_Params__init__S()`.

4.16.4.445 `ti_sysbios_knl_Queue_Object__sizeof__C`

```
const __FAR__ CT__ti_sysbios_knl_Queue_Object__sizeof ti_sysbios_knl_Queue_Object__sizeof__C =
sizeof( ti_sysbios_knl_Queue_Object__)
```

Definition at line 5693 of file dss_mrr_pe674.c.

4.16.4.446 `ti_sysbios_knl_Queue_Object__table__C`

```
const __FAR__ CT__ti_sysbios_knl_Queue_Object__table ti_sysbios_knl_Queue_Object__table__C = 0
```

Definition at line 5697 of file dss_mrr_pe674.c.

4.16.4.447 ti_sysbios_knl_Semaphore_A_badContext__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_A_badContext) ti_sysbios_knl_Semaphore_A_badContext__C = (((xdc_runtime Assert_Id)0) << 16 | 16)
Definition at line 5815 of file dss_mrr_pe674.c.
```

4.16.4.448 ti_sysbios_knl_Semaphore_A_invTimeout__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_A_invTimeout) ti_sysbios_knl_Semaphore_A_invTimeout__C = (((xdc_runtime Assert_Id)0) << 16 | 16)
Definition at line 5811 of file dss_mrr_pe674.c.
```

4.16.4.449 ti_sysbios_knl_Semaphore_A_noEvents__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_A_noEvents) ti_sysbios_knl_Semaphore_A_noEvents__C = (((xdc_runtime Assert_Id)0) << 16 | 16)
Definition at line 5807 of file dss_mrr_pe674.c.
```

4.16.4.450 ti_sysbios_knl_Semaphore_A_overflow__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_A_overflow) ti_sysbios_knl_Semaphore_A_overflow__C = (((xdc_runtime Assert_Id)0) << 16 | 16)
Definition at line 5819 of file dss_mrr_pe674.c.
```

4.16.4.451 ti_sysbios_knl_Semaphore_A_pendTaskDisabled__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_A_pendTaskDisabled) ti_sysbios_knl_Semaphore_A_pendTaskDisabled__C = (((xdc_runtime Assert_Id)0) << 16 | 16)
Definition at line 5823 of file dss_mrr_pe674.c.
```

4.16.4.452 ti_sysbios_knl_Semaphore_E_objectNotInKernelSpace__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_E_objectNotInKernelSpace) ti_sysbios_knl_Semaphore_E_objectNotInKernelSpace__C = (((xdc_runtime Error_Id)19) << 16 | 0)
Definition at line 5827 of file dss_mrr_pe674.c.
```

4.16.4.453 ti_sysbios_knl_Semaphore_eventPost__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_eventPost) ti_sysbios_knl_Semaphore_eventPost__C = ((CT(ti_sysbios_knl_Semaphore_eventPost)0))
Definition at line 5839 of file dss_mrr_pe674.c.
```

4.16.4.454 ti_sysbios_knl_Semaphore_eventSync__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_eventSync) ti_sysbios_knl_Semaphore_eventSync__C = ((CT(ti_sysbios_knl_Semaphore_eventSync)0))
Definition at line 5843 of file dss_mrr_pe674.c.
```

4.16.4.455 ti_sysbios_knl_Semaphore_Instance_State_pendQ__O

```
const __FAR__ xdc_SizeT ti_sysbios_knl_Semaphore_Instance_State_pendQ__O = offsetof( ti_sysbios_knl_Semaphore_Object__, Object_field_pendQ)
```

Definition at line 1959 of file dss_mrr_pe674.c.

4.16.4.456 **ti_sysbios_knl_Semaphore_LM_pend__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_LM_pend ti_sysbios_knl_Semaphore_LM_pend__C = (((xdc←
(runtime_Log_Event)20) << 16 | 768)
```

Definition at line 5803 of file dss_mrr_pe674.c.

4.16.4.457 **ti_sysbios_knl_Semaphore_LM_post__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_LM_post ti_sysbios_knl_Semaphore_LM_post__C = (((xdc←
(runtime_Log_Event)19) << 16 | 768)
```

Definition at line 5799 of file dss_mrr_pe674.c.

4.16.4.458 **ti_sysbios_knl_Semaphore_Module_diagsEnabled__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_diagsEnabled ti_sysbios_knl_Semaphore←
Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 5731 of file dss_mrr_pe674.c.

4.16.4.459 **ti_sysbios_knl_Semaphore_Module_diagsIncluded__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_diagsIncluded ti_sysbios_knl_Semaphore←
Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 5735 of file dss_mrr_pe674.c.

4.16.4.460 **ti_sysbios_knl_Semaphore_Module_diagsMask__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_diagsMask ti_sysbios_knl_Semaphore_Module←
diagsMask__C = ((CT(ti_sysbios_knl_Semaphore_Module_diagsMask)0)
```

Definition at line 5739 of file dss_mrr_pe674.c.

4.16.4.461 **ti_sysbios_knl_Semaphore_Module_gateObj__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_gateObj ti_sysbios_knl_Semaphore_Module←
gateObj__C = ((CT(ti_sysbios_knl_Semaphore_Module_gateObj)0)
```

Definition at line 5743 of file dss_mrr_pe674.c.

4.16.4.462 **ti_sysbios_knl_Semaphore_Module_gatePrms__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_gatePrms ti_sysbios_knl_Semaphore_Module←
gatePrms__C = ((CT(ti_sysbios_knl_Semaphore_Module_gatePrms)0)
```

Definition at line 5747 of file dss_mrr_pe674.c.

4.16.4.463 **ti_sysbios_knl_Semaphore_Module_id__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_id ti_sysbios_knl_Semaphore_Module_id__C =
(xdc_Bits16)0x22
```

Definition at line 5751 of file dss_mrr_pe674.c.

4.16.4.464 ti_sysbios_knl_Semaphore_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerDefined ti_sysbios_knl_Semaphore_←
Module_loggerDefined_C = 0
Definition at line 5755 of file dss_mrr_pe674.c.
```

4.16.4.465 ti_sysbios_knl_Semaphore_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerFxn0 ti_sysbios_knl_Semaphore_←
Module_loggerFxn0_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerFxn0) 0)
Definition at line 5763 of file dss_mrr_pe674.c.
```

4.16.4.466 ti_sysbios_knl_Semaphore_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerFxn1 ti_sysbios_knl_Semaphore_←
Module_loggerFxn1_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerFxn1) 0)
Definition at line 5767 of file dss_mrr_pe674.c.
```

4.16.4.467 ti_sysbios_knl_Semaphore_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerFxn2 ti_sysbios_knl_Semaphore_←
Module_loggerFxn2_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerFxn2) 0)
Definition at line 5771 of file dss_mrr_pe674.c.
```

4.16.4.468 ti_sysbios_knl_Semaphore_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerFxn4 ti_sysbios_knl_Semaphore_←
Module_loggerFxn4_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerFxn4) 0)
Definition at line 5775 of file dss_mrr_pe674.c.
```

4.16.4.469 ti_sysbios_knl_Semaphore_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerFxn8 ti_sysbios_knl_Semaphore_←
Module_loggerFxn8_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerFxn8) 0)
Definition at line 5779 of file dss_mrr_pe674.c.
```

4.16.4.470 ti_sysbios_knl_Semaphore_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Module_loggerObj ti_sysbios_knl_Semaphore_Module_←
__loggerObj_C = ((CT(ti_sysbios_knl_Semaphore_Module_loggerObj) 0)
Definition at line 5759 of file dss_mrr_pe674.c.
```

4.16.4.471 ti_sysbios_knl_Semaphore_Module_root_V

ti_sysbios_knl_Semaphore_Module ti_sysbios_knl_Semaphore_Module_root_V

Initial value:

```
= {
    &ti_sysbios_knl_Semaphore_Module_root_V.link,
    &ti_sysbios_knl_Semaphore_Module_root_V.link},
}
```

Definition at line 5724 of file dss_mrr_pe674.c.

Referenced by **ti_sysbios_knl_Semaphore_Object_first_S()**, and **ti_sysbios_knl_Semaphore_Object_next_S()**.

4.16.4.472 ti_sysbios_knl_Semaphore_Object__count__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Object__count ti_sysbios_knl_Semaphore_Object__count__C = 0
```

Definition at line 5783 of file dss_mrr_pe674.c.

4.16.4.473 ti_sysbios_knl_Semaphore_Object__DESC__C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_knl_Semaphore_Object__DESC__C
```

Initial value:

```
= {
    (xdc_CPtr)-1,
    &ti_sysbios_knl_Semaphore_Module_root__V.link,
    sizeof(ti_sysbios_knl_Semaphore__S1) - sizeof(ti_sysbios_knl_Semaphore_Object2__),
    0,
    0,
    sizeof(ti_sysbios_knl_Semaphore_Object2__),
    (xdc_CPtr)&ti_sysbios_knl_Semaphore_Object__PARAMS__C,
    sizeof(ti_sysbios_knl_Semaphore_Params),
}
```

Definition at line 5705 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Semaphore_construct()`, `ti_sysbios_knl_Semaphore_create()`, `ti_sysbios_knl_Semaphore_destruct()`, `ti_sysbios_knl_Semaphore_Object_create__S()`, and `ti_sysbios_knl_Semaphore_Object_delete__S()`.

4.16.4.474 ti_sysbios_knl_Semaphore_Object__heap__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Object__heap ti_sysbios_knl_Semaphore_Object__heap__C = 0
```

Definition at line 5787 of file dss_mrr_pe674.c.

4.16.4.475 ti_sysbios_knl_Semaphore_Object__PARAMS__C

```
const __FAR__ ti_sysbios_knl_Semaphore_Params ti_sysbios_knl_Semaphore_Object__PARAMS__C
```

Initial value:

```
= {
    sizeof(ti_sysbios_knl_Semaphore_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_knl_Semaphore_Object__PARAMS__C.__iprms,
    0,
    (xdc_UInt)0x1,
    ti_sysbios_knl_Semaphore_Mode_COUNTING,
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 5709 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Semaphore_Params_init__S()`.

4.16.4.476 ti_sysbios_knl_Semaphore_Object__sizeof__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Object__sizeof ti_sysbios_knl_Semaphore_Object__sizeof__C = sizeof(ti_sysbios_knl_Semaphore_Object__)
```

Definition at line 5791 of file dss_mrr_pe674.c.

4.16.4.477 ti_sysbios_knl_Semaphore_Object__table__C

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_Object__table ti_sysbios_knl_Semaphore_Object__table__C = 0
```

Definition at line 5795 of file dss_mrr_pe674.c.

4.16.4.478 **ti_sysbios_knl_Semaphore_supportsEvents__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_supportsEvents ti_sysbios_knl_Semaphore_supportsEvents__C = 0
```

Definition at line 5831 of file dss_mrr_pe674.c.

4.16.4.479 **ti_sysbios_knl_Semaphore_supportsPriority__C**

```
const __FAR__ CT(ti_sysbios_knl_Semaphore_supportsPriority ti_sysbios_knl_Semaphore_supportsPriority__C = 1
```

Definition at line 5835 of file dss_mrr_pe674.c.

4.16.4.480 **ti_sysbios_knl_Task_A_badAffinity__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_badAffinity ti_sysbios_knl_Task_A_badAffinity__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6236 of file dss_mrr_pe674.c.

4.16.4.481 **ti_sysbios_knl_Task_A_badPriority__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_badPriority ti_sysbios_knl_Task_A_badPriority__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6228 of file dss_mrr_pe674.c.

4.16.4.482 **ti_sysbios_knl_Task_A_badTaskState__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_badTaskState ti_sysbios_knl_Task_A_badTaskState__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6216 of file dss_mrr_pe674.c.

4.16.4.483 **ti_sysbios_knl_Task_A_badThreadType__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_badThreadType ti_sysbios_knl_Task_A_badThreadType__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6212 of file dss_mrr_pe674.c.

4.16.4.484 **ti_sysbios_knl_Task_A_badTimeout__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_badTimeout ti_sysbios_knl_Task_A_badTimeout__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6232 of file dss_mrr_pe674.c.

4.16.4.485 **ti_sysbios_knl_Task_A_invalidCoreId__C**

```
const __FAR__ CT(ti_sysbios_knl_Task_A_invalidCoreId ti_sysbios_knl_Task_A_invalidCoreId__C = (((xdc_runtime Assert_Id) 0) << 16 | 16)
```

Definition at line 6244 of file dss_mrr_pe674.c.

4.16.4.486 ti_sysbios_knl_Task_A_noPendElem__C

```
const __FAR__ CT(ti_sysbios_knl_Task_A_noPendElem) ti_sysbios_knl_Task_A_noPendElem__C = (((xdc->
(runtimeAssert_Id)0) << 16 | 16)
```

Definition at line 6220 of file dss_mrr_pe674.c.

4.16.4.487 ti_sysbios_knl_Task_A_sleepTaskDisabled__C

```
const __FAR__ CT(ti_sysbios_knl_Task_A_sleepTaskDisabled) ti_sysbios_knl_Task_A_sleepTask->
Disabled__C = (((xdc_runtimeAssert_Id)0) << 16 | 16)
```

Definition at line 6240 of file dss_mrr_pe674.c.

4.16.4.488 ti_sysbios_knl_Task_A_taskDisabled__C

```
const __FAR__ CT(ti_sysbios_knl_Task_A_taskDisabled) ti_sysbios_knl_Task_A_taskDisabled__C = 
(((xdc_runtimeAssert_Id)0) << 16 | 16)
```

Definition at line 6224 of file dss_mrr_pe674.c.

4.16.4.489 ti_sysbios_knl_Task_allBlockedFunc__C

```
const __FAR__ CT(ti_sysbios_knl_Task_allBlockedFunc) ti_sysbios_knl_Task_allBlockedFunc__C = 
((CT(ti_sysbios_knl_Task_allBlockedFunc)0))
```

Definition at line 6260 of file dss_mrr_pe674.c.

4.16.4.490 ti_sysbios_knl_Task_checkStackFlag__C

```
const __FAR__ CT(ti_sysbios_knl_Task_checkStackFlag) ti_sysbios_knl_Task_checkStackFlag__C = 1
Definition at line 6268 of file dss_mrr_pe674.c.
```

4.16.4.491 ti_sysbios_knl_Task_defaultStackHeap__C

```
const __FAR__ CT(ti_sysbios_knl_Task_defaultStackHeap) ti_sysbios_knl_Task_defaultStackHeap->
_C = 0
Definition at line 6256 of file dss_mrr_pe674.c.
```

4.16.4.492 ti_sysbios_knl_Task_defaultStackSize__C

```
const __FAR__ CT(ti_sysbios_knl_Task_defaultStackSize) ti_sysbios_knl_Task_defaultStackSize->
_C = (xdc_SizeT)0x5dc
Definition at line 6252 of file dss_mrr_pe674.c.
```

4.16.4.493 ti_sysbios_knl_Task_deleteTerminatedTasks__C

```
const __FAR__ CT(ti_sysbios_knl_Task_deleteTerminatedTasks) ti_sysbios_knl_Task_deleteTerminated->
Tasks__C = 0
Definition at line 6272 of file dss_mrr_pe674.c.
```

4.16.4.494 ti_sysbios_knl_Task_E_deleteNotAllowed__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_deleteNotAllowed) ti_sysbios_knl_Task_E_deleteNot->
Allowed__C = (((xdc_runtimeError_Id)22) << 16 | 0)
Definition at line 6196 of file dss_mrr_pe674.c.
```

4.16.4.495 ti_sysbios_knl_Task_E_moduleStateCheckFailed__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_moduleStateCheckFailed) ti_sysbios_knl_Task_E_moduleStateCheckFailed__C = (((xdc_runtime_Error_Id)23) << 16 | 0)
Definition at line 6200 of file dss_mrr_pe674.c.
```

4.16.4.496 ti_sysbios_knl_Task_E_objectCheckFailed__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_objectCheckFailed) ti_sysbios_knl_Task_E_objectCheckFailed__C = (((xdc_runtime_Error_Id)24) << 16 | 0)
Definition at line 6204 of file dss_mrr_pe674.c.
```

4.16.4.497 ti_sysbios_knl_Task_E_objectNotInKernelSpace__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_objectNotInKernelSpace) ti_sysbios_knl_Task_E_objectNotInKernelSpace__C = (((xdc_runtime_Error_Id)25) << 16 | 0)
Definition at line 6208 of file dss_mrr_pe674.c.
```

4.16.4.498 ti_sysbios_knl_Task_E_spOutOfBounds__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_spOutOfBounds) ti_sysbios_knl_Task_E_spOutOfBounds__C = (((xdc_runtime_Error_Id)21) << 16 | 0)
Definition at line 6192 of file dss_mrr_pe674.c.
```

4.16.4.499 ti_sysbios_knl_Task_E_stackOverflow__C

```
const __FAR__ CT(ti_sysbios_knl_Task_E_stackOverflow) ti_sysbios_knl_Task_E_stackOverflow__C = (((xdc_runtime_Error_Id)20) << 16 | 0)
Definition at line 6188 of file dss_mrr_pe674.c.
```

4.16.4.500 ti_sysbios_knl_Task_hooks__A

```
const __T1 ti_sysbios_knl_Task_hooks ti_sysbios_knl_Task_hooks__A
Initial value:
= {
    {
        ((xdc_Void*)(xdc_Int))((xdc_Fxn)ti_sysbios_utils_Load_taskRegHook__E),
        ((xdc_Void*)(ti_sysbios_knl_Task_Handle, xdc_runtime_Error_Block*))((xdc_Fxn)ti_sysbios_utils_Load_taskCreateHook__E),
        ((xdc_Void*)(ti_sysbios_knl_Task_Handle))0,
        ((xdc_Void*)(ti_sysbios_knl_Task_Handle, ti_sysbios_knl_Task_Handle))((xdc_Fxn)ti_sysbios_utils_Load_taskSwitchHook__E),
        ((xdc_Void*)(ti_sysbios_knl_Task_Handle))0,
        ((xdc_Void*)(ti_sysbios_knl_Task_Handle))((xdc_Fxn)ti_sysbios_utils_Load_taskDeleteHook__E),
    },
}
Definition at line 1624 of file dss_mrr_pe674.c.
```

4.16.4.501 ti_sysbios_knl_Task_hooks__C

```
const __FAR__ CT(ti_sysbios_knl_Task_hooks) ti_sysbios_knl_Task_hooks__C = {1, ((__T1 ti_sysbios_knl_Task_hooks__A)}
```

Definition at line 6276 of file dss_mrr_pe674.c.

4.16.4.502 ti_sysbios_knl_Task_initStackFlag__C

```
const __FAR__ CT(ti_sysbios_knl_Task_initStackFlag ti_sysbios_knl_Task_initStackFlag__C = 1
Definition at line 6264 of file dss_mrr_pe674.c.
```

4.16.4.503 ti_sysbios_knl_Task_Instance_State_0_hookEnv__A

```
__T1(ti_sysbios_knl_Task_Instance_State_hookEnv ti_sysbios_knl_Task_Instance_State_0_hookEnv__A
Definition at line 1586 of file dss_mrr_pe674.c.
```

4.16.4.504 ti_sysbios_knl_Task_Instance_State_0_stack__A

```
__T1(ti_sysbios_knl_Task_Instance_State_stack ti_sysbios_knl_Task_Instance_State_0_stack__A
Definition at line 1567 of file dss_mrr_pe674.c.
```

4.16.4.505 ti_sysbios_knl_Task_LD_block__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LD_block ti_sysbios_knl_Task_LD_block__C = (((xdc->
runtime_Log_Event) 24) << 16 | 512)
Definition at line 6160 of file dss_mrr_pe674.c.
```

4.16.4.506 ti_sysbios_knl_Task_LD_exit__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LD_exit ti_sysbios_knl_Task_LD_exit__C = (((xdc->
_log_Event) 27) << 16 | 512)
Definition at line 6172 of file dss_mrr_pe674.c.
```

4.16.4.507 ti_sysbios_knl_Task_LD_ready__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LD_ready ti_sysbios_knl_Task_LD_ready__C = (((xdc->
runtime_Log_Event) 23) << 16 | 512)
Definition at line 6156 of file dss_mrr_pe674.c.
```

4.16.4.508 ti_sysbios_knl_Task_LM_noWork__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_noWork ti_sysbios_knl_Task_LM_noWork__C = (((xdc->
runtime_Log_Event) 30) << 16 | 1024)
Definition at line 6184 of file dss_mrr_pe674.c.
```

4.16.4.509 ti_sysbios_knl_Task_LM_schedule__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_schedule ti_sysbios_knl_Task_LM_schedule__C = (((xdc->
_runtime_Log_Event) 29) << 16 | 1024)
Definition at line 6180 of file dss_mrr_pe674.c.
```

4.16.4.510 ti_sysbios_knl_Task_LM_setAffinity__C

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_setAffinity ti_sysbios_knl_Task_LM_setAffinity__C =
(((xdc_runtime_Log_Event) 28) << 16 | 768)
Definition at line 6176 of file dss_mrr_pe674.c.
```

4.16.4.511 **ti_sysbios_knl_Task_LM_setPri_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_setPri ti_sysbios_knl_Task_LM_setPri_C = (((xdc←
runtime_Log_Event)26) << 16 | 768)
```

Definition at line 6168 of file dss_mrr_pe674.c.

4.16.4.512 **ti_sysbios_knl_Task_LM_sleep_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_sleep ti_sysbios_knl_Task_LM_sleep_C = (((xdc←
runtime_Log_Event)22) << 16 | 768)
```

Definition at line 6152 of file dss_mrr_pe674.c.

4.16.4.513 **ti_sysbios_knl_Task_LM_switch_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_switch ti_sysbios_knl_Task_LM_switch_C = (((xdc←
runtime_Log_Event)21) << 16 | 768)
```

Definition at line 6148 of file dss_mrr_pe674.c.

4.16.4.514 **ti_sysbios_knl_Task_LM_yield_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_LM_yield ti_sysbios_knl_Task_LM_yield_C = (((xdc←
runtime_Log_Event)25) << 16 | 768)
```

Definition at line 6164 of file dss_mrr_pe674.c.

4.16.4.515 **ti_sysbios_knl_Task_Module_diagsEnabled_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_diagsEnabled ti_sysbios_knl_Task_Module_diags←
Enabled_C = (xdc_Bits32)0x90
```

Definition at line 6080 of file dss_mrr_pe674.c.

4.16.4.516 **ti_sysbios_knl_Task_Module_diagsIncluded_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_diagsIncluded ti_sysbios_knl_Task_Module_diags←
Included_C = (xdc_Bits32)0x90
```

Definition at line 6084 of file dss_mrr_pe674.c.

4.16.4.517 **ti_sysbios_knl_Task_Module_diagsMask_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_diagsMask ti_sysbios_knl_Task_Module_diags←
Mask_C = ((CT(ti_sysbios_knl_Task_Module_diagsMask)0))
```

Definition at line 6088 of file dss_mrr_pe674.c.

4.16.4.518 **ti_sysbios_knl_Task_Module_gateObj_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_gateObj ti_sysbios_knl_Task_Module_gateObj_C = (
(CT(ti_sysbios_knl_Task_Module_gateObj)0))
```

Definition at line 6092 of file dss_mrr_pe674.c.

4.16.4.519 **ti_sysbios_knl_Task_Module_gatePrms_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_gatePrms ti_sysbios_knl_Task_Module_gatePrms←
_C = ((CT(ti_sysbios_knl_Task_Module_gatePrms)0))
```

Definition at line 6096 of file dss_mrr_pe674.c.

4.16.4.520 **ti_sysbios_knl_Task_Module_id_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_id) ti_sysbios_knl_Task_Module_id_C = (xdc←  
Bits16) 0x23
```

Definition at line 6100 of file dss_mrr_pe674.c.

4.16.4.521 **ti_sysbios_knl_Task_Module_loggerDefined_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerDefined) ti_sysbios_knl_Task_Module←  
loggerDefined_C = 0
```

Definition at line 6104 of file dss_mrr_pe674.c.

4.16.4.522 **ti_sysbios_knl_Task_Module_loggerFxn0_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerFxn0) ti_sysbios_knl_Task_Module←  
loggerFxn0_C = ((CT(ti_sysbios_knl_Task_Module_loggerFxn0)) 0)
```

Definition at line 6112 of file dss_mrr_pe674.c.

4.16.4.523 **ti_sysbios_knl_Task_Module_loggerFxn1_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerFxn1) ti_sysbios_knl_Task_Module←  
loggerFxn1_C = ((CT(ti_sysbios_knl_Task_Module_loggerFxn1)) 0)
```

Definition at line 6116 of file dss_mrr_pe674.c.

4.16.4.524 **ti_sysbios_knl_Task_Module_loggerFxn2_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerFxn2) ti_sysbios_knl_Task_Module←  
loggerFxn2_C = ((CT(ti_sysbios_knl_Task_Module_loggerFxn2)) 0)
```

Definition at line 6120 of file dss_mrr_pe674.c.

4.16.4.525 **ti_sysbios_knl_Task_Module_loggerFxn4_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerFxn4) ti_sysbios_knl_Task_Module←  
loggerFxn4_C = ((CT(ti_sysbios_knl_Task_Module_loggerFxn4)) 0)
```

Definition at line 6124 of file dss_mrr_pe674.c.

4.16.4.526 **ti_sysbios_knl_Task_Module_loggerFxn8_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerFxn8) ti_sysbios_knl_Task_Module←  
loggerFxn8_C = ((CT(ti_sysbios_knl_Task_Module_loggerFxn8)) 0)
```

Definition at line 6128 of file dss_mrr_pe674.c.

4.16.4.527 **ti_sysbios_knl_Task_Module_loggerObj_C**

```
const __FAR__ CT(ti_sysbios_knl_Task_Module_loggerObj) ti_sysbios_knl_Task_Module←  
loggerObj_C = ((CT(ti_sysbios_knl_Task_Module_loggerObj)) 0)
```

Definition at line 6108 of file dss_mrr_pe674.c.

4.16.4.528 `ti_sysbios_knl_Task_Module__root__V`

`ti_sysbios_knl_Task_Module__ ti_sysbios_knl_Task_Module__root__V`

Initial value:

```
= {
    {&ti_sysbios_knl_Task_Module__root__V.link,
     &ti_sysbios_knl_Task_Module__root__V.link},
}
```

Definition at line 5884 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_knl_Task_Object__first__S()`, and `ti_sysbios_knl_Task_Object__next__S()`.

4.16.4.529 `ti_sysbios_knl_Task_Module__state__V`

`ti_sysbios_knl_Task_Module_State__ ti_sysbios_knl_Task_Module__state__V`

Initial value:

```
= {
    1,
    (xdc_UInt)0x0,
    0,
    (xdc_UInt)0x1,
    0,
    0,
    ((void*)ti_sysbios_knl_Task_Module_State_0_readyQ__A),
    ((void*)0),
    ((void*)0),
    ((void*)0),
    ((void*)0),
    ((void*)ti_sysbios_knl_Task_Module_State_0_idleTask__A),
    ((void*)0),
    1,
    {
        {
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Module__state__V.Object_field_inactiveQ.elem)),
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Module__state__V.Object_field_inactiveQ.elem)),
        },
    },
    {
        {
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Module__state__V.Object_field_terminatedQ.elem)),
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Module__state__V.Object_field_terminatedQ.elem)),
        },
    },
}
```

Definition at line 1618 of file dss_mrr_pe674.c.

4.16.4.530 `ti_sysbios_knl_Task_Module_State_0_idleTask__A`

`__T1_ti_sysbios_knl_Task_Module_State__idleTask ti_sysbios_knl_Task_Module_State_0_idleTask__A`

Initial value:

```
= {
    (ti_sysbios_knl_Task_Handle)&ti_sysbios_knl_Task_Object__table__V[0],
}
```

Definition at line 1615 of file dss_mrr_pe674.c.

4.16.4.531 `ti_sysbios_knl_Task_Module_State_0_readyQ__A`

`__T1_ti_sysbios_knl_Task_Module_State__readyQ ti_sysbios_knl_Task_Module_State_0_readyQ__A`

Definition at line 1612 of file dss_mrr_pe674.c.

4.16.4.532 ti_sysbios_knl_Task_Module_State_inactiveQ__O

```
const __FAR__ xdc_SizeT ti_sysbios_knl_Task_Module_State_inactiveQ__O = offsetof( ti_sysbios←
_knl_Task_Module_State, Object_field_inactiveQ)
Definition at line 1967 of file dss_mrr_pe674.c.
```

4.16.4.533 ti_sysbios_knl_Task_Module_State_terminatedQ__O

```
const __FAR__ xdc_SizeT ti_sysbios_knl_Task_Module_State_terminatedQ__O = offsetof( ti←
sysbios_knl_Task_Module_State, Object_field_terminatedQ)
Definition at line 1969 of file dss_mrr_pe674.c.
```

4.16.4.534 ti_sysbios_knl_Task_moduleStateCheckFlag__C

```
const __FAR__ CT_ti_sysbios_knl_Task_moduleStateCheckFlag ti_sysbios_knl_Task_moduleState←
CheckFlag__C = 0
Definition at line 6288 of file dss_mrr_pe674.c.
```

4.16.4.535 ti_sysbios_knl_Task_moduleStateCheckFxn__C

```
const __FAR__ CT_ti_sysbios_knl_Task_moduleStateCheckFxn ti_sysbios_knl_Task_moduleState←
CheckFxn__C = ((CT_ti_sysbios_knl_Task_moduleStateCheckFxn)((xdc_Fxn)ti_sysbios_knl_Task←
moduleStateCheck__I))
Definition at line 6280 of file dss_mrr_pe674.c.
```

4.16.4.536 ti_sysbios_knl_Task_moduleStateCheckValueFxn__C

```
const __FAR__ CT_ti_sysbios_knl_Task_moduleStateCheckValueFxn ti_sysbios_knl_Task_module←
StateCheckValueFxn__C = ((CT_ti_sysbios_knl_Task_moduleStateCheckValueFxn)((xdc_Fxn)ti_sysbios_knl_Task_getModuleStateCheckValue__I))
Definition at line 6284 of file dss_mrr_pe674.c.
```

4.16.4.537 ti_sysbios_knl_Task_numConstructedTasks__C

```
const __FAR__ CT_ti_sysbios_knl_Task_numConstructedTasks ti_sysbios_knl_Task_numConstructed←
Tasks__C = (xdc_UInt)0x0
Definition at line 6304 of file dss_mrr_pe674.c.
```

4.16.4.538 ti_sysbios_knl_Task_numPriorities__C

```
const __FAR__ CT_ti_sysbios_knl_Task_numPriorities ti_sysbios_knl_Task_numPriorities__C =
(xdc_UInt)0x10
Definition at line 6248 of file dss_mrr_pe674.c.
```

4.16.4.539 ti_sysbios_knl_Task_Object_count__C

```
const __FAR__ CT_ti_sysbios_knl_Task_Object_count ti_sysbios_knl_Task_Object_count__C = 1
Definition at line 6132 of file dss_mrr_pe674.c.
```

4.16.4.540 ti_sysbios_knl_Task_Object__DESC__C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_knl_Task_Object__DESC__C
Initial value:
= {
    (xdc_CPtr)-1,
    &ti_sysbios_knl_Task_Module_root_V.link,
    sizeof(ti_sysbios_knl_Task_S1) - sizeof(ti_sysbios_knl_Task_Object2),
    0,
    0,
    sizeof(ti_sysbios_knl_Task_Object2),
    (xdc_CPtr)&ti_sysbios_knl_Task_Object__PARAMS__C,
    sizeof(ti_sysbios_knl_Task_Params),
}
```

Definition at line 5851 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Task_construct(), ti_sysbios_knl_Task_create(), ti_sysbios_knl_Task_destruct(), ti->-sysbios_knl_Task_Object__create__S(), and ti_sysbios_knl_Task_Object__delete__S().

4.16.4.541 ti_sysbios_knl_Task_Object__heap__C

```
const __FAR__ CT_ti_sysbios_knl_Task_Object__heap ti_sysbios_knl_Task_Object__heap__C = 0
Definition at line 6136 of file dss_mrr_pe674.c.
```

4.16.4.542 ti_sysbios_knl_Task_Object__PARAMS__C

```
const __FAR__ ti_sysbios_knl_Task_Params ti_sysbios_knl_Task_Object__PARAMS__C
Initial value:
= {
    sizeof(ti_sysbios_knl_Task_Params),
    0,
    0,
    (xdc_runtime_IInstance_Params*)&ti_sysbios_knl_Task_Object__PARAMS__C.__iprms,
    ((xdc_UArg)(0x0)),
    ((xdc_UArg)(0x0)),
    (xdc_Int)0x1,
    ((xdc_Ptr)0),
    (xdc_SizET)0x0,
    0,
    ((xdc_Ptr)0),
    1,
    (xdc_UInt)0x0,
    1,
    ((xdc_Ptr)0),
    {
        sizeof(xdc_runtime_IInstance_Params),
        0,
    },
}
```

Definition at line 5855 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Task_Params__init__S().

4.16.4.543 ti_sysbios_knl_Task_Object__sizeof__C

```
const __FAR__ CT_ti_sysbios_knl_Task_Object__sizeof ti_sysbios_knl_Task_Object__sizeof__C =
sizeof( ti_sysbios_knl_Task_Object__)
Definition at line 6140 of file dss_mrr_pe674.c.
```

4.16.4.544 ti_sysbios_knl_Task_Object__table__C

```
const __FAR__ CT_ti_sysbios_knl_Task_Object__table ti_sysbios_knl_Task_Object__table__C =
ti_sysbios_knl_Task_Object__table__V
```

Definition at line 6144 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_knl_Task_Object__get__S().

4.16.4.545 ti_sysbios_knl_Task_Object_table_V

```
ti_sysbios_knl_Task_Object__ ti_sysbios_knl_Task_Object_table__V
```

Initial value:

```
= {
    {
        ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Object_table__V[0].qElem)),
        ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_knl_Task_Object_table__V[0].qElem)),
    },
    (xdc_Int) 0x0,
    (xdc_UInt) 0x1,
    ((xdc_Ptr) 0),
    ti_sysbios_knl_Task_Mode_INACTIVE,
    ((ti_sysbios_knl_Task_PendElem*) 0),
    (xdc_SizeT) 0x320,
    ((void*)ti_sysbios_knl_Task_Instance_State_0_stack__A),
    0,
    ((xdc_Void(*) (xdc_UArg, xdc_UArg)) ((xdc_Fxn)ti_sysbios_knl_Idle_loop__E)),
    ((xdc_UArg) (0x0)),
    ((xdc_UArg) (0x0)),
    ((xdc_Ptr) 0),
    ((void*)ti_sysbios_knl_Task_Instance_State_0_hookEnv__A),
    1,
    0,
    (xdc_UInt) 0x0,
    (xdc_UInt) 0x0,
    1,
    ((xdc_Ptr) 0),
    (xdc_UInt32) 0x0,
    ((xdc_Ptr) 0),
},
}
```

Definition at line 1589 of file dss_mrr_pe674.c.

4.16.4.546 ti_sysbios_knl_Task_objectCheckFlag_C

```
const __FAR__ CT(ti_sysbios_knl_Task_objectCheckFlag ti_sysbios_knl_Task_objectCheckFlag__C =
```

```
0
```

Definition at line 6300 of file dss_mrr_pe674.c.

4.16.4.547 ti_sysbios_knl_Task_objectCheckFxn_C

```
const __FAR__ CT(ti_sysbios_knl_Task_objectCheckFxn ti_sysbios_knl_Task_objectCheckFxn__C =
```

```
((CT(ti_sysbios_knl_Task_objectCheckFxn) ((xdc_Fxn)ti_sysbios_knl_Task_objectCheck__I))
```

Definition at line 6292 of file dss_mrr_pe674.c.

4.16.4.548 ti_sysbios_knl_Task_objectCheckValueFxn_C

```
const __FAR__ CT(ti_sysbios_knl_Task_objectCheckValueFxn ti_sysbios_knl_Task_objectCheckValueFxn__C =
```

```
((CT(ti_sysbios_knl_Task_objectCheckValueFxn) ((xdc_Fxn)ti_sysbios_knl_Task_getObjectCheckValue__I))
```

Definition at line 6296 of file dss_mrr_pe674.c.

4.16.4.549 ti_sysbios_knl_Task_startupHookFunc_C

```
const __FAR__ CT(ti_sysbios_knl_Task_startupHookFunc ti_sysbios_knl_Task_startupHookFunc__C =
```

```
((CT(ti_sysbios_knl_Task_startupHookFunc) 0)
```

Definition at line 6308 of file dss_mrr_pe674.c.

4.16.4.550 ti_sysbios_rts_ti_ThreadLocalStorage_enableTLSSupport_C

```
const __FAR__ CT(ti_sysbios_rts_ti_ThreadLocalStorage_enableTLSSupport ti_sysbios_rts_ti_ThreadLocalStorage_enableTLSSupport__C = 0
```

Definition at line 6411 of file dss_mrr_pe674.c.

4.16.4.551 ti_sysbios_rts_ti_ThreadLocalStorage_heapHandle__C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_heapHandle) ti_sysbios_rts(ti_ThreadLocalStorage_heapHandle__C) = 0
```

Definition at line 6407 of file dss_mrr_pe674.c.

4.16.4.552 ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsEnabled__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsEnabled) ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsEnabled__C) = (xdc_Bits32) 0x90
```

Definition at line 6339 of file dss_mrr_pe674.c.

4.16.4.553 ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsIncluded__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsIncluded) ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsIncluded__C) = (xdc_Bits32) 0x90
```

Definition at line 6343 of file dss_mrr_pe674.c.

4.16.4.554 ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsMask__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsMask) ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsMask__C) = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_diagsMask)) 0)
```

Definition at line 6347 of file dss_mrr_pe674.c.

4.16.4.555 ti_sysbios_rts(ti_ThreadLocalStorage_Module_gateObj__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_gateObj) ti_sysbios_rts(ti_ThreadLocalStorage_Module_gateObj__C) = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_gateObj)) 0)
```

Definition at line 6351 of file dss_mrr_pe674.c.

4.16.4.556 ti_sysbios_rts(ti_ThreadLocalStorage_Module_gatePrms__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_gatePrms) ti_sysbios_rts(ti_ThreadLocalStorage_Module_gatePrms__C) = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_gatePrms)) 0)
```

Definition at line 6355 of file dss_mrr_pe674.c.

4.16.4.557 ti_sysbios_rts(ti_ThreadLocalStorage_Module_id__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_id) ti_sysbios_rts(ti_ThreadLocalStorage_Module_id__C) = (xdc_Bits16) 0x2a
```

Definition at line 6359 of file dss_mrr_pe674.c.

4.16.4.558 ti_sysbios_rts(ti_ThreadLocalStorage_Module_loggerDefined__C)

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module_loggerDefined) ti_sysbios_rts(ti_ThreadLocalStorage_Module_loggerDefined__C) = 0
```

Definition at line 6363 of file dss_mrr_pe674.c.

4.16.4.559 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn0) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn0)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn0)) 0)
```

Definition at line 6371 of file dss_mrr_pe674.c.

4.16.4.560 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn1) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn1)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn1)) 0)
```

Definition at line 6375 of file dss_mrr_pe674.c.

4.16.4.561 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn2) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn2)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn2)) 0)
```

Definition at line 6379 of file dss_mrr_pe674.c.

4.16.4.562 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn4) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn4)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn4)) 0)
```

Definition at line 6383 of file dss_mrr_pe674.c.

4.16.4.563 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn8) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn8)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerFxn8)) 0)
```

Definition at line 6387 of file dss_mrr_pe674.c.

4.16.4.564 ti_sysbios_rts_ti_ThreadLocalStorage_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerObj) ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerObj)_C = ((CT(ti_sysbios_rts(ti_ThreadLocalStorage_Module/loggerObj)) 0)
```

Definition at line 6367 of file dss_mrr_pe674.c.

4.16.4.565 ti_sysbios_rts_ti_ThreadLocalStorage_Module_state_V

```
ti_sysbios_rts(ti_ThreadLocalStorage_Module_State) ti_sysbios_rts(ti_ThreadLocalStorage_Module_state)_V
```

Initial value:

```
= {  
    ((xdc_Ptr)((void*)&__TI_TLS_MAIN_THREAD_Base)),  
    (xdc_UInt)(-0x0 - 1),  
    0,  
}
```

Definition at line 1644 of file dss_mrr_pe674.c.

4.16.4.566 ti_sysbios_rts_ti_ThreadLocalStorage_Object_count_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object_count) ti_sysbios_rts(ti_←
ThreadLocalStorage_Object_count)_C = 0
Definition at line 6391 of file dss_mrr_pe674.c.
```

4.16.4.567 ti_sysbios_rts_ti_ThreadLocalStorage_Object_heap_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object_heap) ti_sysbios_rts(ti_Thread←
LocalStorage_Object_heap)_C = 0
Definition at line 6395 of file dss_mrr_pe674.c.
```

4.16.4.568 ti_sysbios_rts(ti_ThreadLocalStorage_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object_sizeof) ti_sysbios_rts(ti_←
ThreadLocalStorage_Object_sizeof)_C = 0
Definition at line 6399 of file dss_mrr_pe674.c.
```

4.16.4.569 ti_sysbios_rts(ti_ThreadLocalStorage_Object_table_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_Object_table) ti_sysbios_rts(ti_←
ThreadLocalStorage_Object_table)_C = 0
Definition at line 6403 of file dss_mrr_pe674.c.
```

4.16.4.570 ti_sysbios_rts(ti_ThreadLocalStorage_TItls_initSectMemory_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_TItls_initSectMemory) ti_sysbios_rts(ti_←
ti_ThreadLocalStorage_TItls_initSectMemory)_C = 0
Definition at line 6419 of file dss_mrr_pe674.c.
```

4.16.4.571 ti_sysbios_rts(ti_ThreadLocalStorage_TItlsSectMemory_C

```
const __FAR__ CT(ti_sysbios_rts(ti_ThreadLocalStorage_TItlsSectMemory) ti_sysbios_rts(ti_←
ThreadLocalStorage_TItlsSectMemory)_C = 0
Definition at line 6415 of file dss_mrr_pe674.c.
```

4.16.4.572 ti_sysbios_timers_rti_Timer_A_invalidTimer_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_A_invalidTimer) ti_sysbios_timers_rti_Timer_A_←
invalidTimer_C = (((xdc_runtimeAssert_Id)0) << 16 | 16)
Definition at line 6603 of file dss_mrr_pe674.c.
```

4.16.4.573 ti_sysbios_timers_rti_Timer_anyMask_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_anyMask) ti_sysbios_timers_rti_Timer_anyMask_C =
(xdc_UInt)0x3
Definition at line 6623 of file dss_mrr_pe674.c.
```

4.16.4.574 ti_sysbios_timers_rti_Timer_continueOnSuspend__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_continueOnSuspend ti_sysbios_timers_rti_Timer_continueOnSuspend__C = 0
```

Definition at line 6627 of file dss_mrr_pe674.c.

4.16.4.575 ti_sysbios_timers_rti_Timer_E_CANNOT_SUPPORT__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_E_CANNOT_SUPPORT ti_sysbios_timers_rti_Timer_E_CANNOT_SUPPORT__C = (((xdc_runtime_Error_Id)34) << 16 | 0)
```

Definition at line 6619 of file dss_mrr_pe674.c.

4.16.4.576 ti_sysbios_timers_rti_Timer_E_INVALID_HWIMASK__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_E_INVALID_HWIMASK ti_sysbios_timers_rti_Timer_E_INVALID_HWIMASK__C = (((xdc_runtime_Error_Id)33) << 16 | 0)
```

Definition at line 6615 of file dss_mrr_pe674.c.

4.16.4.577 ti_sysbios_timers_rti_Timer_E_INVALID_TIMER__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_E_INVALID_TIMER ti_sysbios_timers_rti_Timer_E_INVALID_TIMER__C = (((xdc_runtime_Error_Id)31) << 16 | 0)
```

Definition at line 6607 of file dss_mrr_pe674.c.

4.16.4.578 ti_sysbios_timers_rti_Timer_E_NOT_AVAILABLE__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_E_NOT_AVAILABLE ti_sysbios_timers_rti_Timer_E_NOT_AVAILABLE__C = (((xdc_runtime_Error_Id)32) << 16 | 0)
```

Definition at line 6611 of file dss_mrr_pe674.c.

4.16.4.579 ti_sysbios_timers_rti_Timer_Module_diagsEnabled__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_diagsEnabled ti_sysbios_timers_rti_Timer_Module_diagsEnabled__C = (xdc_Bits32)0x90
```

Definition at line 6535 of file dss_mrr_pe674.c.

4.16.4.580 ti_sysbios_timers_rti_Timer_Module_diagsIncluded__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_diagsIncluded ti_sysbios_timers_rti_Timer_Module_diagsIncluded__C = (xdc_Bits32)0x90
```

Definition at line 6539 of file dss_mrr_pe674.c.

4.16.4.581 ti_sysbios_timers_rti_Timer_Module_diagsMask__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_diagsMask ti_sysbios_timers_rti_Timer_Module_diagsMask__C = ((CT(ti_sysbios_timers_rti_Timer_Module_diagsMask)0))
```

Definition at line 6543 of file dss_mrr_pe674.c.

4.16.4.582 ti_sysbios_timers_rti_Timer_Module_gateObj__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_gateObj ti_sysbios_timers_rti_Timer_Module_gateObj__C = ((CT(ti_sysbios_timers_rti_Timer_Module_gateObj)0))
```

Definition at line 6547 of file dss_mrr_pe674.c.

4.16.4.583 ti_sysbios_timers_rti_Timer_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_gatePrms) ti_sysbios_timers_rti_Timer->
Module_gatePrms_C = ((CT(ti_sysbios_timers_rti_Timer_Module_gatePrms)0)
```

Definition at line 6551 of file dss_mrr_pe674.c.

4.16.4.584 ti_sysbios_timers_rti_Timer_Module_id_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_id) ti_sysbios_timers_rti_Timer_Module->
_id_C = (xdc_Bits16)0x31
```

Definition at line 6555 of file dss_mrr_pe674.c.

4.16.4.585 ti_sysbios_timers_rti_Timer_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerDefined) ti_sysbios_timers_rti->
Timer_Module_loggerDefined_C = 0
```

Definition at line 6559 of file dss_mrr_pe674.c.

4.16.4.586 ti_sysbios_timers_rti_Timer_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn0) ti_sysbios_timers_rti_Timer->
_Module_loggerFxn0_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn0)0)
```

Definition at line 6567 of file dss_mrr_pe674.c.

4.16.4.587 ti_sysbios_timers_rti_Timer_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn1) ti_sysbios_timers_rti_Timer->
_Module_loggerFxn1_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn1)0)
```

Definition at line 6571 of file dss_mrr_pe674.c.

4.16.4.588 ti_sysbios_timers_rti_Timer_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn2) ti_sysbios_timers_rti_Timer->
_Module_loggerFxn2_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn2)0)
```

Definition at line 6575 of file dss_mrr_pe674.c.

4.16.4.589 ti_sysbios_timers_rti_Timer_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn4) ti_sysbios_timers_rti_Timer->
_Module_loggerFxn4_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn4)0)
```

Definition at line 6579 of file dss_mrr_pe674.c.

4.16.4.590 ti_sysbios_timers_rti_Timer_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn8) ti_sysbios_timers_rti_Timer->
_Module_loggerFxn8_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerFxn8)0)
```

Definition at line 6583 of file dss_mrr_pe674.c.

4.16.4.591 ti_sysbios_timers_rti_Timer_Module_loggerObj_C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Module_loggerObj) ti_sysbios_timers_rti_Timer_Module_loggerObj_C = ((CT(ti_sysbios_timers_rti_Timer_Module_loggerObj) )0)
Definition at line 6563 of file dss_mrr_pe674.c.
```

4.16.4.592 ti_sysbios_timers_rti_Timer_Module_root_V

ti_sysbios_timers_rti_Timer_Module ti_sysbios_timers_rti_Timer_Module_root_V

Initial value:

```
= {
    {&ti_sysbios_timers_rti_Timer_Module_root_V.link,
     &ti_sysbios_timers_rti_Timer_Module_root_V.link},
}
```

Definition at line 6455 of file dss_mrr_pe674.c.

Referenced by **ti_sysbios_timers_rti_Timer_Object_first_S()**, and **ti_sysbios_timers_rti_Timer_Object_next_S()**.

4.16.4.593 ti_sysbios_timers_rti_Timer_Module_state_V

ti_sysbios_timers_rti_Timer_Module_State ti_sysbios_timers_rti_Timer_Module_state_V

Initial value:

```
= {
    (xdc_UInt)0x2,
    ((void*)ti_sysbios_timers_rti_Timer_Module_State_0_device_A),
    ((void*)ti_sysbios_timers_rti_Timer_Module_State_0_intFreqs_A),
    ((void*)ti_sysbios_timers_rti_Timer_Module_State_0_handles_A),
}
```

Definition at line 1672 of file dss_mrr_pe674.c.

4.16.4.594 ti_sysbios_timers_rti_Timer_Module_State_0_device_A

__T1(ti_sysbios_timers_rti_Timer_Module_State_device ti_sysbios_timers_rti_Timer_Module_State_0_device_A

Initial value:

```
= {
    {
        (xdc_Int)0xe,
        (xdc_Int)0x4b,
        ((xdc_Ptr)((void*)0x2020000)),
    },
    {
        (xdc_Int)0xf,
        (xdc_Int)0x4c,
        ((xdc_Ptr)((void*)0x2020000)),
    },
}
```

Definition at line 1663 of file dss_mrr_pe674.c.

4.16.4.595 ti_sysbios_timers_rti_Timer_Module_State_0_handles_A

__T1(ti_sysbios_timers_rti_Timer_Module_State_handles ti_sysbios_timers_rti_Timer_Module_State_0_handles_A

Initial value:

```
= {
    (ti_sysbios_timers_rti_Timer_Handle)&ti_sysbios_timers_rti_Timer_Object_table_V[0],
    0,
}
```

Definition at line 1669 of file dss_mrr_pe674.c.

4.16.4.596 ti_sysbios_timers_rti_Timer_Module_State_0_intFreqs_A

__T1(ti_sysbios_timers_rti_Timer_Module_State_intFreqs ti_sysbios_timers_rti_Timer_Module_State_0_intFreqs_A

State_0_intFreqs__A

Initial value:

```
= {
    {
        (xdc_Bits32)0x0,
        (xdc_Bits32)0xbebc200,
    },
    {
        (xdc_Bits32)0x0,
        (xdc_Bits32)0xbebc200,
    },
}
```

Definition at line 1666 of file dss_mrr_pe674.c.

4.16.4.597 ti_sysbios_timers_rti_Timer_numTimerDevices__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_numTimerDevices ti_sysbios_timers_rti_Timer_<-
numTimerDevices__C = (xdc_Int)0x2
```

Definition at line 6635 of file dss_mrr_pe674.c.

4.16.4.598 ti_sysbios_timers_rti_Timer_Object_count__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object_count ti_sysbios_timers_rti_Timer_<-
Object_count__C = 1
```

Definition at line 6587 of file dss_mrr_pe674.c.

4.16.4.599 ti_sysbios_timers_rti_Timer_Object_DESC__C

```
const __FAR__ xdc_runtime_Core_ObjDesc ti_sysbios_timers_rti_Timer_Object_DESC__C
```

Initial value:

```
= {
    (xdc_CPtr)0,
    &ti_sysbios_timers_rti_Timer_Module_root__V.link,
    sizeof(ti_sysbios_timers_rti_Timer__S1) - sizeof(ti_sysbios_timers_rti_Timer_Object2__),
    0,
    0,
    sizeof(ti_sysbios_timers_rti_Timer_Object2__),
    (xdc_CPtr)&ti_sysbios_timers_rti_Timer_Object_PARAMS__C,
    sizeof(ti_sysbios_timers_rti_Timer_Params),
}
```

Definition at line 6427 of file dss_mrr_pe674.c.

Referenced by ti_sysbios_timers_rti_Timer_construct(), ti_sysbios_timers_rti_Timer_create(), ti_sysbios_timers_rti_Timer_destruct(), ti_sysbios_timers_rti_Timer_Object_create__S(), and ti_sysbios_timers_rti_Timer_Object_delete__S().

4.16.4.600 ti_sysbios_timers_rti_Timer_Object_heap__C

```
const __FAR__ CT(ti_sysbios_timers_rti_Timer_Object_heap ti_sysbios_timers_rti_Timer_<-
Object_heap__C = 0
```

Definition at line 6591 of file dss_mrr_pe674.c.

4.16.4.601 ti_sysbios_timers_rti_Timer_Object_PARAMS__C

```
const __FAR__ ti_sysbios_timers_rti_Timer_Params ti_sysbios_timers_rti_Timer_Object_PARAMS__C
```

Initial value:

```
= {
    sizeof(ti_sysbios_timers_rti_Timer_Params),
    0,
    0,
    (xdc_runtime_IInstance_Parms*)&ti_sysbios_timers_rti_Timer_Object_PARAMS__C.iprms,
    ti_sysbios_interfaces_ITimer_RunMode_CONTINUOUS,
    ti_sysbios_interfaces_ITimer_StartMode_AUTO,
```

```
((xdc_UArg)0),
(xdc_UInt32)0x0,
ti_sysbios_interfaces_ITimer_PeriodType_MICROSECS,
{
    (xdc_Bits32)0x0,
    (xdc_Bits32)0x0,
},
1,
((ti_sysbios_hal_Hwi_Params*)0),
(xdc_UInt8)0x1,
{
    sizeof (xdc_runtime_IInstance_Params),
    0,
},
}
```

Definition at line 6431 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_timers_rti_Timer_Params_init_S()`.

4.16.4.602 `ti_sysbios_timers_rti_Timer_Object_sizeof_C`

```
const __FAR__ CT	ti_sysbios_timers_rti_Timer_Object_sizeof ti_sysbios_timers_rti_Timer_Object_sizeof_C = sizeof( ti_sysbios_timers_rti_Timer_Object )
```

Definition at line 6595 of file dss_mrr_pe674.c.

4.16.4.603 `ti_sysbios_timers_rti_Timer_Object_table_C`

```
const __FAR__ CT	ti_sysbios_timers_rti_Timer_Object_table ti_sysbios_timers_rti_Timer_Object_table_C = ti_sysbios_timers_rti_Timer_Object_table_V
```

Definition at line 6599 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_timers_rti_Timer_Object_get_S()`.

4.16.4.604 `ti_sysbios_timers_rti_Timer_Object_table_V`

```
ti_sysbios_timers_rti_Timer_Object ti_sysbios_timers_rti_Timer_Object_table_V
```

Initial value:

```
= {
    {
        0,
        1,
        (xdc_Int)0x0,
        ti_sysbios_interfaces_ITimer_RunMode_CONTINUOUS,
        ti_sysbios_interfaces_ITimer_StartMode_AUTO,
        (xdc_UInt)0x1,
        (xdc_UInt)0x3e8,
        ti_sysbios_interfaces_ITimer_PeriodType_MICROSECS,
        (xdc_UInt)0xe,
        ((xdc_UArg)0),
        ((xdc_Void(*) (xdc_UArg)) ((xdc_Fxn)ti_sysbios_knl_Clock_doTick_I)),
        {
            (xdc_Bits32)0x0,
            (xdc_Bits32)0x0,
        },
        (ti_sysbios_hal_Hwi_Handle)&ti_sysbios_hal_Hwi_Object_table_V[0],
        1,
    },
}
```

Definition at line 1652 of file dss_mrr_pe674.c.

4.16.4.605 `ti_sysbios_timers_rti_Timer_startupNeeded_C`

```
const __FAR__ CT	ti_sysbios_timers_rti_Timer_startupNeeded ti_sysbios_timers_rti_Timer_startupNeeded_C = (xdc_UInt)0x1
```

Definition at line 6631 of file dss_mrr_pe674.c.

4.16.4.606 **ti_sysbios_utils_Load_autoAddTasks__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_autoAddTasks ti_sysbios_utils_Load_autoAddTasks__C = 1
Definition at line 6841 of file dss_mrr_pe674.c.
```

4.16.4.607 **ti_sysbios_utils_Load_hwiEnabled__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_hwiEnabled ti_sysbios_utils_Load_hwiEnabled__C = 0
Definition at line 6829 of file dss_mrr_pe674.c.
```

4.16.4.608 **ti_sysbios_utils_Load_LS_cpuLoad__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_LS_cpuLoad ti_sysbios_utils_Load_LS_cpuLoad__C = (((xdc←
(runtime_Log_Event) 31) << 16 | 2048)
Definition at line 6801 of file dss_mrr_pe674.c.
```

4.16.4.609 **ti_sysbios_utils_Load_LS_hwiLoad__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_LS_hwiLoad ti_sysbios_utils_Load_LS_hwiLoad__C = (((xdc←
(runtime_Log_Event) 32) << 16 | 2048)
Definition at line 6805 of file dss_mrr_pe674.c.
```

4.16.4.610 **ti_sysbios_utils_Load_LS_swiLoad__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_LS_swiLoad ti_sysbios_utils_Load_LS_swiLoad__C = (((xdc←
(runtime_Log_Event) 33) << 16 | 2048)
Definition at line 6809 of file dss_mrr_pe674.c.
```

4.16.4.611 **ti_sysbios_utils_Load_LS_taskLoad__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_LS_taskLoad ti_sysbios_utils_Load_LS_taskLoad__C =
(((xdc_runtime_Log_Event) 34) << 16 | 2048)
Definition at line 6813 of file dss_mrr_pe674.c.
```

4.16.4.612 **ti_sysbios_utils_Load_Module_diagsEnabled__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_diagsEnabled ti_sysbios_utils_Load_Module←
diagsEnabled__C = (xdc_Bits32) 0x90
Definition at line 6733 of file dss_mrr_pe674.c.
```

4.16.4.613 **ti_sysbios_utils_Load_Module_diagsIncluded__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_diagsIncluded ti_sysbios_utils_Load_Module←
diagsIncluded__C = (xdc_Bits32) 0x90
Definition at line 6737 of file dss_mrr_pe674.c.
```

4.16.4.614 **ti_sysbios_utils_Load_Module_diagsMask__C**

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_diagsMask ti_sysbios_utils_Load_Module←
diagsMask__C = ((CT(ti_sysbios_utils_Load_Module_diagsMask) 0)
Definition at line 6741 of file dss_mrr_pe674.c.
```

4.16.4.615 ti_sysbios_utils_Load_Module_gateObj_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_gateObj) ti_sysbios_utils_Load_Module_gateObj_C = ((CT(ti_sysbios_utils_Load_Module_gateObj))0)
Definition at line 6745 of file dss_mrr_pe674.c.
```

4.16.4.616 ti_sysbios_utils_Load_Module_gatePrms_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_gatePrms) ti_sysbios_utils_Load_Module_gatePrms_C = ((CT(ti_sysbios_utils_Load_Module_gatePrms))0)
Definition at line 6749 of file dss_mrr_pe674.c.
```

4.16.4.617 ti_sysbios_utils_Load_Module_id_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_id) ti_sysbios_utils_Load_Module_id_C = (xdc<Bits16>0x29)
Definition at line 6753 of file dss_mrr_pe674.c.
```

4.16.4.618 ti_sysbios_utils_Load_Module_loggerDefined_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerDefined) ti_sysbios_utils_Load_Module_loggerDefined_C = 0
Definition at line 6757 of file dss_mrr_pe674.c.
```

4.16.4.619 ti_sysbios_utils_Load_Module_loggerFxn0_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerFxn0) ti_sysbios_utils_Load_Module_loggerFxn0_C = ((CT(ti_sysbios_utils_Load_Module_loggerFxn0))0)
Definition at line 6765 of file dss_mrr_pe674.c.
```

4.16.4.620 ti_sysbios_utils_Load_Module_loggerFxn1_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerFxn1) ti_sysbios_utils_Load_Module_loggerFxn1_C = ((CT(ti_sysbios_utils_Load_Module_loggerFxn1))0)
Definition at line 6769 of file dss_mrr_pe674.c.
```

4.16.4.621 ti_sysbios_utils_Load_Module_loggerFxn2_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerFxn2) ti_sysbios_utils_Load_Module_loggerFxn2_C = ((CT(ti_sysbios_utils_Load_Module_loggerFxn2))0)
Definition at line 6773 of file dss_mrr_pe674.c.
```

4.16.4.622 ti_sysbios_utils_Load_Module_loggerFxn4_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerFxn4) ti_sysbios_utils_Load_Module_loggerFxn4_C = ((CT(ti_sysbios_utils_Load_Module_loggerFxn4))0)
Definition at line 6777 of file dss_mrr_pe674.c.
```

4.16.4.623 ti_sysbios_utils_Load_Module_loggerFxn8_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Module_loggerFxn8) ti_sysbios_utils_Load_Module_loggerFxn8_C = ((CT(ti_sysbios_utils_Load_Module_loggerFxn8))0)
```

Definition at line 6781 of file dss_mrr_pe674.c.

4.16.4.624 ti_sysbios_utils_Load_Module_loggerObj_C

```
const __FAR__ CT__ti_sysbios_utils_Load_Module_loggerObj ti_sysbios_utils_Load_Module__←
loggerObj__C = ((CT__ti_sysbios_utils_Load_Module_loggerObj)0)
```

Definition at line 6761 of file dss_mrr_pe674.c.

4.16.4.625 ti_sysbios_utils_Load_Module_state_V

```
ti_sysbios_utils_Load_Module_State ti_sysbios_utils_Load_Module_state_V
```

Definition at line 1718 of file dss_mrr_pe674.c.

4.16.4.626 ti_sysbios_utils_Load_Module_State_0_runningTask_A

```
__T1_ti_sysbios_utils_Load_Module_State_runningTask ti_sysbios_utils_Load_Module_State_0__←
runningTask__A
```

Initial value:

```
= {  
    0,  
}
```

Definition at line 1712 of file dss_mrr_pe674.c.

4.16.4.627 ti_sysbios_utils_Load_Module_State_0_taskEnv_A

```
__T1_ti_sysbios_utils_Load_Module_State_taskEnv ti_sysbios_utils_Load_Module_State_0_taskEnv__←
Env__A
```

Initial value:

```
= {  
    {  
        {  
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_utils_Load_Module_State_0_taskEnv__A[0].qElem)),  
            ((ti_sysbios_knl_Queue_Elem*) ((void*)&ti_sysbios_utils_Load_Module_State_0_taskEnv__A[0].qElem)),  
            (xdc_UInt32)0x0,  
            (xdc_UInt32)0x0,  
            (xdc_UInt32)0x0,  
            (xdc_UInt32)0x0,  
            ((xdc_Ptr)0),  
        },  
    },  
}
```

Definition at line 1715 of file dss_mrr_pe674.c.

4.16.4.628 ti_sysbios_utils_Load_Module_State_0_taskStartTime_A

```
__T1_ti_sysbios_utils_Load_Module_State_taskStartTime ti_sysbios_utils_Load_Module_State_0__←
taskStartTime__A
```

Initial value:

```
= {  
    (xdc_UInt32)0x0,  
}
```

Definition at line 1709 of file dss_mrr_pe674.c.

4.16.4.629 ti_sysbios_utils_Load_Module_State_taskList_O

```
const __FAR__ xdc_SizeT ti_sysbios_utils_Load_Module_State_taskList__O = offsetof( ti_sysbios__←
_utils_Load_Module_State__, Object_field_taskList)
```

Definition at line 1977 of file dss_mrr_pe674.c.

4.16.4.630 ti_sysbios_utils_Load_Object_count_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Object_count) ti_sysbios_utils_Load_Object_count_C = 0
```

Definition at line 6785 of file dss_mrr_pe674.c.

4.16.4.631 ti_sysbios_utils_Load_Object_heap_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Object_heap) ti_sysbios_utils_Load_Object_heap_C = 0
```

Definition at line 6789 of file dss_mrr_pe674.c.

4.16.4.632 ti_sysbios_utils_Load_Object_sizeof_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Object_sizeof) ti_sysbios_utils_Load_Object_sizeof_C = 0
```

Definition at line 6793 of file dss_mrr_pe674.c.

4.16.4.633 ti_sysbios_utils_Load_Object_table_C

```
const __FAR__ CT(ti_sysbios_utils_Load_Object_table) ti_sysbios_utils_Load_Object_table_C = 0
```

Definition at line 6797 of file dss_mrr_pe674.c.

4.16.4.634 ti_sysbios_utils_Load_postUpdate_C

```
const __FAR__ CT(ti_sysbios_utils_Load_postUpdate) ti_sysbios_utils_Load_postUpdate_C = ((CT(ti_sysbios_utils_Load_postUpdate)0)
```

Definition at line 6817 of file dss_mrr_pe674.c.

4.16.4.635 ti_sysbios_utils_Load_swiEnabled_C

```
const __FAR__ CT(ti_sysbios_utils_Load_swiEnabled) ti_sysbios_utils_Load_swiEnabled_C = 0
```

Definition at line 6833 of file dss_mrr_pe674.c.

4.16.4.636 ti_sysbios_utils_Load_taskEnabled_C

```
const __FAR__ CT(ti_sysbios_utils_Load_taskEnabled) ti_sysbios_utils_Load_taskEnabled_C = 1
```

Definition at line 6837 of file dss_mrr_pe674.c.

4.16.4.637 ti_sysbios_utils_Load_updateInIdle_C

```
const __FAR__ CT(ti_sysbios_utils_Load_updateInIdle) ti_sysbios_utils_Load_updateInIdle_C = 0
```

Definition at line 6821 of file dss_mrr_pe674.c.

4.16.4.638 ti_sysbios_utils_Load_windowInMs_C

```
const __FAR__ CT(ti_sysbios_utils_Load_windowInMs) ti_sysbios_utils_Load_windowInMs_C = (xdcc_UInt)0x1f4
```

Definition at line 6825 of file dss_mrr_pe674.c.

4.16.4.639 xdc_runtime Assert_E assertFailed_C

```
const __FAR__ CT_xdc_runtime Assert_E assertFailed xdc_runtime Assert_E assertFailed_C =
(((xdc_runtime_Error_Id)1) << 16 | 0)
Definition at line 6918 of file dss_mrr_pe674.c.
```

4.16.4.640 xdc_runtime Assert_Module diagsEnabled_C

```
const __FAR__ CT_xdc_runtime Assert_Module diagsEnabled xdc_runtime Assert_Module diagsEnabled_C =
(xdc_Bits32)0x10
Definition at line 6850 of file dss_mrr_pe674.c.
```

4.16.4.641 xdc_runtime Assert_Module diagsIncluded_C

```
const __FAR__ CT_xdc_runtime Assert_Module diagsIncluded xdc_runtime Assert_Module diagsIncluded_C =
(xdc_Bits32)0x10
Definition at line 6854 of file dss_mrr_pe674.c.
```

4.16.4.642 xdc_runtime Assert_Module diagsMask_C

```
const __FAR__ CT_xdc_runtime Assert_Module diagsMask xdc_runtime Assert_Module diagsMask_C =
((CT_xdc_runtime Assert_Module diagsMask)0)
Definition at line 6858 of file dss_mrr_pe674.c.
```

4.16.4.643 xdc_runtime Assert_Module gateObj_C

```
const __FAR__ CT_xdc_runtime Assert_Module gateObj xdc_runtime Assert_Module gateObj_C =
((CT_xdc_runtime Assert_Module gateObj)0)
Definition at line 6862 of file dss_mrr_pe674.c.
```

4.16.4.644 xdc_runtime Assert_Module gatePrms_C

```
const __FAR__ CT_xdc_runtime Assert_Module gatePrms xdc_runtime Assert_Module gatePrms_C =
((CT_xdc_runtime Assert_Module gatePrms)0)
Definition at line 6866 of file dss_mrr_pe674.c.
```

4.16.4.645 xdc_runtime Assert_Module id_C

```
const __FAR__ CT_xdc_runtime Assert_Module id xdc_runtime Assert_Module id_C = (xdc_Bits16)0x1
Definition at line 6870 of file dss_mrr_pe674.c.
```

4.16.4.646 xdc_runtime Assert_Module loggerDefined_C

```
const __FAR__ CT_xdc_runtime Assert_Module loggerDefined xdc_runtime Assert_Module loggerDefined_C =
0
Definition at line 6874 of file dss_mrr_pe674.c.
```

4.16.4.647 xdc_runtime Assert_Module loggerFxn0_C

```
const __FAR__ CT_xdc_runtime Assert_Module loggerFxn0 xdc_runtime Assert_Module loggerFxn0_C =
((CT_xdc_runtime Assert_Module loggerFxn0)0)
```

Definition at line 6882 of file dss_mrr_pe674.c.

4.16.4.648 **xdc_runtime Assert Module loggerFxn1_C**

```
const __FAR__ CT_xdc_runtime Assert_Module_loggerFxn1 xdc_runtime Assert_Module_logger←
Fxn1_C = ((CT_xdc_runtime Assert_Module_loggerFxn1) 0)
```

Definition at line 6886 of file dss_mrr_pe674.c.

4.16.4.649 **xdc_runtime Assert Module loggerFxn2_C**

```
const __FAR__ CT_xdc_runtime Assert_Module_loggerFxn2 xdc_runtime Assert_Module_logger←
Fxn2_C = ((CT_xdc_runtime Assert_Module_loggerFxn2) 0)
```

Definition at line 6890 of file dss_mrr_pe674.c.

4.16.4.650 **xdc_runtime Assert Module loggerFxn4_C**

```
const __FAR__ CT_xdc_runtime Assert_Module_loggerFxn4 xdc_runtime Assert_Module_logger←
Fxn4_C = ((CT_xdc_runtime Assert_Module_loggerFxn4) 0)
```

Definition at line 6894 of file dss_mrr_pe674.c.

4.16.4.651 **xdc_runtime Assert Module loggerFxn8_C**

```
const __FAR__ CT_xdc_runtime Assert_Module_loggerFxn8 xdc_runtime Assert_Module_logger←
Fxn8_C = ((CT_xdc_runtime Assert_Module_loggerFxn8) 0)
```

Definition at line 6898 of file dss_mrr_pe674.c.

4.16.4.652 **xdc_runtime Assert Module loggerObj_C**

```
const __FAR__ CT_xdc_runtime Assert_Module_loggerObj xdc_runtime Assert_Module_loggerObj←
_C = ((CT_xdc_runtime Assert_Module_loggerObj) 0)
```

Definition at line 6878 of file dss_mrr_pe674.c.

4.16.4.653 **xdc_runtime Assert Object count_C**

```
const __FAR__ CT_xdc_runtime Assert_Object_count xdc_runtime Assert_Object_count_C = 0
```

Definition at line 6902 of file dss_mrr_pe674.c.

4.16.4.654 **xdc_runtime Assert Object heap_C**

```
const __FAR__ CT_xdc_runtime Assert_Object_heap xdc_runtime Assert_Object_heap_C = 0
```

Definition at line 6906 of file dss_mrr_pe674.c.

4.16.4.655 **xdc_runtime Assert Object sizeof_C**

```
const __FAR__ CT_xdc_runtime Assert_Object_sizeof xdc_runtime Assert_Object_sizeof_C = 0
```

Definition at line 6910 of file dss_mrr_pe674.c.

4.16.4.656 xdc_runtime Assert Object table C

```
const __FAR__ CT_xdc_runtime Assert Object table xdc_runtime Assert Object table_C = 0
Definition at line 6914 of file dss_mrr_pe674.c.
```

4.16.4.657 xdc_runtime Core A initializedParams C

```
const __FAR__ CT_xdc_runtime Core A initializedParams xdc_runtime Core A initializedParams_C =
((xdc_runtime Assert Id) 0) << 16 | 16)
Definition at line 6995 of file dss_mrr_pe674.c.
```

4.16.4.658 xdc_runtime Core Module diagsEnabled C

```
const __FAR__ CT_xdc_runtime Core Module diagsEnabled xdc_runtime Core Module diagsEnabled_C =
(xdc_Bits32) 0x10
Definition at line 6927 of file dss_mrr_pe674.c.
```

4.16.4.659 xdc_runtime Core Module diagsIncluded C

```
const __FAR__ CT_xdc_runtime Core Module diagsIncluded xdc_runtime Core Module diagsIncluded_C =
(xdc_Bits32) 0x10
Definition at line 6931 of file dss_mrr_pe674.c.
```

4.16.4.660 xdc_runtime Core Module diagsMask C

```
const __FAR__ CT_xdc_runtime Core Module diagsMask xdc_runtime Core Module diagsMask_C =
((CT_xdc_runtime Core Module diagsMask) 0)
Definition at line 6935 of file dss_mrr_pe674.c.
```

4.16.4.661 xdc_runtime Core Module gateObj C

```
const __FAR__ CT_xdc_runtime Core Module gateObj xdc_runtime Core Module gateObj_C =
((CT_xdc_runtime Core Module gateObj) 0)
Definition at line 6939 of file dss_mrr_pe674.c.
```

4.16.4.662 xdc_runtime Core Module gatePrms C

```
const __FAR__ CT_xdc_runtime Core Module gatePrms xdc_runtime Core Module gatePrms_C =
((CT_xdc_runtime Core Module gatePrms) 0)
Definition at line 6943 of file dss_mrr_pe674.c.
```

4.16.4.663 xdc_runtime Core Module id C

```
const __FAR__ CT_xdc_runtime Core Module id xdc_runtime Core Module id_C = (xdc_Bits16) 0x2
Definition at line 6947 of file dss_mrr_pe674.c.
```

4.16.4.664 xdc_runtime Core Module loggerDefined C

```
const __FAR__ CT_xdc_runtime Core Module loggerDefined xdc_runtime Core Module loggerDefined_C =
Defined_C = 0
Definition at line 6951 of file dss_mrr_pe674.c.
```

4.16.4.665 xdc_runtime_Core_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn0 xdc_runtime_Core_Module_loggerFxn0_C =
((CT_xdc_runtime_Core_Module_loggerFxn0)0)
Definition at line 6959 of file dss_mrr_pe674.c.
```

4.16.4.666 xdc_runtime_Core_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn1 xdc_runtime_Core_Module_loggerFxn1_C =
((CT_xdc_runtime_Core_Module_loggerFxn1)0)
Definition at line 6963 of file dss_mrr_pe674.c.
```

4.16.4.667 xdc_runtime_Core_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn2 xdc_runtime_Core_Module_loggerFxn2_C =
((CT_xdc_runtime_Core_Module_loggerFxn2)0)
Definition at line 6967 of file dss_mrr_pe674.c.
```

4.16.4.668 xdc_runtime_Core_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn4 xdc_runtime_Core_Module_loggerFxn4_C =
((CT_xdc_runtime_Core_Module_loggerFxn4)0)
Definition at line 6971 of file dss_mrr_pe674.c.
```

4.16.4.669 xdc_runtime_Core_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerFxn8 xdc_runtime_Core_Module_loggerFxn8_C =
((CT_xdc_runtime_Core_Module_loggerFxn8)0)
Definition at line 6975 of file dss_mrr_pe674.c.
```

4.16.4.670 xdc_runtime_Core_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Core_Module_loggerObj xdc_runtime_Core_Module_loggerObj_C =
((CT_xdc_runtime_Core_Module_loggerObj)0)
Definition at line 6955 of file dss_mrr_pe674.c.
```

4.16.4.671 xdc_runtime_Core_Object_count_C

```
const __FAR__ CT_xdc_runtime_Core_Object_count xdc_runtime_Core_Object_count_C = 0
Definition at line 6979 of file dss_mrr_pe674.c.
```

4.16.4.672 xdc_runtime_Core_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Core_Object_heap xdc_runtime_Core_Object_heap_C = 0
Definition at line 6983 of file dss_mrr_pe674.c.
```

4.16.4.673 xdc_runtime_Core_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Core_Object_sizeof xdc_runtime_Core_Object_sizeof_C = 0
Definition at line 6987 of file dss_mrr_pe674.c.
```

4.16.4.674 xdc_runtime_Core_Object_table_C

```
const __FAR__ CT_xdc_runtime_Core_Object_table xdc_runtime_Core_Object_table_C = 0
Definition at line 6991 of file dss_mrr_pe674.c.
```

4.16.4.675 xdc_runtime_Defaults_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_diagsEnabled xdc_runtime_Defaults_Module_diagsEnabled_C = (xdc_Bits32)0x90
Definition at line 7004 of file dss_mrr_pe674.c.
```

4.16.4.676 xdc_runtime_Defaults_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_diagsIncluded xdc_runtime_Defaults_Module_diagsIncluded_C = (xdc_Bits32)0x90
Definition at line 7008 of file dss_mrr_pe674.c.
```

4.16.4.677 xdc_runtime_Defaults_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_diagsMask xdc_runtime_Defaults_Module_diagsMask_C = ((CT_xdc_runtime_Defaults_Module_diagsMask)0)
Definition at line 7012 of file dss_mrr_pe674.c.
```

4.16.4.678 xdc_runtime_Defaults_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_gateObj xdc_runtime_Defaults_Module_gateObj_C = ((CT_xdc_runtime_Defaults_Module_gateObj)0)
Definition at line 7016 of file dss_mrr_pe674.c.
```

4.16.4.679 xdc_runtime_Defaults_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_gatePrms xdc_runtime_Defaults_Module_gatePrms_C = ((CT_xdc_runtime_Defaults_Module_gatePrms)0)
Definition at line 7020 of file dss_mrr_pe674.c.
```

4.16.4.680 xdc_runtime_Defaults_Module_id_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_id xdc_runtime_Defaults_Module_id_C = (xdc_Bits16)0x3
Definition at line 7024 of file dss_mrr_pe674.c.
```

4.16.4.681 xdc_runtime_Defaults_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerDefined xdc_runtime_Defaults_Module_loggerDefined_C = 0
Definition at line 7028 of file dss_mrr_pe674.c.
```

4.16.4.682 xdc_runtime_Defaults_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn0 xdc_runtime_Defaults_Module_loggerFxn0_C = ((CT_xdc_runtime_Defaults_Module_loggerFxn0)0)
Definition at line 7036 of file dss_mrr_pe674.c.
```

4.16.4.683 xdc_runtime_Defaults_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn1 xdc_runtime_Defaults_Module_←  
loggerFxn1_C = ((CT_xdc_runtime_Defaults_Module_loggerFxn1)0)  
Definition at line 7040 of file dss_mrr_pe674.c.
```

4.16.4.684 xdc_runtime_Defaults_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn2 xdc_runtime_Defaults_Module_←  
loggerFxn2_C = ((CT_xdc_runtime_Defaults_Module_loggerFxn2)0)  
Definition at line 7044 of file dss_mrr_pe674.c.
```

4.16.4.685 xdc_runtime_Defaults_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn4 xdc_runtime_Defaults_Module_←  
loggerFxn4_C = ((CT_xdc_runtime_Defaults_Module_loggerFxn4)0)  
Definition at line 7048 of file dss_mrr_pe674.c.
```

4.16.4.686 xdc_runtime_Defaults_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerFxn8 xdc_runtime_Defaults_Module_←  
loggerFxn8_C = ((CT_xdc_runtime_Defaults_Module_loggerFxn8)0)  
Definition at line 7052 of file dss_mrr_pe674.c.
```

4.16.4.687 xdc_runtime_Defaults_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Defaults_Module_loggerObj xdc_runtime_Defaults_Module_logger←  
Obj_C = ((CT_xdc_runtime_Defaults_Module_loggerObj)0)  
Definition at line 7032 of file dss_mrr_pe674.c.
```

4.16.4.688 xdc_runtime_Defaults_Object_count_C

```
const __FAR__ CT_xdc_runtime_Defaults_Object_count xdc_runtime_Defaults_Object_count_C = 0  
Definition at line 7056 of file dss_mrr_pe674.c.
```

4.16.4.689 xdc_runtime_Defaults_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Defaults_Object_heap xdc_runtime_Defaults_Object_heap_C = 0  
Definition at line 7060 of file dss_mrr_pe674.c.
```

4.16.4.690 xdc_runtime_Defaults_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Defaults_Object_sizeof xdc_runtime_Defaults_Object_sizeof_C =  
0  
Definition at line 7064 of file dss_mrr_pe674.c.
```

4.16.4.691 xdc_runtime_Defaults_Object_table_C

```
const __FAR__ CT_xdc_runtime_Defaults_Object_table xdc_runtime_Defaults_Object_table_C = 0
Definition at line 7068 of file dss_mrr_pe674.c.
```

4.16.4.692 xdc_runtime_Diags_dictBase_C

```
const __FAR__ CT_xdc_runtime_Diags_dictBase xdc_runtime_Diags_dictBase_C = ((CT_xdc_runtime_Diags_dictBase) 0)
Definition at line 7149 of file dss_mrr_pe674.c.
```

4.16.4.693 xdc_runtime_Diags_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_diagsEnabled xdc_runtime_Diags_Module_diagsEnabled_C = (xdc_Bits32) 0x10
Definition at line 7077 of file dss_mrr_pe674.c.
```

4.16.4.694 xdc_runtime_Diags_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_diagsIncluded xdc_runtime_Diags_Module_diagsIncluded_C = (xdc_Bits32) 0x10
Definition at line 7081 of file dss_mrr_pe674.c.
```

4.16.4.695 xdc_runtime_Diags_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_diagsMask xdc_runtime_Diags_Module_diagsMask_C = ((CT_xdc_runtime_Diags_Module_diagsMask) 0)
Definition at line 7085 of file dss_mrr_pe674.c.
```

4.16.4.696 xdc_runtime_Diags_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_gateObj xdc_runtime_Diags_Module_gateObj_C = ((CT_xdc_runtime_Diags_Module_gateObj) 0)
Definition at line 7089 of file dss_mrr_pe674.c.
```

4.16.4.697 xdc_runtime_Diags_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_gatePrms xdc_runtime_Diags_Module_gatePrms_C = ((CT_xdc_runtime_Diags_Module_gatePrms) 0)
Definition at line 7093 of file dss_mrr_pe674.c.
```

4.16.4.698 xdc_runtime_Diags_Module_id_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_id xdc_runtime_Diags_Module_id_C = (xdc_Bits16) 0x4
Definition at line 7097 of file dss_mrr_pe674.c.
```

4.16.4.699 xdc_runtime_Diags_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerDefined xdc_runtime_Diags_Module_loggerDefined_C = 0
Definition at line 7101 of file dss_mrr_pe674.c.
```

4.16.4.700 xdc_runtime_Diags_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn0 xdc_runtime_Diags_Module_loggerFxn0_C = ((CT_xdc_runtime_Diags_Module_loggerFxn0) 0)
Definition at line 7109 of file dss_mrr_pe674.c.
```

4.16.4.701 xdc_runtime_Diags_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn1 xdc_runtime_Diags_Module_loggerFxn1_C = ((CT_xdc_runtime_Diags_Module_loggerFxn1) 0)
Definition at line 7113 of file dss_mrr_pe674.c.
```

4.16.4.702 xdc_runtime_Diags_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn2 xdc_runtime_Diags_Module_loggerFxn2_C = ((CT_xdc_runtime_Diags_Module_loggerFxn2) 0)
Definition at line 7117 of file dss_mrr_pe674.c.
```

4.16.4.703 xdc_runtime_Diags_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn4 xdc_runtime_Diags_Module_loggerFxn4_C = ((CT_xdc_runtime_Diags_Module_loggerFxn4) 0)
Definition at line 7121 of file dss_mrr_pe674.c.
```

4.16.4.704 xdc_runtime_Diags_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerFxn8 xdc_runtime_Diags_Module_loggerFxn8_C = ((CT_xdc_runtime_Diags_Module_loggerFxn8) 0)
Definition at line 7125 of file dss_mrr_pe674.c.
```

4.16.4.705 xdc_runtime_Diags_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Diags_Module_loggerObj xdc_runtime_Diags_Module_loggerObj_C = ((CT_xdc_runtime_Diags_Module_loggerObj) 0)
Definition at line 7105 of file dss_mrr_pe674.c.
```

4.16.4.706 xdc_runtime_Diags_Object_count_C

```
const __FAR__ CT_xdc_runtime_Diags_Object_count xdc_runtime_Diags_Object_count_C = 0
Definition at line 7129 of file dss_mrr_pe674.c.
```

4.16.4.707 xdc_runtime_Diags_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Diags_Object_heap xdc_runtime_Diags_Object_heap_C = 0
Definition at line 7133 of file dss_mrr_pe674.c.
```

4.16.4.708 xdc_runtime_Diags_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Diags_Object_sizeof xdc_runtime_Diags_Object_sizeof_C = 0
Definition at line 7137 of file dss_mrr_pe674.c.
```

4.16.4.709 xdc_runtime_Diags_Object_table_C

```
const __FAR__ CT_xdc_runtime_Diags_Object_table xdc_runtime_Diags_Object_table_C = 0
Definition at line 7141 of file dss_mrr_pe674.c.
```

4.16.4.710 xdc_runtime_Diags_setMaskEnabled_C

```
const __FAR__ CT_xdc_runtime_Diags_setMaskEnabled xdc_runtime_Diags_setMaskEnabled_C = 0
Definition at line 7145 of file dss_mrr_pe674.c.
```

4.16.4.711 xdc_runtime_Error_E_generic_C

```
const __FAR__ CT_xdc_runtime_Error_E_generic xdc_runtime_Error_E_generic_C = (((xdc_runtime<-
_Error_Id)2) << 16 | 0)
Definition at line 7245 of file dss_mrr_pe674.c.
```

4.16.4.712 xdc_runtime_Error_E_memory_C

```
const __FAR__ CT_xdc_runtime_Error_E_memory xdc_runtime_Error_E_memory_C = (((xdc_runtime<-
_Error_Id)3) << 16 | 0)
Definition at line 7249 of file dss_mrr_pe674.c.
```

4.16.4.713 xdc_runtime_Error_E_msgCode_C

```
const __FAR__ CT_xdc_runtime_Error_E_msgCode xdc_runtime_Error_E_msgCode_C = (((xdc_runtime<-
_Error_Id)4) << 16 | 0)
Definition at line 7253 of file dss_mrr_pe674.c.
```

4.16.4.714 xdc_runtime_Error_IgnoreBlock

```
xdc_runtime_Error_Block xdc_runtime_Error_IgnoreBlock
Initial value:
= {
    0,
    {
        {0, 0}
    },
    0,
    0,
    {
        0,
        0,
        0
    }
}
```

Definition at line 2034 of file dss_mrr_pe674.c.

4.16.4.715 xdc_runtime_Error_maxDepth_C

```
const __FAR__ CT_xdc_runtime_Error_maxDepth xdc_runtime_Error_maxDepth_C = (xdc_UInt16)0x10
Definition at line 7265 of file dss_mrr_pe674.c.
```

4.16.4.716 xdc_runtime_Error_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Error_Module_diagsEnabled xdc_runtime_Error_Module_diags<-
Enabled_C = (xdc_Bits32)0x90
Definition at line 7173 of file dss_mrr_pe674.c.
```

4.16.4.717 xdc_runtime_Error_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Error_Module_diagsIncluded xdc_runtime_Error_Module_diagsIncluded_C = ((xdc_Bits32)0x90)
```

Definition at line 7177 of file dss_mrr_pe674.c.

4.16.4.718 xdc_runtime_Error_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Error_Module_diagsMask xdc_runtime_Error_Module_diagsMask_C = ((CT_xdc_runtime_Error_Module_diagsMask)0)
```

Definition at line 7181 of file dss_mrr_pe674.c.

4.16.4.719 xdc_runtime_Error_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Error_Module_gateObj xdc_runtime_Error_Module_gateObj_C = ((CT_xdc_runtime_Error_Module_gateObj)0)
```

Definition at line 7185 of file dss_mrr_pe674.c.

4.16.4.720 xdc_runtime_Error_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Error_Module_gatePrms xdc_runtime_Error_Module_gatePrms_C = ((CT_xdc_runtime_Error_Module_gatePrms)0)
```

Definition at line 7189 of file dss_mrr_pe674.c.

4.16.4.721 xdc_runtime_Error_Module_id_C

```
const __FAR__ CT_xdc_runtime_Error_Module_id xdc_runtime_Error_Module_id_C = (xdc_Bits16)0x5
```

Definition at line 7193 of file dss_mrr_pe674.c.

4.16.4.722 xdc_runtime_Error_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerDefined xdc_runtime_Error_Module_loggerDefined_C = 0
```

Definition at line 7197 of file dss_mrr_pe674.c.

4.16.4.723 xdc_runtime_Error_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn0 xdc_runtime_Error_Module_loggerFxn0_C = ((CT_xdc_runtime_Error_Module_loggerFxn0)0)
```

Definition at line 7205 of file dss_mrr_pe674.c.

4.16.4.724 xdc_runtime_Error_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn1 xdc_runtime_Error_Module_loggerFxn1_C = ((CT_xdc_runtime_Error_Module_loggerFxn1)0)
```

Definition at line 7209 of file dss_mrr_pe674.c.

4.16.4.725 xdc_runtime_Error_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn2 xdc_runtime_Error_Module_loggerFxn2_C = ((CT_xdc_runtime_Error_Module_loggerFxn2) 0)
Definition at line 7213 of file dss_mrr_pe674.c.
```

4.16.4.726 xdc_runtime_Error_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn4 xdc_runtime_Error_Module_loggerFxn4_C = ((CT_xdc_runtime_Error_Module_loggerFxn4) 0)
Definition at line 7217 of file dss_mrr_pe674.c.
```

4.16.4.727 xdc_runtime_Error_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerFxn8 xdc_runtime_Error_Module_loggerFxn8_C = ((CT_xdc_runtime_Error_Module_loggerFxn8) 0)
Definition at line 7221 of file dss_mrr_pe674.c.
```

4.16.4.728 xdc_runtime_Error_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Error_Module_loggerObj xdc_runtime_Error_Module_loggerObj_C = ((CT_xdc_runtime_Error_Module_loggerObj) 0)
Definition at line 7201 of file dss_mrr_pe674.c.
```

4.16.4.729 xdc_runtime_Error_Module_state_V

xdc_runtime_Error_Module_State xdc_runtime_Error_Module_state_V
Initial value:
`= {
 (xdc_UInt16) 0x0,
}`

Definition at line 1751 of file dss_mrr_pe674.c.

4.16.4.730 xdc_runtime_Error_Object_count_C

```
const __FAR__ CT_xdc_runtime_Error_Object_count xdc_runtime_Error_Object_count_C = 0
Definition at line 7225 of file dss_mrr_pe674.c.
```

4.16.4.731 xdc_runtime_Error_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Error_Object_heap xdc_runtime_Error_Object_heap_C = 0
Definition at line 7229 of file dss_mrr_pe674.c.
```

4.16.4.732 xdc_runtime_Error_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Error_Object_sizeof xdc_runtime_Error_Object_sizeof_C = 0
Definition at line 7233 of file dss_mrr_pe674.c.
```

4.16.4.733 xdc_runtime_Error_Object_table_C

```
const __FAR__ CT_xdc_runtime_Error_Object_table xdc_runtime_Error_Object_table_C = 0
Definition at line 7237 of file dss_mrr_pe674.c.
```

4.16.4.734 xdc_runtime_Error_policy_C

```
const __FAR__ CT_xdc_runtime_Error_policy xdc_runtime_Error_policy_C = xdc_runtime_Error_U←  
NWIN
```

Definition at line 7257 of file dss_mrr_pe674.c.

4.16.4.735 xdc_runtime_Error_policyFxn_C

```
const __FAR__ CT_xdc_runtime_Error_policyFxn xdc_runtime_Error_policyFxn_C = ((CT_xdc←  
runtime_Error_policyFxn)((xdc_Fxn)xdc_runtime_Error_policyDefault_E))
```

Definition at line 7241 of file dss_mrr_pe674.c.

4.16.4.736 xdc_runtime_Error_raiseHook_C

```
const __FAR__ CT_xdc_runtime_Error_raiseHook xdc_runtime_Error_raiseHook_C = ((CT_xdc←  
runtime_Error_raiseHook)((xdc_Fxn)ti_sysbios_BIOS_errorRaiseHook_I))
```

Definition at line 7261 of file dss_mrr_pe674.c.

4.16.4.737 xdc_runtime_Gate_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_diagsEnabled xdc_runtime_Gate_Module_diagsEnabled_C =  
(xdc_Bits32)0x10
```

Definition at line 7274 of file dss_mrr_pe674.c.

4.16.4.738 xdc_runtime_Gate_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_diagsIncluded xdc_runtime_Gate_Module_diagsIncluded_C =  
(xdc_Bits32)0x10
```

Definition at line 7278 of file dss_mrr_pe674.c.

4.16.4.739 xdc_runtime_Gate_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_diagsMask xdc_runtime_Gate_Module_diagsMask_C =  
(CT_xdc_runtime_Gate_Module_diagsMask)0
```

Definition at line 7282 of file dss_mrr_pe674.c.

4.16.4.740 xdc_runtime_Gate_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_gateObj xdc_runtime_Gate_Module_gateObj_C = ((C←  
T_xdc_runtime_Gate_Module_gateObj)0)
```

Definition at line 7286 of file dss_mrr_pe674.c.

4.16.4.741 xdc_runtime_Gate_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_gatePrms xdc_runtime_Gate_Module_gatePrms_C =  
(CT_xdc_runtime_Gate_Module_gatePrms)0
```

Definition at line 7290 of file dss_mrr_pe674.c.

4.16.4.742 xdc_runtime_Gate_Module_id_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_id xdc_runtime_Gate_Module_id_C = (xdc_Bits16)0x6
```

Definition at line 7294 of file dss_mrr_pe674.c.

4.16.4.743 xdc_runtime_Gate_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerDefined xdc_runtime_Gate_Module_loggerDefined_C = 0
```

Definition at line 7298 of file dss_mrr_pe674.c.

4.16.4.744 xdc_runtime_Gate_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn0 xdc_runtime_Gate_Module_loggerFxn0_C = ((CT_xdc_runtime_Gate_Module_loggerFxn0)0)
```

Definition at line 7306 of file dss_mrr_pe674.c.

4.16.4.745 xdc_runtime_Gate_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn1 xdc_runtime_Gate_Module_loggerFxn1_C = ((CT_xdc_runtime_Gate_Module_loggerFxn1)0)
```

Definition at line 7310 of file dss_mrr_pe674.c.

4.16.4.746 xdc_runtime_Gate_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn2 xdc_runtime_Gate_Module_loggerFxn2_C = ((CT_xdc_runtime_Gate_Module_loggerFxn2)0)
```

Definition at line 7314 of file dss_mrr_pe674.c.

4.16.4.747 xdc_runtime_Gate_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn4 xdc_runtime_Gate_Module_loggerFxn4_C = ((CT_xdc_runtime_Gate_Module_loggerFxn4)0)
```

Definition at line 7318 of file dss_mrr_pe674.c.

4.16.4.748 xdc_runtime_Gate_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerFxn8 xdc_runtime_Gate_Module_loggerFxn8_C = ((CT_xdc_runtime_Gate_Module_loggerFxn8)0)
```

Definition at line 7322 of file dss_mrr_pe674.c.

4.16.4.749 xdc_runtime_Gate_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Gate_Module_loggerObj xdc_runtime_Gate_Module_loggerObj_C = ((CT_xdc_runtime_Gate_Module_loggerObj)0)
```

Definition at line 7302 of file dss_mrr_pe674.c.

4.16.4.750 xdc_runtime_Gate_Object_count_C

```
const __FAR__ CT_xdc_runtime_Gate_Object_count xdc_runtime_Gate_Object_count_C = 0
```

Definition at line 7326 of file dss_mrr_pe674.c.

4.16.4.751 xdc_runtime_Gate_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Gate_Object_heap xdc_runtime_Gate_Object_heap_C = 0
Definition at line 7330 of file dss_mrr_pe674.c.
```

4.16.4.752 xdc_runtime_Gate_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Gate_Object_sizeof xdc_runtime_Gate_Object_sizeof_C = 0
Definition at line 7334 of file dss_mrr_pe674.c.
```

4.16.4.753 xdc_runtime_Gate_Object_table_C

```
const __FAR__ CT_xdc_runtime_Gate_Object_table xdc_runtime_Gate_Object_table_C = 0
Definition at line 7338 of file dss_mrr_pe674.c.
```

4.16.4.754 xdc_runtime_IGateProvider_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_IGateProvider_Interface_BASE_C = {& xdc_runtime_IGateProvider_Interface_C}
Definition at line 759 of file dss_mrr_pe674.c.
```

4.16.4.755 xdc_runtime_IHeap_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_IHeap_Interface_BASE_C = {& xdc_runtime_IHeap_C}
Definition at line 750 of file dss_mrr_pe674.c.
```

4.16.4.756 xdc_runtime_IModule_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_IModule_Interface_BASE_C = {0}
Definition at line 762 of file dss_mrr_pe674.c.
```

4.16.4.757 xdc_runtime_ISystemSupport_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_ISystemSupport_Interface_BASE_C = {& xdc_runtime_ISystemSupport_C}
Definition at line 753 of file dss_mrr_pe674.c.
```

4.16.4.758 xdc_runtime_ITimestampClient_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_ITimestampClient_Interface_BASE_C = {& xdc_runtime_ITimestampClient_C}
Definition at line 765 of file dss_mrr_pe674.c.
```

4.16.4.759 xdc_runtime_ITimestampProvider_Interface_BASE_C

```
const __FAR__ xdc_runtime_Types_Base xdc_runtime_ITimestampProvider_Interface_BASE_C = {& xdc_runtime_ITimestampProvider_C}
Definition at line 756 of file dss_mrr_pe674.c.
```

4.16.4.760 xdc_runtime_Log_L_construct_C

```
const __FAR__ CT_xdc_runtime_Log_L_construct xdc_runtime_Log_L_construct_C = (((xdc_runtime->
_Log_Event)1) << 16 | 4)
```

Definition at line 7415 of file dss_mrr_pe674.c.

4.16.4.761 xdc_runtime_Log_L_create_C

```
const __FAR__ CT_xdc_runtime_Log_L_create xdc_runtime_Log_L_create_C = (((xdc_runtime_Log->
Event)2) << 16 | 4)
```

Definition at line 7419 of file dss_mrr_pe674.c.

4.16.4.762 xdc_runtime_Log_L_delete_C

```
const __FAR__ CT_xdc_runtime_Log_L_delete xdc_runtime_Log_L_delete_C = (((xdc_runtime_Log->
Event)4) << 16 | 4)
```

Definition at line 7427 of file dss_mrr_pe674.c.

4.16.4.763 xdc_runtime_Log_L_destruct_C

```
const __FAR__ CT_xdc_runtime_Log_L_destruct xdc_runtime_Log_L_destruct_C = (((xdc_runtime->
Log_Event)3) << 16 | 4)
```

Definition at line 7423 of file dss_mrr_pe674.c.

4.16.4.764 xdc_runtime_Log_L_error_C

```
const __FAR__ CT_xdc_runtime_Log_L_error xdc_runtime_Log_L_error_C = (((xdc_runtime_Log->
Event)5) << 16 | 192)
```

Definition at line 7431 of file dss_mrr_pe674.c.

4.16.4.765 xdc_runtime_Log_L_info_C

```
const __FAR__ CT_xdc_runtime_Log_L_info xdc_runtime_Log_L_info_C = (((xdc_runtime_Log->
Event)7) << 16 | 16384)
```

Definition at line 7439 of file dss_mrr_pe674.c.

4.16.4.766 xdc_runtime_Log_L_start_C

```
const __FAR__ CT_xdc_runtime_Log_L_start xdc_runtime_Log_L_start_C = (((xdc_runtime_Log->
Event)8) << 16 | 32768)
```

Definition at line 7443 of file dss_mrr_pe674.c.

4.16.4.767 xdc_runtime_Log_L_startInstance_C

```
const __FAR__ CT_xdc_runtime_Log_L_startInstance xdc_runtime_Log_L_startInstance_C = (((xdc->
_runtime_Log_Event)10) << 16 | 32768)
```

Definition at line 7451 of file dss_mrr_pe674.c.

4.16.4.768 xdc_runtime_Log_L_stop_C

```
const __FAR__ CT_xdc_runtime_Log_L_stop xdc_runtime_Log_L_stop_C = (((xdc_runtime_Log->
Event)9) << 16 | 32768)
```

Definition at line 7447 of file dss_mrr_pe674.c.

4.16.4.769 **xdc_runtime_Log_L_stopInstance__C**

```
const __FAR__ CT_xdc_runtime_Log_L_stopInstance xdc_runtime_Log_L_stopInstance__C = (((xdc_←  
runtime_Log_Event)11) << 16 | 32768)
```

Definition at line 7455 of file dss_mrr_pe674.c.

4.16.4.770 **xdc_runtime_Log_L_warning__C**

```
const __FAR__ CT_xdc_runtime_Log_L_warning xdc_runtime_Log_L_warning__C = (((xdc_runtime_←  
Log_Event)6) << 16 | 224)
```

Definition at line 7435 of file dss_mrr_pe674.c.

4.16.4.771 **xdc_runtime_Log_Module_diagsEnabled__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_diagsEnabled xdc_runtime_Log_Module_diagsEnabled__C =  
    (xdc_Bits32)0x10
```

Definition at line 7347 of file dss_mrr_pe674.c.

4.16.4.772 **xdc_runtime_Log_Module_diagsIncluded__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_diagsIncluded xdc_runtime_Log_Module_diagsIncluded__C =  
    (xdc_Bits32)0x10
```

Definition at line 7351 of file dss_mrr_pe674.c.

4.16.4.773 **xdc_runtime_Log_Module_diagsMask__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_diagsMask xdc_runtime_Log_Module_diagsMask__C =  
    ((CT_xdc_runtime_Log_Module_diagsMask)0)
```

Definition at line 7355 of file dss_mrr_pe674.c.

4.16.4.774 **xdc_runtime_Log_Module_gateObj__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_gateObj xdc_runtime_Log_Module_gateObj__C = ((CT_←  
_xdc_runtime_Log_Module_gateObj)0)
```

Definition at line 7359 of file dss_mrr_pe674.c.

4.16.4.775 **xdc_runtime_Log_Module_gatePrms__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_gatePrms xdc_runtime_Log_Module_gatePrms__C = ((C_←  
_xdc_runtime_Log_Module_gatePrms)0)
```

Definition at line 7363 of file dss_mrr_pe674.c.

4.16.4.776 **xdc_runtime_Log_Module_id__C**

```
const __FAR__ CT_xdc_runtime_Log_Module_id xdc_runtime_Log_Module_id__C = (xdc_Bits16)0x7
```

Definition at line 7367 of file dss_mrr_pe674.c.

4.16.4.777 `xdc_runtime_Log_Module_loggerDefined_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerDefined xdc_runtime_Log_Module_loggerDefined_C = 0
```

Definition at line 7371 of file dss_mrr_pe674.c.

4.16.4.778 `xdc_runtime_Log_Module_loggerFxn0_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn0 xdc_runtime_Log_Module_loggerFxn0_C = ((CT_xdc_runtime_Log_Module_loggerFxn0) 0)
```

Definition at line 7379 of file dss_mrr_pe674.c.

4.16.4.779 `xdc_runtime_Log_Module_loggerFxn1_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn1 xdc_runtime_Log_Module_loggerFxn1_C = ((CT_xdc_runtime_Log_Module_loggerFxn1) 0)
```

Definition at line 7383 of file dss_mrr_pe674.c.

4.16.4.780 `xdc_runtime_Log_Module_loggerFxn2_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn2 xdc_runtime_Log_Module_loggerFxn2_C = ((CT_xdc_runtime_Log_Module_loggerFxn2) 0)
```

Definition at line 7387 of file dss_mrr_pe674.c.

4.16.4.781 `xdc_runtime_Log_Module_loggerFxn4_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn4 xdc_runtime_Log_Module_loggerFxn4_C = ((CT_xdc_runtime_Log_Module_loggerFxn4) 0)
```

Definition at line 7391 of file dss_mrr_pe674.c.

4.16.4.782 `xdc_runtime_Log_Module_loggerFxn8_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerFxn8 xdc_runtime_Log_Module_loggerFxn8_C = ((CT_xdc_runtime_Log_Module_loggerFxn8) 0)
```

Definition at line 7395 of file dss_mrr_pe674.c.

4.16.4.783 `xdc_runtime_Log_Module_loggerObj_C`

```
const __FAR__ CT_xdc_runtime_Log_Module_loggerObj xdc_runtime_Log_Module_loggerObj_C = ((CT_xdc_runtime_Log_Module_loggerObj) 0)
```

Definition at line 7375 of file dss_mrr_pe674.c.

4.16.4.784 `xdc_runtime_Log_Object_count_C`

```
const __FAR__ CT_xdc_runtime_Log_Object_count xdc_runtime_Log_Object_count_C = 0
```

Definition at line 7399 of file dss_mrr_pe674.c.

4.16.4.785 `xdc_runtime_Log_Object_heap_C`

```
const __FAR__ CT_xdc_runtime_Log_Object_heap xdc_runtime_Log_Object_heap_C = 0
```

Definition at line 7403 of file dss_mrr_pe674.c.

4.16.4.786 xdc_runtime_Log_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Log_Object_sizeof xdc_runtime_Log_Object_sizeof_C = 0
Definition at line 7407 of file dss_mrr_pe674.c.
```

4.16.4.787 xdc_runtime_Log_Object_table_C

```
const __FAR__ CT_xdc_runtime_Log_Object_table xdc_runtime_Log_Object_table_C = 0
Definition at line 7411 of file dss_mrr_pe674.c.
```

4.16.4.788 xdc_runtime_Main_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Main_Module_diagsEnabled xdc_runtime_Main_Module_diagsEnabled_C = (xdc_Bits32) 0x90
Definition at line 7464 of file dss_mrr_pe674.c.
```

4.16.4.789 xdc_runtime_Main_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Main_Module_diagsIncluded xdc_runtime_Main_Module_diagsIncluded_C = (xdc_Bits32) 0x90
Definition at line 7468 of file dss_mrr_pe674.c.
```

4.16.4.790 xdc_runtime_Main_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Main_Module_diagsMask xdc_runtime_Main_Module_diagsMask_C = ((CT_xdc_runtime_Main_Module_diagsMask) 0)
Definition at line 7472 of file dss_mrr_pe674.c.
```

4.16.4.791 xdc_runtime_Main_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Main_Module_gateObj xdc_runtime_Main_Module_gateObj_C = ((C_T_xdc_runtime_Main_Module_gateObj) ((const void*) (xdc_runtime_IGateProvider_Handle) & ti_sysbios_gates_GateHwi_Object_table_V[0]))
Definition at line 7476 of file dss_mrr_pe674.c.
```

4.16.4.792 xdc_runtime_Main_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Main_Module_gatePrms xdc_runtime_Main_Module_gatePrms_C = ((CT_xdc_runtime_Main_Module_gatePrms) 0)
Definition at line 7480 of file dss_mrr_pe674.c.
```

4.16.4.793 xdc_runtime_Main_Module_id_C

```
const __FAR__ CT_xdc_runtime_Main_Module_id xdc_runtime_Main_Module_id_C = (xdc_Bits16) 0x8
Definition at line 7484 of file dss_mrr_pe674.c.
```

4.16.4.794 xdc_runtime_Main_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerDefined xdc_runtime_Main_Module_loggerDefined_C = 0
Definition at line 7488 of file dss_mrr_pe674.c.
```

4.16.4.795 xdc_runtime_Main_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn0 xdc_runtime_Main_Module_loggerFxn0_C =
((CT_xdc_runtime_Main_Module_loggerFxn0)0)
```

Definition at line 7496 of file dss_mrr_pe674.c.

4.16.4.796 xdc_runtime_Main_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn1 xdc_runtime_Main_Module_loggerFxn1_C =
((CT_xdc_runtime_Main_Module_loggerFxn1)0)
```

Definition at line 7500 of file dss_mrr_pe674.c.

4.16.4.797 xdc_runtime_Main_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn2 xdc_runtime_Main_Module_loggerFxn2_C =
((CT_xdc_runtime_Main_Module_loggerFxn2)0)
```

Definition at line 7504 of file dss_mrr_pe674.c.

4.16.4.798 xdc_runtime_Main_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn4 xdc_runtime_Main_Module_loggerFxn4_C =
((CT_xdc_runtime_Main_Module_loggerFxn4)0)
```

Definition at line 7508 of file dss_mrr_pe674.c.

4.16.4.799 xdc_runtime_Main_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerFxn8 xdc_runtime_Main_Module_loggerFxn8_C =
((CT_xdc_runtime_Main_Module_loggerFxn8)0)
```

Definition at line 7512 of file dss_mrr_pe674.c.

4.16.4.800 xdc_runtime_Main_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Main_Module_loggerObj xdc_runtime_Main_Module_loggerObj_C =
((CT_xdc_runtime_Main_Module_loggerObj)0)
```

Definition at line 7492 of file dss_mrr_pe674.c.

4.16.4.801 xdc_runtime_Main_Module_GateProxy_Module_root_V

```
xdc_runtime_Main_Module_GateProxy_Module xdc_runtime_Main_Module_GateProxy_Module_root_V
```

4.16.4.802 xdc_runtime_Main_Object_count_C

```
const __FAR__ CT_xdc_runtime_Main_Object_count xdc_runtime_Main_Object_count_C = 0
```

Definition at line 7516 of file dss_mrr_pe674.c.

4.16.4.803 xdc_runtime_Main_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Main_Object_heap xdc_runtime_Main_Object_heap_C = 0
```

Definition at line 7520 of file dss_mrr_pe674.c.

4.16.4.804 `xdc_runtime_Main_Object__sizeof__C`

```
const __FAR__ CT_xdc_runtime_Main_Object__sizeof xdc_runtime_Main_Object__sizeof__C = 0
Definition at line 7524 of file dss_mrr_pe674.c.
```

4.16.4.805 `xdc_runtime_Main_Object__table__C`

```
const __FAR__ CT_xdc_runtime_Main_Object__table xdc_runtime_Main_Object__table__C = 0
Definition at line 7528 of file dss_mrr_pe674.c.
```

4.16.4.806 `xdc_runtime_Memory_defaultHeapInstance__C`

```
const __FAR__ CT_xdc_runtime_Memory_defaultHeapInstance xdc_runtime_Memory_defaultHeapInstance__C = (xdc_runtime_IHeap_Handle)& ti_sysbios_heaps_HeapMem_Object_table_V[0]
Definition at line 7625 of file dss_mrr_pe674.c.
```

4.16.4.807 `xdc_runtime_Memory_HeapProxy_Module__root__V`

```
xdc_runtime_Memory_HeapProxy_Module__root__V
```

4.16.4.808 `xdc_runtime_Memory_Module__diagsEnabled__C`

```
const __FAR__ CT_xdc_runtime_Memory_Module__diagsEnabled xdc_runtime_Memory_Module__diagsEnabled__C = (xdc_Bits32) 0x10
Definition at line 7557 of file dss_mrr_pe674.c.
```

4.16.4.809 `xdc_runtime_Memory_Module__diagsIncluded__C`

```
const __FAR__ CT_xdc_runtime_Memory_Module__diagsIncluded xdc_runtime_Memory_Module__diagsIncluded__C = (xdc_Bits32) 0x10
Definition at line 7561 of file dss_mrr_pe674.c.
```

4.16.4.810 `xdc_runtime_Memory_Module__diagsMask__C`

```
const __FAR__ CT_xdc_runtime_Memory_Module__diagsMask xdc_runtime_Memory_Module__diagsMask__C = ((CT_xdc_runtime_Memory_Module__diagsMask) 0)
Definition at line 7565 of file dss_mrr_pe674.c.
```

4.16.4.811 `xdc_runtime_Memory_Module__gateObj__C`

```
const __FAR__ CT_xdc_runtime_Memory_Module__gateObj xdc_runtime_Memory_Module__gateObj__C = ((CT_xdc_runtime_Memory_Module__gateObj) 0)
Definition at line 7569 of file dss_mrr_pe674.c.
```

4.16.4.812 `xdc_runtime_Memory_Module__gatePrms__C`

```
const __FAR__ CT_xdc_runtime_Memory_Module__gatePrms xdc_runtime_Memory_Module__gatePrms__C = ((CT_xdc_runtime_Memory_Module__gatePrms) 0)
Definition at line 7573 of file dss_mrr_pe674.c.
```

4.16.4.813 xdc_runtime_Memory_Module_id_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_id xdc_runtime_Memory_Module_id_C = (xdc_←
Bits16) 0x9
```

Definition at line 7577 of file dss_mrr_pe674.c.

4.16.4.814 xdc_runtime_Memory_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerDefined xdc_runtime_Memory_Module_logger←
Defined_C = 0
```

Definition at line 7581 of file dss_mrr_pe674.c.

4.16.4.815 xdc_runtime_Memory_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn0 xdc_runtime_Memory_Module_logger←
Fxn0_C = ((CT_xdc_runtime_Memory_Module_loggerFxn0) 0)
```

Definition at line 7589 of file dss_mrr_pe674.c.

4.16.4.816 xdc_runtime_Memory_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn1 xdc_runtime_Memory_Module_logger←
Fxn1_C = ((CT_xdc_runtime_Memory_Module_loggerFxn1) 0)
```

Definition at line 7593 of file dss_mrr_pe674.c.

4.16.4.817 xdc_runtime_Memory_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn2 xdc_runtime_Memory_Module_logger←
Fxn2_C = ((CT_xdc_runtime_Memory_Module_loggerFxn2) 0)
```

Definition at line 7597 of file dss_mrr_pe674.c.

4.16.4.818 xdc_runtime_Memory_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn4 xdc_runtime_Memory_Module_logger←
Fxn4_C = ((CT_xdc_runtime_Memory_Module_loggerFxn4) 0)
```

Definition at line 7601 of file dss_mrr_pe674.c.

4.16.4.819 xdc_runtime_Memory_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerFxn8 xdc_runtime_Memory_Module_logger←
Fxn8_C = ((CT_xdc_runtime_Memory_Module_loggerFxn8) 0)
```

Definition at line 7605 of file dss_mrr_pe674.c.

4.16.4.820 xdc_runtime_Memory_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Memory_Module_loggerObj xdc_runtime_Memory_Module_loggerObj←
_C = ((CT_xdc_runtime_Memory_Module_loggerObj) 0)
```

Definition at line 7585 of file dss_mrr_pe674.c.

4.16.4.821 xdc_runtime_Memory_Module_state_V

xdc_runtime_Memory_Module_State xdc_runtime_Memory_Module_state_V

Initial value:

```
= {
    (xdc_SizeT)0x8,
}
```

Definition at line 1784 of file dss_mrr_pe674.c.

4.16.4.822 xdc_runtime_Memory_Object_count_C

```
const __FAR__ CT_xdc_runtime_Memory_Object_count xdc_runtime_Memory_Object_count_C = 0
```

Definition at line 7609 of file dss_mrr_pe674.c.

4.16.4.823 xdc_runtime_Memory_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Memory_Object_heap xdc_runtime_Memory_Object_heap_C = 0
```

Definition at line 7613 of file dss_mrr_pe674.c.

4.16.4.824 xdc_runtime_Memory_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Memory_Object_sizeof xdc_runtime_Memory_Object_sizeof_C = 0
```

Definition at line 7617 of file dss_mrr_pe674.c.

4.16.4.825 xdc_runtime_Memory_Object_table_C

```
const __FAR__ CT_xdc_runtime_Memory_Object_table xdc_runtime_Memory_Object_table_C = 0
```

Definition at line 7621 of file dss_mrr_pe674.c.

4.16.4.826 xdc_runtime_Registry_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_diagsEnabled xdc_runtime_Registry_Module_diagsEnabled_C = (xdc_Bits32)0x90
```

Definition at line 7655 of file dss_mrr_pe674.c.

4.16.4.827 xdc_runtime_Registry_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_diagsIncluded xdc_runtime_Registry_Module_diagsIncluded_C = (xdc_Bits32)0x90
```

Definition at line 7659 of file dss_mrr_pe674.c.

4.16.4.828 xdc_runtime_Registry_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_diagsMask xdc_runtime_Registry_Module_diagsMask_C = ((CT_xdc_runtime_Registry_Module_diagsMask)0)
```

Definition at line 7663 of file dss_mrr_pe674.c.

4.16.4.829 xdc_runtime_Registry_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_gateObj xdc_runtime_Registry_Module_gateObj_C = ((CT_xdc_runtime_Registry_Module_gateObj)0)
```

Definition at line 7667 of file dss_mrr_pe674.c.

4.16.4.830 xdc_runtime_Registry_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_gatePrms xdc_runtime_Registry_Module_gate←
Prms_C = ((CT_xdc_runtime_Registry_Module_gatePrms)0)
Definition at line 7671 of file dss_mrr_pe674.c.
```

4.16.4.831 xdc_runtime_Registry_Module_id_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_id xdc_runtime_Registry_Module_id_C = (xdc←
Bits16)0xa
Definition at line 7675 of file dss_mrr_pe674.c.
```

4.16.4.832 xdc_runtime_Registry_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerDefined xdc_runtime_Registry_Module←
loggerDefined_C = 0
Definition at line 7679 of file dss_mrr_pe674.c.
```

4.16.4.833 xdc_runtime_Registry_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn0 xdc_runtime_Registry_Module←
loggerFxn0_C = ((CT_xdc_runtime_Registry_Module_loggerFxn0)0)
Definition at line 7687 of file dss_mrr_pe674.c.
```

4.16.4.834 xdc_runtime_Registry_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn1 xdc_runtime_Registry_Module←
loggerFxn1_C = ((CT_xdc_runtime_Registry_Module_loggerFxn1)0)
Definition at line 7691 of file dss_mrr_pe674.c.
```

4.16.4.835 xdc_runtime_Registry_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn2 xdc_runtime_Registry_Module←
loggerFxn2_C = ((CT_xdc_runtime_Registry_Module_loggerFxn2)0)
Definition at line 7695 of file dss_mrr_pe674.c.
```

4.16.4.836 xdc_runtime_Registry_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn4 xdc_runtime_Registry_Module←
loggerFxn4_C = ((CT_xdc_runtime_Registry_Module_loggerFxn4)0)
Definition at line 7699 of file dss_mrr_pe674.c.
```

4.16.4.837 xdc_runtime_Registry_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerFxn8 xdc_runtime_Registry_Module←
loggerFxn8_C = ((CT_xdc_runtime_Registry_Module_loggerFxn8)0)
Definition at line 7703 of file dss_mrr_pe674.c.
```

4.16.4.838 xdc_runtime_Registry_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Registry_Module_loggerObj xdc_runtime_Registry_Module_logger←
Obj_C = ((CT_xdc_runtime_Registry_Module_loggerObj)0)
```

Definition at line 7683 of file dss_mrr_pe674.c.

4.16.4.839 `xdc_runtime_Registry_Module_state_V`

`xdc_runtime_Registry_Module_State__ xdc_runtime_Registry_Module_state_V`
Initial value:

```
= {
    ((xdc_runtime_Types_RegDesc*) 0),
    (xdc_Bits16) 0x7fff,
}
```

Definition at line 1803 of file dss_mrr_pe674.c.

4.16.4.840 `xdc_runtime_Registry_Object_count_C`

`const __FAR__ CT_xdc_runtime_Registry_Object_count xdc_runtime_Registry_Object_count_C = 0`
Definition at line 7707 of file dss_mrr_pe674.c.

4.16.4.841 `xdc_runtime_Registry_Object_heap_C`

`const __FAR__ CT_xdc_runtime_Registry_Object_heap xdc_runtime_Registry_Object_heap_C = 0`
Definition at line 7711 of file dss_mrr_pe674.c.

4.16.4.842 `xdc_runtime_Registry_Object_sizeof_C`

`const __FAR__ CT_xdc_runtime_Registry_Object_sizeof xdc_runtime_Registry_Object_sizeof_C = 0`
Definition at line 7715 of file dss_mrr_pe674.c.

4.16.4.843 `xdc_runtime_Registry_Object_table_C`

`const __FAR__ CT_xdc_runtime_Registry_Object_table xdc_runtime_Registry_Object_table_C = 0`
Definition at line 7719 of file dss_mrr_pe674.c.

4.16.4.844 `xdc_runtime_Startup_execImpl_C`

`const __FAR__ CT_xdc_runtime_Startup_execImpl xdc_runtime_Startup_execImpl_C = ((CT_xdc_runtime_Startup_execImpl)((xdc_Fxn) xdc_runtime_Startup_exec_I))`
Definition at line 7866 of file dss_mrr_pe674.c.

4.16.4.845 `xdc_runtime_Startup_firstFxns_A`

`const __T1_xdc_runtime_Startup_firstFxns xdc_runtime_Startup_firstFxns_A`
Initial value:
= {
 ((xdc_Void*)(xdc_Void))((xdc_Fxn)ti_sysbios_heaps_HeapMem_init_I),
 ((xdc_Void*)(xdc_Void))((xdc_Fxn)ti_sysbios_hal_Hwi_initStack)),
}

Definition at line 1824 of file dss_mrr_pe674.c.

4.16.4.846 `xdc_runtime_Startup_firstFxns_C`

`const __FAR__ CT_xdc_runtime_Startup_firstFxns xdc_runtime_Startup_firstFxns_C = {2, ((__T1_xdc_runtime_Startup_firstFxns *) xdc_runtime_Startup_firstFxns_A)}`
Definition at line 7854 of file dss_mrr_pe674.c.

4.16.4.847 `xdc_runtime_Startup_LastFxns_C`

```
const __FAR__ CT_xdc_runtime_Startup_LastFxns xdc_runtime_Startup_LastFxns_C = {0, 0}
Definition at line 7858 of file dss_mrr_pe674.c.
```

4.16.4.848 `xdc_runtime_Startup_maxPasses_C`

```
const __FAR__ CT_xdc_runtime_Startup_maxPasses xdc_runtime_Startup_maxPasses_C = (xdc_←
Int) 0x20
Definition at line 7850 of file dss_mrr_pe674.c.
```

4.16.4.849 `xdc_runtime_Startup_Module_diagsEnabled_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_diagsEnabled xdc_runtime_Startup_Module_diags←
Enabled_C = (xdc_Bits32) 0x10
Definition at line 7782 of file dss_mrr_pe674.c.
```

4.16.4.850 `xdc_runtime_Startup_Module_diagsIncluded_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_diagsIncluded xdc_runtime_Startup_Module_←
diagsIncluded_C = (xdc_Bits32) 0x10
Definition at line 7786 of file dss_mrr_pe674.c.
```

4.16.4.851 `xdc_runtime_Startup_Module_diagsMask_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_diagsMask xdc_runtime_Startup_Module_diags←
Mask_C = ((CT_xdc_runtime_Startup_Module_diagsMask) 0)
Definition at line 7790 of file dss_mrr_pe674.c.
```

4.16.4.852 `xdc_runtime_Startup_Module_gateObj_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_gateObj xdc_runtime_Startup_Module_gateObj_C =
((CT_xdc_runtime_Startup_Module_gateObj) 0)
Definition at line 7794 of file dss_mrr_pe674.c.
```

4.16.4.853 `xdc_runtime_Startup_Module_gatePrms_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_gatePrms xdc_runtime_Startup_Module_gatePrms_←
_C = ((CT_xdc_runtime_Startup_Module_gatePrms) 0)
Definition at line 7798 of file dss_mrr_pe674.c.
```

4.16.4.854 `xdc_runtime_Startup_Module_id_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_id xdc_runtime_Startup_Module_id_C = (xdc_←
Bits16) 0xb
Definition at line 7802 of file dss_mrr_pe674.c.
```

4.16.4.855 `xdc_runtime_Startup_Module_loggerDefined_C`

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerDefined xdc_runtime_Startup_Module_←
loggerDefined_C = 0
Definition at line 7806 of file dss_mrr_pe674.c.
```

4.16.4.856 xdc_runtime_Startup_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn0 xdc_runtime_Startup_Module_logger←
Fxn0_C = ((CT_xdc_runtime_Startup_Module_loggerFxn0) 0)
Definition at line 7814 of file dss_mrr_pe674.c.
```

4.16.4.857 xdc_runtime_Startup_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn1 xdc_runtime_Startup_Module_logger←
Fxn1_C = ((CT_xdc_runtime_Startup_Module_loggerFxn1) 0)
Definition at line 7818 of file dss_mrr_pe674.c.
```

4.16.4.858 xdc_runtime_Startup_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn2 xdc_runtime_Startup_Module_logger←
Fxn2_C = ((CT_xdc_runtime_Startup_Module_loggerFxn2) 0)
Definition at line 7822 of file dss_mrr_pe674.c.
```

4.16.4.859 xdc_runtime_Startup_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn4 xdc_runtime_Startup_Module_logger←
Fxn4_C = ((CT_xdc_runtime_Startup_Module_loggerFxn4) 0)
Definition at line 7826 of file dss_mrr_pe674.c.
```

4.16.4.860 xdc_runtime_Startup_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerFxn8 xdc_runtime_Startup_Module_logger←
Fxn8_C = ((CT_xdc_runtime_Startup_Module_loggerFxn8) 0)
Definition at line 7830 of file dss_mrr_pe674.c.
```

4.16.4.861 xdc_runtime_Startup_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Startup_Module_loggerObj xdc_runtime_Startup_Module_logger←
Obj_C = ((CT_xdc_runtime_Startup_Module_loggerObj) 0)
Definition at line 7810 of file dss_mrr_pe674.c.
```

4.16.4.862 xdc_runtime_Startup_Module_state_V

xdc_runtime_Startup_Module_State xdc_runtime_Startup_Module_state_V

Initial value:

```
= {
    ((xdc_Int*) 0),
    0,
    0,
}
```

Definition at line 1818 of file dss_mrr_pe674.c.

Referenced by `ti_sysbios_family_c64p_Cache_Module_startupDone_F()`, `ti_sysbios_family_c64p_EventCombiner_Module_startupDone_F()`, `ti_sysbios_family_c64p_Exception_Module_startupDone_F()`, `ti_sysbios_family_c64p_Hwi_Module_startupDone_F()`, `ti_sysbios_family_c64p_TimestampProvider_Module_startupDone_F()`, `ti_sysbios_hal_Hwi_Module_startupDone_F()`, `ti_sysbios_knl_Clock_Module_startupDone_F()`, `ti_sysbios_knl_Task_Module_startupDone_F()`, `ti_sysbios_timers_rti_Timer_Module_startupDone_F()`, and `xdc_runtime_System_Module_startupDone_F()`.

4.16.4.863 xdc_runtime_Startup_Object_count_C

```
const __FAR__ CT_xdc_runtime_Startup_Object_count xdc_runtime_Startup_Object_count_C = 0
Definition at line 7834 of file dss_mrr_pe674.c.
```

4.16.4.864 xdc_runtime_Startup_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Startup_Object_heap xdc_runtime_Startup_Object_heap_C = 0
Definition at line 7838 of file dss_mrr_pe674.c.
```

4.16.4.865 xdc_runtime_Startup_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Startup_Object_sizeof xdc_runtime_Startup_Object_sizeof_C = 0
Definition at line 7842 of file dss_mrr_pe674.c.
```

4.16.4.866 xdc_runtime_Startup_Object_table_C

```
const __FAR__ CT_xdc_runtime_Startup_Object_table xdc_runtime_Startup_Object_table_C = 0
Definition at line 7846 of file dss_mrr_pe674.c.
```

4.16.4.867 xdc_runtime_Startup_sfxnRts_A

```
const __T1_xdc_runtime_Startup_sfxnRts xdc_runtime_Startup_sfxnRts_A
```

Initial value:

```
= {  
    1,  
    0,  
    0,  
    0,  
    1,  
    0,  
    0,  
    0,  
    0,  
    0,  
    0,  
}
```

Definition at line 1860 of file dss_mrr_pe674.c.

4.16.4.868 xdc_runtime_Startup_sfxnRts_C

```
const __FAR__ CT_xdc_runtime_Startup_sfxnRts xdc_runtime_Startup_sfxnRts_C = ((CT_xdc_<--  
runtime_Startup_sfxnRts) xdc_runtime_Startup_sfxnRts_A)
```

Definition at line 7874 of file dss_mrr_pe674.c.

4.16.4.869 xdc_runtime_Startup_sfxnTab_A

```
const __T1_xdc_runtime_Startup_sfxnTab xdc_runtime_Startup_sfxnTab_A
```

Initial value:

```
= {  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) xdc_runtime_System_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_family_c64p_EventCombiner_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_family_c64p_Exception_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_family_c64p_Hwi_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_family_c64p_TimestampProvider_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_family_c64p_Cache_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_knl_Clock_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_knl_Task_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_hal_Hwi_Module_startup_E),  
    ((xdc_Int *) (xdc_Int)) ((xdc_Fxn) ti_sysbios_timers_rti_Timer_Module_startup_E),  
}
```

Definition at line 1857 of file dss_mrr_pe674.c.

4.16.4.870 xdc_runtime_Startup_sfxnTab__C

```
const __FAR__ CT_xdc_runtime_Startup_sfxnTab xdc_runtime_Startup_sfxnTab__C = ((CT_xdc_runtime_Startup_sfxnTab) xdc_runtime_Startup_sfxnTab_A)
Definition at line 7870 of file dss_mrr_pe674.c.
```

4.16.4.871 xdc_runtime_Startup_startModsFxn__C

```
const __FAR__ CT_xdc_runtime_Startup_startModsFxn xdc_runtime_Startup_startModsFxn__C = ((CT_xdc_runtime_Startup_startModsFxn) ((xdc_Fxn) xdc_runtime_Startup_startMods_I))
Definition at line 7862 of file dss_mrr_pe674.c.
Referenced by xdc_runtime_Startup_exec_I().
```

4.16.4.872 xdc_runtime_SysStd_Module_diagsEnabled__C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_diagsEnabled xdc_runtime_SysStd_Module_diagsEnabled__C = (xdc_Bits32) 0x10
Definition at line 7883 of file dss_mrr_pe674.c.
```

4.16.4.873 xdc_runtime_SysStd_Module_diagsIncluded__C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_diagsIncluded xdc_runtime_SysStd_Module_diagsIncluded__C = (xdc_Bits32) 0x10
Definition at line 7887 of file dss_mrr_pe674.c.
```

4.16.4.874 xdc_runtime_SysStd_Module_diagsMask__C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_diagsMask xdc_runtime_SysStd_Module_diagsMask__C = ((CT_xdc_runtime_SysStd_Module_diagsMask) 0)
Definition at line 7891 of file dss_mrr_pe674.c.
```

4.16.4.875 xdc_runtime_SysStd_Module_FXNS__C

```
const xdc_runtime_SysStd_Fxns__ xdc_runtime_SysStd_Module_FXNS__C
Initial value:
= {
    &xdc_runtime_ISystemSupport_Interface_BASE__C,
    &xdc_runtime_SysStd_Module_FXNS_C._sfxns,
    xdc_runtime_SysStd_abort_E,
    xdc_runtime_SysStd_exit_E,
    xdc_runtime_SysStd_flush_E,
    xdc_runtime_SysStd_putch_E,
    xdc_runtime_SysStd_ready_E,
    {
        NULL,
        NULL,
        NULL,
        0xd,
    }
}
```

Definition at line 838 of file dss_mrr_pe674.c.

Referenced by xdc_runtime_System_SupportProxy_Proxy_delegate_S().

4.16.4.876 xdc_runtime_SysStd_Module_gateObj__C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_gateObj xdc_runtime_SysStd_Module_gateObj__C =
((CT_xdc_runtime_SysStd_Module_gateObj) 0)
Definition at line 7895 of file dss_mrr_pe674.c.
```

4.16.4.877 xdc_runtime_SysStd_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_gatePrms xdc_runtime_SysStd_Module_gatePrms_C =
((CT_xdc_runtime_SysStd_Module_gatePrms)0)
Definition at line 7899 of file dss_mrr_pe674.c.
```

4.16.4.878 xdc_runtime_SysStd_Module_id_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_id xdc_runtime_SysStd_Module_id_C = (xdc_←
Bits16)0xd
Definition at line 7903 of file dss_mrr_pe674.c.
```

4.16.4.879 xdc_runtime_SysStd_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerDefined xdc_runtime_SysStd_Module_logger←
Defined_C = 0
Definition at line 7907 of file dss_mrr_pe674.c.
```

4.16.4.880 xdc_runtime_SysStd_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn0 xdc_runtime_SysStd_Module_logger←
Fxn0_C = ((CT_xdc_runtime_SysStd_Module_loggerFxn0)0)
Definition at line 7915 of file dss_mrr_pe674.c.
```

4.16.4.881 xdc_runtime_SysStd_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn1 xdc_runtime_SysStd_Module_logger←
Fxn1_C = ((CT_xdc_runtime_SysStd_Module_loggerFxn1)0)
Definition at line 7919 of file dss_mrr_pe674.c.
```

4.16.4.882 xdc_runtime_SysStd_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn2 xdc_runtime_SysStd_Module_logger←
Fxn2_C = ((CT_xdc_runtime_SysStd_Module_loggerFxn2)0)
Definition at line 7923 of file dss_mrr_pe674.c.
```

4.16.4.883 xdc_runtime_SysStd_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn4 xdc_runtime_SysStd_Module_logger←
Fxn4_C = ((CT_xdc_runtime_SysStd_Module_loggerFxn4)0)
Definition at line 7927 of file dss_mrr_pe674.c.
```

4.16.4.884 xdc_runtime_SysStd_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerFxn8 xdc_runtime_SysStd_Module_logger←
Fxn8_C = ((CT_xdc_runtime_SysStd_Module_loggerFxn8)0)
Definition at line 7931 of file dss_mrr_pe674.c.
```

4.16.4.885 xdc_runtime_SysStd_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_SysStd_Module_loggerObj xdc_runtime_SysStd_Module_loggerObj←
_C = ((CT_xdc_runtime_SysStd_Module_loggerObj)0)
```

Definition at line 7911 of file dss_mrr_pe674.c.

4.16.4.886 `xdc_runtime_SysStd_Object_count_C`

```
const __FAR__ CT_xdc_runtime_SysStd_Object_count xdc_runtime_SysStd_Object_count_C = 0
Definition at line 7935 of file dss_mrr_pe674.c.
```

4.16.4.887 `xdc_runtime_SysStd_Object_heap_C`

```
const __FAR__ CT_xdc_runtime_SysStd_Object_heap xdc_runtime_SysStd_Object_heap_C = 0
Definition at line 7939 of file dss_mrr_pe674.c.
```

4.16.4.888 `xdc_runtime_SysStd_Object_sizeof_C`

```
const __FAR__ CT_xdc_runtime_SysStd_Object_sizeof xdc_runtime_SysStd_Object_sizeof_C = 0
Definition at line 7943 of file dss_mrr_pe674.c.
```

4.16.4.889 `xdc_runtime_SysStd_Object_table_C`

```
const __FAR__ CT_xdc_runtime_SysStd_Object_table xdc_runtime_SysStd_Object_table_C = 0
Definition at line 7947 of file dss_mrr_pe674.c.
```

4.16.4.890 `xdc_runtime_System_A_cannotFitIntoArg_C`

```
const __FAR__ CT_xdc_runtime_System_A_cannotFitIntoArg xdc_runtime_System_A_cannotFitIntoArg_C = (((xdruntimeAssertId)0) << 16 | 16)
Definition at line 8040 of file dss_mrr_pe674.c.
```

4.16.4.891 `xdc_runtime_System_abortFxn_C`

```
const __FAR__ CT_xdc_runtime_System_abortFxn xdc_runtime_System_abortFxn_C = ((CT_xdc_runtime_System_abortFxn)((xdruntimeFxn)xdruntimeSystemAbortStd_E))
Definition at line 8048 of file dss_mrr_pe674.c.
```

4.16.4.892 `xdc_runtime_System_exitFxn_C`

```
const __FAR__ CT_xdc_runtime_System_exitFxn xdc_runtime_System_exitFxn_C = ((CT_xdc_runtime_System_exitFxn)((xdruntimeFxn)xdruntimeSystemExitStd_E))
Definition at line 8052 of file dss_mrr_pe674.c.
```

4.16.4.893 `xdc_runtime_System_extendFxn_C`

```
const __FAR__ CT_xdc_runtime_System_extendFxn xdc_runtime_System_extendFxn_C = ((CT_xdc_runtime_System_extendFxn)((xdruntimeFxn)xdruntimeSystemprintfExtend_I))
Definition at line 8056 of file dss_mrr_pe674.c.
```

4.16.4.894 `xdc_runtime_System_maxAtexitHandlers_C`

```
const __FAR__ CT_xdc_runtime_System_maxAtexitHandlers xdc_runtime_System_maxAtexitHandlers_C = (xdruntimeInt)0x0
Definition at line 8044 of file dss_mrr_pe674.c.
```

4.16.4.895 xdc_runtime_System_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_System_Module_diagsEnabled xdc_runtime_System_Module_diagsEnabled_C = (xdc_Bits32) 0x10
Definition at line 7972 of file dss_mrr_pe674.c.
```

4.16.4.896 xdc_runtime_System_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_System_Module_diagsIncluded xdc_runtime_System_Module_diagsIncluded_C = (xdc_Bits32) 0x10
Definition at line 7976 of file dss_mrr_pe674.c.
```

4.16.4.897 xdc_runtime_System_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_System_Module_diagsMask xdc_runtime_System_Module_diagsMask_C = ((CT_xdc_runtime_System_Module_diagsMask) 0)
Definition at line 7980 of file dss_mrr_pe674.c.
```

4.16.4.898 xdc_runtime_System_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_System_Module_gateObj xdc_runtime_System_Module_gateObj_C = ((CT_xdc_runtime_System_Module_gateObj) ((const void*) (xdc_runtime_IGateProvider_Handle) & ti_sysbios_gates_GateHwi_Object_table_V[0]))
Definition at line 7984 of file dss_mrr_pe674.c.
```

4.16.4.899 xdc_runtime_System_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_System_Module_gatePrms xdc_runtime_System_Module_gatePrms_C = ((CT_xdc_runtime_System_Module_gatePrms) 0)
Definition at line 7988 of file dss_mrr_pe674.c.
```

4.16.4.900 xdc_runtime_System_Module_id_C

```
const __FAR__ CT_xdc_runtime_System_Module_id xdc_runtime_System_Module_id_C = (xdc_Bits16) 0xc
Definition at line 7992 of file dss_mrr_pe674.c.
```

4.16.4.901 xdc_runtime_System_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerDefined xdc_runtime_System_Module_loggerDefined_C = 0
Definition at line 7996 of file dss_mrr_pe674.c.
```

4.16.4.902 xdc_runtime_System_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerFxn0 xdc_runtime_System_Module_loggerFxn0_C = ((CT_xdc_runtime_System_Module_loggerFxn0) 0)
Definition at line 8004 of file dss_mrr_pe674.c.
```

4.16.4.903 xdc_runtime_System_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerFxn1 xdc_runtime_System_Module_logger←
Fxn1_C = ((CT_xdc_runtime_System_Module_loggerFxn1)0)
Definition at line 8008 of file dss_mrr_pe674.c.
```

4.16.4.904 xdc_runtime_System_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerFxn2 xdc_runtime_System_Module_logger←
Fxn2_C = ((CT_xdc_runtime_System_Module_loggerFxn2)0)
Definition at line 8012 of file dss_mrr_pe674.c.
```

4.16.4.905 xdc_runtime_System_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerFxn4 xdc_runtime_System_Module_logger←
Fxn4_C = ((CT_xdc_runtime_System_Module_loggerFxn4)0)
Definition at line 8016 of file dss_mrr_pe674.c.
```

4.16.4.906 xdc_runtime_System_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerFxn8 xdc_runtime_System_Module_logger←
Fxn8_C = ((CT_xdc_runtime_System_Module_loggerFxn8)0)
Definition at line 8020 of file dss_mrr_pe674.c.
```

4.16.4.907 xdc_runtime_System_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_System_Module_loggerObj xdc_runtime_System_Module_loggerObj←
_C = ((CT_xdc_runtime_System_Module_loggerObj)0)
Definition at line 8000 of file dss_mrr_pe674.c.
```

4.16.4.908 xdc_runtime_System_Module_state_V

xdc_runtime_System_Module_State xdc_runtime_System_Module_state_V

Initial value:

```
= {  
    ((void*)0),  
    (xdc_Int)0x0,  
}
```

Definition at line 1879 of file dss_mrr_pe674.c.

4.16.4.909 xdc_runtime_System_Module_GateProxy_Module_root_V

```
xdc_runtime_System_Module_GateProxy_Module xdc_runtime_System_Module_GateProxy_Module←
root_V
```

4.16.4.910 xdc_runtime_System_Object_count_C

```
const __FAR__ CT_xdc_runtime_System_Object_count xdc_runtime_System_Object_count_C = 0
Definition at line 8024 of file dss_mrr_pe674.c.
```

4.16.4.911 `xdc_runtime_System_Object_heap_C`

```
const __FAR__ CT_xdc_runtime_System_Object_heap xdc_runtime_System_Object_heap_C = 0
Definition at line 8028 of file dss_mrr_pe674.c.
```

4.16.4.912 `xdc_runtime_System_Object_sizeof_C`

```
const __FAR__ CT_xdc_runtime_System_Object_sizeof xdc_runtime_System_Object_sizeof_C = 0
Definition at line 8032 of file dss_mrr_pe674.c.
```

4.16.4.913 `xdc_runtime_System_Object_table_C`

```
const __FAR__ CT_xdc_runtime_System_Object_table xdc_runtime_System_Object_table_C = 0
Definition at line 8036 of file dss_mrr_pe674.c.
```

4.16.4.914 `xdc_runtime_Text_charCnt_C`

```
const __FAR__ CT_xdc_runtime_Text_charCnt xdc_runtime_Text_charCnt_C = (xdc_Int16)0x1
Definition at line 8198 of file dss_mrr_pe674.c.
```

4.16.4.915 `xdc_runtime_Text_charTab_A`

```
const __T1_xdc_runtime_Text_charTab xdc_runtime_Text_charTab_A
Initial value:
= {
    (xdc_Char)0x0,
}
Definition at line 1909 of file dss_mrr_pe674.c.
```

4.16.4.916 `xdc_runtime_Text_charTab_C`

```
const __FAR__ CT_xdc_runtime_Text_charTab xdc_runtime_Text_charTab_C = ((CT_xdc_runtime_Text_charTab) xdc_runtime_Text_charTab_A)
Definition at line 8190 of file dss_mrr_pe674.c.
```

4.16.4.917 `xdc_runtime_Text_isLoaded_C`

```
const __FAR__ CT_xdc_runtime_Text_isLoaded xdc_runtime_Text_isLoaded_C = 0
Definition at line 8186 of file dss_mrr_pe674.c.
```

4.16.4.918 `xdc_runtime_Text_Module_diagsEnabled_C`

```
const __FAR__ CT_xdc_runtime_Text_Module_diagsEnabled xdc_runtime_Text_Module_diagsEnabled_C = (xdc_Bits32)0x10
Definition at line 8106 of file dss_mrr_pe674.c.
```

4.16.4.919 `xdc_runtime_Text_Module_diagsIncluded_C`

```
const __FAR__ CT_xdc_runtime_Text_Module_diagsIncluded xdc_runtime_Text_Module_diagsIncluded_C = (xdc_Bits32)0x10
Definition at line 8110 of file dss_mrr_pe674.c.
```

4.16.4.920 xdc_runtime_Text_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_Text_Module_diagsMask xdc_runtime_Text_Module_diagsMask_C =  
((CT_xdc_runtime_Text_Module_diagsMask) 0)  
Definition at line 8114 of file dss_mrr_pe674.c.
```

4.16.4.921 xdc_runtime_Text_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_Text_Module_gateObj xdc_runtime_Text_Module_gateObj_C = ((C←  
T_xdc_runtime_Text_Module_gateObj) 0)  
Definition at line 8118 of file dss_mrr_pe674.c.
```

4.16.4.922 xdc_runtime_Text_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_Text_Module_gatePrms xdc_runtime_Text_Module_gatePrms_C =  
((CT_xdc_runtime_Text_Module_gatePrms) 0)  
Definition at line 8122 of file dss_mrr_pe674.c.
```

4.16.4.923 xdc_runtime_Text_Module_id_C

```
const __FAR__ CT_xdc_runtime_Text_Module_id xdc_runtime_Text_Module_id_C = (xdc_Bits16) 0xe  
Definition at line 8126 of file dss_mrr_pe674.c.
```

4.16.4.924 xdc_runtime_Text_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerDefined xdc_runtime_Text_Module_loggerDefined_C =  
Defined_C = 0  
Definition at line 8130 of file dss_mrr_pe674.c.
```

4.16.4.925 xdc_runtime_Text_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn0 xdc_runtime_Text_Module_loggerFxn0_C =  
((CT_xdc_runtime_Text_Module_loggerFxn0) 0)  
Definition at line 8138 of file dss_mrr_pe674.c.
```

4.16.4.926 xdc_runtime_Text_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn1 xdc_runtime_Text_Module_loggerFxn1_C =  
((CT_xdc_runtime_Text_Module_loggerFxn1) 0)  
Definition at line 8142 of file dss_mrr_pe674.c.
```

4.16.4.927 xdc_runtime_Text_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn2 xdc_runtime_Text_Module_loggerFxn2_C =  
((CT_xdc_runtime_Text_Module_loggerFxn2) 0)  
Definition at line 8146 of file dss_mrr_pe674.c.
```

4.16.4.928 xdc_runtime_Text_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn4 xdc_runtime_Text_Module_loggerFxn4_C =  
((CT_xdc_runtime_Text_Module_loggerFxn4) 0)  
Definition at line 8150 of file dss_mrr_pe674.c.
```

4.16.4.929 `xdc_runtime_Text_Module_loggerFxn8_C`

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerFxn8 xdc_runtime_Text_Module_loggerFxn8_C =
((CT_xdc_runtime_Text_Module_loggerFxn8)0)
Definition at line 8154 of file dss_mrr_pe674.c.
```

4.16.4.930 `xdc_runtime_Text_Module_loggerObj_C`

```
const __FAR__ CT_xdc_runtime_Text_Module_loggerObj xdc_runtime_Text_Module_loggerObj_C =
((CT_xdc_runtime_Text_Module_loggerObj)0)
Definition at line 8134 of file dss_mrr_pe674.c.
```

4.16.4.931 `xdc_runtime_Text_Module_state_V`

`xdc_runtime_Text_Module_State` `xdc_runtime_Text_Module_state_V`

Initial value:

```
= {
    ((xdc_CPtr)(&xdc_runtime_Text_charTab_A[0])),
    ((xdc_CPtr)(&xdc_runtime_Text_nodeTab_A[0])),
}
```

Definition at line 1906 of file dss_mrr_pe674.c.

4.16.4.932 `xdc_runtime_Text_nameEmpty_C`

```
const __FAR__ CT_xdc_runtime_Text_nameEmpty xdc_runtime_Text_nameEmpty_C = "{empty-instance-name}"
Definition at line 8178 of file dss_mrr_pe674.c.
```

4.16.4.933 `xdc_runtime_Text_nameStatic_C`

```
const __FAR__ CT_xdc_runtime_Text_nameStatic xdc_runtime_Text_nameStatic_C = "{static-instance-name}"
Definition at line 8182 of file dss_mrr_pe674.c.
```

4.16.4.934 `xdc_runtime_Text_nameUnknown_C`

```
const __FAR__ CT_xdc_runtime_Text_nameUnknown xdc_runtime_Text_nameUnknown_C = "{unknown-instance-name}"
Definition at line 8174 of file dss_mrr_pe674.c.
```

4.16.4.935 `xdc_runtime_Text_nodeCnt_C`

```
const __FAR__ CT_xdc_runtime_Text_nodeCnt xdc_runtime_Text_nodeCnt_C = (xdc_Int16)0x1
Definition at line 8202 of file dss_mrr_pe674.c.
```

4.16.4.936 `xdc_runtime_Text_nodeTab_A`

`const __T1_xdc_runtime_Text_nodeTab xdc_runtime_Text_nodeTab_A`

Initial value:

```
= {
    {
        (xdc_Bits16)0x0,
        (xdc_Bits16)0x0,
    },
}
```

Definition at line 1912 of file dss_mrr_pe674.c.

4.16.4.937 `xdc_runtime_Text_nodeTab__C`

```
const __FAR__ CT_xdc_runtime_Text_nodeTab xdc_runtime_Text_nodeTab__C = ((CT_xdc_runtime_Text_nodeTab) xdc_runtime_Text_nodeTab__A)
```

Definition at line 8194 of file dss_mrr_pe674.c.

4.16.4.938 `xdc_runtime_Text_Object_count__C`

```
const __FAR__ CT_xdc_runtime_Text_Object_count xdc_runtime_Text_Object_count__C = 0
```

Definition at line 8158 of file dss_mrr_pe674.c.

4.16.4.939 `xdc_runtime_Text_Object_heap__C`

```
const __FAR__ CT_xdc_runtime_Text_Object_heap xdc_runtime_Text_Object_heap__C = 0
```

Definition at line 8162 of file dss_mrr_pe674.c.

4.16.4.940 `xdc_runtime_Text_Object_sizeof__C`

```
const __FAR__ CT_xdc_runtime_Text_Object_sizeof xdc_runtime_Text_Object_sizeof__C = 0
```

Definition at line 8166 of file dss_mrr_pe674.c.

4.16.4.941 `xdc_runtime_Text_Object_table__C`

```
const __FAR__ CT_xdc_runtime_Text_Object_table xdc_runtime_Text_Object_table__C = 0
```

Definition at line 8170 of file dss_mrr_pe674.c.

4.16.4.942 `xdc_runtime_Text_registryModsLastId__C`

```
const __FAR__ CT_xdc_runtime_Text_registryModsLastId xdc_runtime_Text_registryModsLastId__C = (xdc_UInt16)0x7fff
```

Definition at line 8210 of file dss_mrr_pe674.c.

4.16.4.943 `xdc_runtime_Text_unnamedModsLastId__C`

```
const __FAR__ CT_xdc_runtime_Text_unnamedModsLastId xdc_runtime_Text_unnamedModsLastId__C = (xdc_UInt16)0x4000
```

Definition at line 8206 of file dss_mrr_pe674.c.

4.16.4.944 `xdc_runtime_Text_visitRopeFxn2__C`

```
const __FAR__ CT_xdc_runtime_Text_visitRopeFxn2 xdc_runtime_Text_visitRopeFxn2__C = ((CT_xdc_runtime_Text_visitRopeFxn2)((xdc_Fxn) xdc_runtime_Text_visitRope2__I))
```

Definition at line 8218 of file dss_mrr_pe674.c.

4.16.4.945 `xdc_runtime_Text_visitRopeFxn__C`

```
const __FAR__ CT_xdc_runtime_Text_visitRopeFxn xdc_runtime_Text_visitRopeFxn__C = ((CT_xdc_runtime_Text_visitRopeFxn)((xdc_Fxn) xdc_runtime_Text_visitRope__I))
```

Definition at line 8214 of file dss_mrr_pe674.c.

4.16.4.946 xdc_runtime_Timestamp_Module_diagsEnabled__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsEnabled xdc_runtime_Timestamp_Module_diagsEnabled__C = (xdc_Bits32)0x10
```

Definition at line 8227 of file dss_mrr_pe674.c.

4.16.4.947 xdc_runtime_Timestamp_Module_diagsIncluded__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsIncluded xdc_runtime_Timestamp_Module_diagsIncluded__C = (xdc_Bits32)0x10
```

Definition at line 8231 of file dss_mrr_pe674.c.

4.16.4.948 xdc_runtime_Timestamp_Module_diagsMask__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_diagsMask xdc_runtime_Timestamp_Module_diagsMask__C = ((CT_xdc_runtime_Timestamp_Module_diagsMask)0)
```

Definition at line 8235 of file dss_mrr_pe674.c.

4.16.4.949 xdc_runtime_Timestamp_Module_FXNS__C

```
const xdc_runtime_Timestamp_Fxns xdc_runtime_Timestamp_Module_FXNS__C
```

Initial value:

```
= {
    &xdc_runtime_ITimestampClient_Interface_BASE__C,
    &xdc_runtime_Timestamp_Module_FXNS__C._sfxns,
    xdc_runtime_Timestamp_get32_E,
    xdc_runtime_Timestamp_get64_E,
    xdc_runtime_Timestamp_getFreq_E,
    {
        NULL,
        NULL,
        NULL,
        0xf,
    }
}
```

Definition at line 861 of file dss_mrr_pe674.c.

4.16.4.950 xdc_runtime_Timestamp_Module_gateObj__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_gateObj xdc_runtime_Timestamp_Module_gateObj__C = ((CT_xdc_runtime_Timestamp_Module_gateObj)0)
```

Definition at line 8239 of file dss_mrr_pe674.c.

4.16.4.951 xdc_runtime_Timestamp_Module_gatePrms__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_gatePrms xdc_runtime_Timestamp_Module_gatePrms__C = ((CT_xdc_runtime_Timestamp_Module_gatePrms)0)
```

Definition at line 8243 of file dss_mrr_pe674.c.

4.16.4.952 xdc_runtime_Timestamp_Module_id__C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_id xdc_runtime_Timestamp_Module_id__C = (xdc_Bits16)0xf
```

Definition at line 8247 of file dss_mrr_pe674.c.

4.16.4.953 xdc_runtime_Timestamp_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerDefined xdc_runtime_Timestamp_Module_←  
loggerDefined_C = 0
```

Definition at line 8251 of file dss_mrr_pe674.c.

4.16.4.954 xdc_runtime_Timestamp_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxn0 xdc_runtime_Timestamp_Module_←  
loggerFxn0_C = ((CT_xdc_runtime_Timestamp_Module_loggerFxn0) 0)
```

Definition at line 8259 of file dss_mrr_pe674.c.

4.16.4.955 xdc_runtime_Timestamp_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxn1 xdc_runtime_Timestamp_Module_←  
loggerFxn1_C = ((CT_xdc_runtime_Timestamp_Module_loggerFxn1) 0)
```

Definition at line 8263 of file dss_mrr_pe674.c.

4.16.4.956 xdc_runtime_Timestamp_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxn2 xdc_runtime_Timestamp_Module_←  
loggerFxn2_C = ((CT_xdc_runtime_Timestamp_Module_loggerFxn2) 0)
```

Definition at line 8267 of file dss_mrr_pe674.c.

4.16.4.957 xdc_runtime_Timestamp_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxn4 xdc_runtime_Timestamp_Module_←  
loggerFxn4_C = ((CT_xdc_runtime_Timestamp_Module_loggerFxn4) 0)
```

Definition at line 8271 of file dss_mrr_pe674.c.

4.16.4.958 xdc_runtime_Timestamp_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerFxn8 xdc_runtime_Timestamp_Module_←  
loggerFxn8_C = ((CT_xdc_runtime_Timestamp_Module_loggerFxn8) 0)
```

Definition at line 8275 of file dss_mrr_pe674.c.

4.16.4.959 xdc_runtime_Timestamp_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Module_loggerObj xdc_runtime_Timestamp_Module_←  
loggerObj_C = ((CT_xdc_runtime_Timestamp_Module_loggerObj) 0)
```

Definition at line 8255 of file dss_mrr_pe674.c.

4.16.4.960 xdc_runtime_Timestamp_Object_count_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Object_count xdc_runtime_Timestamp_Object_count_C =  
0
```

Definition at line 8279 of file dss_mrr_pe674.c.

4.16.4.961 xdc_runtime_Timestamp_Object_heap_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Object_heap xdc_runtime_Timestamp_Object_heap_C = 0
```

Definition at line 8283 of file dss_mrr_pe674.c.

4.16.4.962 xdc_runtime_Timestamp_Object_sizeof_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Object_sizeof xdc_runtime_Timestamp_Object_sizeof_C = 0
```

Definition at line 8287 of file dss_mrr_pe674.c.

4.16.4.963 xdc_runtime_Timestamp_Object_table_C

```
const __FAR__ CT_xdc_runtime_Timestamp_Object_table xdc_runtime_Timestamp_Object_table_C = 0
```

Definition at line 8291 of file dss_mrr_pe674.c.

4.16.4.964 xdc_runtime_TimestampNull_Module_diagsEnabled_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsEnabled xdc_runtime_TimestampNull_Module_diagsEnabled_C = (xdc_Bits32)0x10
```

Definition at line 8300 of file dss_mrr_pe674.c.

4.16.4.965 xdc_runtime_TimestampNull_Module_diagsIncluded_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsIncluded xdc_runtime_TimestampNull_Module_diagsIncluded_C = (xdc_Bits32)0x10
```

Definition at line 8304 of file dss_mrr_pe674.c.

4.16.4.966 xdc_runtime_TimestampNull_Module_diagsMask_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_diagsMask xdc_runtime_TimestampNull_Module_diagsMask_C = ((CT_xdc_runtime_TimestampNull_Module_diagsMask)0)
```

Definition at line 8308 of file dss_mrr_pe674.c.

4.16.4.967 xdc_runtime_TimestampNull_Module_FXNS_C

```
const xdc_runtime_TimestampNull_Fxns xdc_runtime_TimestampNull_Module_FXNS_C
```

Initial value:

```
= {
    &xdc_runtime_ITimestampProvider_Interface__BASE__C,
    &xdc_runtime_TimestampNull_Module_FXNS_C._sfxns,
    xdc_runtime_TimestampNull_get32_E,
    xdc_runtime_TimestampNull_get64_E,
    xdc_runtime_TimestampNull_getFreq_E,
    {
        NULL,
        NULL,
        NULL,
        0x10,
    }
}
```

Definition at line 882 of file dss_mrr_pe674.c.

4.16.4.968 xdc_runtime_TimestampNull_Module_gateObj_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_gateObj xdc_runtime_TimestampNull_Module_gateObj_C = ((CT_xdc_runtime_TimestampNull_Module_gateObj)0)
```

Definition at line 8312 of file dss_mrr_pe674.c.

4.16.4.969 xdc_runtime_TimestampNull_Module_gatePrms_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_gatePrms xdc_runtime_TimestampNull_<-
Module_gatePrms_C = ((CT_xdc_runtime_TimestampNull_Module_gatePrms)0)
Definition at line 8316 of file dss_mrr_pe674.c.
```

4.16.4.970 xdc_runtime_TimestampNull_Module_id_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_id xdc_runtime_TimestampNull_Module_id_<-
_C = (xdc_Bits16)0x10
Definition at line 8320 of file dss_mrr_pe674.c.
```

4.16.4.971 xdc_runtime_TimestampNull_Module_loggerDefined_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerDefined xdc_runtime_TimestampNull_<-
Module_loggerDefined_C = 0
Definition at line 8324 of file dss_mrr_pe674.c.
```

4.16.4.972 xdc_runtime_TimestampNull_Module_loggerFxn0_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn0 xdc_runtime_TimestampNull_<-
Module_loggerFxn0_C = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn0)0)
Definition at line 8332 of file dss_mrr_pe674.c.
```

4.16.4.973 xdc_runtime_TimestampNull_Module_loggerFxn1_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn1 xdc_runtime_TimestampNull_<-
Module_loggerFxn1_C = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn1)0)
Definition at line 8336 of file dss_mrr_pe674.c.
```

4.16.4.974 xdc_runtime_TimestampNull_Module_loggerFxn2_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn2 xdc_runtime_TimestampNull_<-
Module_loggerFxn2_C = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn2)0)
Definition at line 8340 of file dss_mrr_pe674.c.
```

4.16.4.975 xdc_runtime_TimestampNull_Module_loggerFxn4_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn4 xdc_runtime_TimestampNull_<-
Module_loggerFxn4_C = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn4)0)
Definition at line 8344 of file dss_mrr_pe674.c.
```

4.16.4.976 xdc_runtime_TimestampNull_Module_loggerFxn8_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerFxn8 xdc_runtime_TimestampNull_<-
Module_loggerFxn8_C = ((CT_xdc_runtime_TimestampNull_Module_loggerFxn8)0)
Definition at line 8348 of file dss_mrr_pe674.c.
```

4.16.4.977 xdc_runtime_TimestampNull_Module_loggerObj_C

```
const __FAR__ CT_xdc_runtime_TimestampNull_Module_loggerObj xdc_runtime_TimestampNull_<-
Module_loggerObj_C = ((CT_xdc_runtime_TimestampNull_Module_loggerObj)0)
```

Definition at line 8328 of file dss_mrr_pe674.c.

4.16.4.978 `xdc_runtime_TimestampNull_Object_count_C`

```
const __FAR__ CT_xdc_runtime_TimestampNull_Object_count xdc_runtime_TimestampNull_Object__←
count__C = 0
```

Definition at line 8352 of file dss_mrr_pe674.c.

4.16.4.979 `xdc_runtime_TimestampNull_Object_heap_C`

```
const __FAR__ CT_xdc_runtime_TimestampNull_Object_heap xdc_runtime_TimestampNull_Object__←
heap__C = 0
```

Definition at line 8356 of file dss_mrr_pe674.c.

4.16.4.980 `xdc_runtime_TimestampNull_Object_sizeof_C`

```
const __FAR__ CT_xdc_runtime_TimestampNull_Object_sizeof xdc_runtime_TimestampNull_Object__←
_sizeof__C = 0
```

Definition at line 8360 of file dss_mrr_pe674.c.

4.16.4.981 `xdc_runtime_TimestampNull_Object_table_C`

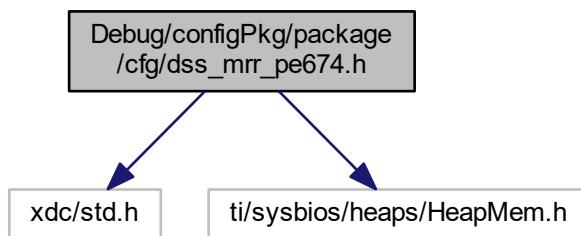
```
const __FAR__ CT_xdc_runtime_TimestampNull_Object_table xdc_runtime_TimestampNull_Object__←
table__C = 0
```

Definition at line 8364 of file dss_mrr_pe674.c.

4.17 Debug/configPkg/package/cfg/dss_mrr_pe674.h File Reference

```
#include <xdc/std.h>
#include <ti/sysbios/heaps/HeapMem.h>
```

Include dependency graph for dss_mrr_pe674.h:



Variables

- const ti_sysbios_heaps_HeapMem_Handle **heap0**
- int **xdc_runtime_Startup_EXECFXN_C**
- int **xdc_runtime_Startup_RESETFXN_C**

4.17.1 Variable Documentation

4.17.1.1 heap0

```
const ti_sysbios_heaps_HeapMem_Handle heap0
Definition at line 13373 of file dss_mrr_pe674.c.
```

4.17.1.2 xdc_runtime_Startup__EXECFXN__C

```
int xdc_runtime_Startup__EXECFXN__C
```

4.17.1.3 xdc_runtime_Startup__RESETFXN__C

```
int xdc_runtime_Startup__RESETFXN__C
```

4.18 Debug/configPkg/package/cfg/dss_mrr_pe674.xdc.inc File Reference

4.19 Debug/configPkg/package/configPkg.java File Reference

Data Structures

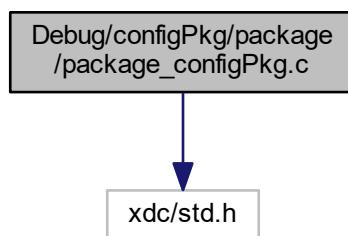
- class configPkg

4.20 Debug/configPkg/package/package.defs.h File Reference

4.21 Debug/configPkg/package/package.xdc.inc File Reference

4.22 Debug/configPkg/package/package_configPkg.c File Reference

```
#include <xdc/std.h>
Include dependency graph for package_configPkg.c:
```



Macros

- #define __xdc_PKGVERS null

- #define __xdc_PKGNAME configPkg
- #define __xdc_PKGPREFIX configPkg_

Variables

- __FAR__ char configPkg_dummy_

4.22.1 Macro Definition Documentation

4.22.1.1 __xdc_PKGNAME

```
#define __xdc_PKGNAME configPkg  
Definition at line 13 of file package_configPkg.c.
```

4.22.1.2 __xdc_PKGPREFIX

```
#define __xdc_PKGPREFIX configPkg_  
Definition at line 14 of file package_configPkg.c.
```

4.22.1.3 __xdc_PKGVERS

```
#define __xdc_PKGVERS null  
Definition at line 12 of file package_configPkg.c.
```

4.22.2 Variable Documentation

4.22.2.1 configPkg_dummy_

```
__FAR__ char configPkg_dummy_  
Definition at line 10 of file package_configPkg.c.
```

4.23 Debug/configPkg/package/rel/configPkg.xdc.inc File Reference

4.24 Debug/dss_config_edma_util.d File Reference

4.25 Debug/dss_data_path.d File Reference

4.26 Debug/dss_main.d File Reference

4.27 Debug/Extended_Kalman_Filter_xyz.d File Reference

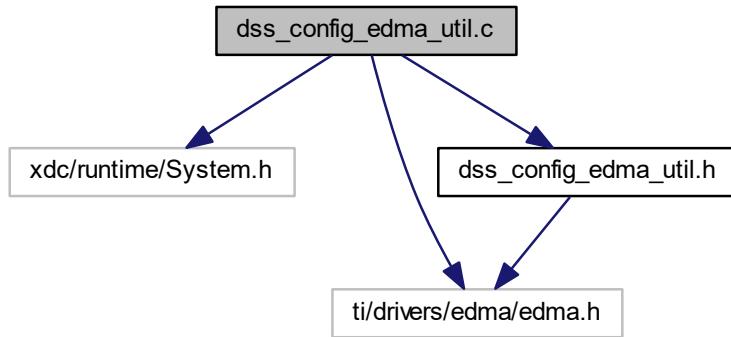
4.28 Debug/gen_twiddle_fft16x16.d File Reference

4.29 Debug/gen_twiddle_fft32x32.d File Reference

4.30 dss_config_edma_util.c File Reference

EDMA Configuration Utility API implementation.

```
#include <xdc/runtime/System.h>
#include <ti/drivers/edma/edma.h>
#include "dss_config_edma_util.h"
Include dependency graph for dss_config_edma_util.c:
```



Macros

- `#define MAX(x, y) ((x) > (y) ? (x) : (y))`

Functions

- `static int32_t EDMA_setup_shadow_link (EDMA_Handle handle, uint8_t chld, uint16_t shadowParamId, EDMA_paramSetConfig_t *config, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)`
- `int32_t EDMAutil_configType1 (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t aCount, uint16_t bCount, int16_t srcBIdx, int16_t dstBIdx, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)`
- `int32_t EDMAutil_configType2a (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t sampleLenInBytes, uint16_t numRangeBins, uint8_t numTxAnt, uint8_t numRxAnt, uint16_t numDopplerBins, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)`
- `int32_t EDMAutil_configType2b (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t sampleLenInBytes, uint16_t numRangeBins, uint8_t numTxAnt, uint8_t numRxAnt, uint16_t numDopplerBins, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)`
- `int32_t EDMAutil_configType3 (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t aCount, uint16_t bCount, int16_t srcBIdx, int16_t destBIdx, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)`
- `int32_t EDMAutil_triggerType3 (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, uint8_t triggerEnabled)`

4.30.1 Detailed Description

EDMA Configuration Utility API implementation.

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4.30.2 Macro Definition Documentation

4.30.2.1 MAX

```
#define MAX(
    x,
    y ) ((x) > (y) ? (x) : (y))
```

Definition at line 44 of file dss_config_edma_util.c.

4.30.3 Function Documentation

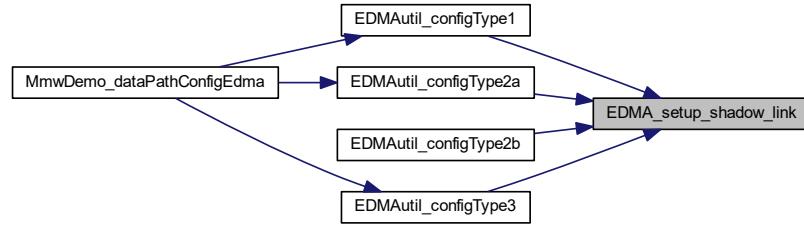
4.30.3.1 EDMA_setup_shadow_link()

```
static int32_t EDMA_setup_shadow_link (
    EDMA_Handle handle,
    uint8_t chId,
    uint16_t shadowParamId,
    EDMA_paramSetConfig_t * config,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg ) [static]
```

Definition at line 51 of file dss_config_edma_util.c.

Referenced by EDMAutil_configType1(), EDMAutil_configType2a(), EDMAutil_configType2b(), and EDMAutil_configType3().

Here is the caller graph for this function:



4.30.3.2 EDMAutil_configType1()

```

int32_t EDMAutil_configType1 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t aCount,
    uint16_t bCount,
    int16_t srcBIdx,
    int16_t dstBIdx,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )

```

Definition at line 86 of file dss_config_edma_util.c.

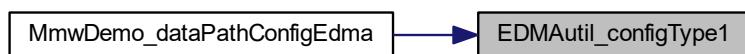
References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.30.3.3 EDMAutil_configType2a()

```
int32_t EDMAutil_configType2a (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t sampleLenInBytes,
    uint16_t numRangeBins,
    uint8_t numTxAnt,
    uint8_t numRxAnt,
    uint16_t numDopplerBins,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )
```

Definition at line 153 of file dss_config_edma_util.c.

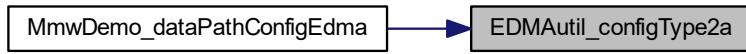
References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.30.3.4 EDMAutil_configType2b()

```
int32_t EDMAutil_configType2b (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t sampleLenInBytes,
    uint16_t numRangeBins,
```

```

    uint8_t numTxAnt,
    uint8_t numRxAnt,
    uint16_t numDopplerBins,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )

```

Definition at line 220 of file dss_config_edma_util.c.

References EDMA_setup_shadow_link(), and MAX.

Here is the call graph for this function:



4.30.3.5 EDMAUtil_configType3()

```

int32_t EDMAUtil_configType3 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t aCount,
    uint16_t bCount,
    int16_t srcBIdx,
    int16_t destBIdx,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )

```

Definition at line 297 of file dss_config_edma_util.c.

References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.30.3.6 EDMAutil_triggerType3()

```
int32_t EDMAutil_triggerType3 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    uint8_t triggerEnabled )
```

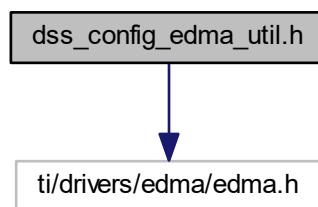
Definition at line 364 of file dss_config_edma_util.c.
Referenced by MmwDemo_processChirp().

Here is the caller graph for this function:

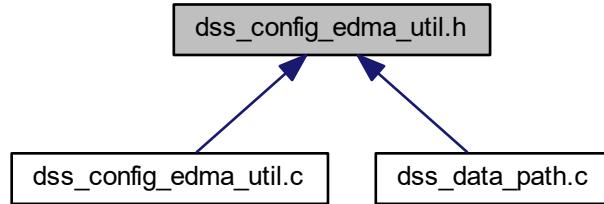


4.31 dss_config_edma_util.h File Reference

EDMA Configuration Utility API definitions.
`#include <ti/drivers/edma/edma.h>`
Include dependency graph for dss_config_edma_util.h:



This graph shows which files directly or indirectly include this file:



Functions

- int32_t **EDMAUtil_configType1** (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t aCount, uint16_t bCount, int16_t srcBIdx, int16_t dstBIdx, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)
- int32_t **EDMAUtil_configType2a** (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t sampleLenInBytes, uint16_t numRangeBins, uint8_t numTxAnt, uint8_t numRxAnt, uint16_t numDopplerBins, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)
- int32_t **EDMAUtil_configType3** (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, bool isEventTriggered, uint16_t shadowParamId, uint16_t aCount, uint16_t bCount, int16_t srcBIdx, int16_t destBIdx, uint8_t eventQueueId, EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn, uintptr_t transferCompletionCallbackFxnArg)
- int32_t **EDMAUtil_triggerType3** (EDMA_Handle handle, uint8_t *srcBuff, uint8_t *dstBuff, uint8_t chld, uint8_t triggerEnabled)

4.31.1 Detailed Description

EDMA Configuration Utility API definitions.

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4.31.2 Function Documentation

4.31.2.1 EDMAutil_configType1()

```
int32_t EDMAutil_configType1 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t aCount,
    uint16_t bCount,
    int16_t srcBIdx,
    int16_t dstBIdx,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )
```

Definition at line 86 of file dss_config_edma_util.c.

References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.31.2.2 EDMAutil_configType2a()

```
int32_t EDMAutil_configType2a (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t sampleLenInBytes,
    uint16_t numRangeBins,
```

```

    uint8_t numTxAnt,
    uint8_t numRxAnt,
    uint16_t numDopplerBins,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )

```

Definition at line 153 of file dss_config_edma_util.c.

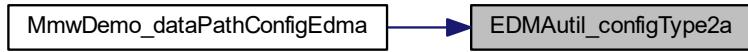
References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.31.2.3 EDMAUtil_configType3()

```

int32_t EDMAUtil_configType3 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    bool isEventTriggered,
    uint16_t shadowParamId,
    uint16_t aCount,
    uint16_t bCount,
    int16_t srcBIdx,
    int16_t destBIdx,
    uint8_t eventQueueId,
    EDMA_transferCompletionCallbackFxn_t transferCompletionCallbackFxn,
    uintptr_t transferCompletionCallbackFxnArg )

```

Definition at line 297 of file dss_config_edma_util.c.

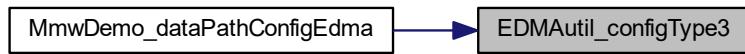
References EDMA_setup_shadow_link().

Referenced by MmwDemo_dataPathConfigEdma().

Here is the call graph for this function:



Here is the caller graph for this function:



4.31.2.4 EDMAUtil_triggerType3()

```

int32_t EDMAUtil_triggerType3 (
    EDMA_Handle handle,
    uint8_t * srcBuff,
    uint8_t * dstBuff,
    uint8_t chId,
    uint8_t triggerEnabled )

```

Definition at line 364 of file dss_config_edma_util.c.

Referenced by MmwDemo_processChirp().

Here is the caller graph for this function:



4.32 dss_data_path.c File Reference

Implements Data path processing functionality.

```

#include <stdint.h>
#include <stdlib.h>
#include <stddef.h>
#include <string.h>
#include <stdio.h>
#include <math.h>
#include <xdc/std.h>

```

```
#include <xdc/cfg/global.h>
#include <xdc/runtime/IHeap.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/Memory.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/knl/Event.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Clock.h>
#include <ti/sysbios/heaps/HeapBuf.h>
#include <ti/sysbios/heaps/HeapMem.h>
#include <ti/drivers/osal/DebugP.h>
#include <assert.h>
#include <ti/common/sys_common.h>
#include <ti/drivers/osal/SemaphoreP.h>
#include <ti/drivers/edma/edma.h>
#include <ti/drivers/esm/esm.h>
#include <ti/drivers/soc/soc.h>
#include <ti/utils/cycleprofiler/cycle_profiler.h>
#include <ti/alg/mmwavelib/mmwavelib.h>
#include "gen_twiddle_fft32x32.h"
#include "gen_twiddle_fft16x16.h"
#include "DSP_fft32x32.h"
#include "DSP_fft16x16.h"
#include <ti/mathlib/mathlib.h>
#include <common/app_cfg.h>
#include "dss_data_path.h"
#include "dss_config_edma_util.h"
#include "clusteringDBscan.h"
#include "EKF_XYZ_Interface.h"
#include "common/mmWave_XSS.h"
Include dependency graph for dss_data_path.c:
```



Macros

- #define **DebugP_ASSERT_ENABLED** 1
- #define **ROUND**(x) ((x) < 0 ? ((x) - 0.5) : ((x) + 0.5))
- #define **SOC_XWR18XX_DSS_L3RAM_SIZE** 0x100000
- #define **MMW_ADCBUF_SIZE** 0x4000U
- #define **MMW_L2_HEAP_SIZE** 0x19000U

L2 heap used for allocating buffers in L2 SRAM, mostly scratch buffers.

- #define **MMW_L1_HEAP_SIZE** 0x4000U

L1 heap used for allocating buffers in L1D SRAM, mostly scratch buffers.

- #define **FFT_WINDOW_INT16** 0
- #define **FFT_WINDOW_INT32** 1
- #define **MMW_WIN_HAMMING** 0
- #define **MMW_WIN_BLACKMAN** 1
- #define **MMW_WIN_RECT** 2
- #define **MMW_WIN_HANNING_RECT** 3
- #define **ROUNDf**(x) ((x) < 0 ? ((x) - 0.5f) : ((x) + 0.5f))
- #define **pingPongId**(x) ((x) & 0x1U)

- #define **isPong**(x) (**pingPongId**(x))
- #define **SOC_MAX_NUM_RX_ANTENNAS** 4
- #define **SOC_MAX_NUM_TX_ANTENNAS** 3
- #define **ALIGN**(x, a) (((x)+((a)-1))&~((a)-1))
- #define **MMW_ALLOC_BUF**(name, nameType, startAddr, alignment, size)
- #define **TWENTY_TWO_DB_DOPPLER_SNR** ((22 *(256))/6)
- #define **EIGHTEEN_DB_DOPPLER_SNR** ((18 *(256))/6)
- #define **ZERO_POINT_FIVE_METERS** (0.5f * 128)
- #define **FOUR_POINT_ZERO_METERS** (4 * 128)

Functions

- void **MmwDemo_genWindow** (void *win, uint32_t windowDatumType, uint32_t winLen, uint32_t winGenLen, int32_t oneQformat, uint32_t winType)
- uint32_t **findKLargestPeaks** (uint16_t *restrict cfarDetObjIndexBuf, uint16_t *restrict cfarDetObjSNR, uint32_t numDetObjPerCfar, uint16_t *restrict sumAbs, uint16_t numBins, uint16_t K)
- uint32_t **pruneToPeaks** (uint16_t *restrict cfarDetObjIndexBuf, uint16_t *restrict cfarDetObjSNR, uint32_t numDetObjPerCfar, uint16_t *restrict sumAbs, uint16_t numBins)
- uint32_t **pruneToPeaksOrNeighbourOfPeaks** (uint16_t *restrict cfarDetObjIndexBuf, uint16_t *restrict cfarDetObjSNR, uint32_t numDetObjPerCfar, uint16_t *restrict sumAbs, uint16_t numBins)
- uint32_t **findandPopulateIntersectionOfDetectedObjects** (**DSS_DataPathObj** *restrict obj, uint32_t numDetObjPerCfar, uint16_t dopplerLine, uint32_t numDetObj2D, uint16_t *restrict sumAbsRange)
- uint32_t **findandPopulateDetectedObjects** (**DSS_DataPathObj** *restrict obj, uint32_t numDetObjPerCfar, uint16_t dopplerLine, uint32_t numDetObj2D, uint16_t *restrict sumAbsRange)
- uint32_t **MmwDemo_cfarPeakGrouping** (**MmwDemo_detectedObjActual** *objOut, uint32_t numDetectedObjects, uint16_t *detMatrix, uint32_t numRangeBins, uint32_t numDopplerBins, uint32_t groupInDopplerDirection, uint32_t groupInRangeDirection)
- uint32_t **secondDimFFTandLog2Computation** (**DSS_DataPathObj** *obj, uint16_t *sumAbs, uint16_t checkDetMatrixTx, uint16_t rangelDX, uint32_t *pingPongIdxPtr)
- uint32_t **cifarCa_SO_dBwrap_withSNR** (const uint16_t inp[restrict], uint16_t out[restrict], uint16_t outSNR[restrict], uint32_t len, uint32_t const1, uint32_t const2, uint32_t guardLen, uint32_t noiseLen)

Function Name : *cifarCa_SO_dBwrap_withSNR*.

- uint32_t **cifarCadB_SO_withSNR** (const uint16_t inp[restrict], uint16_t out[restrict], uint16_t outSNR[restrict], uint32_t len, uint32_t const1, uint32_t const2, uint32_t guardLen, uint32_t noiseLen, uint32_t minIndxToIgnoreHPF)

Function Name : *cifarCadB_SO_withSNR*.

- uint32_t **aziEleProcessing** (**DSS_DataPathObj** *obj, uint32_t subframeIndx)
- uint16_t **computeSinAzimSNR** (float *azimuthMagSqr, uint16_t azimIdx, uint16_t numVirtualAntAzim, uint16_t numAngleBins, uint16_t xyzOutputQFormat)
- float **antilog2** (int32_t inputActual, uint16_t fracBitIn)
- void **associateClustering** (**clusteringDBscanOutput_t** *restrict output, **clusteringDBscanReport_t** *restrict state, uint16_t maxNumTrackers, int32_t epsilon2, int32_t vFactor)
- uint32_t **cifarPeakGroupingAlongDoppler** (**MmwDemo_objRaw2D_t** *restrict objOut, uint32_t numDetectedObjects, uint16_t *detMatrix, uint32_t numRangeBins, uint32_t numDopplerBins)
- void **populateOutputs** (**DSS_DataPathObj** *obj)
- uint32_t **pruneTrackingInput** (**trackingInputReport_t** *trackingInput, uint32_t numCluster)
- float **quadraticInterpFltPeakLoc** (float *restrict y, int32_t len, int32_t idx)
- float **quadraticInterpLog2ShortPeakLoc** (uint16_t *restrict y, int32_t len, int32_t idx, uint16_t fracBitIn)
- void **MmwDemo_XYcalc** (**DSS_DataPathObj** *obj, uint32_t objIndex, uint16_t azimIdx, float *azimuthMagSqr)
- void **MmwDemo_addDopplerCompensation** (int32_t dopplerIdx, int32_t numDopplerBins, uint32_t *azimuthModCoefs, uint32_t *azimuthModCoefsThirdBin, uint32_t *azimuthModCoefsTwoThirdBin, int64_t *azimuthIn, uint32_t numAnt, uint32_t numTxAnt, uint16_t txAntIdx)

Function Name : *MmwDemo_DopplerCompensation*.

- static void **MmwDemo_rxChanPhaseBiasCompensation** (uint32_t *rxChComp, int64_t *input, uint32_t numAnt)

Function Name : MmwDemo_rxChanPhaseBiasCompensation.

- uint8_t **select_channel** (uint8_t subframeIdx, uint8_t pingPongId, uint8_t option0ping, uint8_t option0pong)
- void **MmwDemo_startDmaTransfer** (EDMA_Handle handle, uint8_t channelId0, uint8_t subframeIdx)
- void **MmwDemo_resetDopplerLines** (MmwDemo_1D_DopplerLines_t *ths)
- void **MmwDemo_setDopplerLine** (MmwDemo_1D_DopplerLines_t *ths, uint16_t dopplerIndex)
- uint32_t **MmwDemo_isSetDopplerLine** (MmwDemo_1D_DopplerLines_t *ths, uint16_t index)
- int32_t **MmwDemo_getDopplerLine** (MmwDemo_1D_DopplerLines_t *ths)
- uint32_t **MmwDemo_pow2roundup** (uint32_t x)
- void **MmwDemo_XYestimation** (DSS_DataPathObj *obj, uint32_t objIndex)
- void **MmwDemo_XYZestimation** (DSS_DataPathObj *obj, uint32_t objIndex)
- void **MmwDemo_dataPathWait1DInputData** (DSS_DataPathObj *obj, uint32_t pingPongId, uint32_t subframeIdx)
- void **MmwDemo_dataPathWait1DOOutputData** (DSS_DataPathObj *obj, uint32_t pingPongId, uint32_t subframeIdx)
- void **MmwDemo_dataPathWait2DInputData** (DSS_DataPathObj *obj, uint32_t pingPongId, uint32_t subframeIdx)
- void **MmwDemo_dataPathWait3DInputData** (DSS_DataPathObj *obj, uint32_t pingPongId, uint32_t subframeIdx)
- void **MmwDemo_dataPathWaitTransDetMatrix** (DSS_DataPathObj *obj, uint8_t subframeIdx)
- void **MmwDemo_dataPathWaitTransDetMatrix2** (DSS_DataPathObj *obj, uint32_t subframeIdx)
- int32_t **MmwDemo_dataPathConfigEdma** (DSS_DataPathObj *obj)
- uint32_t **rangeBasedPruning** (MmwDemo_detectedObjActual *restrict objOut, MmwDemo_objRaw2D_t *restrict objRaw, RangeDependantThresh_t *restrict SNRThresh, RangeDependantThresh_t *restrict peakValThresh, uint32_t numDetectedObjects, uint32_t numDopplerBins, uint32_t maxRange, uint32_t minRange)
- void **MmwDemo_magnitudeSquared** (cmplx32ReIm_t *restrict inpBuff, float *restrict magSqrBuff, uint32_t numSamples)
- void **MmwDemo_dcRangeSignatureCompensation** (DSS_DataPathObj *obj, uint8_t chirpPingPongId)
- void **MmwDemo_interChirpProcessing** (DSS_DataPathObj *obj, uint32_t chirpPingPongId, uint8_t subframeIdx)
- void **MmwDemo_interFrameProcessing** (DSS_DataPathObj *obj, uint8_t subframeIdx)
- void **MmwDemo_processChirp** (DSS_DataPathObj *obj, uint8_t subframeIdx)
- void **MmwDemo_waitEndOfChirps** (DSS_DataPathObj *obj, uint8_t subframeIdx)
- void **calc_cmplx_exp** (cmplx16ImRe_t *dftSinCos, float i, uint32_t dftLen)
- void **MmwDemo_genDftSinCosTable** (cmplx16ImRe_t *dftSinCosTable, cmplx16ImRe_t *dftHalfBinVal, cmplx16ImRe_t *dftThirdBinVal, cmplx16ImRe_t *dftTwoThirdBinVal, uint32_t dftLen)
- void **MmwDemo_edmaErrorCallbackFxn** (EDMA_Handle handle, EDMA_errorInfo_t *errorInfo)
- void **MmwDemo_edmaTransferControllerErrorCallbackFxn** (EDMA_Handle handle, EDMA_transferControllerErrorInfo_t *errorInfo)
- void **MmwDemo_dataPathInit1Dstate** (DSS_DataPathObj *obj)
- int32_t **MmwDemo_dataPathInitEdma** (DSS_DataPathObj *obj)
- int32_t **MmwDemo_dataPathCopyEdmaHandle** (DSS_DataPathObj *objOutput, DSS_DataPathObj *objInput)
- void **MmwDemo_printHeapStats** (char *name, uint32_t heapUsed, uint32_t heapSize)
- void **MmwDemo_dataPathConfigBuffers** (DSS_DataPathObj *objIn, uint32_t adcBufAddress)
- void **MmwDemo_dataPathConfigFFTs** (DSS_DataPathObj *obj)
- uint32_t **secondDimFFTandLog2Computation** (DSS_DataPathObj *restrict obj, uint16_t *restrict sumAbs, uint16_t checkDetMatrixTx, uint16_t rangeIdx, uint32_t *pingPongIdxPtr)
- uint32_t **cfarPeakGroupingAlongDoppler** (MmwDemo_objRaw2D_t *restrict objOut, uint32_t numDetectedObjects, uint16_t *restrict detMatrix, uint32_t numRangeBins, uint32_t numDopplerBins)
- float **convertSNRdBToVar** (uint16_t SNRdB, uint16_t bitW, uint16_t n_samples, float resolution)
- float **convertSNRLinToVar** (uint16_t SNRLin, uint16_t bitW, uint16_t n_samples, float resolution)
- void **MmwDemo_XYZcalc** (DSS_DataPathObj *obj, uint32_t objIndex, uint16_t azimIdx, float *azimuthMagSqr)
- void **parkingAssistInit** (DSS_DataPathObj *obj)

Variables

- `uint8_t gMmwL3 [SOC_XWR18XX_DSS_L3RAM_SIZE]`
- `uint8_t gMmwL2 [MMW_L2_HEAP_SIZE]`
- `uint8_t gMmwL1 [MMW_L1_HEAP_SIZE]`
- **MCB gMCB**
Global Variable for tracking information required by the design.
- `volatile cycleLog_t gCycleLog`

4.32.1 Detailed Description

Implements Data path processing functionality.

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4.32.2 Macro Definition Documentation

4.32.2.1 ALIGN

```
#define ALIGN(
    x,
    a ) (( (x)+( (a)-1 )) &~((a)-1))
```

4.32.2.2 DebugP_ASSERT_ENABLED

```
#define DebugP_ASSERT_ENABLED 1
Definition at line 70 of file dss_data_path.c.
```

4.32.2.3 EIGHTEEN_DB_DOPPLER_SNR

```
#define EIGHTEEN_DB_DOPPLER_SNR ((18 *(256))/6)
Definition at line 4449 of file dss_data_path.c.
```

4.32.2.4 FFT_WINDOW_INT16

```
#define FFT_WINDOW_INT16 0
```

Types of FFT window

FFT window 16 - samples format is int16_t
 Definition at line 140 of file dss_data_path.c.

4.32.2.5 FFT_WINDOW_INT32

```
#define FFT_WINDOW_INT32 1
```

FFT window 32 - samples format is int32_t
 Definition at line 142 of file dss_data_path.c.

4.32.2.6 FOUR_POINT_ZERO_METERS

```
#define FOUR_POINT_ZERO_METERS (4 * 128)
```

Definition at line 4451 of file dss_data_path.c.

4.32.2.7 isPong

```
#define isPong( x ) ( pingPongId(x) )
```

Definition at line 159 of file dss_data_path.c.

4.32.2.8 MMW_ADCBUF_SIZE

```
#define MMW_ADCBUF_SIZE 0x4000U
```

Definition at line 113 of file dss_data_path.c.

4.32.2.9 MMW_ALLOC_BUF

```
#define MMW_ALLOC_BUF( name,  
                      nameType,  
                      startAddr,  
                      alignment,  
                      size )
```

Value:

```
obj->name = (nameType *) ALIGN(startAddr, alignment); \  
uint32_t name##_end = (uint32_t) obj->name + (size) * sizeof(nameType);
```

4.32.2.10 MMW_L1_HEAP_SIZE

```
#define MMW_L1_HEAP_SIZE 0x4000U
```

L1 heap used for allocating buffers in L1D SRAM, mostly scratch buffers.

Definition at line 121 of file dss_data_path.c.

4.32.2.11 MMW_L2_HEAP_SIZE

```
#define MMW_L2_HEAP_SIZE 0x19000U
```

L2 heap used for allocating buffers in L2 SRAM, mostly scratch buffers.

Definition at line 117 of file dss_data_path.c.

4.32.2.12 MMW_WIN_BLACKMAN

```
#define MMW_WIN_BLACKMAN 1
Blackman window
Definition at line 148 of file dss_data_path.c.
```

4.32.2.13 MMW_WIN_HAMMING

```
#define MMW_WIN_HAMMING 0
Hamming window
Definition at line 146 of file dss_data_path.c.
```

4.32.2.14 MMW_WIN_HANNING_RECT

```
#define MMW_WIN_HANNING_RECT 3
rectangularized Hanning window
Definition at line 152 of file dss_data_path.c.
```

4.32.2.15 MMW_WIN_RECT

```
#define MMW_WIN_RECT 2
Rectangular window
Definition at line 150 of file dss_data_path.c.
```

4.32.2.16 pingPongId

```
#define pingPongId(
    x ) ((x) & 0x1U)
Definition at line 158 of file dss_data_path.c.
```

4.32.2.17 ROUND

```
#define ROUND (
    x ) ((x) < 0 ? ((x) - 0.5) : ((x) + 0.5) )
Definition at line 109 of file dss_data_path.c.
```

4.32.2.18 ROUNDf

```
#define ROUNDf(
    x ) ((x) < 0 ? ((x) - 0.5f) : ((x) + 0.5f) )
simple rounding function for float
Definition at line 154 of file dss_data_path.c.
```

4.32.2.19 SOC_MAX_NUM_RX_ANTENNAS

```
#define SOC_MAX_NUM_RX_ANTENNAS 4
```

Description

This function assigns memory locations to the different data buffers used in the MRR design.

Processing radar signals require a large number of scratch buffers for each step each of the processing stages (be it 1D-FFT, 2D-FFT, 3D-FFT, detection, angle estimation etc. However, since these stages occur serially, the memory assigned to a scratch buffer used in a previous stage can be re-used in the current stage. The Macro MMW_ALLOC_BUF in the following code allows specifying the start addresses such that the memory locations can be overlaid for efficient memory utilization.

In the MRR TI Design, there are two sub-frames per frame, and both sub-frames are processed separately. Therefore, nearly every scratch buffer memory location can be overlaid between the two. The allocation code is called twice to allocate memory for both sub-frames.

Certain memory locations are only necessary for a given processing path and are left unassigned for different programming paths.

Memory locations that correspond to the windowing functions, and twiddle factors, and estimated mean chirp need to be saved between sub-frames and as such cannot be overlaid.

Parameters

in, out	<i>obj</i>	data path object.
---------	------------	-------------------

Return values

<i>na.</i>

Definition at line 2329 of file dss_data_path.c.

4.32.2.20 SOC_MAX_NUM_TX_ANTENNAS

```
#define SOC_MAX_NUM_TX_ANTENNAS 3
```

Definition at line 2330 of file dss_data_path.c.

4.32.2.21 SOC_XWR18XX_DSS_L3RAM_SIZE

```
#define SOC_XWR18XX_DSS_L3RAM_SIZE 0x100000
```

Definition at line 111 of file dss_data_path.c.

4.32.2.22 TWENTY_TWO_DB_DOPPLER_SNR

```
#define TWENTY_TWO_DB_DOPPLER_SNR ((22 * (256)) / 6)
```

Description

The function populates the object location arrays for transmission to MSS. The reason we do this additional step is to minimize the size of the the transmission because it shouldn't saturate the hold only the minimum information necessary for the external GUI are populated.

Parameters

in	<i>obj</i>	data path object.
----	------------	-------------------

Return values

<i>none</i>

Definition at line 4448 of file dss_data_path.c.

4.32.2.23 ZERO_POINT_FIVE_METERS

```
#define ZERO_POINT_FIVE_METERS (0.5f * 128)
```

Definition at line 4450 of file dss_data_path.c.

4.32.3 Function Documentation

4.32.3.1 antilog2()

```
float antilog2 (
    int32_t inputActual,
    uint16_t fracBitIn )
```

Description

The function computes an antilog2 on the input which has a0 specified bitwidth.

Parameters

in	<i>input</i>	16 bit input with specified bitwidth.
in	<i>frac←BitIn</i>	input fractional bitwidth.

Return values

$2^{\wedge} (input/(2^{\wedge} fracBitIn))$	
---	--

Definition at line 4037 of file dss_data_path.c.

4.32.3.2 associateClustering()

```
void associateClustering (
    clusteringDBscanOutput_t *restrict output,
    clusteringDBscanReport_t *restrict state,
    uint16_t maxNumClusters,
    int32_t epsilon2,
    int32_t vFactor )
```

Description

The function performs an association between the pre-existing clusters and the new clusters, with the intent that the cluster sizes are filtered.

Parameters

in	<i>output</i>	The output of the clustering algorithm.
in	<i>state</i>	The previous clustering output.
in	<i>maxNumTrackers</i>	The maximum number of trackers.
in	<i>epsilon2</i>	distance metric param for association.

Return values

<i>none</i>	
-------------	--

Definition at line 4322 of file dss_data_path.c.

4.32.3.3 aziEleProcessing()

```
uint32_t aziEleProcessing (
    DSS_DataPathObj * obj,
```

```
    uint32_t subframeIndx )
```

Description

The function performs the third dimension processing, including the computation of the azimuth, and the x and y co-ordinate.

Parameters

in	<i>input</i>	data path object.
----	--------------	-------------------

Return values

none

Azimuth calculation

Definition at line 4059 of file dss_data_path.c.

4.32.3.4 calc_cmplx_exp()

```
void calc_cmplx_exp (
    cmplx16ImRe_t * dftSinCos,
    float i,
    uint32_t dftLen )
```

Definition at line 2090 of file dss_data_path.c.

4.32.3.5 cfarCa_SO_dBwrap_withSNR()

```
uint32_t cfarCa_SO_dBwrap_withSNR (
    const uint16_t inp[restrict],
    uint16_t out[restrict],
    uint16_t outSNR[restrict],
    uint32_t len,
    uint32_t const1,
    uint32_t const2,
    uint32_t guardLen,
    uint32_t noiseLen )
```

Function Name : cfarCa_SO_dBwrap_withSNR.

Description : Performs a CFAR on an 16-bit unsigned input vector (CFAR-CA). The input values are assumed to be in logarithmic scale. So the comparison between the CUT and the noise samples is additive rather than multiplicative. Comparison is two-sided (wrap around when needed) for all CUTs.

Parameters

in	<i>inp</i>	: input array (16 bit unsigned numbers)
in	<i>out</i>	: output array (indices of detected peaks (zero based counting))
in	<i>outSNR</i>	: SNR array (SNR of detected peaks)
in	<i>len</i>	: number of elements in input array
in	<i>const1,const2</i>	: used to compare the Cell Under Test (CUT) to the sum of the noise cells: [noise sum /(2^(const2))] +const1 for two sided comparison.
in	<i>guardLen</i>	: one sided guard length
in	<i>noiseLen</i>	: one sided Noise length
out	<i>out</i>	: output array with indices of the detected peaks

Returns

Number of detected peaks (i.e length of out)

Precondition

Input (inp) and Output (out) arrays are non-aliased.

Definition at line 3546 of file dss_data_path.c.

4.32.3.6 cfarCadB_SO_withSNR()

```
uint32_t cfarCadB_SO_withSNR (
    const uint16_t inp[restrict],
    uint16_t out[restrict],
    uint16_t outSNR[restrict],
    uint32_t len,
    uint32_t const1,
    uint32_t const2,
    uint32_t guardLen,
    uint32_t noiseLen,
    uint32_t minIndxToIgnoreHPF )
```

Function Name : cfarCadB_SO_withSNR.

Description

The MRR subframe achieves a maximum unambiguous velocity of 90kmph by using signal processing techniques that help disambiguate velocity. This method works by using two different estimates of velocity from the two kinds of chirps ('fast chirps' and 'slow chirps') transmitted in the MRR subframe. If the two velocity estimates do not agree, then velocity disambiguation is necessary. To disambiguate it is necessary to rationalize the two velocity measurements, and find out the disambiguation factor, k. If the naive maximum unambiguous velocity of the 'fast chirp' is v_f , and that of the 'slow chirp' is v_s . Then after the disambiguation process, the disambiguated velocity would $2kv_f+v$, where v is the naïve estimated velocity from the 'fast chirps'.

The disambiguation process works by using the 'fast chirp' velocity to compute different disambiguated velocity hypotheses. This is done by taking the 'fast chirp' velocity and adding $2k v_f$, where $k \{-1,0,1\}$ (an unwrapping process on the velocity estimate). These hypotheses are then converted to indices of the 'slow chirp' by finding the equivalent estimated velocities in the 'slow chirp' configuration (essentially, undoing the unwrapping using v_s as the maximum unambiguous velocity).

If the index corresponding to one of the hypotheses has significant energy, then that hypothesis is considered to be valid. Disambiguation of up to 3x of the naive max-velocity is possible with this method, however, testing has only been done up to 90 kmph.

Parameters

in,out	<i>sumAbs</i>	The slow chirps doppler bins at a certain range.
in	<i>fastChirpVel</i>	The velocity estimate using the fast chirps (pre-disambiguation).
in	<i>fastChirpPeakVal</i>	The peak value of the index of the detected object from the fast chirp.
in	<i>obj</i>	data path object.

Return values

<i>Ambiguity</i>	index.
------------------	--------

Description : Performs a CFAR SO on an 16-bit unsigned input vector. The input values are assumed to be in logarithmic scale. So the comparision between the CUT and the noise samples is additive rather than multiplicative.

Parameters

in	<i>inp</i>	: input array (16 bit unsigned numbers)
in	<i>out</i>	: output array (indices of detected peaks (zero based counting))
in	<i>outSNR</i>	: output array (SNR of detected peaks)
in	<i>len</i>	: number of elements in input array
in	<i>const1,const2</i>	: used to compare the Cell Under Test (CUT) to the sum of the noise cells: [noise sum /(2^(const2-1))] + const1 for one sided comparison (at the begining and end of the input vector). [noise sum /(2^(const2))] + const1 for two sided comparison
in	<i>guardLen</i>	: one sided guard length
in	<i>noiseLen</i>	: one sided noise length
in	<i>minIndxTolgnoreHPF</i>	: the number of indices to force one sided CFAR, so as to avoid false detections due to effect of the HPF.
out	<i>out</i>	: output array with indices of the detected peaks

Returns

Number of detected peaks (i.e length of out)

Precondition

Input (*inp*) and Output (*out*) arrays are non-aliased.

Definition at line 3400 of file dss_data_path.c.

4.32.3.7 cfarPeakGroupingAlongDoppler() [1/2]

```
uint32_t cfarPeakGroupingAlongDoppler (
    MmwDemo_objRaw2D_t *restrict objOut,
    uint32_t numDetectedObjects,
    uint16_t * detMatrix,
    uint32_t numRangeBins,
    uint32_t numDopplerBins )
```

4.32.3.8 cfarPeakGroupingAlongDoppler() [2/2]

```
uint32_t cfarPeakGroupingAlongDoppler (
    MmwDemo_objRaw2D_t *restrict objOut,
    uint32_t numDetectedObjects,
    uint16_t *restrict detMatrix,
    uint32_t numRangeBins,
    uint32_t numDopplerBins )
```

Description

The function groups neighboring peaks (only in the doppler direction) into one. For each detected peak the function checks if the peak is greater than its neighbors. If this is true, the peak is copied to the output list of detected objects. The neighboring peaks that are used for checking are taken from the detection matrix and copied into 1x3 kernel regardless of whether they are CFAR detected or not.

Note: Function always reads 3 samples per detected object from L3 memory into local array.

Parameters

out	<i>objOut</i>	Output array of detected objects after peak grouping
in	<i>objRaw</i>	Array of detected objects after CFAR detection
in	<i>numDetectedObjects</i>	Number of detected objects by CFAR
in	<i>detMatrix</i>	Detection Range/Doppler matrix
in	<i>numDopplerBins</i>	Number of Doppler bins3401

Return values

<i>Number</i>	of detected objects after grouping
---------------	------------------------------------

Definition at line 3845 of file dss_data_path.c.

4.32.3.9 computeSinAzimSNR()

```
uint16_t computeSinAzimSNR (
    float * azimuthMagSqr,
    uint16_t azimIdx,
    uint16_t numVirtualAntAzim,
    uint16_t numAngleBins,
    uint16_t xyzOutputQFormat )
```

Description

The function

Parameters

out	<i>azimuthMagSqr</i>	Input array of the sum of the squares of the zero padded FFT output.
in	<i>azimIdx</i>	The location of the peak of the detected object.
in	<i>numVirtualAntAzim</i>	the size of the FFT input.
in	<i>numAngleBins</i>	The size of the FFT output.
in	<i>xyzOutputQFormat</i>	number of fractional bits in the output.

Return values

<i>SNRlinear</i>	with the programmed fractional bitwidth
------------------	---

Definition at line 3919 of file dss_data_path.c.

4.32.3.10 convertSNRdBToVar()

```
float convertSNRdBToVar (
    uint16_t SNRdB,
    uint16_t bitW,
    uint16_t n_samples,
    float resolution )
```

Description

The function computes the CRLB of the given estimate given an SNR input (dB) and the number of samples used in the estimate, and the resolution of the estimate.

Parameters

in	<i>SNRdB</i>	16 bit input with specified bitwidth.
----	--------------	---------------------------------------

Parameters

in	<i>bitW</i>	input fractional bitwidth (for SNR in dB).
in	<i>n_samples</i>	number of samples per chirp.
in	<i>resolution</i>	range resolution in meters.

Return values

<i>CRLB</i>	in the specified resolution (with some lower bounds).
-------------	---

Definition at line 3971 of file dss_data_path.c.

4.32.3.11 convertSNRLinToVar()

```
float convertSNRLinToVar (
    uint16_t SNRLin,
    uint16_t bitW,
    uint16_t n_samples,
    float resolution )
```

Description

The function computes the CRLB of the given estimate given an SNR input (linear) and the number of samples used in the estimate, and the resolution of the estimate.

The CRLB is lower bounded by the resolution.

Parameters

in	<i>SNRdB</i>	16 bit input with specified bitwidth.
in	<i>bitW</i>	input fractional bitwidth.
in	<i>n_samples</i>	number of samples per chirp.
in	<i>resolution</i>	resolution in meters.

Return values

$2^{\wedge}(\text{input}/(2^{\wedge}\text{fracBitIn}))$	
---	--

Definition at line 4008 of file dss_data_path.c.

4.32.3.12 findandPopulateDetectedObjects()

```
uint32_t findandPopulateDetectedObjects (
    DSS_DataPathObj *restrict obj,
    uint32_t numDetObjPerCfar,
    uint16_t dopplerLine,
    uint32_t numDetObj2D,
    uint16_t *restrict sumAbsRange )
```

Description

This function populates the 2D cfar object

Parameters

in, out	<i>obj</i>	data path object.
in	<i>numDetObjPerCfar</i>	number of detected objects from the CFAR function.

Parameters

in	<i>dopplerLine</i>	The index of the doppler gate being processed.
in	<i>numDetObj2D</i>	The total number of detected objects.
in	<i>sumAbsRange</i>	The sumAbs Array for the range dimension. It is used to populate the 'peakVal' on a per object basis.

Return values

<i>The</i>	total number of detected objects (including the results of the current intersection).
------------	---

Definition at line 3130 of file dss_data_path.c.

4.32.3.13 findandPopulateIntersectionOfDetectedObjects()

```
uint32_t findandPopulateIntersectionOfDetectedObjects (
    DSS_DataPathObj *restrict obj,
    uint32_t numDetObjPerCfar,
    uint16_t dopplerLine,
    uint32_t numDetObj2D,
    uint16_t *restrict sumAbsRange )
```

Description

This function finds the intersection of the 1D cfar objects (computed previously) and the outputs of the 2D CFAR function. The purpose is to select only those objects which have been detected in both the 1D and 2D CFARS.

Parameters

in,out	<i>obj</i>	data path object.
in	<i>numDetObjPerCfar</i>	number of detected objects from the CFAR function.
in	<i>dopplerLine</i>	The index of the doppler gate being processed.
in	<i>numDetObj2D</i>	The total number of detected objects.
in	<i>sumAbsRange</i>	The sumAbs Array for the range dimension. It is used to populate the 'peakVal' on a per object basis.

Return values

<i>The</i>	total number of detected objects (including the results of the current intersection).
------------	---

Definition at line 3031 of file dss_data_path.c.

4.32.3.14 findKLargestPeaks()

```
uint32_t findKLargestPeaks (
    uint16_t *restrict cfarDetObjIndexBuf,
    uint16_t *restrict cfarDetObjSNR,
    uint32_t numDetObjPerCfar,
    uint16_t *restrict sumAbs,
    uint16_t numBins,
    uint16_t K )
```

Description

This function finds the K strongest objects in a given list of objects. The 'strength' of an object is its 'peak value' corresponding to its index in the sumAbs Array.

Parameters

in,out	<i>cifarDetObjIndexBuf</i>	The indices of the detected objects.
in,out	<i>cifarDetObjSNR</i>	The SNR of the detected objects.
in	<i>numDetObjPerCfar</i>	The number of detected objects.
in	<i>sumAbs</i>	The sumAbs array on which the CFAR was run.
in	<i>numBins</i>	The length of the <i>cifarDetObjSNR</i> and <i>cifarDetObjIndexBuf</i> array.
in	<i>K</i>	The maximum number of objects to be returned by this function.

Return values

<i>min(K,numDetObjPerCfar).</i>	
---------------------------------	--

Definition at line 3276 of file dss_data_path.c.

4.32.3.15 MmwDemo_addDopplerCompensation()

```
void MmwDemo_addDopplerCompensation (
    int32_t dopplerIdx,
    int32_t numDopplerBins,
    uint32_t * azimuthModCoefs,
    uint32_t * azimuthModCoefsThirdBin,
    uint32_t * azimuthModCoefsTwoThirdBin,
    int64_t * azimuthIn,
    uint32_t numAnt,
    uint32_t numTxAnt,
    uint16_t txAntIdx )
```

Function Name : MmwDemo_DopplerCompensation.

Description : Compensation of Doppler phase shift in the virtual antennas, (corresponding to second Tx antenna chirps). Symbols corresponding to virtual antennas, are rotated by half of the Doppler phase shift measured by Doppler FFT. The phase shift read from the table using half of the object Doppler index value. If the Doppler index is odd, an extra half of the bin phase shift is added.

Parameters

in	<i>dopplerIdx</i>	: Doppler index of the object
in	<i>numDopplerBins</i>	: Number of Doppler bins
in	<i>azimuthModCoefs</i>	Table with cos/sin values SIN in even position, COS in odd position $\exp(1j*2*pi*k/N)$ for $k=0,...,N-1$ where N is number of Doppler bins.
out	<i>azimuthModCoefsHalfBin</i>	: $\exp(1j*2*pi* 0.5 /N)$ //TODO change to 1/3 instead of 1/2 for the correction.
in,out	<i>azimuthIn</i>	:Pointer to antenna symbols to be Doppler compensated
in	<i>numAnt</i>	: Number of antenna symbols to be Doppler compensated

Returns

```
void
```

Definition at line 4866 of file dss_data_path.c.

4.32.3.16 MmwDemo_cfarPeakGrouping()

```
uint32_t MmwDemo_cfarPeakGrouping (
    MmwDemo_detectedObjActual * objOut,
    uint32_t numDetectedObjects,
    uint16_t * detMatrix,
    uint32_t numRangeBins,
    uint32_t numDopplerBins,
    uint32_t groupInDopplerDirection,
    uint32_t groupInRangeDirection )
```

Description

The function groups neighboring peaks into one. The grouping is done according to two input flags: groupInDopplerDirection and groupInRangeDirection. For each detected peak the function checks if the peak is greater than its neighbors. If this is true, the peak is copied to the output list of detected objects. The neighboring peaks that are used for checking are taken from the detection matrix and copied into 3x3 kernel regardless of whether they are CFAR detected or not. Note: Function always reads 9 samples per detected object from L3 memory into local array tempBuff, but it only needs to read according to input flags. For example if only the groupInDopplerDirection flag is set, it only needs to read middle row of the kernel, i.e. 3 samples per target from detection matrix.

Parameters

<i>out</i>	<i>objOut</i>	Output array of detected objects after peak grouping
<i>in</i>	<i>objRaw</i>	Array of detected objects after CFAR detection
<i>in</i>	<i>numDetectedObjects</i>	Number of detected objects by CFAR
<i>in</i>	<i>detMatrix</i>	Detection Range/Doppler matrix
<i>in</i>	<i>numDopplerBins</i>	Number of Doppler bins
<i>in</i>	<i>groupInDopplerDirection</i>	Flag enables grouping in Doppler direction
<i>in</i>	<i>groupInRangeDirection</i>	Flag enables grouping in Range direction

Return values

<i>Number</i>	of detected objects after grouping
---------------	------------------------------------

Definition at line 3710 of file dss_data_path.c.

4.32.3.17 MmwDemo_dataPathConfigBuffers()

```
void MmwDemo_dataPathConfigBuffers (
    DSS_DataPathObj * obj,
    uint32_t adcBufAddress )
```

Description

Creates heap in L2 and L3 and allocates data path buffers, The heap is destroyed at the end of the function.

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 2331 of file dss_data_path.c.

4.32.3.18 MmwDemo_dataPathConfigEdma()

```
int32_t MmwDemo_dataPathConfigEdma (
    DSS_DataPathObj * obj )
```

Description

Configures all EDMA channels and param sets used in data path processing

This function is very similar to the dataPathConfigEDMA from the OOB demo, but with the difference that we have two subframes, and one subframe can support the maximum velocity enhancement modification. In this method , the 2nd dimension has two kinds of chirps and each chirp is repeated 'numDopplerBins' times, and each chirp has the same number of adc samples.

We would also like to ensure that when the data is transferred to L3 RAM, a range gate (i.e. doppler bins corresponding to a range bin) of each 'chirptype' is contiguous, so that a single EDMA can pull them both out in the 2nd dimension processing.

Hence the EDMAs corresponding to the transfer of 1D data to L3 and the transfer of data from L3 to L2 are modified.

Parameters

in	<i>obj</i>	Pointer to data path object array.
----	------------	------------------------------------

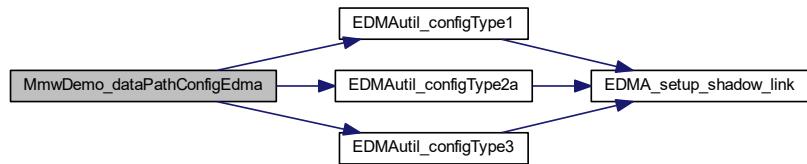
Return values

-1	if error, 0 for no error
----	--------------------------

Definition at line 936 of file dss_data_path.c.

References DSS_DataPathObj_t::ADCdataBuf, DSS_DataPathObj_t::adcDataIn, BYTES_PER_SAMP_1D, BYTES_PER_SAMP_DET, EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, EDMAutil_configType1(), EDMAutil_configType2a(), EDMAutil_configType3(), MAX, MAX_VEL_ENH_PROCESSING, MRR_SF0_EDMA_CH_1D_IN_PING, MRR_SF0_EDMA_CH_1D_IN_PONG, MRR_SF0_EDMA_CH_1D_OUT_PING, MRR_SF0_EDMA_CH_1D_OUT_PONG, MRR_SF0_EDMA_CH_2D_IN_PING, MRR_SF0_EDMA_CH_2D_IN_PONG, MRR_SF0_EDMA_CH_3D_IN_PING, MRR_SF0_EDMA_CH_3D_IN_PONG, MRR_SF0_EDMA_CH_DET_MATRIX, MRR_SF0_EDMA_CH_DET_MATRIX2, NUM_SUBFRAMES, DSS_DataPathObj_t::numAdcSamples, DSS_DataPathObj_t::numRxAntennas, and DSS_DataPathObj_t::processingPath.

Here is the call graph for this function:



4.32.3.19 MmwDemo_dataPathConfigFFTs()

```
void MmwDemo_dataPathConfigFFTs (
    DSS_DataPathObj * obj )
```

Description

Function to populate the twiddle factors for FFTs needed for the data path object.

Parameters

in, out	<i>obj</i>	data path object.
---------	------------	-------------------

Return values

<i>waitingTime.</i>	
---------------------	--

Definition at line 2746 of file dss_data_path.c.

4.32.3.20 MmwDemo_dataPathCopyEdmaHandle()

```
int32_t MmwDemo_dataPathCopyEdmaHandle (
    DSS_DataPathObj * objOutput,
    DSS_DataPathObj * objInput )
```

Description

This function copies the EDMA handles to all of the remaining data path objects.

Parameters

<i>in, out</i>	<i>obj</i>	data path object.
----------------	------------	-------------------

Return values

<i>success.</i>	
-----------------	--

Definition at line 2272 of file dss_data_path.c.

4.32.3.21 MmwDemo_dataPathInit1Dstate()

```
void MmwDemo_dataPathInit1Dstate (
    DSS_DataPathObj * obj )
```

Description

This function initializes some of the states (counters) used for 1D processing.

Parameters

<i>in, out</i>	<i>obj</i>	data path object.
----------------	------------	-------------------

Return values

<i>success/failure.</i>	
-------------------------	--

Definition at line 2198 of file dss_data_path.c.

4.32.3.22 MmwDemo_dataPathInitEdma()

```
int32_t MmwDemo_dataPathInitEdma (
    DSS_DataPathObj * obj )
```

Description

This function copies the EDMA handles to all of the remaining data path objects.

Parameters

<i>in, out</i>	<i>obj</i>	data path object.
----------------	------------	-------------------

Return values

<i>success/failure.</i>	
-------------------------	--

Definition at line 2223 of file dss_data_path.c.

4.32.3.23 MmwDemo_dataPathWait1DInputData()

```
void MmwDemo_dataPathWait1DInputData (
    DSS_DataPathObj * obj,
    uint32_t pingPongId,
    uint32_t subframeIndx )
```

Description

Waits for 1D FFT data to be transferred to input buffer. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>pingPongId</i>	ping-pong id (ping is 0 and pong is 1)
in	<i>subframeIndx</i>	

Return values

<i>NONE</i>	
-------------	--

Definition at line 703 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, MRR_SF0_E←DMA_CH_1D_IN_PING, MRR_SF0_EDMA_CH_1D_IN_PONG, and pingPongId.

4.32.3.24 MmwDemo_dataPathWait1DOOutputData()

```
void MmwDemo_dataPathWait1DOOutputData (
    DSS_DataPathObj * obj,
    uint32_t pingPongId,
    uint32_t subframeIndx )
```

Description

Waits for 1D FFT data to be transferred to output buffer. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>pingPongId</i>	ping-pong id (ping is 0 and pong is 1)
in	<i>subframeIndx</i>	

Return values

<i>NONE</i>	
-------------	--

Definition at line 755 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, MRR_SF0_E←DMA_CH_1D_OUT_PING, MRR_SF0_EDMA_CH_1D_OUT_PONG, pingPongId, and select_channel().

Referenced by MmwDemo_processChirp().

Here is the call graph for this function:



Here is the caller graph for this function:



4.32.3.25 MmwDemo_dataPathWait2DInputData()

```
void MmwDemo_dataPathWait2DInputData (
    DSS_DataPathObj * obj,
    uint32_t pingPongId,
    uint32_t subframeIndx )
```

Description

Waits for 1D FFT data to be transferred to input buffer for 2D-FFT calculation. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>ping</i> ↔ <i>PongId</i>	ping-pong id (ping is 0 and pong is 1)
in	<i>subframe</i>	Index

Return values

NONE	
------	--

Definition at line 786 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, MRR_SF0_E↔DMA_CH_2D_IN_PING, MRR_SF0_EDMA_CH_2D_IN_PONG, pingPongId, and select_channel().

Here is the call graph for this function:



4.32.3.26 MmwDemo_dataPathWait3DInputData()

```
void MmwDemo_dataPathWait3DInputData (
    DSS_DataPathObj * obj,
    uint32_t pingPongId,
    uint32_t subframeIndx )
```

Description

Waits for 1D FFT data to be transferred to input buffer for 3D-FFT calculation. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>pingPongId</i>	ping-pong id (ping is 0 and pong is 1)
in	<i>subframeIndx</i>	

Return values

<i>NONE</i>	
-------------	--

Definition at line 818 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, MRR_SF0_E ↔ DMA_CH_3D_IN_PING, MRR_SF0_EDMA_CH_3D_IN_PONG, pingPongId, and select_channel().

Here is the call graph for this function:



4.32.3.27 MmwDemo_dataPathWaitTransDetMatrix()

```
void MmwDemo_dataPathWaitTransDetMatrix (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Waits for 2D FFT calculated data to be transferred out from L2 memory to detection matrix located in L3 memory. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>subframeIdx</i>	

Return values

<i>NONE</i>	
-------------	--

Definition at line 848 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, and MRR_S←F0_EDMA_CH_DET_MATRIX.

4.32.3.28 MmwDemo_dataPathWaitTransDetMatrix2()

```
void MmwDemo_dataPathWaitTransDetMatrix2 (
    DSS_DataPathObj * obj,
    uint32_t subframeIdx )
```

Description

Waits for 2D FFT data to be transferred from detection matrix in L3 memory to L2 memory for CFAR detection in range direction. This is a blocking function.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>subframeIdx</i>	

Return values

<i>NONE</i>	
-------------	--

Definition at line 885 of file dss_data_path.c.

References EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, MmwDemo_dssAssert, and MRR_S←F0_EDMA_CH_DET_MATRIX2.

4.32.3.29 MmwDemo_dcRangeSignatureCompensation()

```
void MmwDemo_dcRangeSignatureCompensation (
    DSS_DataPathObj * obj,
    uint8_t chirpPingPongId )
```

Description

Compensation of DC range antenna signature (unused currently)

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1447 of file dss_data_path.c.

4.32.3.30 MmwDemo_edmaErrorCallbackFxn()

```
void MmwDemo_edmaErrorCallbackFxn (
```

```
EDMA_Handle handle,
EDMA_errorInfo_t * errorInfo )
```

Description

This is a callback function for EDMA errors.

Parameters

in	<i>handle</i>	EDMA Handle.
in	<i>errorInfo</i>	EDMA error info.

Return values

n/a.	
------	--

Definition at line 2163 of file dss_data_path.c.

4.32.3.31 MmwDemo_edmaTransferControllerErrorCallbackFxn()

```
void MmwDemo_edmaTransferControllerErrorCallbackFxn (
    EDMA_Handle handle,
    EDMA_transferControllerErrorInfo_t * errorInfo )
```

Description

This is a callback function for EDMA TC errors.

Parameters

in	<i>handle</i>	EDMA Handle.
in	<i>errorInfo</i>	EDMA TC error info.

Return values

n/a.	
------	--

Definition at line 2178 of file dss_data_path.c.

4.32.3.32 MmwDemo_genDftSinCosTable()

```
void MmwDemo_genDftSinCosTable (
    cmplx16ImRe_t * dftSinCosTable,
    cmplx16ImRe_t * dftHalfBinVal,
    cmplx16ImRe_t * dftThirdBinVal,
    cmplx16ImRe_t * dftTwoThirdBinVal,
    uint32_t dftLen )
```

Description

Generate SIN/COS table in Q15 (SIN to even int16 location, COS to odd int16 location. Also generates Sin/Cos at half the bin value The table is generated as $T[i]=\cos[2*\pi*i/N] - 1j*\sin[2*\pi*i/N]$ for $i=0,...,N$ where N is $dftLen$ The half bn value is calculated as: $TH = \cos[2*\pi*0.5/N] - 1j*\sin[2*\pi*0.5/N]$

Parameters

out	<i>dftSinCosTable</i>	Array with generated Sin Cos table
out	<i>dftHalfBinVal</i>	Sin/Cos value at half the bin
in	<i>dftLen</i>	Length of the DFT

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 2130 of file dss_data_path.c.

4.32.3.33 MmwDemo_genWindow()

```
void MmwDemo_genWindow (
    void * win,
    uint32_t windowDatumType,
    uint32_t winLen,
    uint32_t winGenLen,
    int32_t oneQformat,
    uint32_t winType )
```

Description

Function to generate a single FFT window sample.

Parameters

out	<i>win</i>	Pointer to calculated window samples.
in	<i>windowDatumType</i>	Window samples data format. For windowDatumType = FFT_WINDOW_INT16 (p. ??), the samples format is int16_t. For windowDatumType = FFT_WINDOW_INT32 (p. ??), the samples format is int32_t.
in	<i>winLen</i>	Nominal window length
in	<i>winGenLen</i>	Number of generated samples
in	<i>oneQformat</i>	Q format of samples, oneQformat is the value of one in the desired format.
in	<i>winType</i>	Type of window, one of MMW_WIN_BLACKMAN (p. ??), MMW_WIN_HANNING , or MMW_WIN_RECT (p. ??).

Return values

<i>none.</i>	
--------------	--

Definition at line 2796 of file dss_data_path.c.

4.32.3.34 MmwDemo_getDopplerLine()

```
int32_t MmwDemo_getDopplerLine (
    MmwDemo_1D_DopplerLines_t * ths )
```

Description

Gets the Doppler index from the Doppler line bit mask, starting from the smallest active Doppler lin (bin). Subsequent calls return the next active Doppler line.

See also

MmwDemo_resetDopplerLines (p. ??)

Definition at line 377 of file dss_data_path.c.

References MmwDemo_1D_DopplerLines::currentIndex, and MmwDemo_1D_DopplerLines::dopplerLineMask.

4.32.3.35 MmwDemo_interChirpProcessing()

```
void MmwDemo_interChirpProcessing (
    DSS_DataPathObj * obj,
```

```
    uint32_t chirpPingPongId,
    uint8_t subframeIndx )
```

Description

Interchirp processing. It is executed per chirp event, after ADC buffer is filled with chirp samples.

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1562 of file dss_data_path.c.

Referenced by MmwDemo_processChirp().

Here is the caller graph for this function:

**4.32.3.36 MmwDemo_interFrameProcessing()**

```
void MmwDemo_interFrameProcessing (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Interframe processing. It is called from MmwDemo_dssDataPathProcessEvents after all chirps of the frame have been received and 1D FFT processing on them has been completed.

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1633 of file dss_data_path.c.

4.32.3.37 MmwDemo_isSetDopplerLine()

```
uint32_t MmwDemo_isSetDopplerLine (
    MmwDemo_1D_DopplerLines_t * ths,
    uint16_t index )
```

Description

Checks whether Doppler line is active in the observed frame. It checks whether the bit is set in the Doppler line bit mask corresponding to Doppler line on which CFAR in Doppler direction detected object.

See also

MmwDemo_resetDopplerLines (p. ??)

Definition at line 352 of file dss_data_path.c.

References MmwDemo_1D_DopplerLines::dopplerLineMask.

4.32.3.38 MmwDemo_magnitudeSquared()

```
void MmwDemo_magnitudeSquared (
```

```
cmplx32ReIm_t *restrict inpBuff,
float *restrict magSqrBuff,
uint32_t numSamples )
```

Description

Outputs magnitude squared float array of input complex32 array

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1428 of file dss_data_path.c.

4.32.3.39 MmwDemo_pow2roundup()

```
uint32_t MmwDemo_pow2roundup (
    uint32_t x )
```

Description

Power of 2 round up function.

Definition at line 404 of file dss_data_path.c.

4.32.3.40 MmwDemo_printHeapStats()

```
void MmwDemo_printHeapStats (
    char * name,
    uint32_t heapUsed,
    uint32_t heapSize )
```

Description

This function holds the last remaining 'printf' in the entire MRR, and prints the space remaining in the global heap.

Parameters

<i>in, out</i>	<i>obj</i>	data path object.
----------------	------------	-------------------

Return values

<i>na.</i>	
------------	--

Definition at line 2297 of file dss_data_path.c.

4.32.3.41 MmwDemo_processChirp()

```
void MmwDemo_processChirp (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Chirp processing. It is called from MmwDemo_dssDataPathProcessEvents. It is executed per chirp.

The range FFT output is transferred in a transpose manner to L3 using an EDMA. This is done so that the 2nd FFT data can be pulled out using a non-transpose EDMA (which is more efficient)

The EDMA transfer requires a destination offset (radarCubeOffset) that is proportional with the chirp number.

For the MAX_VEL_ENH chirp, there are two chirp types (fast and slow), they are stored consecutively (for e.g. chirp 1 of the fast chirp is directly followed by chirp 1 of the slow chirp).

Return values

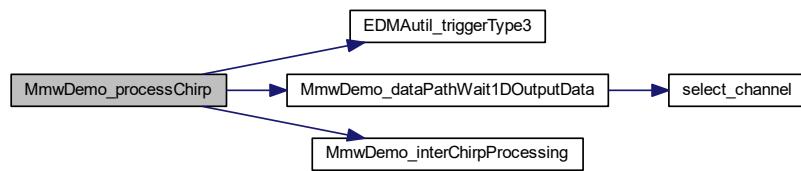
<i>Not</i>	Applicable.
------------	-------------

1. Book keeping.
2. Range processing.

Definition at line 1974 of file dss_data_path.c.

References DSS_DataPathObj_t::chirpCount, DSS_DataPathObj_t::chirpTypeCount, DSS_DataPathObj_t::dopplerBinCount, EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, EDMAutil_triggerType3(), gCycleLog, cycleLog_t::interChirpWaitTime, isPong, MAX_VEL_ENH_PROCESSING, MmwDemo_dataPathWait1DOutputData(), MmwDemo_interChirpProcessing(), MRR_EDMA_TRIGGER_ENABLE, MRR_SF0_EDM_A_CH_1D_OUT_PING, MRR_SF0_EDMA_CH_1D_OUT_PONG, DSS_DataPathObj_t::numDopplerBins, DS_S_DataPathObj_t::numRxAntennas, DSS_DataPathObj_t::numTxAntennas, pingPongId, DSS_DataPathObj_t::processingPath, DSS_DataPathObj_t::radarCube, and DSS_DataPathObj_t::txAntennaCount.

Here is the call graph for this function:



4.32.3.42 MmwDemo_resetDopplerLines()

```
void MmwDemo_resetDopplerLines (
    MmwDemo_1D_DopplerLines_t * ths )
```

Description

Resets the Doppler line bit mask. Doppler line bit mask indicates Doppler lines (bins) on which the CFAR in Doppler direction detected objects. After the CFAR in Doppler direction is completed for all range bins, the CFAR in range direction is performed on indicated Doppler lines. The array dopplerLineMask is uint32_t array. The LSB bit of the first word corresponds to Doppler line (bin) zero.

Definition at line 323 of file dss_data_path.c.

References MmwDemo_1D_DopplerLines::currentIndex, MmwDemo_1D_DopplerLines::dopplerLineMask, and MmwDemo_1D_DopplerLines::dopplerLineMaskLen.

4.32.3.43 MmwDemo_rxChanPhaseBiasCompensation()

```
static void MmwDemo_rxChanPhaseBiasCompensation (
    uint32_t * rxChComp,
    int64_t * input,
    uint32_t numAnt ) [inline], [static]
```

Function Name : MmwDemo_rxChanPhaseBiasCompensation.

Description : Compensation of rx channel phase bias

Parameters

in	<i>rxChComp</i>	: rx channel compensation coefficient
in	<i>input</i>	: 32-bit complex input symbols must be 64 bit aligned
in	<i>numAnt</i>	: number of symbols

Returns

```
void
```

Definition at line 4939 of file dss_data_path.c.

4.32.3.44 MmwDemo_setDopplerLine()

```
void MmwDemo_setDopplerLine (
    MmwDemo_1D_DopplerLines_t * ths,
    uint16_t dopplerIndex )
```

Description

Sets the bit in the Doppler line bit mask dopplerLineMask corresponding to Doppler line on which CFAR in Doppler direction detected object. Indicating the Doppler line being active in observed frame.

See also

MmwDemo_resetDopplerLines (p. ??)

Definition at line 336 of file dss_data_path.c.

References MmwDemo_1D_DopplerLines::dopplerLineMask.

4.32.3.45 MmwDemo_startDmaTransfer()

```
void MmwDemo_startDmaTransfer (
    EDMA_Handle handle,
    uint8_t channelId0,
    uint8_t subframeIndx )
```

Description

Starts a DMA transfer on a specified channel corresponding to a given subframe.

Definition at line 303 of file dss_data_path.c.

4.32.3.46 MmwDemo_waitEndOfChirps()

```
void MmwDemo_waitEndOfChirps (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Wait for transfer of data corresponding to the last 2 chirps (ping/pong) to the radarCube matrix before starting interframe processing.

Return values

Not	Applicable.
-----	-------------

Definition at line 2079 of file dss_data_path.c.

4.32.3.47 MmwDemo_XYcalc()

```
void MmwDemo_XYcalc (
    DSS_DataPathObj * obj,
    uint32_t objIndex,
    uint16_t azimIdx,
    float * azimuthMagSqr )
```

Description

Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index
in	<i>azimIdx</i>	Index of the peak position in Azimuth FFT output
in	<i>azimuthMagSqr</i>	azimuth energy array

Return values

<i>NONE</i>	
-------------	--

Definition at line 4982 of file dss_data_path.c.

Referenced by MmwDemo_XYestimation().

Here is the caller graph for this function:

**4.32.3.48 MmwDemo_XYestimation()**

```
void MmwDemo_XYestimation (
    DSS_DataPathObj * obj,
    uint32_t objIndex )
```

Description

Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object. The detected object structure already has populated range and doppler indices. This function finds maximum index in the azimuth FFT, calculates X and Y and coordinates and stores them into object fields along with the peak height. Also it populates the azimuth index in azimuthMagSqr array.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index

Return values

<i>NONE</i>	
-------------	--

Definition at line 431 of file dss_data_path.c.

References DSS_DataPathObj_t::azimuthMagSqr, DSS_DataPathObj_t::detObj2D, MmwDemo_MultiObjBeamFormingCfg_t::enabled, DSS_DataPathObj_t::maxUnambiguousVel, MmwDemo_XYcalc(), MRR_MAX_OBJ_UT, DSS_DataPathObj_t::multiObjBeamFormingCfg, MmwDemo_MultiObjBeamFormingCfg_t::multiPeakThrsScal, DSS_DataPathObj_t::numAngleBins, DSS_DataPathObj_t::numDetObj, POINT_CLOUD_PROCESSING, DSS_DataPathObj_t::processingPath, MmwDemo_detectedObjActual_t::speed, and DSS_DataPathObj_t::xyzOutput

QFormat.

Here is the call graph for this function:



4.32.3.49 MmwDemo_XYZcalc()

```
void MmwDemo_XYZcalc (
    DSS_DataPathObj * obj,
    uint32_t objIndex,
    uint16_t azimIdx,
    float * azimuthMagSqr )
```

Description

Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index
in	<i>azimIdx</i>	Index of the peak position in Azimuth FFT output
in	<i>azimuthMagSqr</i>	azimuth energy array

Return values

<i>NONE</i>	
-------------	--

Definition at line 5052 of file dss_data_path.c.

Referenced by MmwDemo_XYZestimation().

Here is the caller graph for this function:



4.32.3.50 MmwDemo_XYZestimation()

```
void MmwDemo_XYZestimation (
```

```
DSS_DataPathObj * obj,
uint32_t objIndex )
```

Description

Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object. The detected object structure already has populated range and doppler indices. This function finds maximum index in the azimuth FFT, calculates X and Y and coordinates and stores them into object fields along with the peak height. Also it populates the azimuth index in azimuthMagSqr array.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index

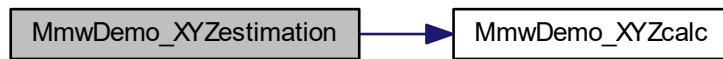
Return values

NONE	
------	--

Definition at line 563 of file dss_data_path.c.

References DSS_DataPathObj_t::azimuthMagSqr, DSS_DataPathObj_t::detObj2D, MmwDemo_MultiObjBeamFormingCfg_t::enabled, MAX_VEL_POINT_CLOUD_PROCESSING_IS_ENABLED, DSS_DataPathObj_t::maxUnambiguousVel, MmwDemo_XYZcalc(), MRR_MAX_OBJ_OUT, DSS_DataPathObj_t::multiObjBeamFormingCfg, MmwDemo_MultiObjBeamFormingCfg_t::multiPeakThrsScal, DSS_DataPathObj_t::numAngleBins, DSS_DataPathObj_t::numDetObj, POINT_CLOUD_PROCESSING, DSS_DataPathObj_t::processingPath, MmwDemo_detectedObjActual_t::speed, and DSS_DataPathObj_t::xyzOutputQFormat.

Here is the call graph for this function:

**4.32.3.51 parkingAssistInit()**

```
void parkingAssistInit (
    DSS_DataPathObj * obj )
```

Description

Initialize the 'parking assist bins' state which is essentially the closest obstruction upper bounded by its maximum value.

Parameters

in	<i>obj</i>	Pointer to data path object
----	------------	-----------------------------

Return values

NONE	
------	--

Definition at line 5146 of file dss_data_path.c.

4.32.3.52 populateOutputs()

```
void populateOutputs (
    DSS_DataPathObj * obj )
```

Description

The function populates the object location arrays for transmission to MSS. The reason we do this additional step is to minimize the size of the the transmission by populating new structure which hold only the minimum information necessary for the GUI.

Parameters

in	<i>input</i>	data path object.
----	--------------	-------------------

Return values

none	
------	--

Definition at line 4452 of file dss_data_path.c.

References AGED_OBJ_DELETION_THRESH, DSS_DataPathObj_t::clusterOpFinal, DSS_DataPathObj_t::dbScanInstance, DSS_DataPathObj_t::dbScanReport, DSS_DataPathObj_t::dbScanState, DSS_DataPathObj_t::detObj2D, DSS_DataPathObj_t::detObjFinal, EIGHTEEN_DB_DOPPLER_SNR, FOUR_POINT_ZERO_METERS, iX, iXd, iY, iYd, MAX_VEL_ENH_PROCESSING, clusteringDBscanInstance::maxClusters, KFtracker::Instance::maxTrackers, MIN_TICK_FOR_TX, MmwDemo_dssAssert, DSS_DataPathObj_t::numActiveTrackers, clusteringDBscanOutput::numCluster, DSS_DataPathObj_t::numDetObj, DSS_DataPathObj_t::parkingAssistBins, DSS_DataPathObj_t::parkingAssistBinsState, DSS_DataPathObj_t::parkingAssistBinsStateCnt, DSS_DataPathObj_t::parkingAssistMaxRange, DSS_DataPathObj_t::parkingAssistNumBins, DSS_DataPathObj_t::parkingAssistNumBinsLog2, POINT_CLOUD_PROCESSING, DSS_DataPathObj_t::processingPath, DSS_DataPathObj_t::sinAzimQFormat, DSS_DataPathObj_t::trackerInstance, DSS_DataPathObj_t::trackerOpFinal, DSS_DataPathObj_t::trackerState, TWENTY_TWO_DB_DOPPLER_SNR, KFstate::vec, KFstate::xSize, DSS_DataPathObj_t::xyzOutputQFormat, KFstate::ySize, and ZERO_POINT_FIVE_METERS.

4.32.3.53 pruneToPeaks()

```
uint32_t pruneToPeaks (
    uint16_t *restrict cfarDetObjIndexBuf,
    uint16_t *restrict cfarDetObjSNR,
    uint32_t numDetObjPerCfar,
    uint16_t *restrict sumAbs,
    uint16_t numBins )
```

Description

This function pruneToPeaks selects the peaks from within the list of objects detected by CFAR.

Parameters

in, out	<i>cfarDetObjIndexBuf</i>	The indices of the detected objects.
in, out	<i>cfarDetObjSNR</i>	The SNR of the detected objects.
in	<i>numDetObjPerCfar</i>	The number of detected objects.
in	<i>sumAbs</i>	The sumAbs array on which the CFAR was run.

Return values

<i>The</i>	number of detected objects that are peaks.
------------	--

Definition at line 3208 of file dss_data_path.c.

4.32.3.54 pruneToPeaksOrNeighbourOfPeaks()

```
uint32_t pruneToPeaksOrNeighbourOfPeaks (
    uint16_t *restrict cfarDetObjIndexBuf,
    uint16_t *restrict cfarDetObjSNR,
    uint32_t numDetObjPerCfar,
    uint16_t *restrict sumAbs,
    uint16_t numBins )
```

Description

A slightly weaker implementation of the 'pruneToPeaks' algorithm. This variation passes peaks as well as their largest neighbour.

Parameters

in, out	<i>cfarDetObjIndexBuf</i>	The indices of the detected objects.
in, out	<i>cfarDetObjSNR</i>	The SNR of the detected objects.
in	<i>numDetObjPerCfar</i>	The number of detected objects.
in	<i>sumAbs</i>	The sumAbs array on which the CFAR was run.
in	<i>numBins</i>	The length of the sumAbs array.

Return values

<i>NONE</i>	
-------------	--

Definition at line 5173 of file dss_data_path.c.

4.32.3.55 pruneTrackingInput()

```
uint32_t pruneTrackingInput (
    trackingInputReport_t * trackingInput,
    const uint32_t numCluster )
```

Description

The function removes objects from extreme angles and with poor angle SNR from the tracking input.

Parameters

in	<i>trackinginput</i>	List of tracking inputs.
in	<i>numClusters</i>	number of tracking inputs (from the clustering output).

Return values

<i>number</i>	of tracking inputs after pruning.
---------------	-----------------------------------

Definition at line 4706 of file dss_data_path.c.

4.32.3.56 quadraticInterpFltPeakLoc()

```
float quadraticInterpFltPeakLoc (
    float *restrict y,
    int32_t len,
```

```
int32_t indx )
```

Description

The function performs a quadratic peak interpolation to compute the fractional offset of the peak location. It is primarily intended to be used in oversampled FFTs.

Parameters

in	<i>y</i>	the array of data.
in	<i>len</i>	length of the array.
in	<i>indx</i>	coarse peak location.

Return values

<i>interpolated</i>	peak location (varies from -1 to +1).
---------------------	---------------------------------------

Definition at line 4741 of file dss_data_path.c.

4.32.3.57 quadraticInterpLog2ShortPeakLoc()

```
float quadraticInterpLog2ShortPeakLoc (
    uint16_t *restrict y,
    int32_t len,
    int32_t indx,
    uint16_t fracBitIn )
```

Description

The function performs a quadratic peak interpolation to compute the fractional offset of the peak location. It is primarily intended to be used in oversampled FFTs. The input is assumed to be an unsigned short.

Parameters

in	<i>y</i>	the array of data.
in	<i>len</i>	length of the array.
in	<i>indx</i>	coarse peak location.
in	<i>frac← BitIn</i>	fractional bits in the input array.

Return values

<i>interpolated</i>	peak location (varies from -1 to +1).
---------------------	---------------------------------------

Definition at line 4795 of file dss_data_path.c.

4.32.3.58 rangeBasedPruning()

```
uint32_t rangeBasedPruning (
    MmwDemo_detectedObjActual *restrict objOut,
    MmwDemo_objRaw2D_t *restrict objRaw,
    RangeDependantThresh_t *restrict SNRThresh,
    RangeDependantThresh_t *restrict peakValThresh,
    uint32_t numDetectedObjects,
    uint32_t numDopplerBins,
    uint32_t maxRange,
    uint32_t minRange )
```

Description

This function populates the ObjOut based on the objRaw. It includes one more layer of pruning which prevent objects beyond the maximum range or minimum range from being populated. Additionally we change the SNR requirement as a function of the range, requiring larger SNR for objects closer to the car, and lower SNR for objects farther from the car.

Parameters

out	<i>objOut</i>	Output array of detected objects after peak grouping
in	<i>objRaw</i>	Array of detected objects after CFAR detection
in	<i>SNRThresh</i>	A list of SNR thresholds for a list of ranges.
in	<i>SNRThresh</i>	A list of peakVal thresholds for a list of ranges.
in	<i>numDetectedObjects</i>	Number of detected objects by CFAR
in	<i>numDopplerBins</i>	Number of Doppler bins
in	<i>maxRange</i>	Maximum range (in ONE_QFORMAT)
in	<i>minRange</i>	Minimum range (in ONE_QFORMAT)

Return values

Number	of detected objects after grouping
--------	------------------------------------

Definition at line 1317 of file dss_data_path.c.

4.32.3.59 secondDimFFTandLog2Computation() [1/2]

```
uint32_t secondDimFFTandLog2Computation (
    DSS_DataPathObj * obj,
    uint16_t * sumAbs,
    uint16_t checkDetMatrixTx,
    uint16_t rangeIdx,
    uint32_t * pingPongIdxPtr )
```

4.32.3.60 secondDimFFTandLog2Computation() [2/2]

```
uint32_t secondDimFFTandLog2Computation (
    DSS_DataPathObj *restrict obj,
    uint16_t *restrict sumAbs,
    uint16_t checkDetMatrixTx,
    uint16_t rangeIdx,
    uint32_t * pingPongIdxPtr )
```

Description

Function to perform 2D-FFT on all Rxs corresponding to one range gatewindow sample. Following the FFT it computes the Log2 Abs and optionally stores it in detMatrix.

Parameters

in,out	<i>obj</i>	Data path object.
out	<i>sumAbs</i>	Sum of the log2 of absolute value.
in	<i>checkDetMatrixTx</i>	Optionally check whether the detection matrix has been transferred to L3.
in	<i>rangeIdx</i>	The index of the range gate being processed.
in,out	<i>pingPongIdxPtr</i>	Pointer to the current ping-pong indx

Return values

<code>waitingTime.</code>	
---------------------------	--

Definition at line 2926 of file dss_data_path.c.

4.32.3.61 select_channel()

```
uint8_t select_channel (
    uint8_t subframeIdx,
    uint8_t pingPongId,
    uint8_t option0ping,
    uint8_t option0pong )
```

Description

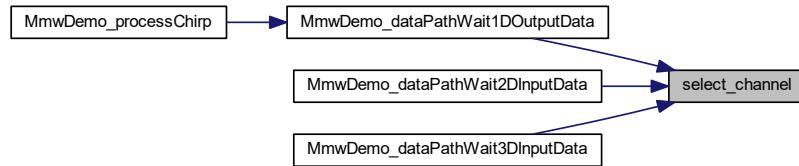
selects one of four channels based on the subframe and the 'ping pong' ID

Definition at line 266 of file dss_data_path.c.

References pingPongId.

Referenced by MmwDemo_dataPathWait1DOutputData(), MmwDemo_dataPathWait2DInputData(), and MmwDemo_dataPathWait3DInputData().

Here is the caller graph for this function:

**4.32.4 Variable Documentation****4.32.4.1 gCycleLog**

```
volatile cycleLog_t gCycleLog
```

Definition at line 99 of file dss_main.c.

Referenced by MmwDemo_processChirp().

4.32.4.2 gMCB

```
MCB gMCB
```

Global Variable for tracking information required by the design.

Definition at line 97 of file dss_main.c.

4.32.4.3 gMmwL1

```
uint8_t gMmwL1[ MMW_L1_HEAP_SIZE ]
```

L1 Heap

Definition at line 136 of file dss_data_path.c.

4.32.4.4 gMmwL2

`uint8_t gMmwL2[MMW_L2_HEAP_SIZE]`
L2 Heap

Definition at line 131 of file dss_data_path.c.

4.32.4.5 gMmwL3

`uint8_t gMmwL3[SOC_XWR18XX_DSS_L3RAM_SIZE]`
L3 RAM buffer

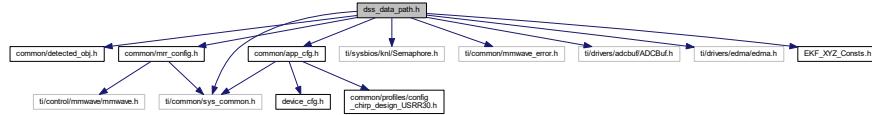
Definition at line 126 of file dss_data_path.c.

4.33 dss_data_path.h File Reference

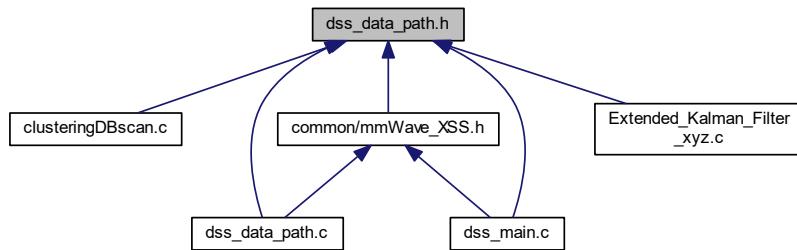
This is the data path processing header.

```
#include "common/detected_obj.h"
#include "common/mrr_config.h"
#include "common/app_cfg.h"
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/common/sys_common.h>
#include <ti/common/mmwave_error.h>
#include <ti/drivers/adcbuf/ADCBuf.h>
#include <ti/drivers/edma/edma.h>
#include "EKF_XYZ_Consts.h"
```

Include dependency graph for dss_data_path.h:



This graph shows which files directly or indirectly include this file:



Data Structures

- struct `cycleLog_t`
DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods.
- struct `maxVelEnhStruct_t`
pre-computed parameters of the max-velocity-enhancement.
- struct `MmwDemo_objRaw1D`

- struct **MmwDemo_objRaw2D**

Parameters of CFAR detected object during the first round of CFAR detections.
- struct **MmwDemo_detectedObjActual_t**

Parameters of CFAR detected object during the second round of CFAR detections.
- struct **clusteringDBscanReportForTx_t**

Detected object estimated parameters.
- struct **trackingReportForTx_t**

Structure for each cluster information report .
- struct **MmwDemo_detectedObjForTx_t**

Detected object estimated parameters to be transmitted out.
- struct **SNRThresholds**

These parameters allow the SNR requirements to be varied as a function of range.
- struct **MmwDemo_1D_DopplerLines**

Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction.
- struct **MmwDemo_timingInfo**

Timing information.
- struct **clusteringDBscanInstance**

error code for clusteringDBscan.
- struct **clusteringDBscanConfig**

Structure element of the list of descriptors for clusteringDBscan configuration.
- struct **clusteringDBscanReport**

Structure for each cluster information report .
- struct **trackingInputReport**

Input to tracking from the clustering output.
- struct **clusteringDBscanOutput**

Structure of clustering output.
- struct **KFtrackerInstance**

Struct for the tracker configuration, and pointers to scratch buffers.
- struct **KFstate**

Kalman filter state.
- struct **DSS_DataPathObj_t**

Millimeter Wave Demo Data Path Information.

Macros

- #define **BYTES_PER_SAMP_1D** (2*sizeof(int16_t)) /*16 bit real, 16 bit imaginary => 4 bytes */
- #define **BYTES_PER_SAMP_2D** (2*sizeof(int32_t)) /*32 bit real, 32 bit imaginary => 8 bytes */
- #define **BYTES_PER_SAMP_DET** sizeof(uint16_t) /*pre-detection matrix is 16 bit unsigned =>2 bytes*/
- #define **DET_THRESH_MULT** 25
- #define **DET_THRESH_SHIFT** 5
- #define **DET_GUARD_LEN** 4
- #define **DET_NOISE_LEN** 16
- #define **PI_** 3.1415926535897
- #define **ONE_Q15** (1 << 15)
- #define **ONE_Q19** (1 << 19)
- #define **ONE_Q8** (1 << 8)
- #define **MMWDEMO_MEMORY_ALLOC_DOUBLE_WORD_ALIGN** 8
- #define **MMWDEMO_MEMORY_ALLOC_MAX_STRUCT_ALIGN** sizeof(uint64_t)
- #define **MMW_PEAK_GROUPING_DET_MATRIX_BASED** 1
- #define **MMW_PEAK_GROUPING_CFAr_PEAK_BASED** 2

- #define MMW_NOISE_AVG_MODE_CFAR_CA ((uint8_t)0U)
- #define MMW_NOISE_AVG_MODE_CFAR_CAGO ((uint8_t)1U)
- #define MMW_NOISE_AVG_MODE_CFAR_CASO ((uint8_t)2U)
- #define DBSCAN_ERROR_CODE_OFFSET 100
- #define TRACKER_SCRATCHPAD_FLT_SIZE
- #define TRACKER_SCRATCHPAD_SHORT_SIZE (MRR_MAX_OBJ_OUT + 2* MAX_TRK_OBJS)

- #define MMWDEMO_OUTPUT_MSG_CLUSTERS 2
Message ID for the custom messages from the MRR demo.
- #define MMWDEMO_OUTPUT_MSG_TRACKED_OBJECTS 3
- #define MMWDEMO_OUTPUT_MSG_PARKING_ASSIST 4

Typedefs

- typedef struct **cycleLog_t_** **cycleLog_t**
DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods.
- typedef struct **maxVelEnhStruct_t_** **maxVelEnhStruct_t**
pre-computed parameters of the max-velocity-enhancement.
- typedef struct **MmwDemo_objRaw1D** **MmwDemo_objRaw1D_t**
Parameters of CFAR detected object during the first round of CFAR detections.
- typedef struct **MmwDemo_objRaw2D** **MmwDemo_objRaw2D_t**
Parameters of CFAR detected object during the second round of CFAR detections.
- typedef struct **MmwDemo_detectedObjActual_t** **MmwDemo_detectedObjActual**
Detected object estimated parameters.
- typedef struct **clusteringDBscanReportForTx_t** **clusteringDBscanReportForTx**
Structure for each cluster information report .
- typedef struct **trackingReportForTx_t** **trackingReportForTx**
Structure for tracking report.
- typedef struct **MmwDemo_detectedObjForTx_t** **MmwDemo_detectedObjForTx**
Detected object estimated parameters to be transmitted out.
- typedef struct **SNRThresholds RangeDependantThresh_t**
These parameters allow the SNR requirements to be varied as a function of range.
- typedef struct **MmwDemo_1D_DopplerLines** **MmwDemo_1D_DopplerLines_t**
Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction.
- typedef struct **MmwDemo_timingInfo** **MmwDemo_timingInfo_t**
Timing information.
- typedef struct **clusteringDBscanInstance** **clusteringDBscanInstance_t**
error code for clusteringDBscan.
- typedef struct **clusteringDBscanConfig** **clusteringDBscanConfig_t**
Structure element of the list of descriptors for clusteringDBscan configuration.
- typedef struct **clusteringDBscanReport** **clusteringDBscanReport_t**
Structure for each cluster information report .
- typedef struct **trackingInputReport** **trackingInputReport_t**
Input to tracking from the clustering output.
- typedef struct **clusteringDBscanOutput** **clusteringDBscanOutput_t**
Structure of clustering output.
- typedef struct **KFtrackerInstance** **KFtrackerInstance_t**
Struct for the tracker configuration, and pointers to scratch buffers.
- typedef struct **KFstate** **KFstate_t**
Kalman filter state.
- typedef struct **DSS_DataPathObj_t** **DSS_DataPathObj**
Millimeter Wave Demo Data Path Information.

Enumerations

- enum **clusteringDBscanErrorCodes** { **DBSCAN_OK** = 0, **DBSCAN_ERROR_MEMORY_ALLOC_FAILED** = DBSCAN_ERROR_CODE_OFFSET, **DBSCAN_ERROR_NOT_SUPPORTED**, **DBSCAN_ERROR_CLUSTER_LIMIT_REACHED** }

error code for clusteringDBscan.

Functions

- void **MmwDemo_dataPathInit1Dstate** (**DSS_DataPathObj** *obj)
 - void **MmwDemo_dataPathDeleteSemaphore** (**DSS_DataPathObj** *obj)
 - int32_t **MmwDemo_dataPathInitEdma** (**DSS_DataPathObj** *obj)
 - int32_t **MmwDemo_dataPathCopyEdmaHandle** (**DSS_DataPathObj** *objOutput, **DSS_DataPathObj** *objInput)
 - int32_t **MmwDemo_dataPathConfigEdma** (**DSS_DataPathObj** *obj)
 - void **MmwDemo_dataPathConfigBuffers** (**DSS_DataPathObj** *obj, uint32_t adcBufAddress)
 - void **MmwDemo_dataPathConfigAzimuthHeatMap** (**DSS_DataPathObj** *obj)
 - void **MmwDemo_dataPathConfigFFTs** (**DSS_DataPathObj** *obj)
 - void **MmwDemo_waitEndOfChirps** (**DSS_DataPathObj** *obj, uint8_t subframeIdx)
 - void **MmwDemo_processChirp** (**DSS_DataPathObj** *obj, uint8_t subframeIdx)
 - void **MmwDemo_interFrameProcessing** (**DSS_DataPathObj** *obj, uint8_t subframeIdx)
 - uint32_t **MmwDemo_pow2roundup** (uint32_t x)
 - float **convertSNRdBToVar** (uint16_t SNRdB, uint16_t bitW, uint16_t n_samples, float resolution)
 - float **convertSNRLinToVar** (uint16_t SNRLin, uint16_t bitW, uint16_t n_samples, float resolution)
 - void **populateOutputs** (**DSS_DataPathObj** *obj)
 - uint32_t **pruneTrackingInput** (**trackingInputReport_t** *trackingInput, uint32_t numCluster)
 - float **quadraticInterpFltPeakLoc** (float *restrict y, int32_t len, int32_t indx)
 - void **MmwDemo_addDopplerCompensation** (int32_t dopplerIdx, int32_t numDopplerBins, uint32_t *azimuthModCoefs, uint32_t *azimuthModCoefsThirdBin, uint32_t *azimuthModCoefsTwoThirdBin, int64_t *azimuthIn, uint32_t numAnt, uint32_t numTxAnt, uint16_t txAntIdx)
- Function Name : MmwDemo_DopplerCompensation.*
- void **MmwDemo_XYcalc** (**DSS_DataPathObj** *obj, uint32_t objIndex, uint16_t azimIdx, float *azimuthMagSqr)
 - void **MmwDemo_XYZcalc** (**DSS_DataPathObj** *obj, uint32_t objIndex, uint16_t azimIdx, float *azimuthMagSqr)
 - void **parkingAssistInit** (**DSS_DataPathObj** *obj)

4.33.1 Detailed Description

This is the data path processing header.

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4.33.2 Macro Definition Documentation

4.33.2.1 BYTES_PER_SAMP_1D

```
#define BYTES_PER_SAMP_1D (2*sizeof(int16_t)) /*16 bit real, 16 bit imaginary => 4 bytes */  
Definition at line 58 of file dss_data_path.h.
```

4.33.2.2 BYTES_PER_SAMP_2D

```
#define BYTES_PER_SAMP_2D (2*sizeof(int32_t)) /*32 bit real, 32 bit imaginary => 8 bytes */  
Definition at line 59 of file dss_data_path.h.
```

4.33.2.3 BYTES_PER_SAMP_DET

```
#define BYTES_PER_SAMP_DET sizeof(uint16_t) /*pre-detection matrix is 16 bit unsigned =>2  
bytes*/  
Definition at line 60 of file dss_data_path.h.
```

4.33.2.4 DBSCAN_ERROR_CODE_OFFSET

```
#define DBSCAN_ERROR_CODE_OFFSET 100  
Definition at line 294 of file dss_data_path.h.
```

4.33.2.5 DET_GUARD_LEN

```
#define DET_GUARD_LEN 4  
Definition at line 65 of file dss_data_path.h.
```

4.33.2.6 DET_NOISE_LEN

```
#define DET_NOISE_LEN 16  
Definition at line 66 of file dss_data_path.h.
```

4.33.2.7 DET_THRESH_MULT

```
#define DET_THRESH_MULT 25  
Definition at line 63 of file dss_data_path.h.
```

4.33.2.8 DET_THRESH_SHIFT

```
#define DET_THRESH_SHIFT 5  
Definition at line 64 of file dss_data_path.h.
```

4.33.2.9 MMW_NOISE_AVG_MODE_CFAR_CA

```
#define MMW_NOISE_AVG_MODE_CFAR_CA ((uint8_t)0U)
cumulative average of the side (left or right) that is greater
Definition at line 83 of file dss_data_path.h.
```

4.33.2.10 MMW_NOISE_AVG_MODE_CFAR_CAGO

```
#define MMW_NOISE_AVG_MODE_CFAR_CAGO ((uint8_t)1U)
cumulative average of the side (left or right) that is smaller
Definition at line 86 of file dss_data_path.h.
```

4.33.2.11 MMW_NOISE_AVG_MODE_CFAR_CASO

```
#define MMW_NOISE_AVG_MODE_CFAR_CASO ((uint8_t)2U)
Definition at line 89 of file dss_data_path.h.
```

4.33.2.12 MMW_PEAK_GROUPING_CFAR_PEAK_BASED

```
#define MMW_PEAK_GROUPING_CFAR_PEAK_BASED 2
cumulative average of left+right
Definition at line 80 of file dss_data_path.h.
```

4.33.2.13 MMW_PEAK_GROUPING_DET_MATRIX_BASED

```
#define MMW_PEAK_GROUPING_DET_MATRIX_BASED 1
Peak grouping scheme of CFAR detected objects based only on peaks of neighboring cells that are already detected
by CFAR
Definition at line 77 of file dss_data_path.h.
```

4.33.2.14 MMWDEMO_MEMORY_ALLOC_DOUBLE_WORD_ALIGN

```
#define MMWDEMO_MEMORY_ALLOC_DOUBLE_WORD_ALIGN 8
Definition at line 73 of file dss_data_path.h.
```

4.33.2.15 MMWDEMO_MEMORY_ALLOC_MAX_STRUCT_ALIGN

```
#define MMWDEMO_MEMORY_ALLOC_MAX_STRUCT_ALIGN sizeof(uint64_t)
Peak grouping scheme of CFAR detected objects based on peaks of neighboring cells taken from detection matrix
Definition at line 74 of file dss_data_path.h.
```

4.33.2.16 MMWDEMO_OUTPUT_MSG_CLUSTERS

```
#define MMWDEMO_OUTPUT_MSG_CLUSTERS 2
Message ID for the custom messages from the MRR demo.
Definition at line 92 of file dss_data_path.h.
```

4.33.2.17 MMWDEMO_OUTPUT_MSG_PARKING_ASSIST

```
#define MMWDEMO_OUTPUT_MSG_PARKING_ASSIST 4
Definition at line 94 of file dss_data_path.h.
```

4.33.2.18 MMWDEMO_OUTPUT_MSG_TRACKED_OBJECTS

```
#define MMWDEMO_OUTPUT_MSG_TRACKED_OBJECTS 3
Definition at line 93 of file dss_data_path.h.
```

4.33.2.19 ONE_Q15

```
#define ONE_Q15 (1 << 15)
Definition at line 69 of file dss_data_path.h.
```

4.33.2.20 ONE_Q19

```
#define ONE_Q19 (1 << 19)
Definition at line 70 of file dss_data_path.h.
```

4.33.2.21 ONE_Q8

```
#define ONE_Q8 (1 << 8)
Definition at line 71 of file dss_data_path.h.
```

4.33.2.22 PI_

```
#define PI_ 3.1415926535897
Definition at line 68 of file dss_data_path.h.
```

4.33.2.23 TRACKER_SCRATCHPAD_FLT_SIZE

```
#define TRACKER_SCRATCHPAD_FLT_SIZE
Value:
(N_MEASUREMENTS + \
 N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT + \
 N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT + \
 N_UNIQ_ELEM_IN_HMAT + \
 (N_STATES*N_MEASUREMENTS) + \
 N_UNIQ_ELEM_IN_SYM_COVMAT + \
 (N_STATES*N_MEASUREMENTS) + \
 (N_STATES*N_STATES) + \
 N_UNIQ_ELEM_IN_SYM_COVMAT + \
 N_MEASUREMENTS)
```

Definition at line 376 of file dss_data_path.h.

4.33.2.24 TRACKER_SCRATCHPAD_SHORT_SIZE

```
#define TRACKER_SCRATCHPAD_SHORT_SIZE ( MRR_MAX_OBJ_OUT + 2* MAX_TRK_OBJS)
Definition at line 386 of file dss_data_path.h.
```

4.33.3 Typedef Documentation**4.33.3.1 clusteringDBscanConfig_t**

```
typedef struct clusteringDBscanConfig clusteringDBscanConfig_t
Structure element of the list of descriptors for clusteringDBscan configuration.
```

4.33.3.2 clusteringDBscanInstance_t

```
typedef struct clusteringDBscanInstance clusteringDBscanInstance_t
error code for clusteringDBscan.
```

4.33.3.3 clusteringDBscanOutput_t

```
typedef struct clusteringDBscanOutput clusteringDBscanOutput_t
Structure of clustering output.
```

4.33.3.4 clusteringDBscanReport_t

```
typedef struct clusteringDBscanReport clusteringDBscanReport_t
Structure for each cluster information report .
```

4.33.3.5 clusteringDBscanReportForTx

```
typedef struct clusteringDBscanReportForTx_t clusteringDBscanReportForTx
Structure for each cluster information report .
```

4.33.3.6 cycleLog_t

```
typedef struct cycleLog_t_ cycleLog_t
DSP cycle profiling structure to accumulate different processing times in chirp and frame processing periods.
```

4.33.3.7 DSS_DataPathObj

```
typedef struct DSS_DataPathObj_t DSS_DataPathObj
Millimeter Wave Demo Data Path Information.
```

The structure is used to hold all the relevant information for the data path.

4.33.3.8 KFstate_t

```
typedef struct KFstate KFstate_t
Kalman filter state.
```

4.33.3.9 KFtrackerInstance_t

```
typedef struct KFtrackerInstance KFtrackerInstance_t
Struct for the tracker configuration, and pointers to scratch buffers.
```

4.33.3.10 maxVelEnhStruct_t

```
typedef struct maxVelEnhStruct_t_ maxVelEnhStruct_t
pre-computed parameters of the max-velocity-enhancement.
```

4.33.3.11 MmwDemo_1D_DopplerLines_t

```
typedef struct MmwDemo_1D_DopplerLines MmwDemo_1D_DopplerLines_t
Active Doppler lines, lines (bins) on which the CFAR detector detected objects during the detections in Doppler direction.
```

4.33.3.12 MmwDemo_detectedObjActual

```
typedef struct MmwDemo_detectedObjActual_t MmwDemo_detectedObjActual
Detected object estimated parameters.
```

4.33.3.13 MmwDemo_detectedObjForTx

```
typedef struct MmwDemo_detectedObjForTx_t MmwDemo_detectedObjForTx
Detected object estimated parameters to be transmitted out.
```

4.33.3.14 MmwDemo_objRaw1D_t

```
typedef struct MmwDemo_objRaw1D MmwDemo_objRaw1D_t
Parameters of CFAR detected object during the first round of CFAR detections.
```

4.33.3.15 MmwDemo_objRaw2D_t

```
typedef struct MmwDemo_objRaw2D MmwDemo_objRaw2D_t
Parameters of CFAR detected object during the second round of CFAR detections.
```

4.33.3.16 MmwDemo_timingInfo_t

```
typedef struct MmwDemo_timingInfo MmwDemo_timingInfo_t
Timing information.
```

4.33.3.17 RangeDependantThresh_t

```
typedef struct SNRThresholds RangeDependantThresh_t
These parameters allow the SNR requirements to be varied as a function of range.
```

4.33.3.18 trackingInputReport_t

```
typedef struct trackingInputReport trackingInputReport_t
Input to tracking from the clustering output.
```

4.33.3.19 trackingReportForTx

```
typedef struct trackingReportForTx_t trackingReportForTx
Structure for tracking report.
```

4.33.4 Enumeration Type Documentation

4.33.4.1 clusteringDBscanErrorCodes

```
enum clusteringDBscanErrorCodes
error code for clusteringDBscan.
```

Enumerator

	DBSCAN_OK	To be added
	DBSCAN_ERROR_MEMORY_ALLOC_FAILED	To be added

Enumerator

DBSCAN_ERROR_NOT_SUPPORTED	To be added
DBSCAN_ERROR_CLUSTER_LIMIT_REACHED	To be added

Definition at line 297 of file dss_data_path.h.

4.33.5 Function Documentation

4.33.5.1 convertSNRdBToVar()

```
float convertSNRdBToVar (
    uint16_t SNRdB,
    uint16_t bitW,
    uint16_t n_samples,
    float resolution )
```

Description

The function computes the CRLB of the given estimate given an SNR input (dB) and the number of samples used in the estimate, and the resolution of the estimate.

Parameters

in	<i>SNRdB</i>	16 bit input with specified bitwidth.
in	<i>bitW</i>	input fractional bitwidth.
in	<i>n_samples</i>	number of samples per chirp.
in	<i>rangeResolution</i>	range resolution in meters.

Return values

$2^{\lceil \text{input}/(2^{\lceil \text{fracBitIn} \rceil}) \rceil}$	
---	--

Description

The function computes the CRLB of the given estimate given an SNR input (dB) and the number of samples used in the estimate, and the resolution of the estimate.

Parameters

in	<i>SNRdB</i>	16 bit input with specified bitwidth.
in	<i>bitW</i>	input fractional bitwidth (for SNR in dB).
in	<i>n_samples</i>	number of samples per chirp.
in	<i>resolution</i>	range resolution in meters.

Return values

<i>CRLB</i>	in the specified resolution (with some lower bounds).
-------------	---

Definition at line 3971 of file dss_data_path.c.

4.33.5.2 convertSNRLinToVar()

```
float convertSNRLinToVar (
```

```
uint16_t SNRLin,
uint16_t bitW,
uint16_t n_samples,
float resolution )
```

Description

The function computes the CRLB of the given estimate given an SNR input (linear) and the number of samples used in the estimate, and the resolution of the estimate.

The CRLB is lower bounded by the resolution.

Parameters

in	<i>SNRLin</i>	16 bit input with specified bitwidth.
in	<i>bitW</i>	input fractional bitwidth.
in	<i>n_samples</i>	number of samples per chirp.
in	<i>resolution</i>	resolution in meters.

Return values

$2^{\lceil \text{input}/(2^{\lceil \text{fracBitIn} \rceil}) \rceil}$	
---	--

Description

The function computes the CRLB of the given estimate given an SNR input (linear) and the number of samples used in the estimate, and the resolution of the estimate.

The CRLB is lower bounded by the resolution.

Parameters

in	<i>SNRdB</i>	16 bit input with specified bitwidth.
in	<i>bitW</i>	input fractional bitwidth.
in	<i>n_samples</i>	number of samples per chirp.
in	<i>resolution</i>	resolution in meters.

Return values

$2^{\lceil \text{input}/(2^{\lceil \text{fracBitIn} \rceil}) \rceil}$	
---	--

Definition at line 4008 of file dss_data_path.c.

4.33.5.3 MmwDemo_addDopplerCompensation()

```
void MmwDemo_addDopplerCompensation (
    int32_t dopplerIdx,
    int32_t numDopplerBins,
    uint32_t * azimuthModCoefs,
    uint32_t * azimuthModCoefsThirdBin,
    uint32_t * azimuthModCoefsTwoThirdBin,
    int64_t * azimuthIn,
    uint32_t numAnt,
    uint32_t numTxAnt,
    uint16_t txAntIdx )
```

Function Name : MmwDemo_DopplerCompensation.

Description : Compensation of Doppler phase shift in the virtual antennas, (corresponding to second Tx antenna chirps). Symbols corresponding to virtual antennas, are rotated by half of the Doppler phase shift measured by Doppler FFT. The phase shift read from the table using half of the object Doppler index value. If the Doppler index is odd, an extra half of the bin phase shift is added.

Parameters

in	<i>dopplerIdx</i>	: Doppler index of the object
in	<i>numDopplerBins</i>	: Number of Doppler bins
in	<i>azimuthModCoefs</i>	Table with cos/sin values SIN in even position, COS in odd position $\exp(1j*2*pi*k/N)$ for $k=0, \dots, N-1$ where N is number of Doppler bins.
out	<i>azimuthModCoefsHalfBin</i>	: $\exp(1j*2*pi* 0.5 /N)$
in, out	<i>azimuthIn</i>	: Pointer to antenna symbols to be Doppler compensated
in	<i>numAnt</i>	: Number of antenna symbols to be Doppler compensated
in	<i>numTxAnt</i>	: Number of Tx antenna
in	<i>txAntIdx</i>	: Tx Antenna index (Tx1:0 Tx2:1 Tx3:2)

Returns

void

Description : Compensation of Doppler phase shift in the virtual antennas, (corresponding to second Tx antenna chirps). Symbols corresponding to virtual antennas, are rotated by half of the Doppler phase shift measured by Doppler FFT. The phase shift read from the table using half of the object Doppler index value. If the Doppler index is odd, an extra half of the bin phase shift is added.

Parameters

in	<i>dopplerIdx</i>	: Doppler index of the object
in	<i>numDopplerBins</i>	: Number of Doppler bins
in	<i>azimuthModCoefs</i>	Table with cos/sin values SIN in even position, COS in odd position $\exp(1j*2*pi*k/N)$ for $k=0, \dots, N-1$ where N is number of Doppler bins.
out	<i>azimuthModCoefsHalfBin</i>	: $\exp(1j*2*pi* 0.5 /N)$ //TODO change to 1/3 instead of 1/2 for the correction.
in, out	<i>azimuthIn</i>	: Pointer to antenna symbols to be Doppler compensated
in	<i>numAnt</i>	: Number of antenna symbols to be Doppler compensated

Returns

void

Definition at line 4866 of file dss_data_path.c.

4.33.5.4 MmwDemo_dataPathConfigAzimuthHeatMap()

```
void MmwDemo_dataPathConfigAzimuthHeatMap (
    DSS_DataPathObj * obj )
```

Description

Configures azimuth heat map related processing.

Return values

<i>Not</i>	Applicable.
------------	-------------

4.33.5.5 MmwDemo_dataPathConfigBuffers()

```
void MmwDemo_dataPathConfigBuffers (
    DSS_DataPathObj * obj,
    uint32_t adcBufAddress )
```

Description

Creates heap in L2 and L3 and allocates data path buffers. The heap is destroyed at the end of the function.

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 2331 of file dss_data_path.c.

4.33.5.6 MmwDemo_dataPathConfigEdma()

```
int32_t MmwDemo_dataPathConfigEdma (
    DSS_DataPathObj * obj )
```

Description

Configures EDMA driver for all of the data path processing.

Return values

<i>Not</i>	Applicable.
------------	-------------

Description

Configures all EDMA channels and param sets used in data path processing

This function is very similar to the dataPathConfigEDMA from the OOB demo, but with the difference that we have two subframes, and one subframe can support the maximum velocity enhancement modification. In this method , the 2nd dimension has two kinds of chirps and each chirp is repeated 'numDopplerBins' times, and each chirp has the same number of adc samples.

We would also like to ensure that when the data is transferred to L3 RAM, a range gate (i.e. doppler bins corresponding to a range bin) of each 'chirptype' is contiguous, so that a single EDMA can pull them both out in the 2nd dimension processing.

Hence the EDMAs corresponding to the transfer of 1D data to L3 and the transfer of data from L3 to L2 are modified.

Parameters

<i>in</i>	<i>obj</i>	Pointer to data path object array.
-----------	------------	------------------------------------

Return values

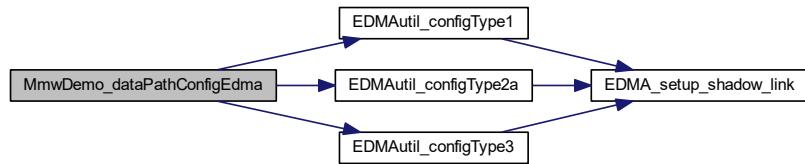
<i>-1</i>	if error, 0 for no error
-----------	--------------------------

Definition at line 936 of file dss_data_path.c.

References DSS_DataPathObj_t::ADCdataBuf, DSS_DataPathObj_t::adcDataIn, BYTES_PER_SAMP_1D, BYT←ES_PER_SAMP_DET, EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, EDMAUtil_configType1(), E←DMAUtil_configType2a(), EDMAUtil_configType3(), MAX, MAX_VEL_ENH_PROCESSING, MRR_SF0_EDMA←CH_1D_IN_PING, MRR_SF0_EDMA_CH_1D_IN_PONG, MRR_SF0_EDMA_CH_1D_OUT_PING, MRR_SF0←

EDMA_CH_1D_OUT_PONG, MRR_SF0_EDMA_CH_2D_IN_PING, MRR_SF0_EDMA_CH_2D_IN_PONG, MR \leftarrow R_SF0_EDMA_CH_3D_IN_PING, MRR_SF0_EDMA_CH_3D_IN_PONG, MRR_SF0_EDMA_CH_DET_MATRIX \leftarrow IX, MRR_SF0_EDMA_CH_DET_MATRIX2, NUM_SUBFRAMES, DSS_DataPathObj_t::numAdcSamples, DSS \leftarrow DSS_DataPathObj_t::numRxAntennas, and DSS_DataPathObj_t::processingPath.

Here is the call graph for this function:



4.33.5.7 MmwDemo_dataPathConfigFFTs()

```
void MmwDemo_dataPathConfigFFTs (
    DSS_DataPathObj * obj )
```

Description

Configures FFTs (twiddle tables etc) involved in 1D, 2D and Azimuth processing.

Return values

Not	Applicable.
-----	-------------

Description

Function to populate the twiddle factors for FFTS needed for the data path object.

Parameters

in, out	obj	data path object.
---------	-----	-------------------

Return values

waitingTime.	
--------------	--

Definition at line 2746 of file dss_data_path.c.

4.33.5.8 MmwDemo_dataPathCopyEdmaHandle()

```
int32_t MmwDemo_dataPathCopyEdmaHandle (
    DSS_DataPathObj * objOutput,
    DSS_DataPathObj * objInput )
```

Description

Since there may be multiple subframes, we keep a copy of the handle for each data path object.

Return values

Not	Applicable.
-----	-------------

Description

This function copies the EDMA handles to all of the remaining data path objects.

Parameters

in, out		<i>obj</i>	data path object.	
---------	--	------------	-------------------	--

Return values

success.		
----------	--	--

Definition at line 2272 of file dss_data_path.c.

4.33.5.9 MmwDemo_dataPathDeleteSemaphore()

```
void MmwDemo_dataPathDeleteSemaphore (
    DSS_DataPathObj * obj )
```

Description

Delete Semaphores which are created in **MmwDemo_dataPathInitEdma()** (p. ??).

Return values

Not	Applicable.	
-----	-------------	--

4.33.5.10 MmwDemo_dataPathInit1Dstate()

```
void MmwDemo_dataPathInit1Dstate (
    DSS_DataPathObj * obj )
```

Description

Initializes data path state variables for 1D processing.

Return values

Not	Applicable.	
-----	-------------	--

Description

This function initializes some of the states (counters) used for 1D processing.

Parameters

in, out		<i>obj</i>	data path object.	
---------	--	------------	-------------------	--

Return values

success/failure.		
------------------	--	--

Definition at line 2198 of file dss_data_path.c.

4.33.5.11 MmwDemo_dataPathInitEdma()

```
int32_t MmwDemo_dataPathInitEdma (
    DSS_DataPathObj * obj )
```

Description

Initializes EDMA driver.

Return values

Not	Applicable.
-----	-------------

Description

This function copies the EDMA handles to all of the remaining data path objects.

Parameters

in, out	obj	data path object.
---------	-----	-------------------

Return values

success/failure.

Definition at line 2223 of file dss_data_path.c.

4.33.5.12 MmwDemo_interFrameProcessing()

```
void MmwDemo_interFrameProcessing (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Interframe processing. It is called from MmwDemo_dssDataPathProcessEvents after all chirps of the frame have been received and 1D FFT processing on them has been completed.

Return values

Not	Applicable.
-----	-------------

Definition at line 1633 of file dss_data_path.c.

4.33.5.13 MmwDemo_pow2roundup()

```
uint32_t MmwDemo_pow2roundup (
    uint32_t x )
```

Description

Power of 2 round up function.

Definition at line 404 of file dss_data_path.c.

4.33.5.14 MmwDemo_processChirp()

```
void MmwDemo_processChirp (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Chirp processing. It is called from MmwDemo_dssDataPathProcessEvents. It is executed per chirp

Return values

Not	Applicable.
-----	-------------

Description

Chirp processing. It is called from MmwDemo_dssDataPathProcessEvents. It is executed per chirp. The range FFT output is transferred in a transpose manner to L3 using an EDMA. This is done so that the 2nd FFT data can be pulled out using a non-transpose EDMA (which is more efficient) The EDMA transfer requires a destination offset (radarCubeOffset) that is proportional with the chirp number. For the MAX_VEL_ENH chirp, there are two chirp types (fast and slow), they are stored consecutively (for e.g. chirp 1 of the fast chirp is directly followed by chirp 1 of the slow chirp).

Return values

Not	Applicable.
-----	-------------

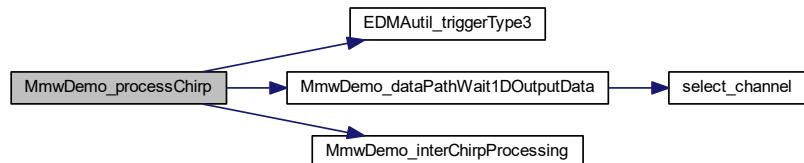
1. Book keeping.

2. Range processing.

Definition at line 1974 of file dss_data_path.c.

References DSS_DataPathObj_t::chirpCount, DSS_DataPathObj_t::chirpTypeCount, DSS_DataPathObj_t::dopplerBinCount, EDMA_INSTANCE_DSS, DSS_DataPathObj_t::edmaHandle, EDMAutil_triggerType3(), gCycleLog, cycleLog_t::interChirpWaitTime, isPong, MAX_VEL_ENH_PROCESSING, MmwDemo_dataPathWait1DOutputData(), MmwDemo_interChirpProcessing(), MRR_EDMA_TRIGGER_ENABLE, MRR_SF0_EDM_A_CH_1D_OUT_PING, MRR_SF0_EDMA_CH_1D_OUT_PONG, DSS_DataPathObj_t::numDopplerBins, DS_S_DataPathObj_t::numRxAntennas, DSS_DataPathObj_t::numTxAntennas, pingPongId, DSS_DataPathObj_t::processingPath, DSS_DataPathObj_t::radarCube, and DSS_DataPathObj_t::txAntennaCount.

Here is the call graph for this function:

**4.33.5.15 MmwDemo_waitEndOfChirps()**

```
void MmwDemo_waitEndOfChirps (
    DSS_DataPathObj * obj,
    uint8_t subframeIndx )
```

Description

Wait for transfer of data corresponding to the last 2 chirps (ping/pong) to the radarCube matrix before starting interframe processing.

Return values

Not	Applicable.
-----	-------------

Definition at line 2079 of file dss_data_path.c.

4.33.5.16 MmwDemo_XYcalc()

```
void MmwDemo_XYcalc (
    DSS_DataPathObj * obj,
    uint32_t objIndex,
    uint16_t azimIdx,
    float * azimuthMagSqr )
```

Description

Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index
in	<i>azimIdx</i>	Index of the peak position in Azimuth FFT output
in	<i>azimuthMagSqr</i>	azimuth energy array

Return values

<i>NONE</i>	
-------------	--

Definition at line 4982 of file dss_data_path.c.

Referenced by MmwDemo_XYestimation().

Here is the caller graph for this function:



4.33.5.17 MmwDemo_XYZcalc()

```
void MmwDemo_XYZcalc (
    DSS_DataPathObj * obj,
    uint32_t objIndex,
    uint16_t azimIdx,
    float * azimuthMagSqr )
```

Description

Calculates X/Y/Z coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object.

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIndex</i>	Detected object index
in	<i>azimIdx</i>	Index of the peak position in Azimuth FFT output
in	<i>azimuthMagSqr</i>	azimuth energy array

Return values

NONE	Description
Calculates X/Y coordinates in meters based on the maximum position in the magnitude square of the azimuth FFT output. The function is called per detected object.	

Parameters

in	<i>obj</i>	Pointer to data path object
in	<i>objIdx</i>	Detected object index
in	<i>azimIdx</i>	Index of the peak position in Azimuth FFT output
in	<i>azimuthMagSqr</i>	azimuth energy array

Return values

NONE	
-------------	--

Definition at line 5052 of file dss_data_path.c.

Referenced by MmwDemo_XYZestimation().

Here is the caller graph for this function:

**4.33.5.18 parkingAssistInit()**

```
void parkingAssistInit (
    DSS_DataPathObj * obj )
```

Description

Initialize the 'parking assist bins' state which is essentially the closest obstruction upper bounded by an initial value

Parameters

in	<i>obj</i>	Pointer to data path object
----	------------	-----------------------------

Return values

NONE	Description
Initialize the 'parking assist bins' state which is essentially the closest obstruction upper bounded by its maximum value.	

Parameters

in	<i>obj</i>	Pointer to data path object
----	------------	-----------------------------

Return values

<i>NONE</i>	
-------------	--

Definition at line 5146 of file dss_data_path.c.

4.33.5.19 populateOutputs()

```
void populateOutputs (
    DSS_DataPathObj * obj )
```

Description

The function populates the object location arrays for transmission to MSS. The reason we do this additional step is to minimize the size of the the transmission by populating new structure which hold only the minimum information necessary for the GUI.

Parameters

<i>in</i>	<i>input</i>	data path object.
-----------	--------------	-------------------

Return values

<i>none</i>	
-------------	--

Definition at line 4452 of file dss_data_path.c.

References AGED_OBJ_DELETION_THRESH, DSS_DataPathObj_t::clusterOpFinal, DSS_DataPathObj_t::dbScanInstance, DSS_DataPathObj_t::dbScanReport, DSS_DataPathObj_t::dbScanState, DSS_DataPathObj_t::detObj2D, DSS_DataPathObj_t::detObjFinal, EIGHTEEN_DB_DOPPLER_SNR, FOUR_POINT_ZERO_METERS, iX, iXd, iY, iYd, MAX_VEL_ENH_PROCESSING, clusteringDBscanInstance::maxClusters, KFtracker::Instance::maxTrackers, MIN_TICK_FOR_TX, MmwDemo_dssAssert, DSS_DataPathObj_t::numActiveTrackers, clusteringDBscanOutput::numCluster, DSS_DataPathObj_t::numDetObj, DSS_DataPathObj_t::parkingAssistBins, DSS_DataPathObj_t::parkingAssistBinsState, DSS_DataPathObj_t::parkingAssistBinsStateCnt, DSS_DataPathObj_t::parkingAssistMaxRange, DSS_DataPathObj_t::parkingAssistNumBins, DSS_DataPathObj_t::parkingAssistNumBinsLog2, POINT_CLOUD_PROCESSING, DSS_DataPathObj_t::processingPath, DSS_DataPathObj_t::sinAzimQFormat, DSS_DataPathObj_t::trackerInstance, DSS_DataPathObj_t::trackerOpFinal, DSS_DataPathObj_t::trackerState, TWENTY_TWO_DB_DOPPLER_SNR, KFstate::vec, KFstate::xSize, DSS_DataPathObj_t::xyzOutputQFormat, KFstate::ySize, and ZERO_POINT_FIVE_METERS.

4.33.5.20 pruneTrackingInput()

```
uint32_t pruneTrackingInput (
    trackingInputReport_t * trackingInput,
    uint32_t numCluster )
```

Description

The function removes objects from extreme angles and with poor angle SNR from the tracking input.

Parameters

<i>in</i>	<i>trackinginput</i>	List of tracking inputs.
<i>in</i>	<i>numClusters</i>	number of tracking inputs (from the clustering output).

Return values

<i>number</i>	of tracking inputs after pruning.
---------------	-----------------------------------

Definition at line 4706 of file dss_data_path.c.

4.33.5.21 quadraticInterpFltPeakLoc()

```
float quadraticInterpFltPeakLoc (
    float *restrict y,
    int32_t len,
    int32_t indx )
```

Description

The function performs a quadratic peak interpolation to compute the fractional offset of the 'true' peak location. It is primarily intended to be used in oversampled FFTs.

Parameters

in	<i>y</i>	Input array.
in	<i>len</i>	length of the input array.
in	<i>indx</i>	indx of the peak location.

Return values

<i>interpolated</i>	offset.
---------------------	---------

Description

The function performs a quadratic peak interpolation to compute the fractional offset of the peak location. It is primarily intended to be used in oversampled FFTs.

Parameters

in	<i>y</i>	the array of data.
in	<i>len</i>	length of the array.
in	<i>indx</i>	coarse peak location.

Return values

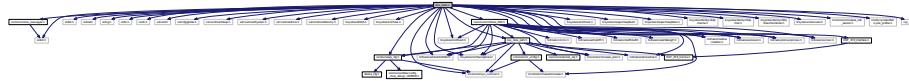
<i>interpolated</i>	peak location (varies from -1 to +1).
---------------------	---------------------------------------

Definition at line 4741 of file dss_data_path.c.

4.34 dss_main.c File Reference

```
#include <stdint.h>
#include <stdlib.h>
#include <stddef.h>
#include <string.h>
#include <stdio.h>
#include <math.h>
#include <xdc/std.h>
#include <xdc/cfg/global.h>
#include <xdc/runtime/IHeap.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/Memory.h>
#include <ti/sysbios/BIOS.h>
```

```
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/knl/Event.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Clock.h>
#include <ti/sysbios/heaps/HeapBuf.h>
#include <ti/sysbios/heaps/HeapMem.h>
#include <ti/sysbios/family/c64p/Cache.h>
#include <ti/sysbios/family/c64p/Hwi.h>
#include <ti/sysbios/family/c64p/EventCombiner.h>
#include <ti/sysbios/utils/Load.h>
#include <ti/common/sys_common.h>
#include <ti/common/mmwave_sdk_version.h>
#include <ti/utils/cycleprofiler/cycle_profiler.h>
#include <ti/drivers/mailbox/mailbox.h>
#include <ti/drivers/adcbuf/ADCBuf.h>
#include <ti/drivers/esm/esm.h>
#include <ti/drivers/pinmux/pinmux.h>
#include <ti/drivers/soc/soc.h>
#include "common/mmWave_XSS.h"
#include "dss_data_path.h"
#include "EKF_XYZ_Interface.h"
#include "common/mmw_messages.h"
#include "common/app_cfg.h"
#include <cpy_tbl.h>
Include dependency graph for dss_main.c:
```



Data Structures

- struct **DSS2MSS_HSRAM**

Macros

- #define **SOC_XWR18XX_DSS_HSRAM_SIZE** 0x8000U /* Size: 32KB */

DSS stores the application output and DSS2MSS ISR information (for fast exception signalling) in HSRAM.
- #define **DATAPATH_DET_PAYLOAD_SIZE** (**SOC_XWR18XX_DSS_HSRAM_SIZE** - sizeof(uint8_t))
- #define **MMW_NUM_ANGLE_BINS** 64
- #define **SPEED_OF_LIGHT_IN_METERS_PER_SEC** (3.0e8)
- #define **SPEED_OF_LIGHT_IN_METERS_PER_USEC** (3.0e2)

Typedefs

- typedef struct **DSS2MSS_HSRAM** **DSS2MSS_HSRAM_t**

Functions

- static void **DSS_mmWaveInitTASK** (UArg arg0, UArg arg1)
- static void **DSS_mmWaveCtrlTask** (UArg arg0, UArg arg1)
- static void **DSS_chirpIntCallback** (uintptr_t arg)
- static void **DSS_frameStartIntCallback** (uintptr_t arg)
- static int32_t **DSS_DataPathConfigAdcBuf** ()
- void **DSS_dataPathConfigPopulate** (**DSS_DataPathObj** *obj)

- void **DSS_populateUSR** (**DSS_DataPathObj** *obj, uint16_t subframeIdx)
- void **DSS_dBScanConfigBuffers** (**DSS_DataPathObj** *obj)
- static void **DSS_edmaBlockCopy** (EDMA_Handle handle, uint32_t loadAddr, uint32_t runAddr, uint16_t size)
- static void **DSS_copyTable** (EDMA_Handle handle, COPY_TABLE *tp)
- int32_t **DSS_DataPathInit** (void)
- static int32_t **DSS_SendProcessOutputToMSS** (uint8_t *ptrHsmBuffer, uint32_t outputBufSize, **DSS_DataPathObj** *obj)
- static void **DSS_DataPathOutputLogging** (**DSS_DataPathObj** *dataPathObj)
- static void **mboxIn_uartOut_TASK** ()
- void **mboxCallbackFxn_MSS_ch0** (Mbox_Handle handle, Mailbox_Type peer)
- void **DSP_sleep** (void)
- uint16_t **convertSNRdBtoThreshold** (uint16_t numInteg, float ThresholdIndB, uint16_t bitwidth)
- static int32_t **DSS_mboxWrite** (mmWaveMSG *message)
- void **MmwDemo_dssAssert** (int32_t expression, const char *file, int32_t line)
- int32_t **main** (void)
- void **rxGainPhaseParam_Init** (**DSS_DataPathObj** *obj)

Variables

- **DSS2MSS_HSRAM_t gHSRAM**
- far COPY_TABLE **_MmwDemo_fastCode_L1PSRAM_copy_table**
- **MCB gMCB**

Global Variable for tracking information required by the design.
- volatile **cycleLog_t gCycleLog**

4.34.1 Macro Definition Documentation

4.34.1.1 DATAPATH_DET_PAYLOAD_SIZE

```
#define DATAPATH_DET_PAYLOAD_SIZE ( SOC_XWR18XX_DSS_HSRAM_SIZE - sizeof(uint8_t))
```

Definition at line 78 of file dss_main.c.

4.34.1.2 MMW_NUM_ANGLE_BINS

```
#define MMW_NUM_ANGLE_BINS 64
```

Azimuth FFT size

Definition at line 89 of file dss_main.c.

4.34.1.3 SOC_XWR18XX_DSS_HSRAM_SIZE

```
#define SOC_XWR18XX_DSS_HSRAM_SIZE 0x8000U /* Size: 32KB */
```

DSS stores the application output and DSS2MSS ISR information (for fast exception signalling) in HSRAM.

Definition at line 75 of file dss_main.c.

4.34.1.4 SPEED_OF_LIGHT_IN_METERS_PER_SEC

```
#define SPEED_OF_LIGHT_IN_METERS_PER_SEC (3.0e8)
```

Definition at line 90 of file dss_main.c.

4.34.1.5 SPEED_OF_LIGHT_IN_METERS_PER_USEC

```
#define SPEED_OF_LIGHT_IN_METERS_PER_USEC (3.0e2)
Definition at line 91 of file dss_main.c.
```

4.34.2 Typedef Documentation

4.34.2.1 DSS2MSS_HSRAM_t

```
typedef struct DSS2MSS_HSRAM DSS2MSS_HSRAM_t
```

4.34.3 Function Documentation

4.34.3.1 _MmwDemo_dssAssert()

```
void _MmwDemo_dssAssert (
    int32_t expression,
    const char * file,
    int32_t line )
```

Description

Sends DSS assert information to MSS

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 302 of file dss_main.c.

4.34.3.2 convertSNRdBtoThreshold()

```
uint16_t convertSNRdBtoThreshold (
    uint16_t numInteg,
    float ThresholdIndB,
    uint16_t bitwidth )
```

Description

Converts an SNR (in dB) to an SNR Threshold that the CFAR algo can use.

Parameters

in	<i>number</i>	of integrations in the detection matrix Typically the number of virtual antennas
in	<i>Threshold</i>	in dB (float)

Return values

<i>Threshold</i>	for CFAR algorithm.
------------------	---------------------

Definition at line 1267 of file dss_main.c.

4.34.3.3 DSP_sleep()

```
void DSP_sleep (
    void )
```

Description

Function to sleep the DSP using IDLE instruction. When DSP has no work left to do, the BIOS will be in Idle thread and will call this function. The DSP will wake-up on any interrupt (e.g chirp interrupt).

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 288 of file dss_main.c.

4.34.3.4 DSS_chirpIntCallback()

```
static void DSS_chirpIntCallback (
    uintptr_t arg ) [static]
```

Description

This is the callback function registered with the ADC Driver which is invoked when a chirp is available. This is executed in the ISR context.

Parameters

<i>in</i>	<i>arg</i>	Application registered argument
-----------	------------	---------------------------------

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 158 of file dss_main.c.

4.34.3.5 DSS_copyTable()

```
static void DSS_copyTable (
    EDMA_Handle handle,
    COPY_TABLE * tp ) [static]
```

Description

Performs linker generated copy table copy using EDMA. Currently this is used to page in fast code from L3 to L1PSRAM.

Parameters

<i>in</i>	<i>handle</i>	EDMA handle
<i>in</i>	<i>tp</i>	Pointer to copy table

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1147 of file dss_main.c.

4.34.3.6 DSS_DataPathConfigAdcBuf()

```
static int32_t DSS_DataPathConfigAdcBuf ( ) [static]
```

Description

Function to configure ADCBUF driver based on CLI inputs.

Parameters

<code>out</code>	<code>numRxChannels</code>	Number of receive channels.
------------------	----------------------------	-----------------------------

Return values

<code>Not</code>	Applicable.
------------------	-------------

Definition at line 1048 of file dss_main.c.

4.34.3.7 DSS_dataPathConfigPopulate()

```
void DSS_dataPathConfigPopulate (
    DSS_DataPathObj * obj )
```

Description

populates the data path object array with the MRR configuration.

Parameters

<code>in</code>	<code>obj</code>	pointer to the data path object.
-----------------	------------------	----------------------------------

Return values

<code>Not</code>	Applicable.
------------------	-------------

Definition at line 613 of file dss_main.c.

4.34.3.8 DSS_DataPathInit()

```
int32_t DSS_DataPathInit (
    void )
```

4.34.3.9 DSS_DataPathOutputLogging()

```
void DSS_DataPathOutputLogging (
    DSS_DataPathObj * dataPathObj ) [static]
```

Description

Function to send data path detection output.

Return values

<code>Not</code>	Applicable.
------------------	-------------

Definition at line 995 of file dss_main.c.

4.34.3.10 DSS_dBScanConfigBuffers()

```
void DSS_dBScanConfigBuffers (
    DSS_DataPathObj * obj )
```

Description

Configures the dBScan buffers.

Parameters

in	<i>data</i>	path Object
----	-------------	-------------

Return values

Not	applicable.
-----	-------------

Definition at line 1284 of file dss_main.c.

4.34.3.11 DSS_edmaBlockCopy()

```
static void DSS_edmaBlockCopy (
    EDMA_Handle handle,
    uint32_t loadAddr,
    uint32_t runAddr,
    uint16_t size ) [static]
```

Description

Performs simple block copy using EDMA. Used for the purpose of copying linker table for L3 to L1PSRAM copy. memcpy cannot be used because there is no data bus access to L1PSRAM.

Parameters

in	<i>handle</i>	EDMA handle
in	<i>loadAddr</i>	load address
in	<i>runAddr</i>	run address
in	<i>size</i>	size in bytes

Return values

Not	Applicable.
-----	-------------

Definition at line 1185 of file dss_main.c.

4.34.3.12 DSS_frameStartIntCallback()

```
static void DSS_frameStartIntCallback (
    uintptr_t arg ) [static]
```

Description

This is the callback function registered when a frame is triggered. This is executed in the ISR context.

Parameters

in	<i>arg</i>	Application registered argument
----	------------	---------------------------------

Return values

Not	Applicable.
-----	-------------

Definition at line 177 of file dss_main.c.

4.34.3.13 DSS_mboxWrite()

```
static int32_t DSS_mboxWrite (
    mmWaveMSG * message ) [static]
```

Description

Function to send a message to peer through Mailbox virtual channel

Parameters

in	<i>message</i>	Pointer to the Capture demo message.
----	----------------	--------------------------------------

Returns

Success - 0 Fail < -1

Definition at line 640 of file dss_main.c.

4.34.3.14 DSS_mmWaveCtrlTask()

```
static void DSS_mmWaveCtrlTask (
    UArg arg0,
    UArg arg1 ) [static]
```

Description

This is the task which provides an execution context for the mmWave control module.

Parameters

in	<i>arg0</i>	Argument0 with which the task was created
in	<i>arg1</i>	Argument1 with which the task was created

Return values

Not	Applicable.
-----	-------------

Definition at line 199 of file dss_main.c.

4.34.3.15 DSS_mmWaveInitTASK()

```
static void DSS_mmWaveInitTASK (
    UArg arg0,
    UArg arg1 ) [static]
```

Description

DSS Initialization Task which initializes the various components in the DSS subsystem.

Parameters

in	<i>arg0</i>	Argument0 with which the task was created
in	<i>arg1</i>	Argument1 with which the task was created

Return values

Not	Applicable.
-----	-------------

Definition at line 472 of file dss_main.c.

4.34.3.16 DSS_populateUSR()

```
void DSS_populateUSR (
    DSS_DataPathObj * obj,
    uint16_t subframeIndx )
```

Description

Populates the configuration for the USR point cloud subframe.

Parameters

in	<i>data</i>	path Object
----	-------------	-------------

Return values

Not	applicable.
-----	-------------

Q format of the output x/y/z coordinates
CFAR configuration in Doppler direction
CFAR configuration in Range direction
min and max range configuration
Multi object beam forming configuration
DC Range antenna signature calibration configuration.
CFAR thresholds are varied as a function of range
peakVal thresholds are varied as a function of range (meant to remove cases of clutter being detected too when we drive the car.) Thresholds were derived from experiments.
Configuring the dbScan for car like objects. These numbers are derived from field tests
Configuring the 'parking assist' occupancy detect.
Definition at line 1316 of file dss_main.c.

4.34.3.17 DSS_SendProcessOutputToMSS()

```
int32_t DSS_SendProcessOutputToMSS (
    uint8_t * ptrHsmBuffer,
    uint32_t outputBufSize,
    DSS_DataPathObj * obj ) [static]
```

Description

Function to send detected objects to MSS logger.

Parameters

in	<i>ptrOutputBuffer</i>	Pointer to the output buffer
in	<i>outputBufSize</i>	Size of the output buffer
in	<i>obj</i>	Handle to the Data Path Object

Return values

=0	Success <0 Failed
----	-------------------

Definition at line 766 of file dss_main.c.

4.34.3.18 main()

```
int32_t main (
    void )
```

Description

Entry point into the DSS MRR TI Design

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 331 of file dss_main.c.

4.34.3.19 mboxCallbackFxn_MSS_ch0()

```
void mboxCallbackFxn_MSS_ch0 (
    Mbox_Handle handle,
    Mailbox_Type peer )
```

Description

This function is a callback function that invoked when a message is received from the peer.

Parameters

in	<i>handle</i>	Handle to the Mailbox on which data was received
in	<i>peer</i>	Peer from which data was received

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 741 of file dss_main.c.

4.34.3.20 mboxIn_uartOut_TASK()

```
static void mboxIn_uartOut_TASK ( ) [static]
```

Description

The Task is used to handle the mmw demo messages received from Mailbox virtual channel.

Parameters

in	<i>arg0</i>	arg0 of the Task. Not used
in	<i>arg1</i>	arg1 of the Task. Not used

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 667 of file dss_main.c.

4.34.3.21 rxGainPhaseParam_Init()

```
void rxGainPhaseParam_Init (
    DSS_DataPathObj * obj )
```

Definition at line 393 of file dss_main.c.

References MAX_VEL_ENH_PROCESSING, POINT_CLOUD_PROCESSING, DSS_DataPathObj_t::processing←Path, and DSS_DataPathObj_t::rxChPhaseComp.

4.34.4 Variable Documentation

4.34.4.1 _MmwDemo_fastCode_L1PSRAM_copy_table

```
far COPY_TABLE _MmwDemo_fastCode_L1PSRAM_copy_table
```

4.34.4.2 gCycleLog

```
volatile cycleLog_t gCycleLog
Definition at line 99 of file dss_main.c.
Referenced by MmwDemo_processChirp().
```

4.34.4.3 gHSRAM

```
DSS2MSS_HSRAM_t gHSRAM
Definition at line 86 of file dss_main.c.
```

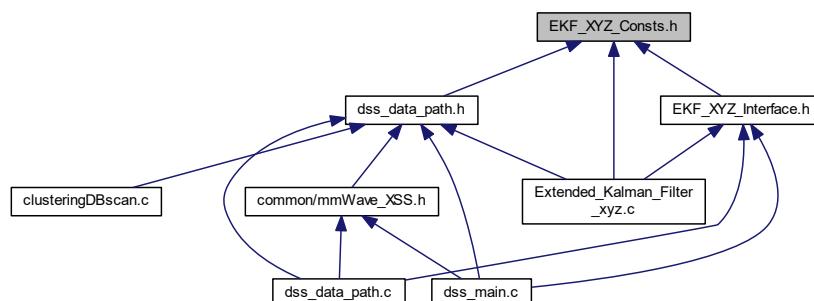
4.34.4.4 gMCB

```
MCB gMCB
Global Variable for tracking information required by the design.
Definition at line 97 of file dss_main.c.
```

4.35 EKF_XYZ_Consts.h File Reference

Constants for the Extended Kalman Filter.

This graph shows which files directly or indirectly include this file:



Macros

- #define **EPS** 0.00001
- #define **KF_2D** 1
- #define **KF_3D** 0
- #define **iRANGE** 0 /* Index of Range (meters)*/

- #define **iRANGE_RATE** 1 /* Index of Range rate (meters/sec)*/
- #define **iSIN_AZIM** 2 /* Index of sin(azim) (x/r)*/
- #define **iX** 0 /* Index of x-coord (meters)*/
- #define **iY** 1 /* Index of y-coord (meters)*/
- #define **iXd** 2 /* Index of Speed in x direction (meters/sec)*/
- #define **iYd** 3 /* Index of Speed in y direction (meters/sec)*/
- #define **N_STATES** 4
- #define **N_MEASUREMENTS** 3
- #define **N_UNIQ_ELEM_IN_HMAT** 6
- #define **N_UNIQ_ELEM_IN_SYM_COVMAT** (**N_STATES***(**N_STATES**+1)/2)
- #define **N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT** (**N_MEASUREMENTS***(**N_MEASUREMENTS**+1)/2)
- #define **iXX iX**
- #define **iXY (iXX + 1)**
- #define **iXXd (iXY + 1)**
- #define **iXYd (iXXd + 1)**
- #define **iYY (iXYd + 1)**
- #define **iYXd (iYY + 1)**
- #define **iYYd (iYXd + 1)**
- #define **iXdXd (iYYd + 1)**
- #define **iXdYd (iXdXd + 1)**
- #define **iYdYd (iXdYd + 1)**
- #define **iRR iRANGE**
- #define **iRRd (iRR + 1)**
- #define **iRAz (iRRd + 1)**
- #define **iRdRd (iRAz + 1)**
- #define **iRdAz (iRdRd + 1)**
- #define **iAzAz (iRdAz + 1)**
- #define **NOT_ASSOCIATED** (-1)

object has not been associated.
- #define **IS_ASSOCIATED** (1)

object has been associated.
- #define **IS_INVALID** (0)

object is invalid.
- #define **IS_VALID** (1)

object is valid.
- #define **MAX_ACCEL_Y_M_P_SECSQ** 12 /* m/s²*/

Unmodelled acceleration parameter in the Y direction (perpendicular to the antenna).
- #define **MAX_ACCEL_X_M_P_SECSQ** 5 /* m/s²/

Unmodelled acceleration parameter in the X direction (parallel to the antenna).
- #define **AGED_OBJ_DELETION_THRESH** 10

Delete objects that haven't been associated after these many ticks.
- #define **HIGH_SNR_RVAR_THRESH** 6

Objects with range variance lower than this are considered to be high SNR tracks.

4.35.1 Detailed Description

Constants for the Extended Kalman Filter.

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4.35.2 Macro Definition Documentation

4.35.2.1 AGED_OBJ_DELETION_THRESH

```
#define AGED_OBJ_DELETION_THRESH 10
```

Delete objects that haven't been associated after these many ticks.

Definition at line 186 of file EKF_XYZ_Consts.h.

4.35.2.2 EPS

```
#define EPS 0.00001
```

Definition at line 43 of file EKF_XYZ_Consts.h.

4.35.2.3 HIGH_SNR_RVAR_THRESH

```
#define HIGH_SNR_RVAR_THRESH 6
```

Objects with range variance lower than this are considered to be high SNR tracks.

Definition at line 188 of file EKF_XYZ_Consts.h.

4.35.2.4 iAzAz

```
#define iAzAz ( iRdAz + 1)
```

Definition at line 149 of file EKF_XYZ_Consts.h.

4.35.2.5 iRANGE

```
#define iRANGE 0 /* Index of Range (meters) */
```

Definition at line 50 of file EKF_XYZ_Consts.h.

4.35.2.6 iRANGE_RATE

```
#define iRANGE_RATE 1 /* Index of Range rate (meters/sec) */
```

Definition at line 51 of file EKF_XYZ_Consts.h.

4.35.2.7 iRAZ

```
#define iRAZ ( iRRd + 1)
```

Definition at line 144 of file EKF_XYZ_Consts.h.

4.35.2.8 iRdAz

```
#define iRdAz ( iRdRd + 1)  
Definition at line 147 of file EKF_XYZ_Consts.h.
```

4.35.2.9 iRdRd

```
#define iRdRd ( iRaz + 1)  
Definition at line 146 of file EKF_XYZ_Consts.h.
```

4.35.2.10 iRR

```
#define iRR iRANGE  
Definition at line 142 of file EKF_XYZ_Consts.h.
```

4.35.2.11 iRRd

```
#define iRRd ( iRR + 1)  
Definition at line 143 of file EKF_XYZ_Consts.h.
```

4.35.2.12 IS_ASSOCIATED

```
#define IS_ASSOCIATED (1)  
object has been associated.  
Definition at line 173 of file EKF_XYZ_Consts.h.
```

4.35.2.13 IS_INVALID

```
#define IS_INVALID (0)  
object is invalid.  
Definition at line 175 of file EKF_XYZ_Consts.h.
```

4.35.2.14 IS_VALID

```
#define IS_VALID (1)  
object is valid.  
Definition at line 177 of file EKF_XYZ_Consts.h.
```

4.35.2.15 iSIN_AZIM

```
#define iSIN_AZIM 2 /* Index of sin(azim) (x/r) */  
Definition at line 52 of file EKF_XYZ_Consts.h.
```

4.35.2.16 iX

```
#define iX 0 /* Index of x-coord (meters) */  
Definition at line 55 of file EKF_XYZ_Consts.h.
```

4.35.2.17 iXd

```
#define iXd 2 /* Index of Speed in x direction (meters/sec)*/  
Definition at line 57 of file EKF_XYZ_Consts.h.
```

4.35.2.18 iXdXd

```
#define iXdXd ( iYYd + 1)  
Definition at line 102 of file EKF_XYZ_Consts.h.
```

4.35.2.19 iXdYd

```
#define iXdYd ( iXdXd + 1)  
Definition at line 103 of file EKF_XYZ_Consts.h.
```

4.35.2.20 iXX

```
#define iXX ix  
Definition at line 93 of file EKF_XYZ_Consts.h.
```

4.35.2.21 iXXd

```
#define iXXd ( iXY + 1)  
Definition at line 95 of file EKF_XYZ_Consts.h.
```

4.35.2.22 iXY

```
#define iXY ( iXX + 1)  
Definition at line 94 of file EKF_XYZ_Consts.h.
```

4.35.2.23 iXYd

```
#define iXYd ( iXXd + 1)  
Definition at line 96 of file EKF_XYZ_Consts.h.
```

4.35.2.24 iY

```
#define iY 1 /* Index of y-coord (meters)*/  
Definition at line 56 of file EKF_XYZ_Consts.h.
```

4.35.2.25 iYd

```
#define iYd 3 /* Index of Speed in y direction (meters/sec)*/  
Definition at line 58 of file EKF_XYZ_Consts.h.
```

4.35.2.26 iYdYd

```
#define iYdYd ( iXdXd + 1)  
Definition at line 105 of file EKF_XYZ_Consts.h.
```

4.35.2.27 iYXd

```
#define iYXd ( iYY + 1)  
Definition at line 99 of file EKF_XYZ_Consts.h.
```

4.35.2.28 iYY

```
#define iYY ( iXYd + 1)  
Definition at line 98 of file EKF_XYZ_Consts.h.
```

4.35.2.29 iYYd

```
#define iYYd ( iYXd + 1)  
Definition at line 100 of file EKF_XYZ_Consts.h.
```

4.35.2.30 KF_2D

```
#define KF_2D 1  
Definition at line 45 of file EKF_XYZ_Consts.h.
```

4.35.2.31 KF_3D

```
#define KF_3D 0  
Definition at line 46 of file EKF_XYZ_Consts.h.
```

4.35.2.32 MAX_ACCEL_X_M_P_SECSQ

```
#define MAX_ACCEL_X_M_P_SECSQ 5 /* m/s^2 */  
Unmodelled acceleration parameter in the X direction (parallel to the antenna).  
Definition at line 182 of file EKF_XYZ_Consts.h.
```

4.35.2.33 MAX_ACCEL_Y_M_P_SECSQ

```
#define MAX_ACCEL_Y_M_P_SECSQ 12 /* m/s^2 */  
Unmodelled acceleration parameter in the Y direction (perpendicular to the antenna).  
Definition at line 180 of file EKF_XYZ_Consts.h.
```

4.35.2.34 N_MEASUREMENTS

```
#define N_MEASUREMENTS 3  
Definition at line 62 of file EKF_XYZ_Consts.h.
```

4.35.2.35 N_STATES

```
#define N_STATES 4  
Definition at line 61 of file EKF_XYZ_Consts.h.
```

4.35.2.36 N_UNIQ_ELEM_IN_HMAT

```
#define N_UNIQ_ELEM_IN_HMAT 6  
Definition at line 64 of file EKF_XYZ_Consts.h.
```

4.35.2.37 N_UNIQ_ELEM_IN_SYM_COVMAT

```
#define N_UNIQ_ELEM_IN_SYM_COVMAT ( N_STATES*( N_STATES+1 ) /2 )
Definition at line 88 of file EKF_XYZ_Consts.h.
```

4.35.2.38 N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT

```
#define N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT ( N_MEASUREMENTS*( N_MEASUREMENTS+1 ) /2 )
Definition at line 89 of file EKF_XYZ_Consts.h.
```

4.35.2.39 NOT_ASSOCIATED

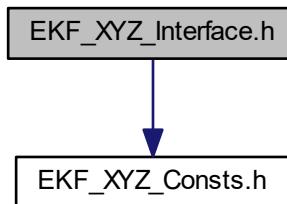
```
#define NOT_ASSOCIATED (-1)
object has not been associated.
Definition at line 171 of file EKF_XYZ_Consts.h.
```

4.36 EKF_XYZ_Interface.h File Reference

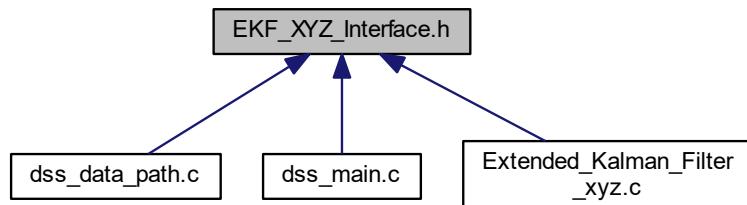
Interface the Extended Kalman Filter.

```
#include "EKF_XYZ_Consts.h"
```

Include dependency graph for EKF_XYZ_Interface.h:



This graph shows which files directly or indirectly include this file:



Functions

- void **ekfInit** (**DSS_DataPathObj** *restrict obj)

- void **ekfRun** (const int32_t numMeas, **trackingInputReport_t** const *restrict measArray, **DSS_DataPathObj** *restrict obj, float const *restrict QvecList, const float td)

4.36.1 Detailed Description

Interface the Extended Kalman Filter.

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4.36.2 Function Documentation

4.36.2.1 **ekfInit()**

```
void ekfInit (
    DSS_DataPathObj *restrict dataPathObj )
```

Description

This function initializes the memory used by the tracking function. It also initializes all the trackers to an invalid state, and then populates the 'process noise matrix' or 'Qmat'.

Parameters

in	<i>dataPathObj</i>	data path object
----	--------------------	------------------

Return values

Not	Applicable.
-----	-------------

Definition at line 86 of file Extended_Kalman_Filter_xyz.c.

References FRAME_PERIODICITY_SEC, IS_INVALID, iX, iXd, iY, iYd, MAX_ACCEL_X_M_P_SECSQ, MAX_ACCEL_Y_M_P_SECSQ, MAX_TRK_OBJS, MRR_MAX_OBJ_OUT, N_MEASUREMENTS, N_STATES, N_UNIQ_ELEM_IN_HMAT, N_UNIQ_ELEM_IN_SYM_COVMAT, N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT, and KFtrackerInstance::scratchPadFlt.

4.36.2.2 **ekfRun()**

```
void ekfRun (
    const int32_t numMeas,
    trackingInputReport_t const *restrict measArray,
```

```
DSS_DataPathObj *restrict dataPathObj,
float const *restrict QvecList,
const float td )
```

Description

This function runs the kalmanfilter on a list of input data

Parameters

in	<i>numMeas</i>	The number of tracking inputs.
in	<i>measArray</i>	An array of structures holding the measurements.
in	<i>dataPathObj</i>	data path object
in	<i>QvecList</i>	A array of structures holding the 'process noise matrix.
in	<i>td</i>	The update rate of the measurement.

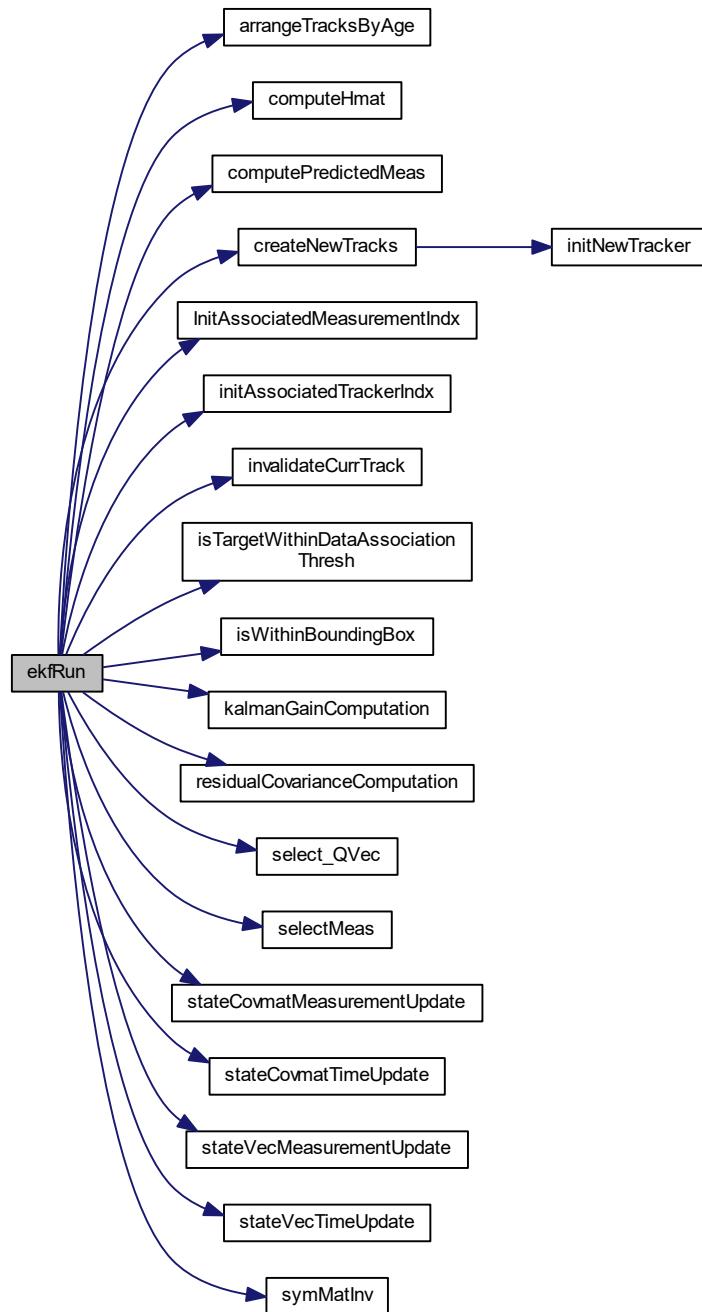
Return values

Not	Applicable.
-----	-------------

Definition at line 166 of file Extended_Kalman_Filter_xyz.c.

References AGED_OBJ_DELETION_THRESH, arrangeTracksByAge(), KFtrackerInstance::assocMeasIdx, computeHmat(), computePredictedMeas(), createNewTracks(), InitAssociatedMeasurementIdx(), initAssociated< TrackerIdx(), invalidateCurrTrack(), iRANGE, iRANGE_RATE, IS_ASSOCIATED, IS_INVALID, iSIN_AZIM, isTargetWithinDataAssociationThresh(), isWithinBoundingBox(), iY, kalmanGainComputation(), MAX_TRK<_OBJs, NOT_ASSOCIATED, residualCovarianceComputation(), select_QVec(), selectMeas(), stateCovmat< MeasurementUpdate(), stateCovmatTimeUpdate(), stateVecMeasurementUpdate(), stateVecTimeUpdate(), and symMatInv().

Here is the call graph for this function:

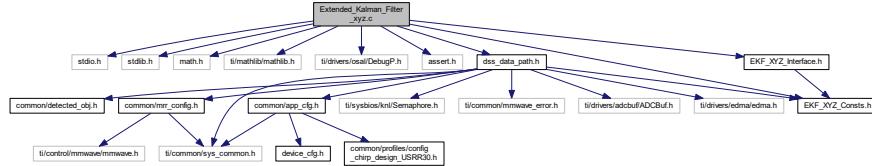


4.37 Extended_Kalman_Filter_xyz.c File Reference

Implements an 'Extended Kalman Filter' for radar tracking.

```
#include <stdio.h>
#include <stdlib.h>
#include <math.h>
#include <ti/mathlib/mathlib.h>
#include <ti/drivers/osal/DebugP.h>
```

```
#include <assert.h>
#include "dss_data_path.h"
#include "EKF_XYZ_Consts.h"
#include "EKF_XYZ_Interface.h"
Include dependency graph for Extended_Kalman_Filter_xyz.c:
```



Macros

- #define DebugP_ASSERT_ENABLED 1

Functions

- void **InitAssociatedMeasurementIdx** (int16_t *restrict assocMeasIdx, const int32_t numMeas)
- void **initAssociatedTrackerIdx** (int16_t *restrict assocObjIdx, const int32_t numObj)
- int32_t **stateVecTimeUpdate** (float *restrict state, const float td)
- void **stateCovmatTimeUpdate** (float *restrict covmat, float const *restrict Qvec, const float td, **KFtracker** Instance_t *restrict inst)
- int32_t **computePredictedMeas** (float *restrict rrd, float const *restrict xyz, float *restrict p_invRange)
- int32_t **isTargetWithinDataAssociationThresh** (float *restrict measResidual, float const *restrict state_rrd, float const *restrict meas_rrd, float const *restrict state_xyz, **KFtrackerInstance** t *restrict inst, float *restrict pdistSq, int32_t tick, int32_t age)
- int32_t **symMatInv** (float *restrict inv, float const *restrict m)
- void **computeHmat** (float *restrict hMat, float const *restrict statevec_xyz, float const *restrict stateVec, const float invrange)
- void **residualCovarianceComputation** (float *restrict residCovmat, float const *restrict state_covmat, float const *restrict measCovVec, float const *restrict hMat)
- void **kalmanGainComputation** (float *restrict kalmanGain, float const *restrict state_covmat, float const *restrict hMat, float const *restrict invResidCovmat, **KFtrackerInstance** t *inst)
- int32_t **stateVecMeasurementUpdate** (float *restrict state, float const *restrict kalmanGain, float const *restrict measResidual)
- void **stateCovmatMeasurementUpdate** (float *restrict covmat, float const *restrict kalmanGain, float const *restrict hMat, **KFtrackerInstance** t *inst)
- int32_t **selectMeas** (int16_t *restrict selectedMeas, **trackingInputReport** t const *restrict measArray, int16_t *restrict assocMeasIdx, const int32_t nFree, const int32_t numUnassociatedMeas, const int32_t numMeasTotal)
- void **createNewTracks** (**DSS_DataPathObj** *restrict dataPathObj, **trackingInputReport** t const *restrict measArray, int16_t const *restrict freeTrackerIdxArray, int16_t const *restrict selectedIdxArr, const int32_t numSelected)
- void **initNewTracker** (**KFstate** t *restrict obj, **trackingInputReport** t const *restrict meas)
- float * **select_QVec** (float const *restrict QvecList, uint8_t tick, uint8_t age, const float range)
- uint32_t **isWithinBoundingBox** (**KFstate** t *restrict currTrack, **DSS_DataPathObj** *restrict dataPathObj)
- void **arrangeTracksByAge** (**KFstate** t *restrict TrackList, const uint32_t numTracksTotal)
- int32_t **invalidateCurrTrack** (**KFstate** t *restrict currTrack, int16_t *restrict freeTrackerIdxArray, int32_t nFree, int32_t iTrack)
- void **ekfInit** (**DSS_DataPathObj** *restrict dataPathObj)
- void **ekfRun** (const int32_t numMeas, **trackingInputReport** t const *restrict measArray, **DSS_DataPath** Obj *restrict dataPathObj, float const *restrict QvecList, const float td)

4.37.1 Detailed Description

Implements an 'Extended Kalman Filter' for radar tracking.

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4.37.2 Macro Definition Documentation

4.37.2.1 DebugP_ASSERT_ENABLED

```
#define DebugP_ASSERT_ENABLED 1
Definition at line 44 of file Extended_Kalman_Filter_xyz.c.
```

4.37.3 Function Documentation

4.37.3.1 arrangeTracksByAge()

```
void arrangeTracksByAge (
    KFstate_t *restrict TrackList,
    const uint32_t numTracksTotal )
```

Description

Since we use a greedy algorithm to associate tracks and measurements, it is important that stable (older) tracks are associated before the newer (more unstable) tracks. This will ensure that newer tracks do not associate with measurements that would have better associated with older tracks.

Return values

Not	Applicable.
-----	-------------

Definition at line 400 of file Extended_Kalman_Filter_xyz.c.

References IS_VALID, KFstate::tick, and KFstate::validity.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.2 computeHmat()

```
void computeHmat (
    float *restrict hMat,
    float const *restrict statevec_xyz,
    float const *restrict stateVecRrd,
    const float invrange )
```

Description

This function computes the observation matrix (hMat).

Parameters

out	<i>hMat</i>	a vector containing the non-zero elements of the hmat.
in	<i>statevec_xyz</i>	predicted state (in x y co-ordinates)
in	<i>stateVecRrd</i>	predicted measurements.
in	<i>invrange</i>	1/r - computed in the computePredictedMeas function.

Return values

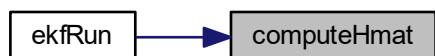
Not	Applicable.
-----	-------------

Definition at line 819 of file Extended_Kalman_Filter_xyz.c.

References iRANGE_RATE, iSIN_AZIM, iXd, iY, and iYd.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.3 computePredictedMeas()

```
int32_t computePredictedMeas (
```

```

    float *restrict rrd,
    float const *restrict xyz,
    float *restrict p_invRange )

```

Description

This function computes the predicted measurements from the state vector. i.e. $R = H(x)$;

Parameters

out	<i>rrd</i>	predicted measurement.
in	<i>xyz</i>	state.
in	<i>p_invRange</i>	The inv sqrt of the range is also computed and used in the hMat computation.

Return values

Not	Applicable.
-----	-------------

Definition at line 595 of file Extended_Kalman_Filter_xyz.c.

References iRANGE, iRANGE_RATE, IS_INVALID, IS_VALID, iSIN_AZIM, iX, iXd, iY, and iYd.

Referenced by ekfRun().

Here is the caller graph for this function:

**4.37.3.4 createNewTracks()**

```

void createNewTracks (
    DSS_DataPathObj *restrict dataPathObj,
    trackingInputReport_t const *restrict measArray,
    int16_t const *restrict freeTrackerIndxArray,
    int16_t const *restrict selectedIndxArr,
    const int32_t numSelected )

```

Description

This function creates new tracks.

Parameters

in,out	<i>dataPathObj</i>	Data path object - for the 'trackerState' struct array.
in	<i>measArray</i>	List of input measurements
in	<i>freeTrackerIndexArray</i>	Number of free tracks.
in	<i>selectedIndxArr</i>	An array of the selected measurements for new tracks.
in	<i>numSelected</i> .	number of selected measurements.

Return values

<i>not</i>	applicable
------------	------------

Definition at line 1313 of file Extended_Kalman_Filter_xyz.c.

References initNewTracker(), and IS_VALID.

Referenced by ekfRun().

Here is the call graph for this function:



Here is the caller graph for this function:

**4.37.3.5 ekfInit()**

```
void ekfInit (
    DSS_DataPathObj *restrict dataPathObj )
```

Description

This function initializes the memory used by the tracking function. It also initializes all the trackers to an invalid state, and then populates the 'process noise matrix' or 'Qmat'.

Parameters

in	<i>dataPathObj</i>	data path object
----	--------------------	------------------

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 86 of file Extended_Kalman_Filter_xyz.c.

References FRAME_PERIODICITY_SEC, IS_INVALID, iX, iXd, iY, iYd, MAX_ACCEL_X_M_P_SECSQ, MAX_ACCEL_Y_M_P_SECSQ, MAX_TRK_OBJS, MRR_MAX_OBJ_OUT, N_MEASUREMENTS, N_STATES, N_UNIQ_ELEM_IN_HMAT, N_UNIQ_ELEM_IN_SYM_COVMAT, N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT, and KFtrackerInstance::scratchPadFlt.

4.37.3.6 `ekfRun()`

```
void ekfRun (
    const int32_t numMeas,
    trackingInputReport_t const *restrict measArray,
    DSS_DataPathObj *restrict dataPathObj,
    float const *restrict QvecList,
    const float td )
```

Description

This function runs the kalmanfilter on a list of input data

Parameters

in	<i>numMeas</i>	The number of tracking inputs.
in	<i>measArray</i>	An array of structures holding the measurements.
in	<i>dataPathObj</i>	data path object
in	<i>QvecList</i>	A array of structures holding the 'process noise matrix.
in	<i>td</i>	The update rate of the measurement.

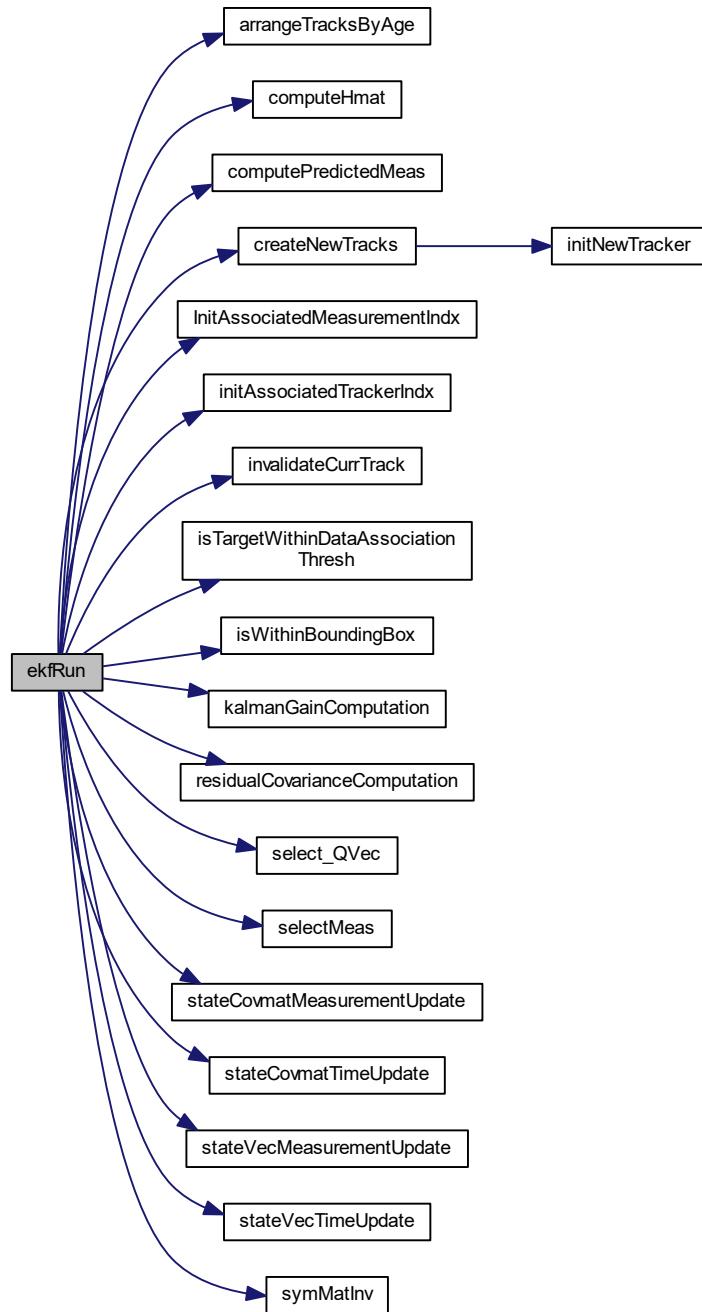
Return values

Not	Applicable.
-----	-------------

Definition at line 166 of file Extended_Kalman_Filter_xyz.c.

References AGED_OBJ_DELETION_THRESH, arrangeTracksByAge(), KFtrackerInstance::assocMeasIdx, computeHmat(), computePredictedMeas(), createNewTracks(), InitAssociatedMeasurementIdx(), initAssociatedTrackerIdx(), invalidateCurrTrack(), iRANGE, iRANGE_RATE, IS_ASSOCIATED, IS_INVALID, iSIN_AZIM, isTargetWithinDataAssociationThresh(), isWithinBoundingBox(), iY, kalmanGainComputation(), MAX_TRK_OBJS, NOT_ASSOCIATED, residualCovarianceComputation(), select_QVec(), selectMeas(), stateCovmatMeasurementUpdate(), stateCovmatTimeUpdate(), stateVecMeasurementUpdate(), stateVecTimeUpdate(), and symMatInv().

Here is the call graph for this function:



4.37.3.7 InitAssociatedMeasurementIndx()

```
void InitAssociatedMeasurementIndx (
    int16_t *restrict assocMeasIndx,
    const int32_t numMeas )
```

Description

This function initializes the assocMeasIndx to state that no measurements have been associated to an existing

tracker.

Parameters

in	<i>assocMeasIdx</i>	An array that will hold the mapping of the incoming measurement to the existing tracker to which it is associated to.
in	<i>nuMeas</i>	number of measurement (for this epoch).

Return values

Not	Applicable.
-----	-------------

Definition at line 454 of file Extended_Kalman_Filter_xyz.c.

References NOT_ASSOCIATED.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.8 initAssociatedTrackerIdx()

```
void initAssociatedTrackerIdx (
    int16_t *restrict freeTrackerIdxArray,
    const int32_t numObj )
```

Description

This function initializes the freeTrackerIdxArray to state that no objects have been associated to an existing measurement.

Parameters

in	<i>freeTrackerIdxArray</i>	An array that will hold the mapping of the existing tracker to a measurement.
in	<i>numObj</i>	number of trackers (for this epoch).

Return values

Not	Applicable.
-----	-------------

Definition at line 477 of file Extended_Kalman_Filter_xyz.c.

References NOT_ASSOCIATED.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.9 initNewTracker()

```
void initNewTracker (
    KFstate_t *restrict obj,
    trackingInputReport_t const *restrict meas )
```

Description

This function initializes a new tracker.

Parameters

out	<i>obj</i>	pointer to the new tracker's state.
in	<i>meas</i>	set of measurements to initialize the tracker.

Return values

<i>not</i>	applicable
------------	------------

Definition at line 1354 of file Extended_Kalman_Filter_xyz.c.

References iRANGE, iRANGE_RATE, iSIN_AZIM, iX, iXd, iXdXd, iXX, iY, iYd, iYdYd, iYY, and N_UNIQ_ELEM_← IN_SYM_COVMAT.

Referenced by createNewTracks().

Here is the caller graph for this function:



4.37.3.10 invalidateCurrTrack()

```
int32_t invalidateCurrTrack (
    KFstate_t *restrict currTrack,
    int16_t *restrict freeTrackerIndxArray,
    int32_t nFree,
    int32_t iTrack )
```

Description

This function invalidates a track by marking it as invalid, marking its index as invalid, incrementing the number of free tracks.

Parameters

in, out	<i>currTrack</i>	pointer to the tracker's state.
in, out	<i>freeTrackerIdxArray</i>	List of unassociated trackers.
in	<i>nFree</i>	current number of free trackers.
in	<i>iTrack</i>	current track Index.

Return values

<i>updated</i>	number of free trackers.
----------------	--------------------------

Definition at line 1518 of file Extended_Kalman_Filter_xyz.c.

References IS_INVALID.

Referenced by ekfRun().

Here is the caller graph for this function:

**4.37.3.11 isTargetWithinDataAssociationThresh()**

```
int32_t isTargetWithinDataAssociationThresh (
    float *restrict measResidual,
    float const *restrict state_rrd,
    float const *restrict meas_rrd,
    float const *restrict state_xyz,
    KFtrackerInstance_t *restrict inst,
    float *restrict pdistSq,
    int32_t tick,
    int32_t age )
```

Description

This function checks whether the predicted measurements are close to the new measurements (i.e. can they be associated). If they can be then the mean sq distance between the predicted and the new is passed back, so that the closest measurement is used.

Track association is not a traditional part of Kalman filtering, and hence our approach (based on experiment) is ad-hoc.

Parameters

out	<i>measResidual</i>	measurement residual.
-----	---------------------	-----------------------

Parameters

in	<i>state_rrd</i>	predicted measurements.
in	<i>meas_rrd</i>	new measurements.
in	<i>state_xyz</i>	predicted state (in x y co-ordinates)
in	<i>inst</i>	KFtrackerInstance_t object (for scratchpad.)
out	<i>pdistSq</i>	The distance sq between predicted and new measurement. Only valid if the measurement is deemed to be 'associatable'.
in	<i>tick</i>	age of the Track. Newer tracks are given more leeway & in associating, as they take some time to converge.

Return values

<i>IS_ASSOCIATED</i>	or NOT_ASSOCIATED.
----------------------	--------------------

Definition at line 648 of file Extended_Kalman_Filter_xyz.c.

References iRANGE, iRANGE_RATE, IS_ASSOCIATED, iSIN_AZIM, iX, iY, and NOT_ASSOCIATED.

Referenced by ekfRun().

Here is the caller graph for this function:

**4.37.3.12 isWithinBoundingBox()**

```
uint32_t isWithinBoundingBox (
    KFstate_t *restrict currTrack,
    DSS_DataPathObj *restrict dataPathObj )
```

Description

This function validates a state by checking whether it is within a specific bounding box. For e.g if an object is behind the radar, there is no need to track it.

Parameters

out	<i>currTrack</i>	pointer to the tracker's state.
in	<i>dataPathObj</i>	data path object -> for max range.

Return values

<i>true/false</i>

Definition at line 1455 of file Extended_Kalman_Filter_xyz.c.

References IS_INVALID, IS_VALID, iX, iY, N_STATES, and N_UNIQ_ELEM_IN_SYM_COVMAT.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.13 kalmanGainComputation()

```
void kalmanGainComputation (
    float *restrict kalmanGain,
    float const *restrict state_covmat,
    float const *restrict hMat,
    float const *restrict invResidCovmat,
    KFtrackerInstance_t * inst )
```

Description

This function performs the (near-optimal) kalman gain computation. i.e. in matlab $\text{kalmanGain} = \text{state_covmat}^{-1} \text{hMat}^T \text{inv}(\text{residCovmat})$

Parameters

out	<i>kalmanGain</i>	kalman gain matrix.
in	<i>state_covmat</i>	state covariance matrix.
in	<i>hMat</i>	(observation matrix) Hmat
in	<i>measCovVec</i>	inverse of the residual covMat.
in	<i>inst</i>	KFtrackerInstance_t object (for scratchpad.)

Return values

Not	Applicable.
-----	-------------

Definition at line 962 of file Extended_Kalman_Filter_xyz.c.

References iAzAz, iRAz, iRdAz, iRdRd, iRR, iRRd, iXdXd, iXdYd, iXX, iXXd, iXY, iXYd, iYdYd, iYXd, iYY, iYYd, and KFtrackerInstance::kalmanGainTemp.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.14 residualCovarianceComputation()

```
void residualCovarianceComputation (
    float *restrict residCovmat,
    float const *restrict state_covmat,
    float const *restrict measCovVec,
    float const *restrict hMat )
```

Description

This function performs the residual covariance matrix computation. i.e. $\text{residCovmat} = \text{hMat} * \text{state_covmat} * \text{transpose}(\text{hMat}) + \text{measCovVec}$

Parameters

out	<i>residCovmat</i>	residual covariance matrix.
in	<i>state_covmat</i>	state covariance matrix.
in	<i>measCovVec</i>	variance of the measurement.
in	<i>hMat</i>	(observation matrix) Hmat

Return values

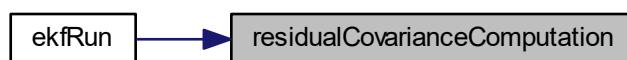
Not	Applicable.
-----	-------------

Definition at line 861 of file Extended_Kalman_Filter_xyz.c.

References iAzAz, iRANGE, iRANGE_RATE, iRAz, iRdAz, iRdRd, iRR, iRRd, iSIN_AZIM, iXdXd, iXdYd, iXX, iXXd, iXY, iXYd, iYdYd, iYXd, iYY, and iYYd.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.15 select_QVec()

```
float * select_QVec (
    float const *restrict QvecList,
    uint8_t tick,
    uint8_t age,
    const float range )
```

Description

This function selects a 'process noise matrix' based on the age of the tracker. If the tracker is new, then the 'process noise' is assumed to be high, so as to allow faster convergence. If the tracker is old, the process noise is computed as per the the unmodeled parameters. We also allow higher 'process noise' for closer objects, assuming that their higher SNR will compensate for the noisier measurements.

Parameters

in	<i>QvecList</i>	3 arrays [N_STATES] long. A list of possible Qvec matrices.
in	<i>tick</i>	the number of valid association of the tracker.
in	<i>age</i>	the age of the tracker (i.e. the number of ticks before the last associated measurement).
in	<i>range</i>	the distance of the target being tracked.

Return values

<i>pointer</i>	to the 'process noise diagonal'
----------------	---------------------------------

Definition at line 1418 of file Extended_Kalman_Filter_xyz.c.

References N_STATES.

Referenced by ekfRun().

Here is the caller graph for this function:

**4.37.3.16 selectMeas()**

```
int32_t selectMeas (
    int16_t *restrict selectedMeas,
    trackingInputReport_t const *restrict measArray,
    int16_t *restrict assocMeasIdx,
    const int32_t nFree,
    const int32_t numUnassociatedMeas,
    const int32_t numMeasTotal )
```

Description

This function selects the unassociated measurements that are to be used to create the new tracks.

Parameters

out	<i>selectedMeas</i>	The unassociated measurements selected for tracking.
in	<i>measArray</i>	List of input measurements
in	<i>assocMeasIdx</i>	An array declaring whether a measurement has been associated or not.
in	<i>nFree</i>	Number of free tracks.
in	<i>numUnassociatedMeas</i>	Number of unassociated measurements.
in	<i>numMeasTotal</i>	Total number of measurements.

Return values

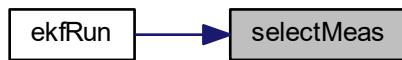
<i>number</i>	of selected measurements. .
---------------	-----------------------------

Definition at line 1244 of file Extended_Kalman_Filter_xyz.c.

References HIGH_SNR_RVAR_THRESH, iRANGE, IS_ASSOCIATED, and NOT_ASSOCIATED.

Referenced by ekfRun().

Here is the caller graph for this function:

**4.37.3.17 stateCovmatMeasurementUpdate()**

```
void stateCovmatMeasurementUpdate (
    float *restrict covmat,
    float const *restrict const kalmanGain,
    float const *restrict const hMat,
    KFtrackerInstance_t * inst )
```

Description

This function performs the state covariance matrix update. i.e. in matlab $P = (I - KH)*P$

Parameters

in,out	<i>covmat</i>	updated state covariance matrix (P).
in	<i>kalmanGain</i>	Kalman gain (K).
in	<i>hMat</i>	observation matrix (H).
in	<i>KFtrackerInstance_t</i>	scratchpad pointers.

Return values

<i>Not</i>	Applicable.
------------	-------------

Definition at line 1129 of file Extended_Kalman_Filter_xyz.c.

References iXdXd, iXdYd, iXX, iXXd, iXY, iXYd, iYdYd, iYY, iYYd, N_MEASUREMENTS, N_STATES, N_U, NIQ_ELEM_IN_SYM_COVMAT, KFtrackerInstance::stateCovMattemp, and KFtrackerInstance::stateCovMattempP. Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.18 stateCovmatTimeUpdate()

```
void stateCovmatTimeUpdate (
    float *restrict covmat,
    float const *restrict Q,
    const float td,
    KFtrackerInstance_t *restrict inst )
```

Description

This function updates the state covariance matrix.

Parameters

out	<i>covmat</i>	state covariance matrix.
in	<i>Q</i>	process noise diagonal .
in	<i>td</i>	time delta.
in	<i>inst</i>	KFtrackerInstance_t (for scratchpad).

Return values

Not	Applicable.
-----	-------------

Definition at line 526 of file Extended_Kalman_Filter_xyz.c.

References iX, iXd, iXdXd, iXdYd, iXX, iXXd, iXY, iXYd, iY, iYd, iYdYd, iYXd, iYY, iYYd, and N_UNIQ_ELEM_IN_←SYM_COVMAT.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.19 stateVecMeasurementUpdate()

```
int32_t stateVecMeasurementUpdate (
    float *restrict state,
    float const *restrict kalmanGain,
    float const *restrict meas_residual )
```

Description

This function performs the state measurement update. i.e. in matlab state = state + kalmanGain*measResidual

Parameters

in,out	<i>state</i>	updated state .
in	<i>kalmanGain</i>	kalman gain matrix.
in	<i>meas_residual</i>	measurement residual.

Return values

Not	Applicable.
-----	-------------

Definition at line 1089 of file Extended_Kalman_Filter_xyz.c.

References IS_INVALID, IS_VALID, iY, N_MEASUREMENTS, and N_STATES.

Referenced by ekfRun().

Here is the caller graph for this function:



4.37.3.20 stateVecTimeUpdate()

```
int32_t stateVecTimeUpdate (
    float *restrict state,
    const float td )
```

Description

This function updates the state vector.

Parameters

in	<i>state</i>	state vector.
in	<i>td</i>	time delta.

Return values

Is	the object behind the radar?.
----	-------------------------------

Definition at line 497 of file Extended_Kalman_Filter_xyz.c.
 References IS_INVALID, IS_VALID, iX, iXd, iY, and iYd.
 Referenced by ekfRun().
 Here is the caller graph for this function:



4.37.3.21 symMatInv()

```
int32_t symMatInv (
    float *restrict inv,
    float const *restrict m )
```

Description

This function computes the inverse of a symmetric N_MEASUREMENTS x N_MEASUREMENTS matrix.

Parameters

out	<i>inv</i>	inverted matrix.
in	<i>m</i>	input matrix.

Return values

<i>Not</i>	Applicable.
------------	-------------

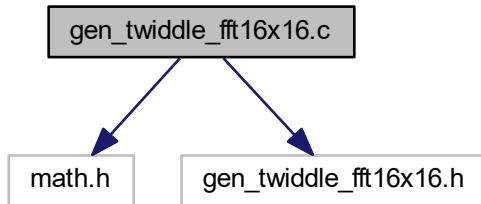
Definition at line 752 of file Extended_Kalman_Filter_xyz.c.
 References EPS, and N_UNIQ_ELEM_IN_SYM_RESIDCOVMAT.
 Referenced by ekfRun().
 Here is the caller graph for this function:



4.38 gen_twiddle_fft16x16.c File Reference

```
#include <math.h>
#include "gen_twiddle_fft16x16.h"
```

Include dependency graph for gen_twiddle_fft16x16.c:



Macros

- `#define PI 3.14159265358979323846`

Functions

- `static short d2s (double d)`
- `int gen_twiddle_fft16x16 (short *w, int n)`

4.38.1 Macro Definition Documentation

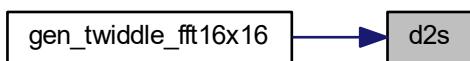
4.38.1.1 PI

```
#define PI 3.14159265358979323846
Definition at line 65 of file gen_twiddle_fft16x16.c.
```

4.38.2 Function Documentation

4.38.2.1 d2s()

```
static short d2s (
    double d )  [static]
Definition at line 73 of file gen_twiddle_fft16x16.c.
Referenced by gen_twiddle_fft16x16().
Here is the caller graph for this function:
```



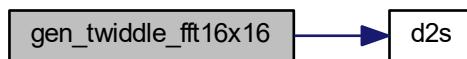
4.38.2.2 gen_twiddle_fft16x16()

```
int gen_twiddle_fft16x16 (
    short * w,
    int n )
```

Definition at line 128 of file gen_twiddle_fft16x16.c.

References d2s(), and PI.

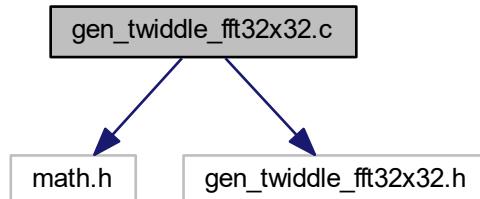
Here is the call graph for this function:



4.39 gen_twiddle_fft32x32.c File Reference

```
#include <math.h>
#include "gen_twiddle_fft32x32.h"
```

Include dependency graph for gen_twiddle_fft32x32.c:



Macros

- #define PI 3.14159265358979323846

Functions

- static int d2i (double d)
- int gen_twiddle_fft32x32 (int *w, int n, double scale)

4.39.1 Macro Definition Documentation

4.39.1.1 PI

```
#define PI 3.14159265358979323846
```

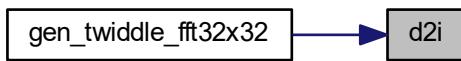
Definition at line 65 of file gen_twiddle_fft32x32.c.

4.39.2 Function Documentation

4.39.2.1 d2i()

```
static int d2i (
    double d ) [static]
```

Definition at line 73 of file gen_twiddle_fft32x32.c.
Referenced by gen_twiddle_fft32x32().
Here is the caller graph for this function:



4.39.2.2 gen_twiddle_fft32x32()

```
int gen_twiddle_fft32x32 (
    int * w,
    int n,
    double scale )
```

Definition at line 98 of file gen_twiddle_fft32x32.c.
References d2i(), and PI.
Here is the call graph for this function:

