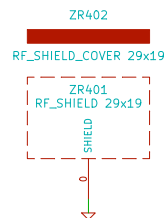
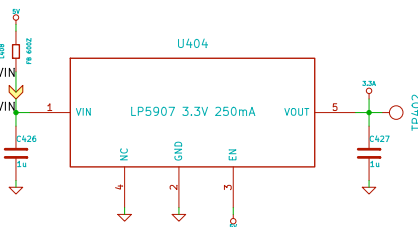
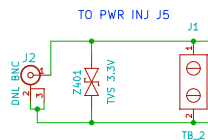
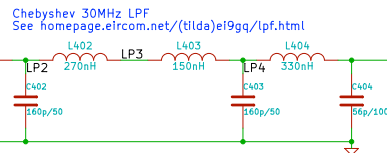


1	2	3	4	5
A	<div>Sheet: ADC</div> <div>File: wrx.ADC.sch</div>	<div>Sheet: FPGA_Power</div> <div>File: wrx.FPGA_power.sch</div>		
B	<div>Sheet: GPS</div> <div>File: wrx.GPS.sch</div>	<div>Sheet: Power_Supplies</div> <div>File: wrx.power_supplies.sch</div>		
C	<div>Sheet: BeagleBone</div> <div>File: wrx.BeagleBone.sch</div>			
	<div>Sheet: FPGA_IO</div> <div>File: wrx.FPGA_IO.sch</div>			
				<div>Point Reyes Ventures, LLC</div> <div>File:</div> <div>Sheet: /</div> <div>Title: WRX Prototype v0.1</div> <div>Size: A4Date: 16 jul 2015</div> <div>KiCad E.D.A. eeschema (2013-mar-25)-stable</div> <div>Rev: 0.1</div> <div>Id: 1/7</div>
1	2	3	4	5

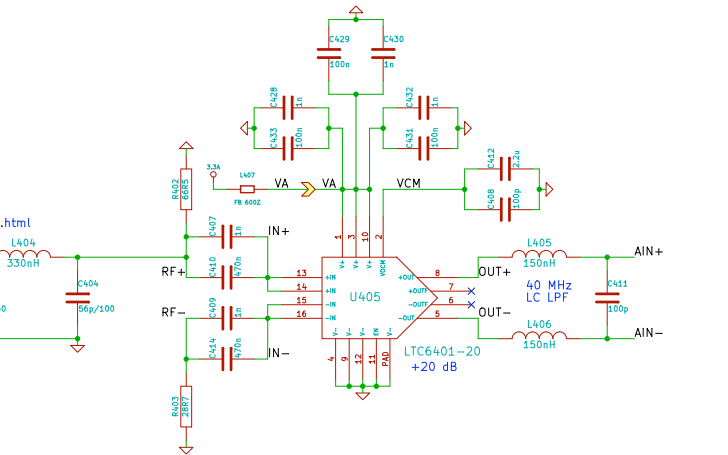
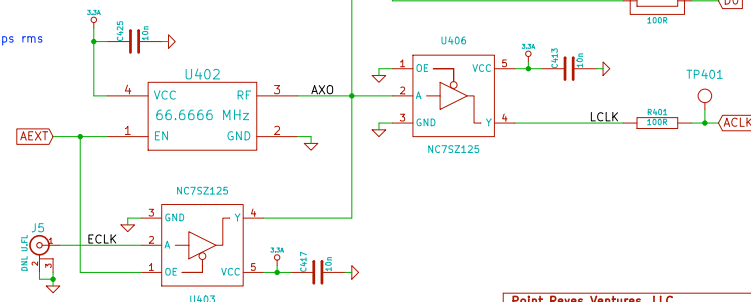
using BNC_SMA_EM
footprint to allow
SMA connector option

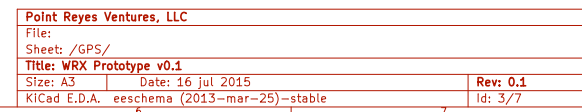


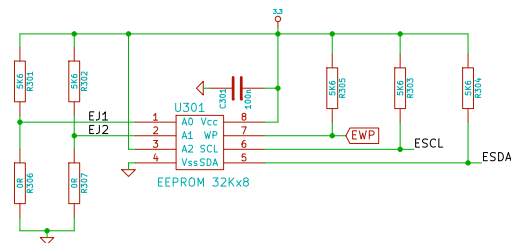
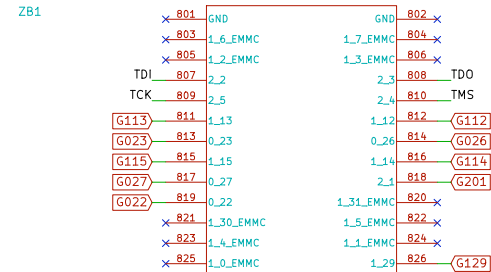
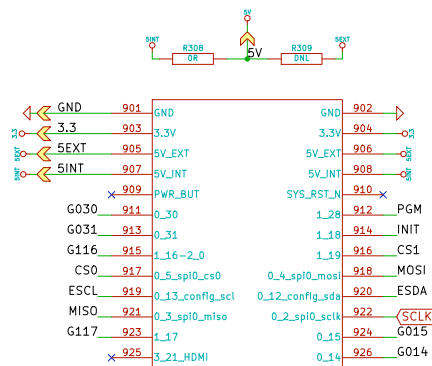
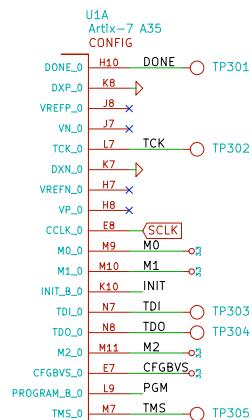
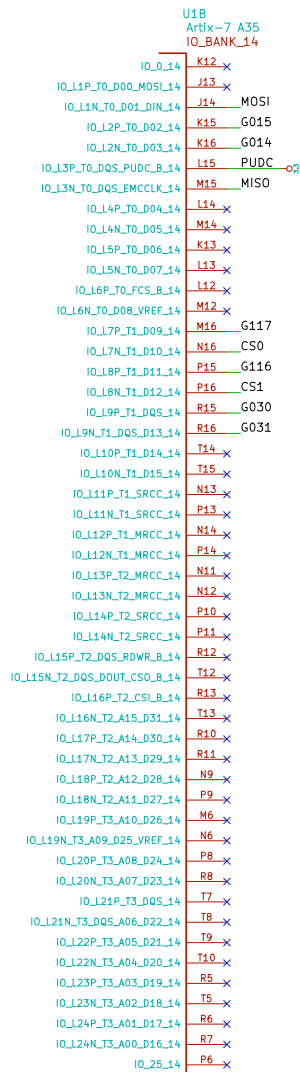
Chebyshev 30MHz LPF
See homepage.eircom.net/~tilda/ef9gq/lpf.html

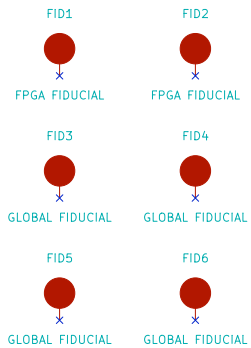
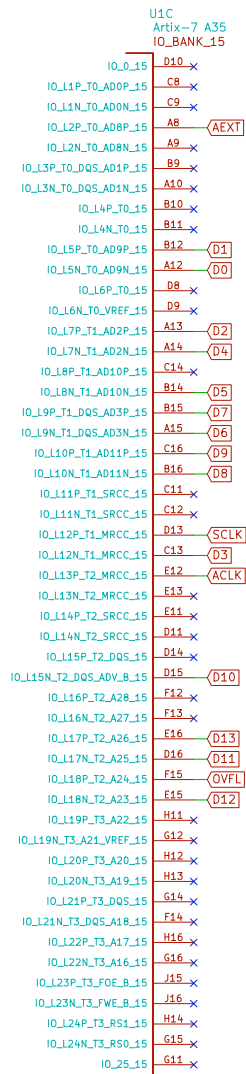
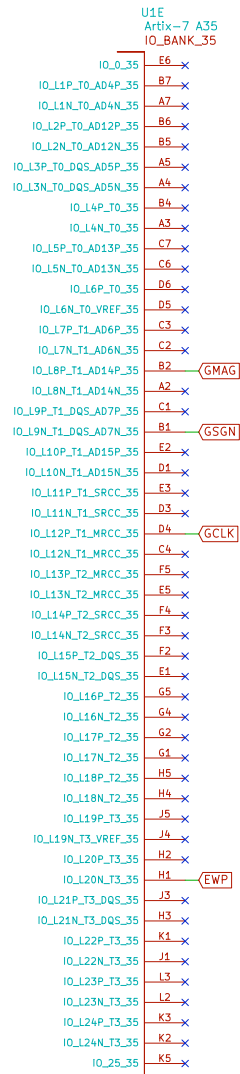
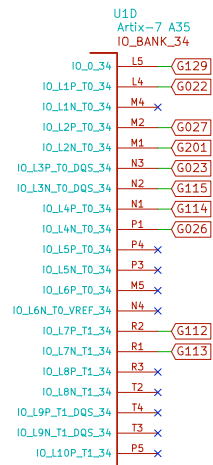


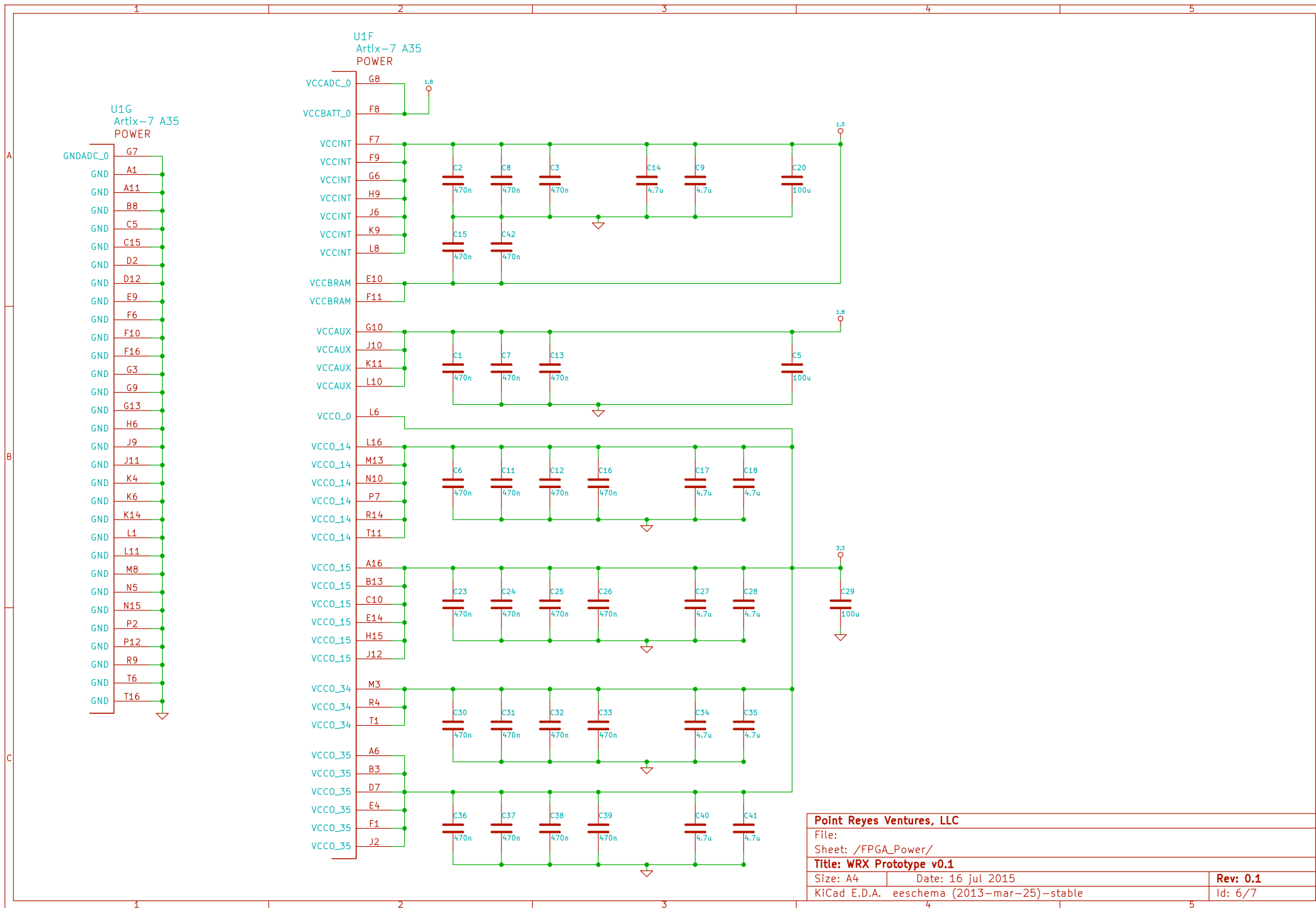
CTS 357 series
pull +/- 50 ppm
stab +/- 50 ppm
phase jitter <= 1 ps rms
-90 @ 100
-130 @ 10K
-150 > 100K

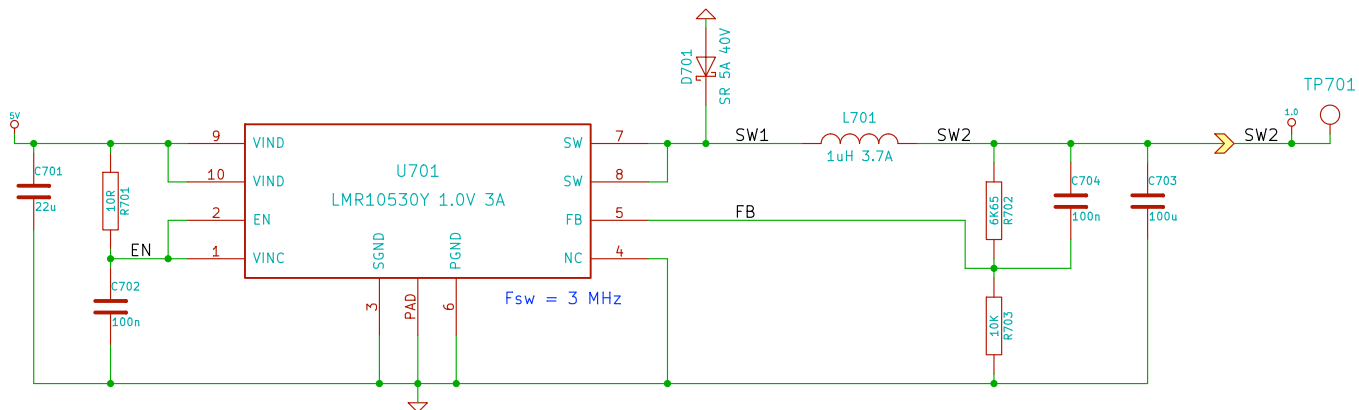












Point Reyes Ventures, LLC

File:

Sheet: /Power_Supplies/

Title: WRX Prototype v0.1

Size: A4

Date: 16 jul 2015

Rev: 0.1

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