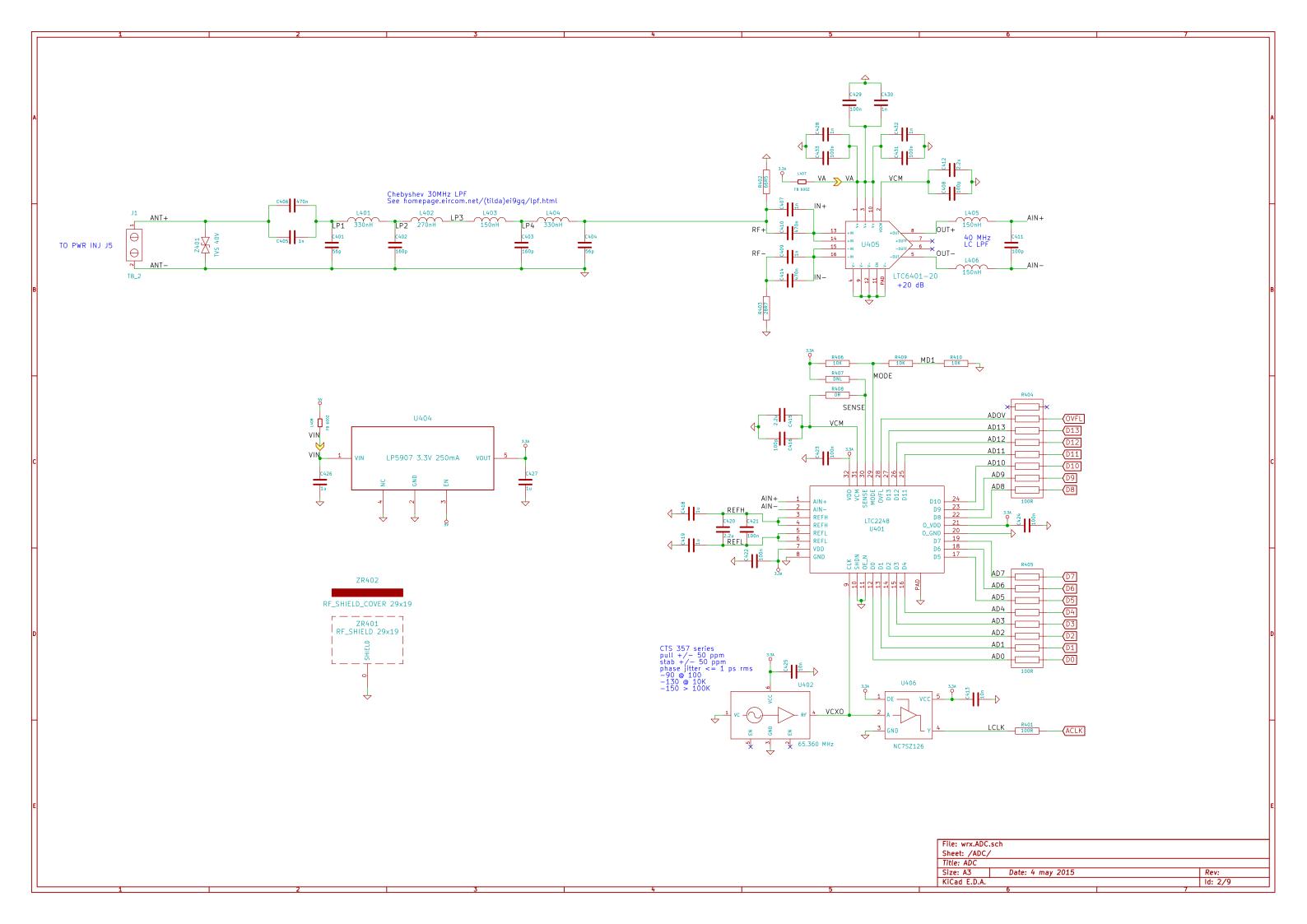
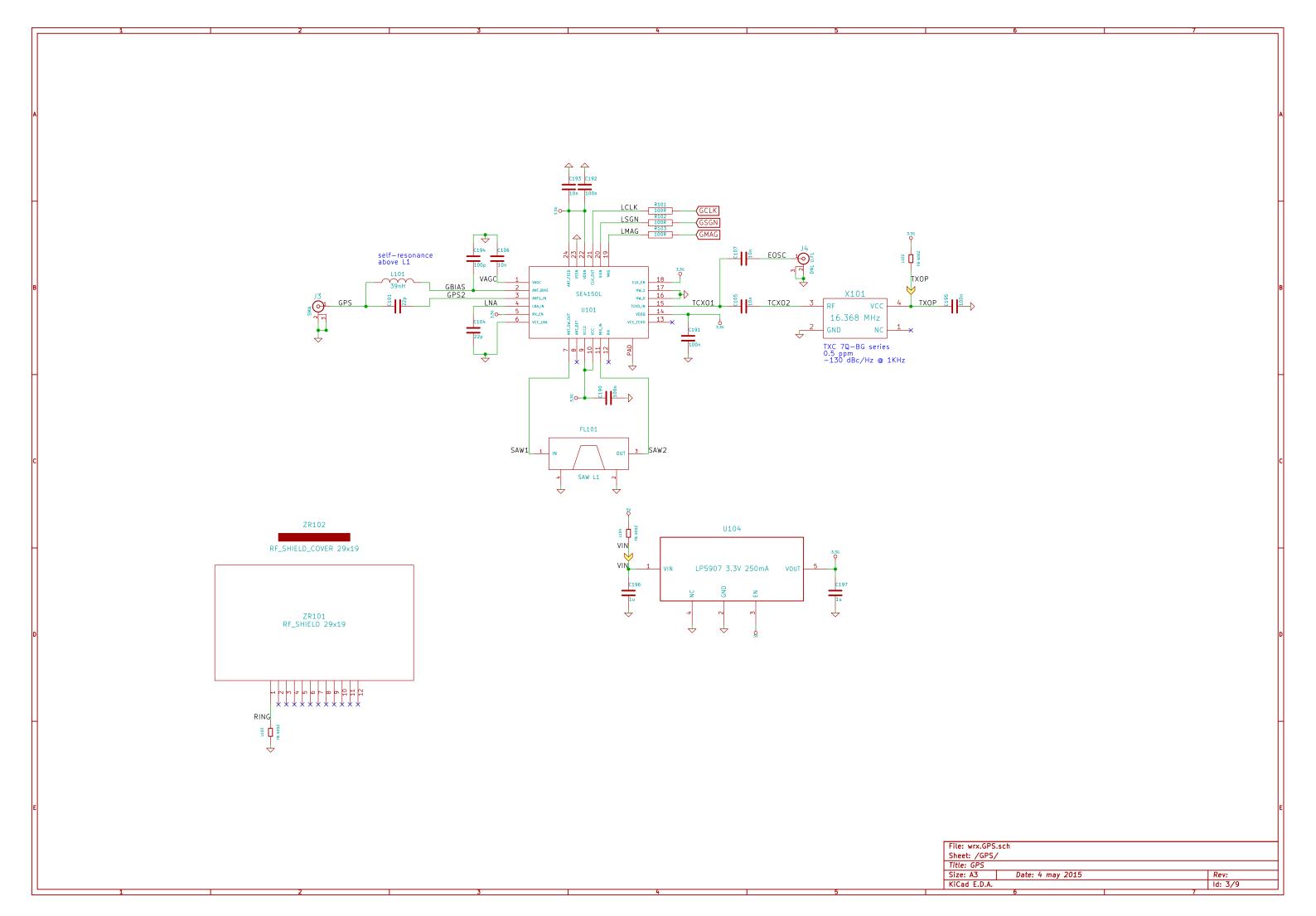
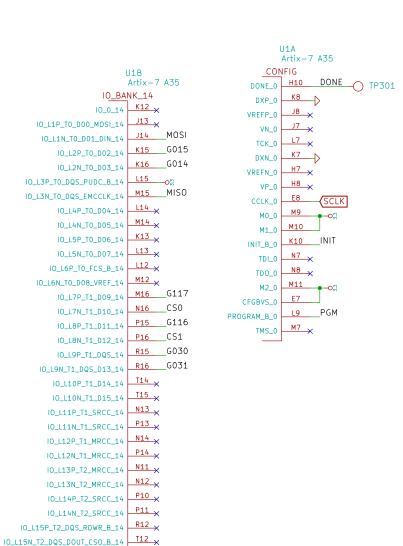
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		ADC	F	FPGA_Power				
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		606						
		GPS		Power_Supplies				
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		<u>BeagleBone</u>		Active_Antenna				
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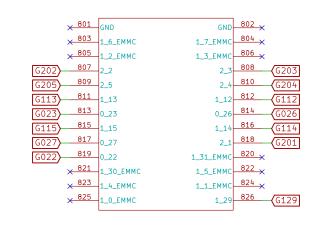


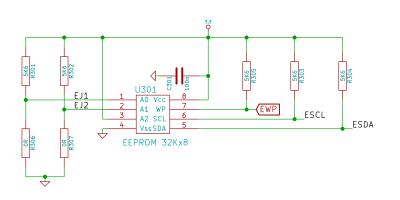
IO_L17N_T2_A13_D29_14 R11 ×

IO_L18P_T2_A12_D28_14 N9 ×

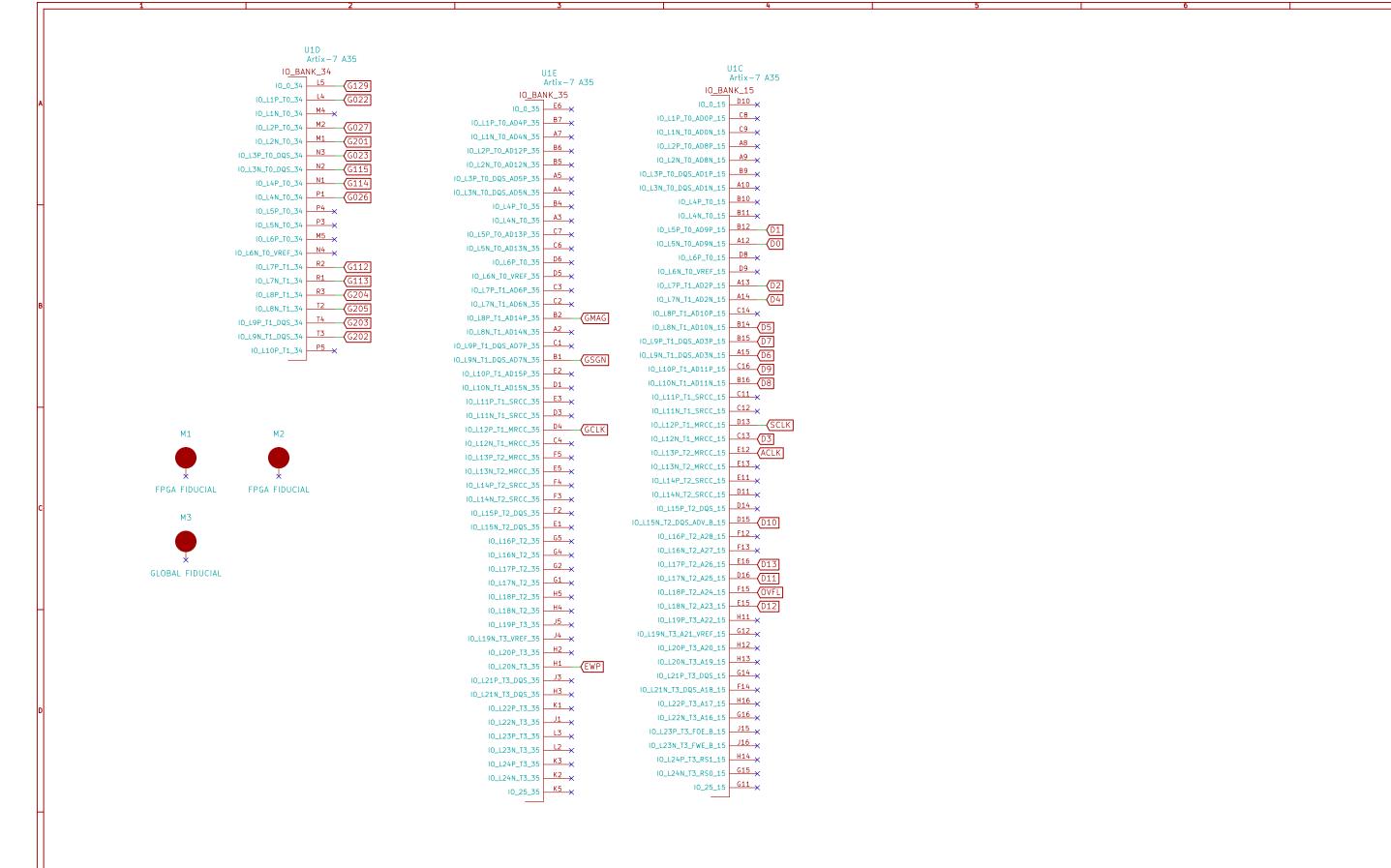
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√ GND	901	GND	GND	902	→
20 ₹ 3.3	903	·3.3V	3.3V	904	_o¤
5EXT	905	5V_EXT	5V_EXT	906	-o\(\frac{1}{2}\)
5INT	907	5V_INT	5V_INT	908	
- 2	× 909	PWR_BUT	SYS_RST_N	910 ×	
G030_	911	0.30	1_28	912	_PGM
G031_	913	0_31	1_18	914	_INIT
G116_	915	1_16-2_0	1_19	916	_CS1
CS0_	917	0_5_spi0_cs0	0_4_spi0_mosi	918	_MOSI
ESCL_	919	0_13_config_scl	0_12_config_sda	920	_ESDA
MISO_	921	0_3_spi0_miso	0_2_spi0_sclk	922	SCLK
G117_	923	1 17	0_15	924	G015
	× 925	3_21_HDMI	0_14	926	_G014





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Sheet: /BeagleBone/					
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	File: wrx.FPG	A_10.sch		
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Title: FPGA I/O				
	Size: A3	Date: 4 may 2015	Rev:	
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