WROCŁAW UNIVERSITY OF SCIENCE AND TECHNOLOGY FACULTY OF FUNDAMENTAL PROBLEMS OF TECHNOLOGY

FIELD: Computer Science SPECIALIZATION: Computer Security

MASTER OF SCIENCE THESIS

Timing Attack Resistant Implementation of RSA on GPU.

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GRADE:

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Chapter 1

Introduction

Public key cryptography is the key factor in providing secure communication between two parties. Fast development of distributed system requiring not only security, but also integrity and non-repudiation has pushed cryptography to the limit. Since 1978[4] most commonly used cryptosystem is RSA, which provides asymmetric encryption, as well as generation of digital signatures. The security of RSA is mainly base on the bitwise key length. As computational power of modern CPUs arises, the minimal bit length of RSA key gets significantly bigger to provide sufficient security. At least 4096 bits long keys are considered secure nowadays. This leads to very high workload required to perform encryption/decryption. RSA is mainly based on modular arithmetics and simple computations become infeasible for moder CPUs, when dealing with so large integers.

One of the solution to this problem is to parallelize. GPGPU[7] (for General-Purpose computing on the Graphics Processing Unit) enables the use of GPU for parallel computation other than graphics. GPUs are designed to perform computations in parallel. Since every PC is equipped with some kind of GPU, one can easily exploit its capabilities. NVIDIA has made it even more accessible by creating CUDA (Compute Unified Device Architecture)[5]. It is a parallel computing platform and API, which exposes GPUs true potential.

1.1 Environment Specification

This project was developed in Microsoft Visual Studio 2015[3] with NSight plugin and CUDA API.[5] Source code is written mainly in C++ and inline assembly - PTX.[6] Table 1.1 more precisely illustrates GPU specification on which the program and all tests are run.

Table 1.1: Device specification

Device name:	GeForce GTX 960
CUDA Driver Version:	9.0
CUDA Runtime Version:	8.0
CUDA Capability version number:	5.2
Total amount of global memory:	4096 MBytes (4294967296 bytes)
GPU Max Clock rate:	1304 MHz (1.30 GHz)
Total amount of shared memory per block:	49152 bytes
Warp size:	32

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Table below presents full platform and system information.

Table 1.2: PC specification

Operating System: Windows 10 Education 64-bit
Motherboard: Gigabyte Technology Co., Ltd. P55A-UD4
CPU: Intel(R) Core(TM) i5 CPU 750 @ 2.67GHz (4 CPUs), 2.7GHz
RAM Memory: 4096MB

Chapter 2

Theory

This chapter basically presents minimal amount of theory required to fully understand the concepts, problems and solutions which were applied and implemented within this project.

2.1 RSA

The RSA algorithm was created by Ronald Rivest, Adi Shamir, and Leonard Adleman in 1970s. The security of the algorithm is based on the problem of integer factorization.

- Key generation
 - Choose two large and distinct prime numbers p and q.
 - Compute the modulus n

$$n = pq$$

- Compute

$$\lambda(n) = lcm(\lambda(p), \lambda(q)) = lcm(p-1, q-1)$$

where λ is the Carmichael Totient Function[1]. This value must be kept in private.

- Choose an integer e such that:

$$1 < e < \lambda(n)$$

and

$$gcd(e, \lambda(n)) = 1$$

This means e and $\lambda(n)$ are coprime.[8]

- Choose value d for decryption and solve for d:

$$de \equiv 1(mod\lambda(n))$$

e is used for encryption. Usually this is done the other way around. e is chosen first so it has short bit length and small Hamming weight,[2] which provides for much faster encryption. In most cases e=3.5 or 7. High security is provided if larger number is used. Most common is Fermat's four: $e=2^{16}+1=65537=0x10001$

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- Public key is then:

(e, n)

and the private key:

(d, n)

• Encryption

For message m the ciphertext c is in relation:

 $c \equiv m^e \mod n$

• Decryption

$$m \equiv c^d \bmod n \equiv (m^e)^d \bmod n \equiv m \bmod n$$

2.2 Side channel attacks

2.2.1 Timing attacks

2.3 Parallel programming on CUDA

Chapter 3

Implementation

The code was written in C++ language. In order to minimize data movement between host and the device, most of logics and computation are executed fully on GPU. Implementation of RSA encryption requires only one function - modular exponentiation of a multi precision integer. Implemented Big Integer class provides much more functions, to properly handle data and validate results. Code listing below shows the header of class BigInteger.

```
class BigInteger
//fields
public:
// 4096 bits
static const int ARRAY_SIZE = 128;
private:
// Magnitude array in little endian order.
                                                                               10
// Most-significant int is mag[length-1].
                                                                               1.1
// Least-significant int is mag[0].
                                                                               12
// Allocated on the device.
                                                                               13
unsigned int* deviceMagnitude;
                                                                               14
                                                                               15
// same array allocated on the host
                                                                               16
// provides faster access if nothing was changed
                                                                               17
unsigned int* hostMagnitude;
                                                                               18
                                                                               19
// flag indicating if hostMagnitude matches deviceMagnitude
                                                                               20
bool upToDate;
                                                                               21
                                                                               22
// Device wrapper instance diffrent for every integer
                                                                               23
// to provide parallel execution
                                                                               24
DeviceWrapper* deviceWrapper;
                                                                               25
                                                                               26
// methods
                                                                               27
public:
                                                                               28
BigInteger();
                                                                               29
BigInteger(const BigInteger& x);
                                                                               30
BigInteger(unsigned int value);
                                                                               31
~BigInteger();
                                                                               32
const unsigned int& operator[](int index);
                                                                               33
                                                                               34
                                                                               35
static BigInteger* fromHexString(const char* string);
```

```
static BigInteger* createRandom(int bitLength);
                                                                              38
// setters, getters
                                                                               39
void set(const BigInteger& x);
                                                                               40
unsigned int* getDeviceMagnitude(void) const;
                                                                               41
                                                                               42
// arithmetics
                                                                               43
void add(const BigInteger& x);
                                                                               44
void subtract(const BigInteger& x);
                                                                               45
void multiply(const BigInteger& x);
                                                                               46
void square(void);
                                                                               47
void mod(const BigInteger& modulus);
                                                                               48
void multiplyMod(const BigInteger& x, const BigInteger& modulus);
                                                                               49
void squareMod(const BigInteger& modulus);
                                                                               50
void powerMod(BigInteger& exponent, const BigInteger& modulus);
                                                                               51
                                                                               52
// logics
void shiftLeft(int bits);
                                                                               54
void shiftRight(int bits);
                                                                               55
                                                                               56
// extras
                                                                               57
bool equals(const BigInteger& value) const;
                                                                               58
int compare(const BigInteger& value) const;
                                                                               59
int getBitwiseLengthDiffrence(const BigInteger& value) const;
int getBitwiseLength(void) const;
                                                                              61
int getLSB(void) const;
                                                                              62
bool testBit(int bit);
                                                                               63
void synchronize(void);
                                                                               64
char* toHexString(void);
                                                                               65
void print(const char* title);
                                                                               66
                                                                               67
//timer
void startTimer(void);
                                                                              69
unsigned long long stopTimer(void);
                                                                               70
                                                                               71
// async calls
                                                                               72
// must call synchronize to read from
                                                                               73
void modAsync(const BigInteger& modulus);
                                                                               74
void multiplyModAsync(const BigInteger& x, const BigInteger& modulus);
                                                                               75
void squareModAsync(const BigInteger& modulus);
                                                                               76
                                                                              77
private:
                                                                               78
                                                                               79
void setMagnitude(const unsigned int* magnitude);
                                                                               80
void clear(void);
                                                                               81
void updateDeviceMagnitiude(void);
                                                                               82
void updateHostMagnitiude(void);
                                                                               83
static unsigned int random32(void);
                                                                               84
                                                                               85
                                                                               86
Parses hex string to unsigned int type.
                                                                               87
Accepts both upper and lower case, no "0x" at the beginning.
                                                                               88
E.g.: 314Da43F
                                                                               89
*/
                                                                               90
static unsigned int parseUnsignedInt(const char* hexString);
                                                                               91
};
                                                                               92
```

Listing 3.1: BigInteger.h

Big Integer class handles mathematical logic, validates input / output, and provides comfortable and readable interface.

The intermediary between Big Integer and GPU is another class - Device Wrapper. It handles GPU kernel launches, synchronization and data movement. Class definition is listed below.

```
class DeviceWrapper
private:
// main stream for kernel launches
cudaStream_t mainStream;
// lauch config
dim3 block_1, block_2, block_4;
                                                                              10
dim3 thread_warp, thread_2_warp, thread_4_warp;
                                                                              11
                                                                              12
// 4 ints to help store results
                                                                              13
int* deviceWords;
                                                                              14
                                                                              15
// auxiliary arrays
                                                                              16
unsigned int* device4arrays;
                                                                              17
unsigned int* device128arrays;
                                                                              18
unsigned int* deviceArray;
                                                                              19
                                                                              20
unsigned long long* deviceStartTime;
                                                                              21
unsigned long long* deviceStopTime;
                                                                              22
                                                                              23
public:
                                                                              24
                                                                              25
DeviceWrapper();
                                                                              26
~DeviceWrapper();
                                                                              27
                                                                              ^{28}
// sync
                                                                              29
unsigned int* init(int size) const;
                                                                              30
unsigned int* init(int size, const unsigned int* initial) const;
                                                                              31
void updateDevice(unsigned int* device_array, const unsigned int*
                                                                              32
   host_array, int size) const;
void updateHost(unsigned int* host_array, const unsigned int*
                                                                              33
   device_array, int size) const;
void free(unsigned int* device_x) const;
                                                                              34
// extras
                                                                              36
void clearParallel(unsigned int* device_x) const;
                                                                              37
void cloneParallel(unsigned int* device_x, const unsigned int* device_y)
    const;
int compareParallel(const unsigned int* device_x, const unsigned int*
                                                                              39
   device_y) const;
bool equalsParallel(const unsigned int* device_x, const unsigned int*
                                                                              40
   device_y) const;
int getLSB(const unsigned int* device_x) const;
                                                                              41
int getBitLength(const unsigned int* device_x) const;
                                                                              42
void synchronize(void);
                                                                              43
                                                                              44
// measure time
                                                                              45
void startClock(void);
                                                                              46
unsigned long long stopClock(void);
                                                                              47
```

```
// logics
void shiftLeftParallel(unsigned int* device_x, int bits) const;
                                                                           50
void shiftRightParallel(unsigned int* device_x, int bits) const;
                                                                            51
                                                                            52
// arithmetics
                                                                            53
void addParallel(unsigned int* device_x, const unsigned int* device_y)
                                                                            54
void subtractParallel(unsigned int* device_x, const unsigned int*
                                                                            55
   device_y) const;
void multiplyParallel(unsigned int* device_x, const unsigned int*
                                                                            56
   device_y) const;
void squareParallel(unsigned int* device_x) const;
void squareParallelAsync(unsigned int* device_x) const;
void modParallel(unsigned int* device_x, unsigned int* device_m) const;
                                                                            59
void modParallelAsync(unsigned int* device_x, unsigned int* device_m)
                                                                            60
   const;
void multiplyModParallel(unsigned int* device_x, const unsigned int*
   device_y, const unsigned int* device_m) const;
void multiplyModParallelAsync(unsigned int* device_x, const unsigned int
   * device_y, const unsigned int* device_m) const;
void squareModParallel(unsigned int* device_x, const unsigned int*
                                                                            63
   device_m) const;
void squareModParallelAsync(unsigned int* device_x, const unsigned int*
                                                                            64
   device_m) const;
                                                                            65
private:
                                                                            66
void inline addParallelWithOverflow(unsigned int* device_x, const
                                                                            67
   unsigned int* device_y, int blocks) const;
};
                                                                            68
```

Listing 3.2: DeviceWrapper.h

Project also contains Test class to validate computations, measure times and simulate encryption. RSA class contains single "encrypt" function, which encrypts provided value. Full class diagram is presented on figure 3.1.

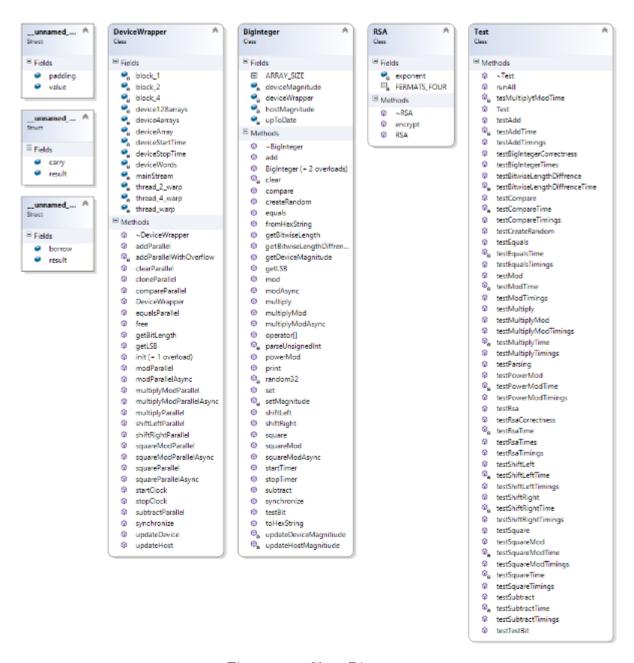


Figure 3.1: Class Diagram

- 3.1 Parallel equals
- 3.2 Parallel compare
- 3.3 Parallel bit length
- 3.4 Parallel left shift
- 3.5 Parallel right shift
- 3.6 Parallel add
- 3.7 Parallel subtract
- 3.8 Parallel multiply
- 3.9 Parallel modulo reduction
- 3.10 Parallel multiply modulo
- 3.11 Parallel power modulo

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