

# AN13539

## OM-SE05xARD hardware overview

Rev. 1.2 — 8 November 2022

Application note

### Document information

Information	Content
Keywords	OM-SE050ARD-E, OM-SE050ARD-F, OM-SE051ARD, OM-SE051ARD-W, OM-SE051ARD-H, OM-SE05xARD, EdgeLock SE05x
Abstract	This document describes the OM-SE05xARD development kits and details how to use its jumpers to configure the different communication options with the EdgeLock SE05x security IC.



## Revision history

### Revision history

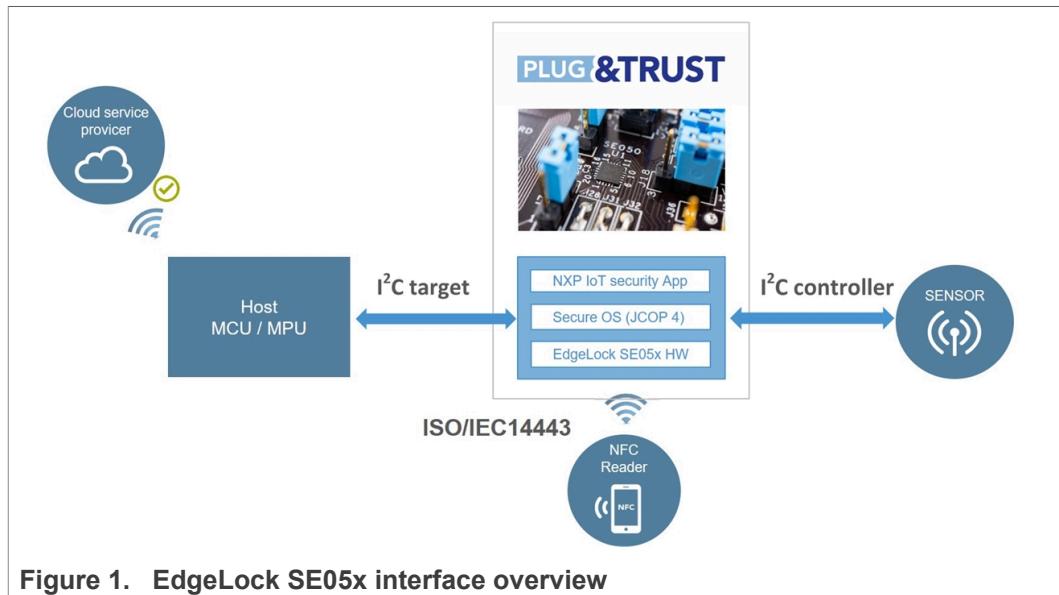
Revision number	Date	Description
1.0	2022-03-23	First release
1.1	2022-05-19	Correct J22 pin assignment in <a href="#">Figure 2</a>
1.2	2022-11-08	Add board OM-SE051ARD-H

## 1 Overview

The EdgeLock SE05x product family offers enhanced Common Criteria EAL 6+ based security, for unprecedented protection against the latest attack scenarios. This ready-to-use family of secure elements for IoT devices provides a root of trust at the IC level and supports the increasing demand for easy-to-design and scalable IoT security.

The EdgeLock SE05x uses I<sup>2</sup>C as communication interface and its commands are wrapped using the Smartcard T=1 over I<sup>2</sup>C (T=1oI2C) protocol. In addition, the EdgeLock SE05x supports the following interfaces, as shown in [Figure 1](#):

- I<sup>2</sup>C interface in target mode with date rates up to 3.4 Mbps on some product variants.
- I<sup>2</sup>C interface in controller mode with date rates up to 400 kHz.
- ISO/IEC 14443 T=CL (passive) protocol on some product variants.



**Figure 1. EdgeLock SE05x interface overview**

**Note:** Only the I<sup>2</sup>C target interface is mandatory. The I<sup>2</sup>C controller and ISO/IEC 14443 interfaces are optional.

The OM-SE05xARD are the development kits for the EdgeLock SE05x security IC's. This development kit allows you to evaluate the EdgeLock SE05x product features and simplifies the development of your custom applications. [Table 1](#) list the ordering details of OM-SE05xARD development kits.

**Table 1. OM-SE05xARD development kit ordering details**

Part number	12NC	SE05x chip type	Picture
<a href="#">OM-SE050ARD-F</a>	9354 357 63598	SE050F2HQ1/Z018H	

**Table 1.** OM-SE05xARD development kit ordering details...continued

Part number	12NC	SE05x chip type	Picture
<a href="#">OM-SE050ARD-E</a>	9354 332 66598	SE050E2HQ1/Z01Z3	
<a href="#">OM-SE051ARD</a>	935399187598	SE051C2HQ1/Z01XD	
<a href="#">OM-SE051ARD-W</a>	9354 210 01598	SE051W2HQ1/Z013Y	
<a href="#">OM-SE051ARD-H</a>	9354 449 78598	SE051H2HQ1/Z0112	

**Note:** All listed boards OM-SE050ARD-F, OM-SE050ARD-E, OM-SE051ARD, OM-SE051ARD-W, OM-SE051ARD-H do have the same schematic and layout. In the following we use the OM-SE051ARD board as reference.

**Table 2.** Supported interfaces

Part number	ISO/IEC14443-4-A (passive)	I <sup>2</sup> C Target	I <sup>2</sup> C Controller
OM-SE051ARD	✓	✓ (up to 3.4 Mbit/s)	✓
OM-SE051ARD-W	✓	✓ (up to 3.4 Mbit/s)	✓
OM-SE051ARD-H	✓	✓ (up to 3.4 Mbit/s)	✓
OM-SE050ARD-F	✓	✓ (up to 3.4 Mbit/s)	✓
OM-SE050ARD-E		✓ (up to 1 Mbit/s)	✓

## 1.1 RED information

Table 3. RED information

P/N	RF Technology	Frequency Range (MHz)	Maximum Reader TX Power (dBm)
OM-SE050ARD-F	ISO/IEC14443-4-A (passive)	10 to 15	+23dBm
OM-SE051ARD	ISO/IEC14443-4-A (passive)	10 to 15	+23dBm
OM-SE051ARD-W	ISO/IEC14443-4-A (passive)	10 to 15	+23dBm
OM-SE051ARD-H	ISO/IEC14443-4-A (passive)	10 to 15	+23dBm

**Note:** OM-SE050ARD-E kit is not listed here as the IC does not support ISO/IEC 14443-4-A.

According to the Radio Equipment Directive (RED) 2014/53/EU, this user manual states that:

- frequency bands in which the equipment operates ranges from 10MHz to 15MHz; carrier signal is 13.56MHz, according to ISO/IEC14443-4-A.
- maximum RF power transmitted: +23dBm, response signal is modulated on subcarrier, and transmitted over 13.56MHz signal coming from external NFC active device.

### EUROPEAN DECLARATION OF CONFORMITY (Simplified DoC per Article 10.9 of the Radio Equipment Directive 2014/53/EU)

- This apparatus, namely OM-SE050ARD-F board for contactless operation, conforms to Radio Equipment directive 2014/53/EU.
- This apparatus, namely OM-SE051ARD board for contactless operation, conforms to Radio Equipment directive 2014/53/EU.
- This apparatus, namely OM-SE051ARD-W board for contactless operation, conforms to Radio Equipment directive 2014/53/EU.
- This apparatus, namely OM-SE051ARD-H board for contactless operation, conforms to Radio Equipment directive 2014/53/EU.

The full EU Declaration of conformity for all apparatus can (will) be found at this location: [www.nxp.com/SE051](http://www.nxp.com/SE051)

## 2 Headers and connectors

The OM-SE05xARD is designed with several headers and connectors that allow you to interface with EdgeLock SE05x. The OM-SE05xARD is equipped with:

- **Arduino-R3 header:** It allows you to easily attach it to any NXP MCU/MPU development board with Arduino compatible headers such as many Kinetis, LPC and i.MX MCU boards. The Arduino-R3 female connectors come soldered in the OM-SE05xARD.
- **External I<sup>2</sup>C connector:** It allows you to connect any non-Arduino compatible MCU boards via I<sup>2</sup>C target interface. The OM-SE05xARD includes the mounting holes for the External I<sup>2</sup>C connector.
- **10-pin header:** It allows you to access several pins of the EdgeLock SE05x, including the I<sup>2</sup>C controller interface to attach sensors or peripherals to the board. The 10-pin header male connectors come soldered in the OM-SE05xARD.
- **DB15 header:** It allows you to access several pins of the EdgeLock SE05x, including the ISO/IEC 14443 or the I<sup>2</sup>C controller interface to attach sensors or peripherals to the board. The OM-SE05xARD includes the mounting holes for the DB15 connector.

[Figure 2](#) shows an overview to OM-SE05xARD headers and connectors together with its corresponding pin description.

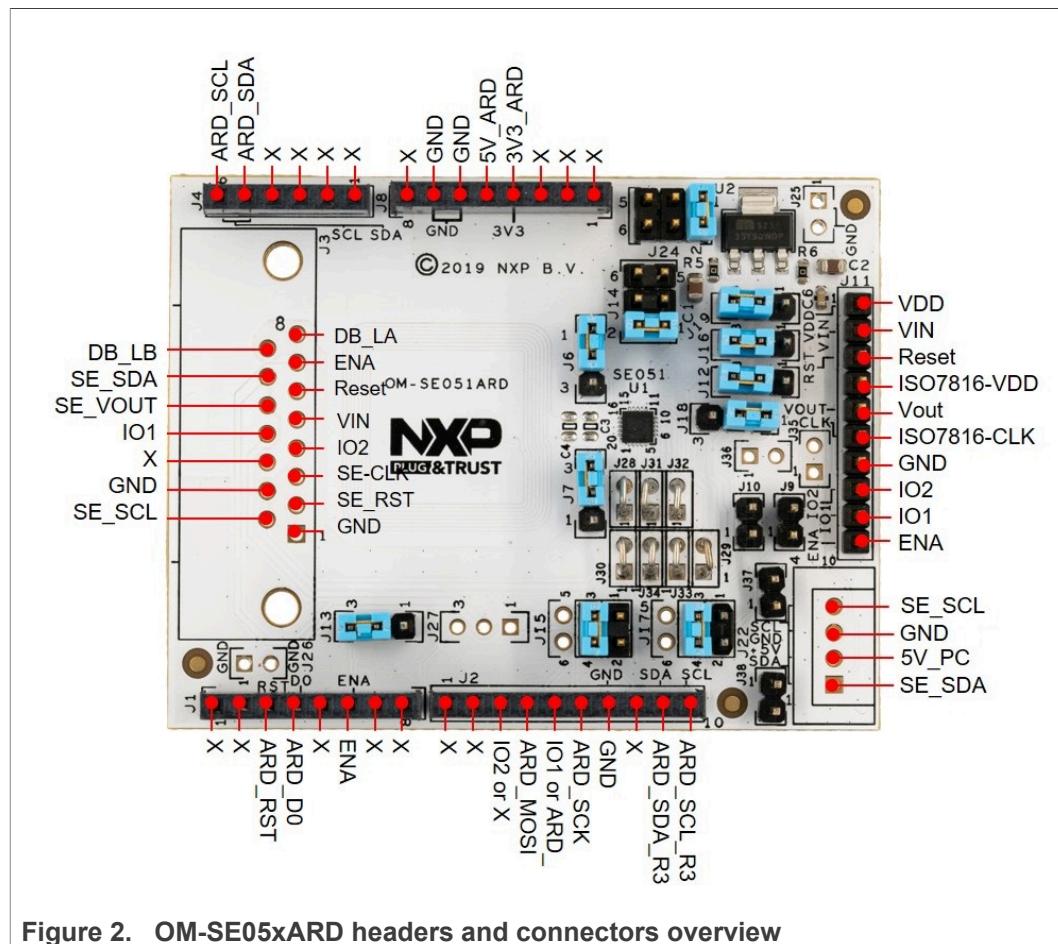


Figure 2. OM-SE05xARD headers and connectors overview

**Note:** The OM-SE05xARD schematic is available in [SE05xARD-SCH](#).

### 3 OM-SE05xARD board schematics

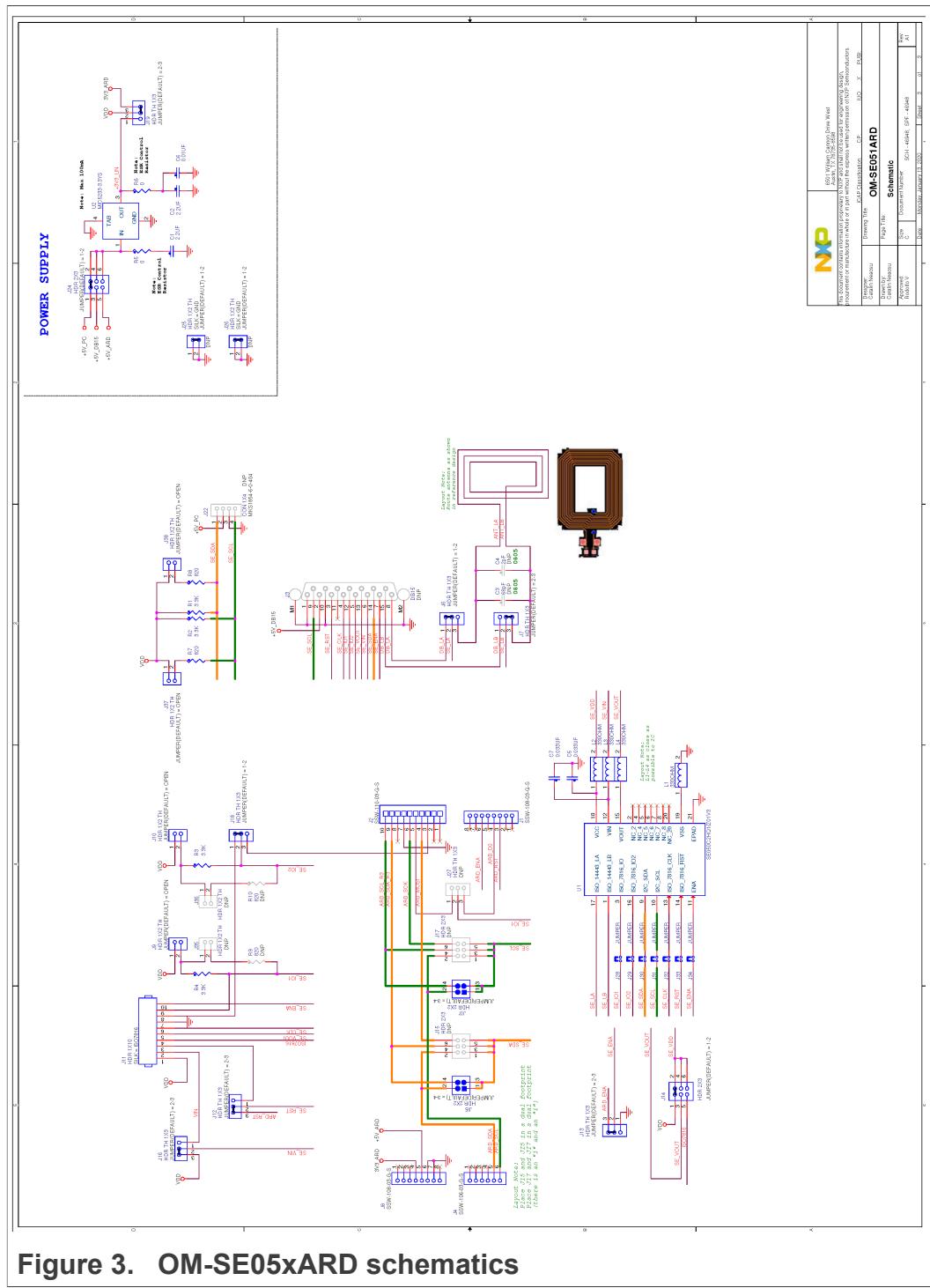


Figure 3. OM-SE05xARD schematics

Note: The OM-SE05xARD schematic is available in [SE05xARD-SCH](#).

## 4 Jumpers overview

The OM-SE05xARD board uses individual jumpers to configure settings related with the EdgeLock SE05x interfaces, power supply and power modes. This section provides an overview to the OM-SE05xARD jumpers and its configuration options.

### 4.1 I<sup>2</sup>C configuration

The OM-SE05xARD has jumpers that allow you to control the configuration of the I<sup>2</sup>C target and controller interfaces available in EdgeLock SE05x. These jumpers are:

- J9, J10: Configures the I<sup>2</sup>C controller pull up connection.
- J15, J17: Configures the I<sup>2</sup>C target connection.
- J37, J38: Configures the I<sup>2</sup>C target interface pull up resistor.

[Table 4](#) describes the OM-SE05xARD jumper settings for each I<sup>2</sup>C setting configuration.

**Table 4. Jumpers for I<sup>2</sup>C configuration**

Jumper	Description	Open	1-2	3-4
J9	I <sup>2</sup> C controller pull up connection	not connected (Default)	3k3 Ohm	n.a.
J10	I <sup>2</sup> C controller pull up connection	not connected (Default)	3k3 Ohm	n.a.
J15	I <sup>2</sup> C target SDA connection	not connected	Arduino R3 J4:5	Arduino R3 J2:9 (Default)
J17	I <sup>2</sup> C target SCL connection	not connected	Arduino R3 J4:6	Arduino R3 J2:10 (Default)
J18	SE05x_IO2 routing	n.a	Routed to J11:9 (Default)	Routed to J2:3
J37	I <sup>2</sup> C target SCL pull up	3k3 Ohm (Default, FastMode)	660 Ohm (HS-Mode)	n.a.
J38	I <sup>2</sup> C target SDA pull up	3k3 Ohm (Default, FastMode)	660 Ohm (HS-Mode)	n.a.

[Figure 4](#) highlights in blue the location of the OM-SE05xARD for I<sup>2</sup>C settings configuration.

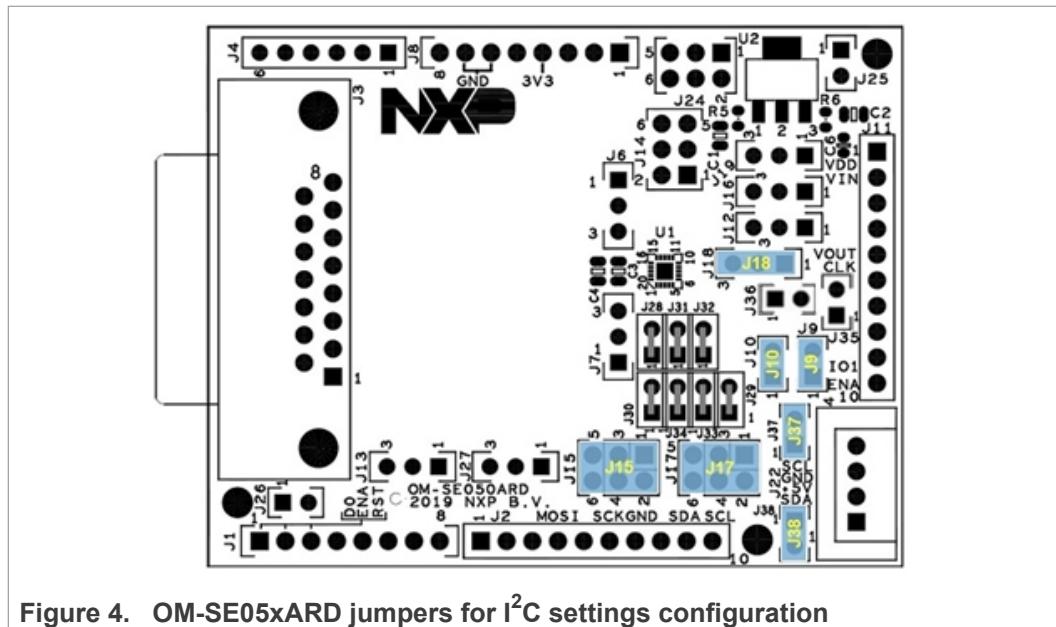


Figure 4. OM-SE05xARD jumpers for  $I^2C$  settings configuration

## 4.2 Power supply options

The jumpers that allow you to change the OM-SE05xARD power supply settings are:

- J19: Configures  $V_{DD}$  supply voltage options.
- J16: Configures SE05x\_  $V_{IN}$  supply options.
- J24: Configures  $V_{DD}$  supply voltage options in case the LDO is used.

[Table 5](#) describes the OM-SE05xARD jumper settings for each power supply settings configuration.

Table 5. Jumpers for power supply settings configuration

Jumper	Description	1-2	2-3	3-4	5-6
J16	EdgeLock SE05x_ $V_{IN}$ supply	Supplied by J11:2 pin	Supplied by the $V_{DD}$ (see J19) (Default)	n.a.	n.a.
J19	$V_{DD}$ supply voltage	From LDO	From 3V3_ ARD pin (Default)	n.a.	n.a.
J24	$V_{DD}$ supply voltage (if LDO is used)	From 5V_ PC (External $I^2C$ connector - Default)	n.a.	From 5V_ DB15 pin	From 5V_ ARD pin

[Figure 5](#) shows the power supply unit schematics.

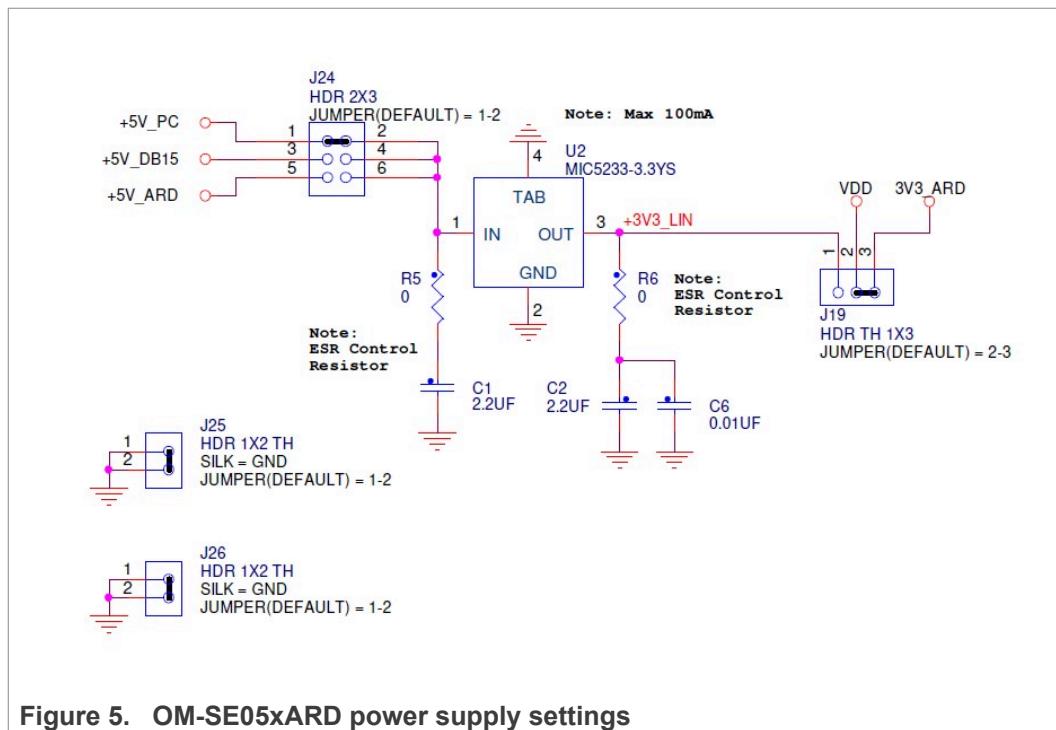


Figure 5. OM-SE05xARD power supply settings

Figure 6 highlights in blue the location of the OM-SE05xARD for power supply settings configuration.

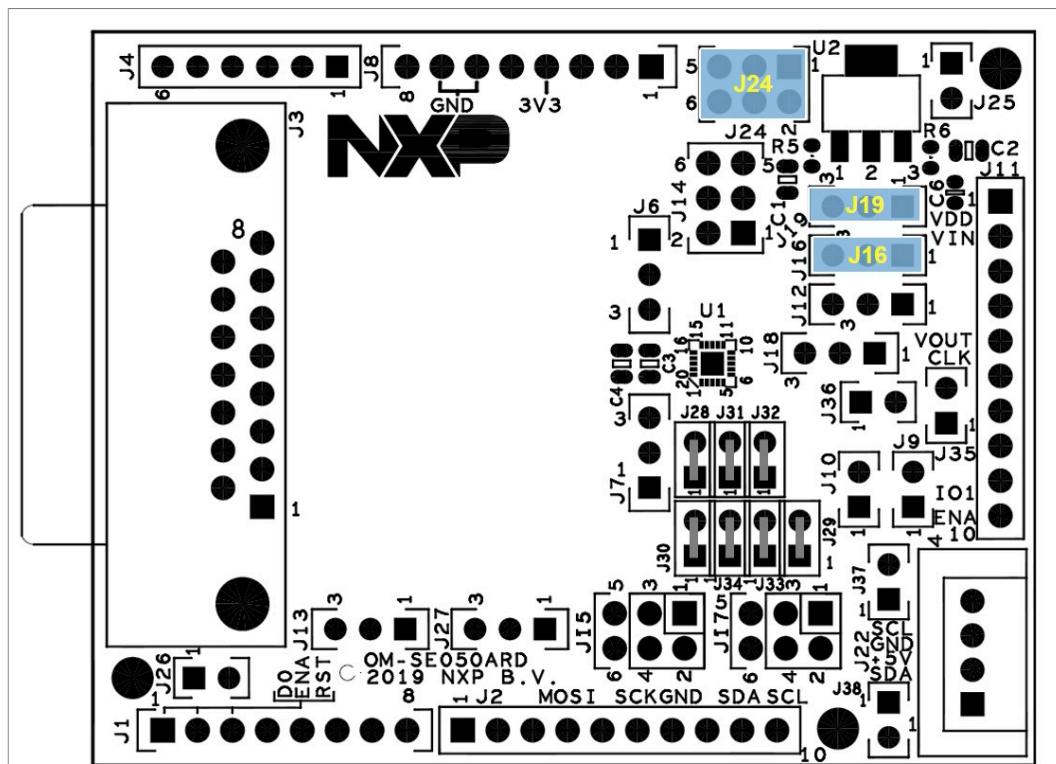
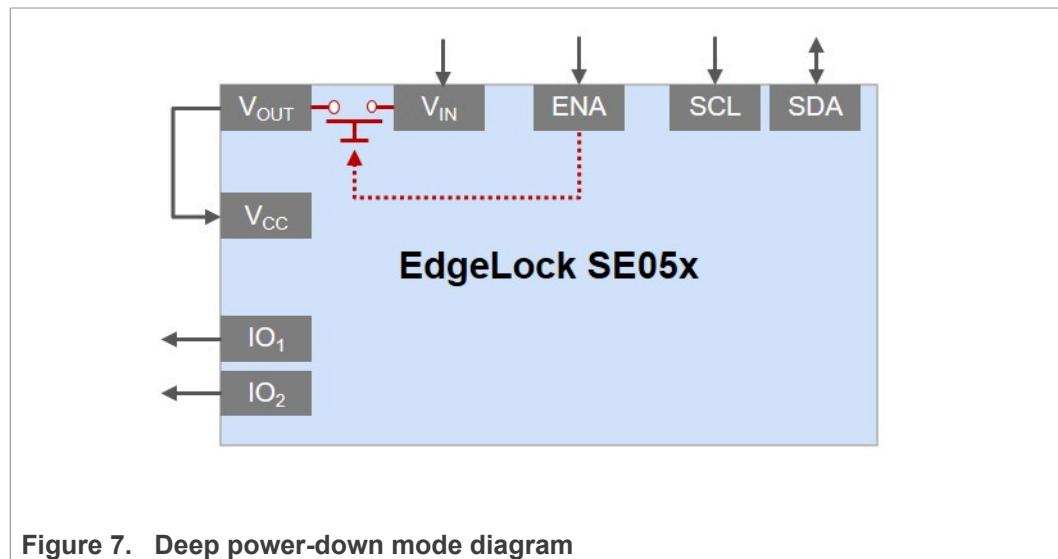


Figure 6. OM-SE05xARD jumpers for power settings configuration

### 4.3 Deep power-down mode

The deep power-down mode reduces the EdgeLock SE05x power consumption to the minimum. In this mode, only I<sup>2</sup>C pads stay supplied via V<sub>in</sub>. The deep power-down mode is enabled by setting the ENA pin to a logic zero. In addition, it is required to supply V<sub>in</sub> pin and connect V<sub>out</sub> and V<sub>cc</sub> pins at the PCB level.

The ENA pin controls an internal switch between V<sub>out</sub> and V<sub>in</sub> as shown in [Figure 7](#). Therefore, if V<sub>out</sub> is connected to V<sub>cc</sub>, the ENA pin can effectively switch the power on and off to V<sub>cc</sub>.



**Figure 7. Deep power-down mode diagram**

The jumpers J13 and J14 of the OM-SE05xARD allow you to control the EdgeLock SE05x deep power-down mode. To enable the deep power-down mode using the OM-SE05xARD:

- J13: Must be set to position 2-3.
- J14: Must be set to position 3-4.

[Table 6](#) describes the OM-SE05xARD jumper settings for the deep power-down mode configuration

**Table 6. Jumpers for deep power-down mode configuration**

Jumper	Description	1-2	2-3	3-4	5-6
J13	EdgeLock SE05x_ENA pin routing	ENA low. Switch disabled	ENA controlled by Arduino R3 (Default)	n.a.	n.a.
J14	EdgeLock SE05x_VCC pin routing	Routed to V <sub>DD</sub> supply voltage	n.a.	Routed to SE05x_V <sub>out</sub> pin (Default)	Routed to J11:4 pin

[Figure 8](#) highlights in blue the location of jumper J13 and J14.

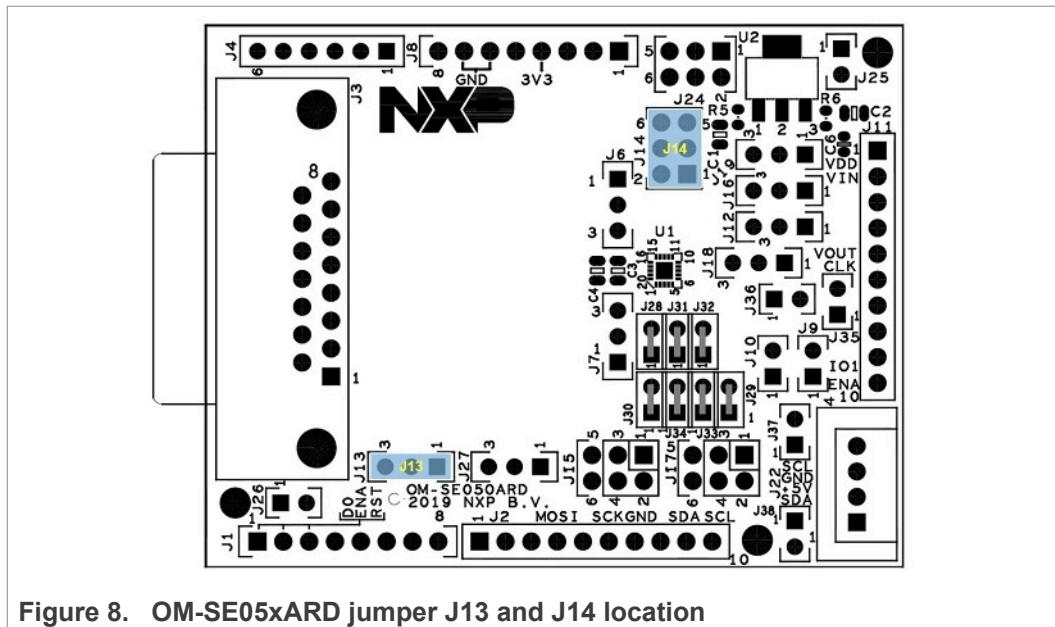


Figure 8. OM-SE05xARD jumper J13 and J14 location

#### 4.4 Reset pin routing

Jumper J12 allows you to control the I<sup>2</sup>C reset pin routing of the EdgeLock SE05x. [Table 7](#) indicates the J12 configuration.

**Note:** The EdgeLock SE05x reset pin does not apply for the I<sup>2</sup>C interface.

Table 7. Jumpers for reset pin routing configuration

Jumper	Description	Open	1-2	2-3
J12	EdgeLock SE05x_RST pin	Not connected	Routed to J11:3 strip pin connector	Routed to Arduino R3 (Default)

[Figure 9](#) highlights in blue the location of Jumper J12.

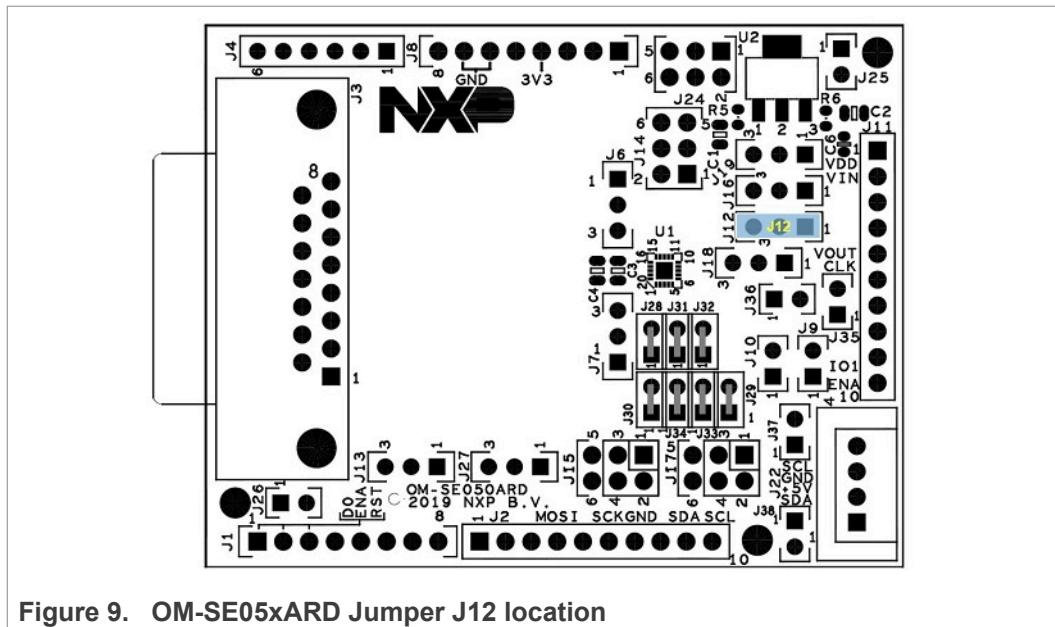


Figure 9. OM-SE05xARD Jumper J12 location

#### 4.5 ISO/IEC14443 contactless interface

Jumper J6 and J7 allow you to control the EdgeLock SE05x contactless interface and allows you to select which antenna shall be used for contactless communication. [Table 8](#) indicates J6 and J7 jumper settings.

Table 8. Jumpers for ISO/IEC14443 contactless interface settings

Jumper position	Description
J6: 2-3 and J7: 1-2	Contactless operation disabled
J6: 1-2 and J7: 2-3	Contactless operation disabled (Default)
J6: 2-3 and J7: 2-3	Contactless operation enabled with OM-SE05xARD internal antenna
J6: 1-2 and J7: 1-2	Contactless operation enabled with external ID1 antenna through DB15 connector

**Note:** OM-SE050ARD-E kit does not support ISO/IEC 14443-4-A.

[Figure 10](#) highlights in blue the location of jumpers J6 and J7.

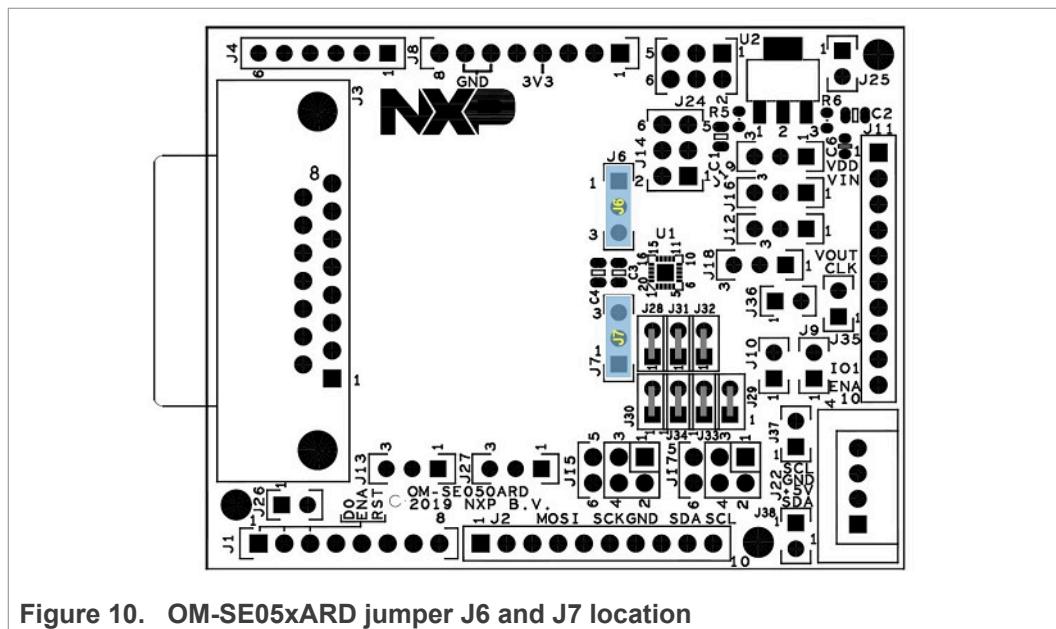


Figure 10. OM-SE05xARD jumper J6 and J7 location

## 5 OM-SE05xARD board use cases

This section details the jumper settings to configure the different interfaces and to enable specific use cases with the OM-SE05xARD board.

### 5.1 EdgeLock SE05x via Arduino header

This section details the jumper configuration to enable the I<sup>2</sup>C target interface in the Arduino header. The related jumpers of the OM-SE050ARD-E for I<sup>2</sup>C target interface configuration are:

- J37 and J38: Configure the pull up resistors of the I<sup>2</sup>C interface.
- J19: Configures V<sub>DD</sub> supply voltage options.
- J24: Configures V<sub>DD</sub> supply voltage options in case the LDO is used.

**Table 9. Jumper settings for I<sup>2</sup>C target interface configuration**

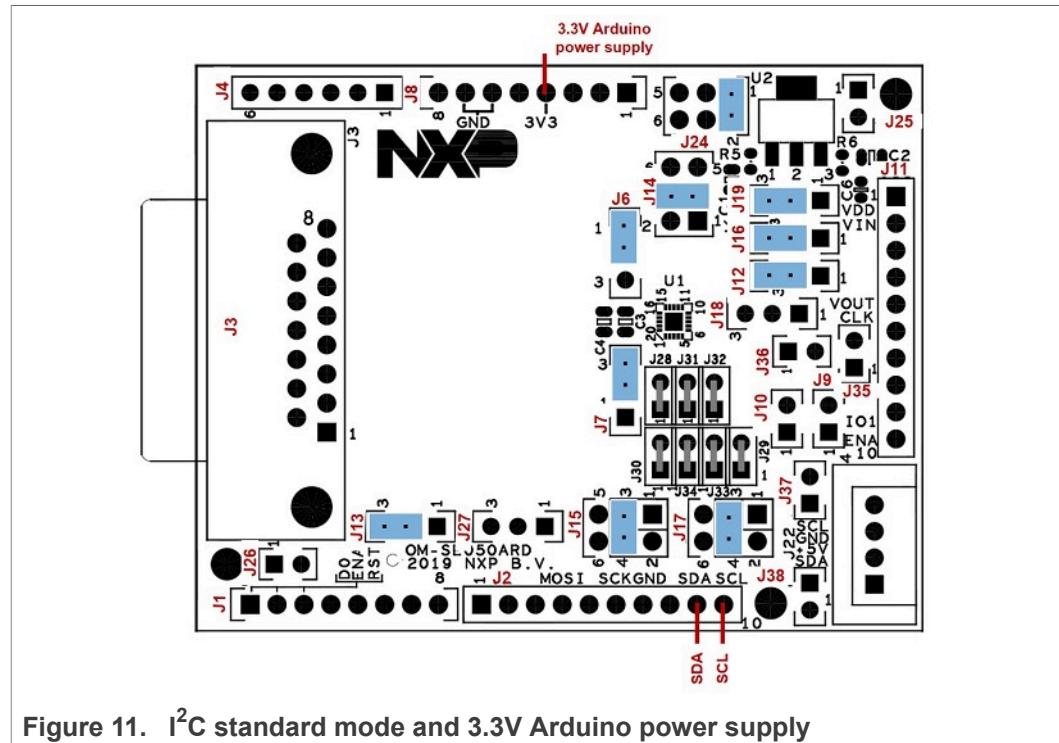
Jumper	Configuration	Comment
J6	<b>Set to 1-2 (Default)</b>	Contactless operation disabled
J7	<b>Set to 2-3 (Default)</b>	Contactless operation disabled
J9, J10	<b>Set to “Open” (Default)</b>	I <sup>2</sup> C controller pull ups disabled
J12	<b>Set to 2-3 (Default)</b>	SE_RST routed to ARD_RST on J1:3
J13	<b>Set to 2-3 (Default)</b>	SE_ENA set to ARD_ENA on J1:6
J14	<b>Set to 3-4 (Default)</b>	SE_V <sub>OUT</sub> as SE_V <sub>DD</sub>
J15	<b>Set to 3-4 (Default)</b>	I <sup>2</sup> C_SDA routed to ARD_SDA_R3 (J2:9)
	Set to 1-2	I <sup>2</sup> C_SDA routed to ARD_SDA (J4:5)
J16	Set to 2-3	V <sub>DD</sub> as SE_V <sub>IN</sub>
J17	<b>Set to 3-4 (Default)</b>	I <sup>2</sup> C_SCL routed to ARD_SCL_R3 (J2:10)
	Set to 1-2	I <sup>2</sup> C_SCL routed to ARD_SCL (J4:6)
J19	<b>Set to 2-3 (Default)</b>	SE_V <sub>DD</sub> =3.3V from Arduino-R3 voltages
	Set to 1-2	SE_V <sub>DD</sub> =3.3V from LDO.
J24	<b>Set to 1-2 (Default)</b>	No input LDO
	Set to 5-6	5V_ARD to LDO
J25, J26	Do not care	Dummy jumpers
J37, J38	<b>Set to “Open” (Default)</b>	3k3 pull-up resistor for I <sup>2</sup> C standard mode

**Table 9.** Jumper settings for I<sup>2</sup>C target interface configuration...continued

Jumper	Configuration	Comment
	Set to "Closed"	Additional 820 Ohm parallel pull-up resistor for I <sup>2</sup> C high speed mode

[Figure 11](#) shows the jumper settings to configure the I<sup>2</sup>C target in standard mode and 3.3V\_ARD supply voltage (no LDO).

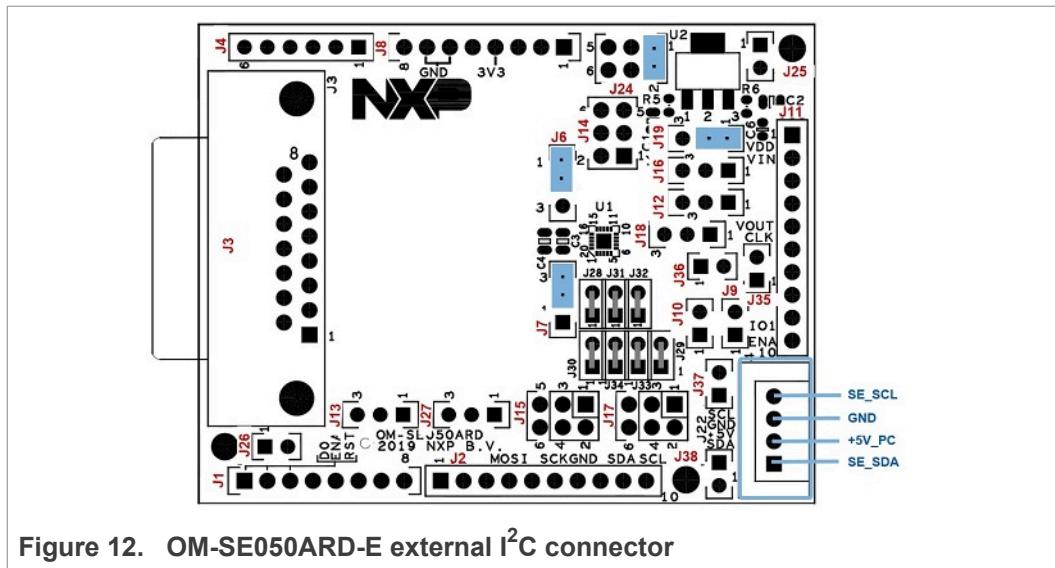
In this example, the jumper configuration used in [Figure 11](#) correspond to the values highlighted in bold in [Table 9](#) (J15, J17, J19, J24, J37 and J38).

**Figure 11.** I<sup>2</sup>C standard mode and 3.3V Arduino power supply

You may modify the I<sup>2</sup>C mode or power supply settings just changing the jumper settings accordingly as indicated in [Table 9](#).

## 5.2 SE05x via external I<sup>2</sup>C connector

[Figure 12](#) shows the jumper settings to configure EdgeLock SE05x communication via external I<sup>2</sup>C connector:

Figure 12. OM-SE050ARD-E external I<sup>2</sup>C connector

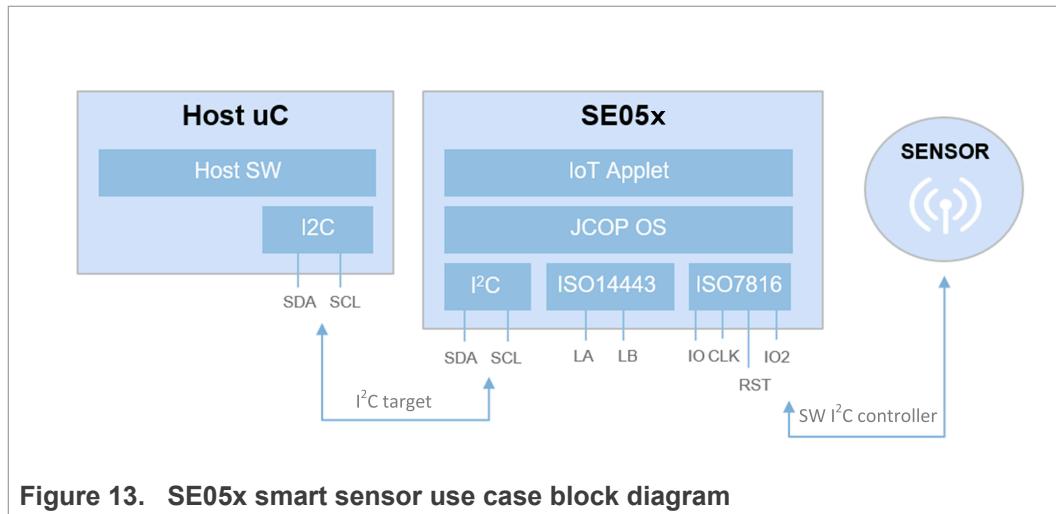
[Table 10](#) details the jumper settings for this configuration (External I<sup>2</sup>C connector).

Table 10. OM-SE050ARD-E external I<sup>2</sup>C connector

Jumper	Configuration	Comment
J6	Set to 1-2 (Default)	Contactless operation disabled
J7	Set to 2-3 (Default)	Contactless operation disabled
J9, J10	Do not care	
J12	Do not care	
J13	Do not care	
J14	Do not care	
J15	Do not care	
J16	Do not care	
J17	Do not care	
J19	Set to 1-2	3.3V from LDO as SE_V <sub>DD</sub>
J24	Set to 1-2 (Default)	5V_PC from external MCU board to LDO
J25, J26	Do not care	Dummy jumpers
J37, J38	Set to “Open” (Default)	3k3 pull-up resistor for I <sup>2</sup> C standard mode

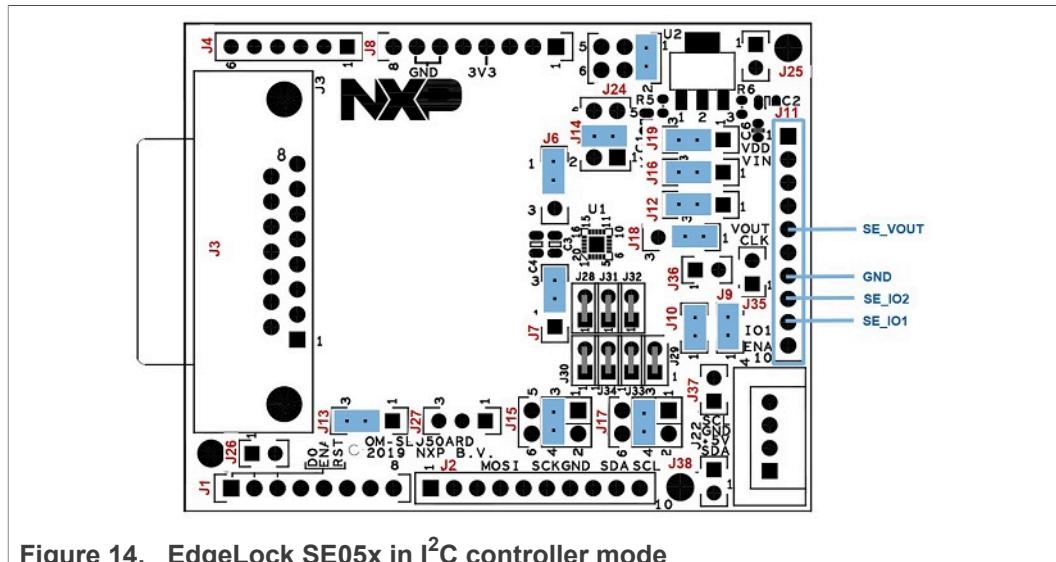
### 5.3 SE05x in I<sup>2</sup>C controller mode

This section details the jumper configuration to enable the I<sup>2</sup>C controller of the SE05x. The I<sup>2</sup>C controller interface can be used to connect a sensor securely. The SE05x guarantees the privacy and the authenticity of the data extracted by sensor. The data collected in the application over the SE05x private sensor can be transferred to the cloud for further treatment and analysis. The [Figure 13](#) shows the SE05x solution block diagram for this use case:



**Figure 13.** SE05x smart sensor use case block diagram

[Figure 14](#) shows the jumper settings to enable the SE05x I<sup>2</sup>C controller interface.



**Figure 14.** EdgeLock SE05x in I<sup>2</sup>C controller mode

[Table 11](#) details the jumper settings for the configuration of the SE05x I<sup>2</sup>C controller interface.

**Table 11. Jumper settings for EdgeLock SE05x in I<sup>2</sup>C controller mode**

Jumper	Configuration	Comment
J6	Set to 1-2 (Default)	Contactless operation disabled
J7	Set to 2-3 (Default)	Contactless operation disabled
J9, J10	Set to "Closed"	Set to "Closed" to enable pull-up resistors for I <sup>2</sup> C controller signals SE_IO1 and SE_IO2 ( <i>if IOT sensor board not already provides pull-up resistors</i> ).
J12	Set to 2-3 (Default)	SE_RST routed to ARD_RST on J1:3

**Table 11.** Jumper settings for EdgeLock SE05x in I<sup>2</sup>C controller mode...continued

Jumper	Configuration	Comment
J13	Set to 2-3 (Default)	SE_ENA set to ARD_ENA on J1:6
J14	Set to 3-4 (Default)	SE_V <sub>OUT</sub> as SE_V <sub>DD</sub>
J15	Set to 3-4 (Default)	I <sup>2</sup> C_SDA routed to ARD_SDA_R3 (J2:9)
J16	Set to 2-3	V <sub>DD</sub> as SE_V <sub>IN</sub>
J17	Set to 3-4 (Default)	I <sup>2</sup> C_SCL routed to ARD_SCL_R3 (J2:10)
J18	Set 1-2 (Default)	SE_IO2 to pin 9 of header J11
J19	Set to 2-3 (Default)	SE_V <sub>DD</sub> =3.3V from Arduino-R3 voltages
J24	Set to 1-2 (Default)	No input LDO
J25, J26	Do not care	Dummy jumpers
J37, J38	Set to “Open” (Default)	3k3 pull-up resistor for I <sup>2</sup> C standard mode

## 5.4 EdgeLock SE05x via ISO14443 mode

This section details the jumper settings to operate the OM-SE05xARD via the ISO/IEC14443 interface.

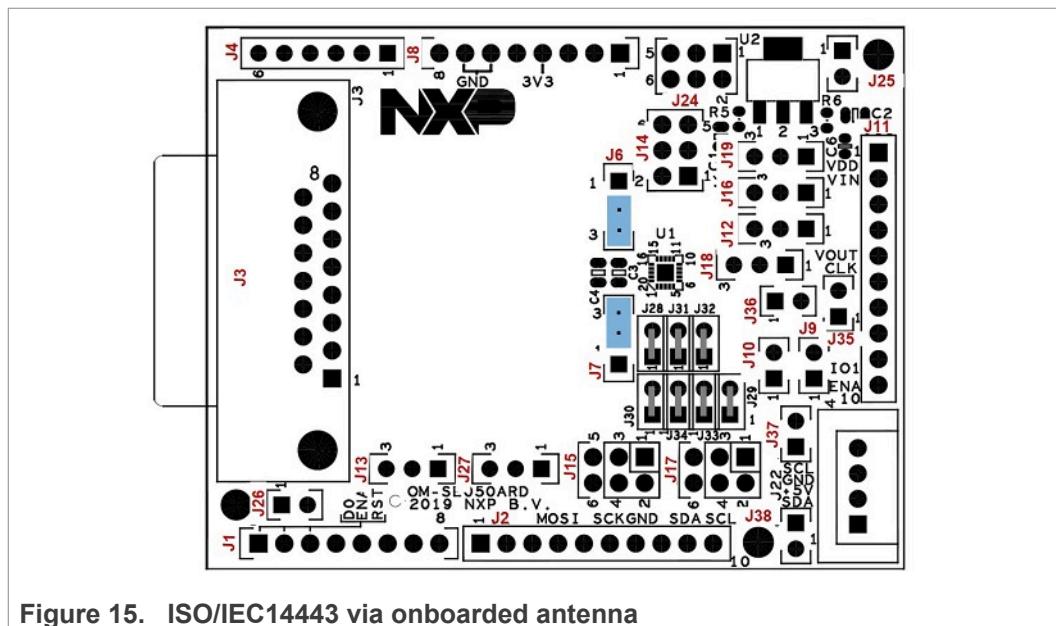
**Note:** Only the I<sup>2</sup>C target interface is mandatory. The I<sup>2</sup>C controller and ISO/IEC 14443 interfaces are optional.

**Note:** OM-SE050ARD-E kit does not support ISO/IEC 14443-4-A.

### 5.4.1 ISO/IECC 144443-A via onboarded antenna

[Figure 15](#) shows the jumper settings to configure the contactless interface via the onboarded antenna in the OM-SE05xARD board.

**Note:** The IC selects the active interface on boot up, only one interface will be active. Take care for the interface precedence on IC boot up as described in the datasheet section "startup behavior" as I<sup>2</sup>C takes precedence over the contactless interface.



**Figure 15.** ISO/IEC14443 via onboarded antenna

[Table 12](#) details the jumper settings for this configuration (ISO/IEC14443 via onboarded antenna).

**Table 12. ISO/IEC14443 via onboarded antenna**

Jumper	Configuration	Comment
J6	Set to 2-3	Contactless operation enabled with onboarded antenna
J7	Set to 2-3	Contactless operation enabled with onboarded antenna

#### 5.4.2 ISO/IECC 144443-A via external antenna

[Figure 16](#) shows the jumper settings to configure the contactless interface via an IN-CLA7816 probe connected through DB15 connector.

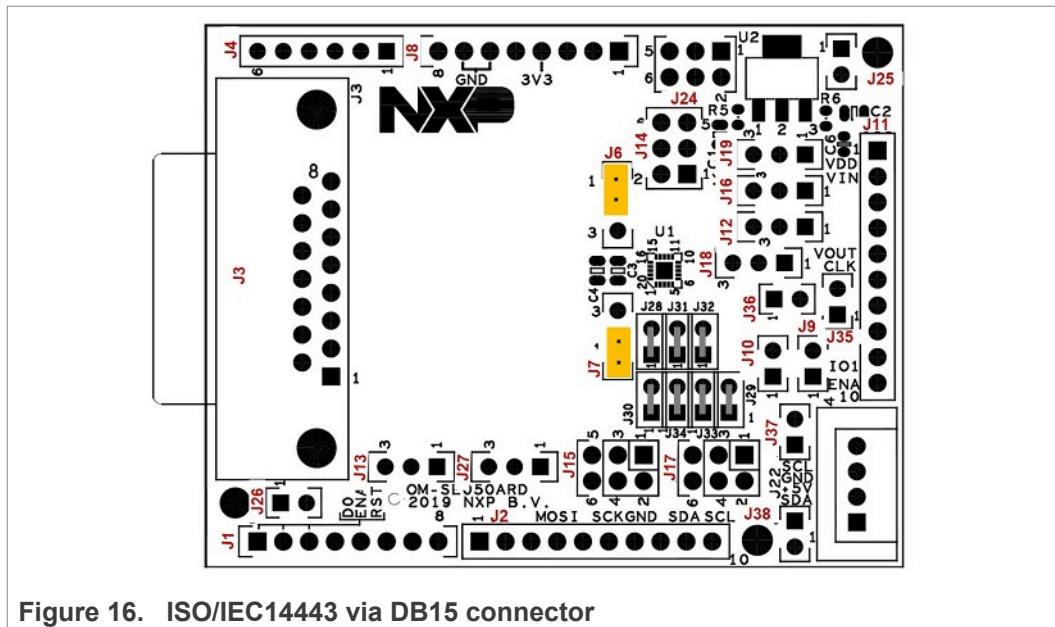


Figure 16. ISO/IEC14443 via DB15 connector

[Table 13](#) details the jumper settings for this configuration (ISO/IECC 144443-A via external antenna).

Table 13. ISO/IEC14443 via DB15 connector

Jumper	Configuration	Comment
J6	Set to 1-2	Contactless operation enabled with external ID1 antenna through DB15 connector
J7	Set to 1-2	Contactless operation enabled with external ID1 antenna through DB15 connector

#### 5.4.3 ISO/IEC 14443 via DB15 connector

[Figure 17](#) shows an external contactless interface connected to an IN-CLA7816 probe through DB15 connector.



Figure 17. External contactless interface connected to an IN-CLA7816 probe through DB15 connector

## 6 OM-SE05xARD technical operation description

Please refer to application note 'AN13013 - Get started with EdgeLock SE05x support package' how to get started with the OM-SE05xARD board and for getting familiar with EdgeLock SE050 and SE051 support package. The document is available at this location: <http://www.nxp.com/SE050> respectively [www.nxp.com/SE051](http://www.nxp.com/SE051).

## 7 Legal information

### 7.1 Definitions

**Draft** — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

### 7.2 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors. In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based

on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Evaluation products** — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer. In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages. Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

**Security** — Customer understands that all NXP products may be subject to unidentified or documented vulnerabilities. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP. NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

### 7.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## Tables

Tab. 1.	OM-SE05xARD development kit ordering details .....	3
Tab. 2.	Supported interfaces .....	4
Tab. 3.	RED information .....	5
Tab. 4.	Jumpers for I2C configuration .....	8
Tab. 5.	Jumpers for power supply settings configuration .....	9
Tab. 6.	Jumpers for deep power-down mode configuration .....	11
Tab. 7.	Jumpers for reset pin routing configuration .....	12
Tab. 8.	Jumpers for ISO/IEC14443 contactless interface settings .....	13
Tab. 9.	Jumper settings for I2C target interface configuration .....	15
Tab. 10.	OM-SE050ARD-E external I2C connector .....	17
Tab. 11.	Jumper settings for EdgeLock SE05x in I2C controller mode .....	18
Tab. 12.	ISO/IEC14443 via onboarded antenna .....	20
Tab. 13.	ISO/IEC14443 via DB15 connector .....	21

## Figures

Fig. 1.	EdgeLock SE05x interface overview .....	3
Fig. 2.	OM-SE05xARD headers and connectors overview .....	6
Fig. 3.	OM-SE05xARD schematics .....	7
Fig. 4.	OM-SE05xARD jumpers for I2C settings configuration .....	9
Fig. 5.	OM-SE05xARD power supply settings .....	10
Fig. 6.	OM-SE05xARD jumpers for power settings configuration .....	10
Fig. 7.	Deep power-down mode diagram .....	11
Fig. 8.	OM-SE05xARD jumper J13 and J14 location .....	12
Fig. 9.	OM-SE05xARD Jumper J12 location .....	13
Fig. 10.	OM-SE05xARD jumper J6 and J7 location .....	14
Fig. 11.	I2C standard mode and 3.3V Arduino power supply .....	16
Fig. 12.	OM-SE050ARD-E external I2C connector .....	17
Fig. 13.	SE05x smart sensor use case block diagram .....	18
Fig. 14.	EdgeLock SE05x in I2C controller mode .....	18
Fig. 15.	ISO/IEC14443 via onboarded antenna .....	20
Fig. 16.	ISO/IEC14443 via DB15 connector .....	21
Fig. 17.	External contactless interface connected to an IN-CLA7816 probe through DB15 connector .....	21

## Contents

---

<b>1</b>	<b>Overview</b>	<b>3</b>
1.1	RED information	5
<b>2</b>	<b>Headers and connectors</b>	<b>6</b>
<b>3</b>	<b>OM-SE05xARD board schematics</b>	<b>7</b>
<b>4</b>	<b>Jumpers overview</b>	<b>8</b>
4.1	I2C configuration	8
4.2	Power supply options	9
4.3	Deep power-down mode	11
4.4	Reset pin routing	12
4.5	ISO/IEC14443 contactless interface	13
<b>5</b>	<b>OM-SE05xARD board use cases</b>	<b>15</b>
5.1	EdgeLock SE05x via Arduino header	15
5.2	SE05x via external I2C connector	16
5.3	SE05x in I2C controller mode	17
5.4	EdgeLock SE05x via ISO14443 mode	19
5.4.1	ISO/IECC 144443-A via onboarded antenna	19
5.4.2	ISO/IECC 144443-A via external antenna	20
5.4.3	ISO/IEC 14443 via DB15 connector	21
<b>6</b>	<b>OM-SE05xARD technical operation</b>	
	<b>description</b>	<b>22</b>
<b>7</b>	<b>Legal information</b>	<b>23</b>

---

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.