

| 序号 | 31 条 | ucosii V2.52 | 指令 | 指令说明 | 指令格式 | OP 31-26 | RS 25-21 | RT 20-16 | RD 15-11 | SA 10-6 | FUNCT 5-0 | 指令码 16 进制 |
|----|---------|-----------------|-------|---------------|-------------------------|-------------|-------------|-------------|-------------|------------|--------------|--------------|
| 1 | √ | √ | addi | 加立即数 | addi rt, rs, immediate | 001000 | | | | 00000 | 100000 | 20000000 |
| 2 | √ | √ | addiu | 加立即数（无符号） | addiu rd, rs, immediate | 001001 | | | | | | 24000000 |
| 3 | √ | √ | andi | 立即数与 | andi rt, rs, immediate | 001100 | | | | | | 30000000 |
| 4 | √ | √ | ori | 或立即数 | ori rt, rs, immediate | 001101 | | | | | | 34000000 |
| 5 | √ | √ | sltiu | 小于立即数置 1（无符号） | sltiu rt, rs, immediate | 001011 | | | | | | 2C000000 |
| 6 | √ | √ | lui | 立即数加载高位 | lui rt, immediate | 001111 | 00000 | | | | | 3C000000 |
| 7 | √ | | xori | 异或（立即数） | xori rt, rs, immediate | 001110 | | | 00000 | 00000 | 000000 | 38000000 |
| 8 | √ | | slti | 小于置 1（立即数） | slti rt, rs, immediate | 001010 | | | 00000 | 00000 | 000000 | 28000000 |
| 9 | √ | √ | addu | 加（无符号） | addu rd, rs, rt | 000000 | | | | 00000 | 100001 | 00000021 |
| 10 | √ | √ | and | 与 | and rd, rs, rt | 000000 | | | | 00000 | 100100 | 00000024 |
| 11 | √ | √ | beq | 相等时分支 | beq rs, rt, offset | 000100 | | | | | | 10000000 |
| 12 | √ | √ | bne | 不等时分支 | bne rs, rt, offset | 000101 | | | | | | 14000000 |
| 13 | √ | √ | j | 跳转 | j target | 000010 | | | | | | 08000000 |
| 14 | √ | √ | jal | 跳转并链接 | jal target | 000011 | | | | | | 0C000000 |
| 15 | √ | √ | jr | 跳转至寄存器所指地址 | jr rs | 000000 | | | | | 001000 | 00000009 |
| 16 | √ | √ | lw | 取字 | lw rt, offset(base) | 100011 | | | | | | 8C000000 |
| 17 | √ | √ | xor | 异或 | xor rd, rs, rt | 000000 | | | | 00000 | 100110 | 00000026 |
| 18 | √ | √ | nor | 或非 | nor rd, rs, rt | 000000 | | | | 00000 | 100111 | 00000027 |

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| 3 | 19 | ✓ | ✓ | or | 或 | or rd, rs, rt | 000000 | | | | 00000 | 100101 | 00000025 |
| 3 | 20 | ✓ | ✓ | sll | 逻辑左移 | sll rd, rt, sa | 000000 | 00000 | | | | 000000 | 00000000 |
| 3 | 21 | ✓ | ✓ | sliv | 逻辑左移 (位数可变) | sliv rd, rt, rs | 000000 | | | | 00000 | 000100 | 00000004 |
| 3 | 22 | ✓ | ✓ | sltu | 小于置 1 (无符号) | sltu rd, rs, rt | 000000 | | | | 00000 | 101011 | 0000002B |
| 3 | 23 | ✓ | ✓ | sra | 算数右移 | sra rd, rt, sa | 000000 | 00000 | | | | 000011 | 00000003 |
| 3 | 24 | ✓ | ✓ | srl | 逻辑右移 | srl rd, rt, sa | 000000 | 00000 | | | | 000010 | 00000002 |
| 3 | 25 | ✓ | ✓ | subu | 减 (无符号) | sub rd, rs, rt | 000000 | | | | 00000 | 100011 | 00000022 |
| 3 | 26 | ✓ | ✓ | sw | 存字 | sw rt, offset(base) | 101011 | | | | | | AC000000 |
| 3 | 27 | ✓ | | add | 加 | add rd, rs, rt | 000000 | | | | 00000 | 100000 | 00000020 |
| 3 | 28 | ✓ | | sub | 减 | sub rd, rs, rt | 000000 | | | | 00000 | 100010 | 00000022 |
| 3 | 29 | ✓ | | slt | 小于置 1 | slt rd, rs, rt | 000000 | | | | 00000 | 101010 | 0000002A |
| 3 | 30 | ✓ | | srlv | 逻辑右移 (位数可变) | srlv rd, rt, rs | 000000 | | | | 00000 | 000110 | 00000006 |
| 3 | 31 | ✓ | | srav | 算数右移 (位数可变) | srav rd, rt, rs | 000000 | | | | 00000 | 000111 | 00000007 |
| 4 | 32 | | ✓ | clz | 前导零计数 | clz rd, rs | 011100 | | | | 00000 | 100000 | 70000020 |
| 4 | 33 | | ✓ | divu | 除 (无符号) | divu rs, rt | 000000 | | | 00000 | 00000 | 011011 | 0000001B |
| 3 | 34 | | ✓ | eret | 异常返回 | eret | 010000 | 10000 | 00000 | 00000 | 00000 | 011000 | 42000018 |
| 3 | 35 | | ✓ | jalr | 跳转至寄存器所指定地址, 返回地址保存在 | jalr rs | 000000 | | 00000 | | | 001001 | 00000008 |
| 4 | 36 | | ✓ | lb | 取字节 | lb rt, offset(base) | 100000 | | | | | | 80000000 |
| 4 | 37 | | ✓ | lbu | 取字节 (无符号) | lbu rt, offset(base) | 100100 | | | | | | 90000000 |

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| 38 | | v | lhu | 取半字（无符号） | lhu rt, offset(base) | 100101 | | | | | | 94000000 |
| 39 | | v | sb | 存字节 | sb rt, offset(base) | 101000 | | | | | | A0000000 |
| 40 | | v | sh | 存半字 | sh rt, offset(base) | 101001 | | | | | | A4000000 |
| 41 | | | lh | 取半字 | lh rt, offset(base) | 100001 | | | | | | 84000000 |
| 42 | | v | mfc0 | 读 CP0 寄存器 | mfc0 rt, rd | 010000 | 00000 | | | 00000 | 000000 | 40000000 |
| 43 | | v | mfhi | 读 Hi 寄存器 | mfhi rd | 000000 | 00000 | 00000 | | 00000 | 010000 | 00000010 |
| 44 | | v | mflo | 读 Lo 寄存器 | mflo rd | 000000 | 00000 | 00000 | | 00000 | 010010 | 00000012 |
| 45 | | v | mtc0 | 写 CP0 寄存器 | mtc0 rt, rd | 010000 | 00100 | | | 00000 | 000000 | 40800000 |
| 46 | | v | mthi | 写 Hi 寄存器 | mthi rd | 000000 | | 00000 | 00000 | 00000 | 010001 | 00000011 |
| 47 | | v | mtlo | 写 Lo 寄存器 | mtlo rd | 000000 | | 00000 | 00000 | 00000 | 010011 | 00000013 |
| 48 | | v | mul | 乘 | mul rd, rs, rt | 011100 | | | | 00000 | 000010 | 70000002 |
| 49 | | v | multu | 乘（无符号） | multu rs, rt | 000000 | | | 00000 | 00000 | 011001 | 00000019 |
| 50 | | v | syscall | 系统调用 | syscall | 000000 | | | | | 001100 | 0000000C |
| 51 | | v | teq | 相等异常 | teq rs, rt | 000000 | | | | | 110100 | 00000034 |
| 52 | | v | bgez | 大于等于 0 时分支 | bgez rs, offset | 000001 | | 0001 | | | | 04010000 |
| 53 | | | break | 断点 | break | 000000 | | | | | 001101 | 0000000D |
| 54 | | | div | 除 | div rs, rt | 000000 | | | 00000 | 00000 | 011010 | 0000001A |